Intel® StrongARM® SA-1111 Development Module

Schematics

Advance Information

February 2000

Phase 4

Notice: This document contains information on products in the sampling and initial production phases of development. Revised information will be published when this product is available.

Order No: 278282-004

Schematics

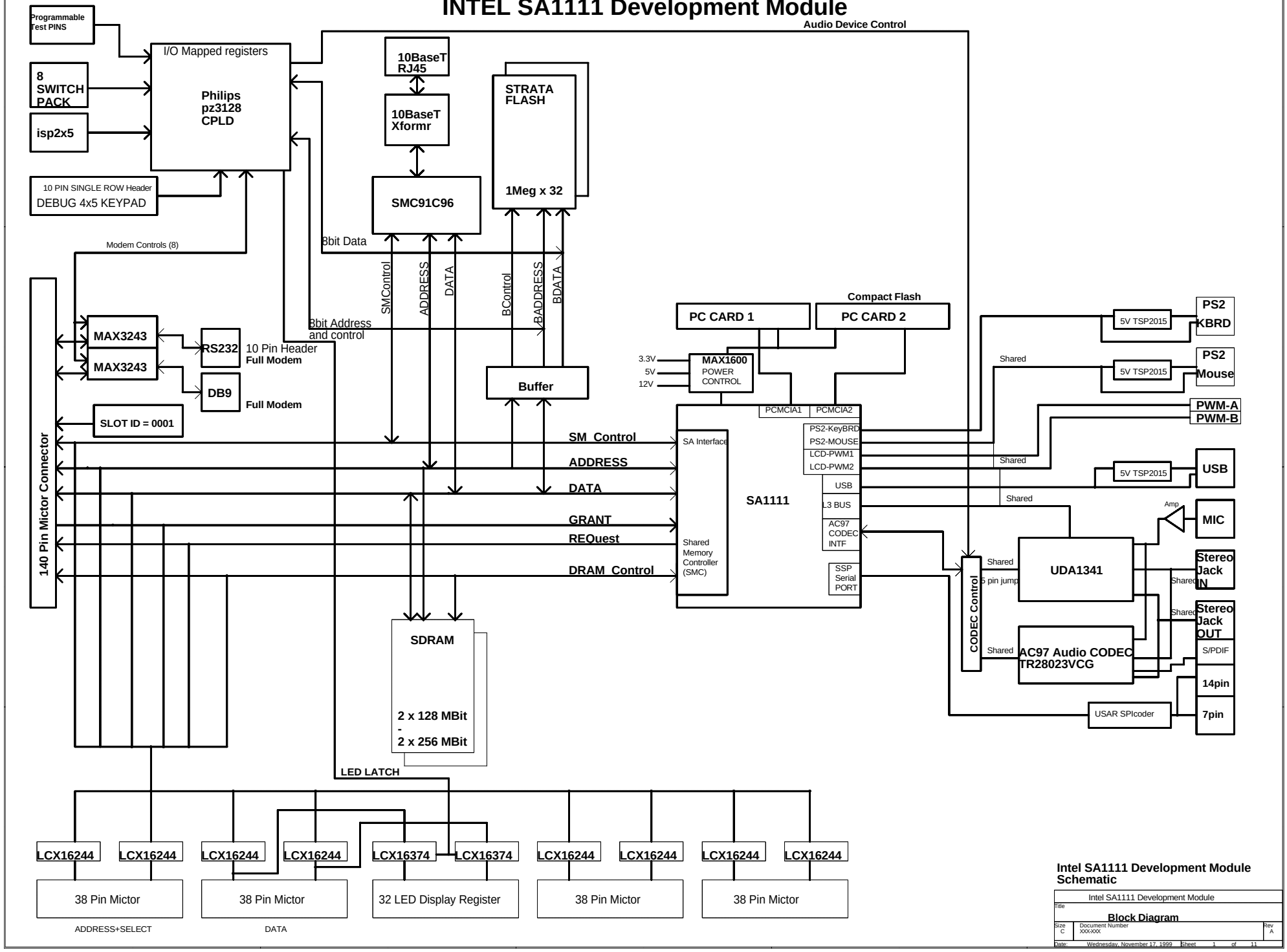
1.0 SA-1111 Development Module Schematics

This document contains the electrical schematics and revision information for the Intel® StrongARM® SA-1111 Development Module (SA-1111 Development Module).

Note: This document and module are for the Phase 4 hardware build of this product. For the latest information and updates, see the hardware release notes that are provided in hardcopy format, and the software readme.txt files that are provided in the software kits.
Sheet 11 of 11 has been omitted because it contains historical information.

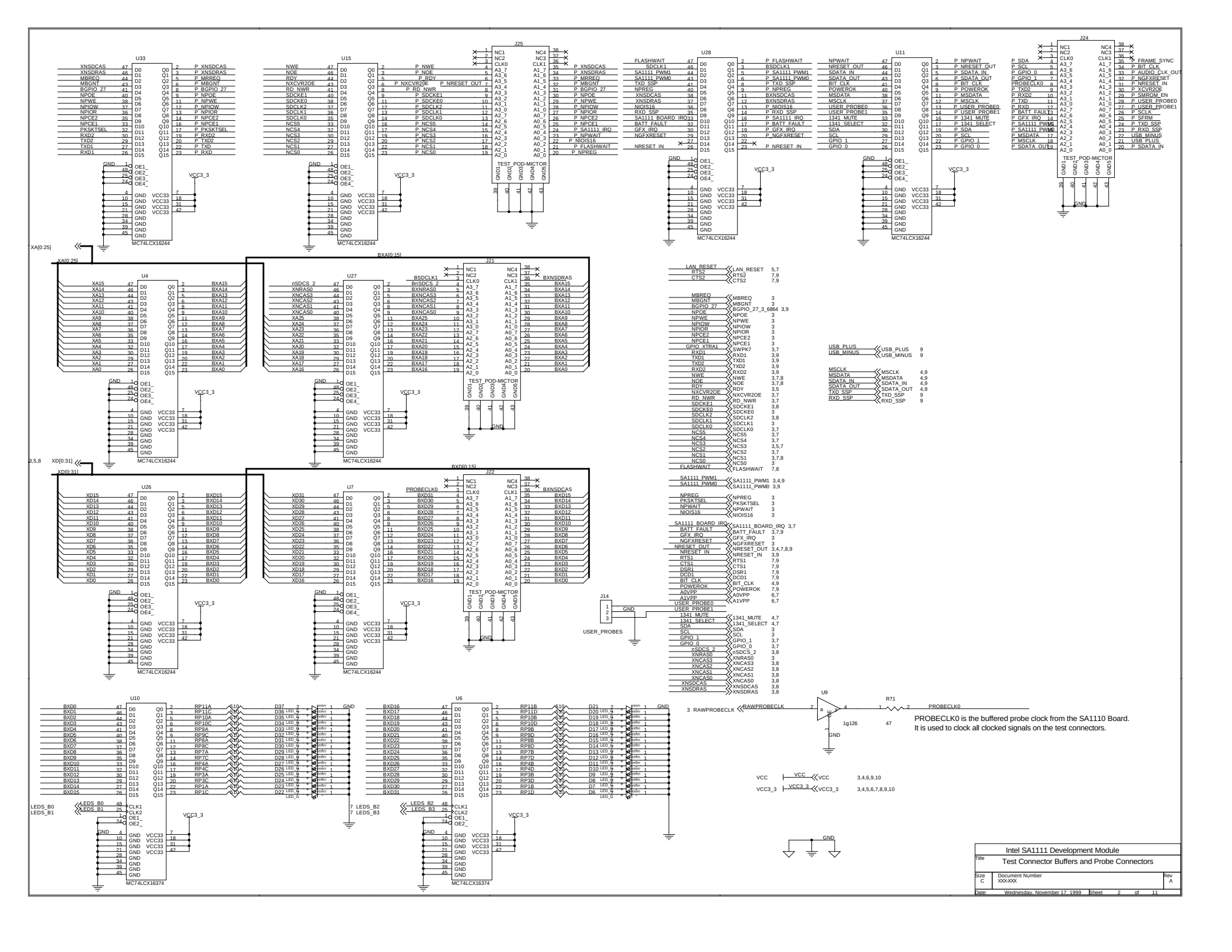
Schematics

INTEL SA1111 Development Module



Intel SA1111 Development Module Schematic

Intel SA1111 Development Module		
Title: **Block Diagram**		
Size: C	Document Number: XXX-XXX	Rev: A
Date: Wednesday, November 17, 1999	Sheet 1 of 11	


U33
U15
J25
U28
U11
J24
U4
U27
J21
U26
U7
J22
U10
U6
J14
U9
R71
XA[0:25]
BXA[0:15]
XD[0:31]
BXD[0:15]
MC74LCX16244
MC74LCX16374
TEST_POD-MICTOR
VCC3_3
GND
USER_PROBES
PROBECLK0 is the buffered probe clock from the SA1110 Board.
It is used to clock all clocked signals on the test connectors.
Intel SA1111 Development Module
Test Connector Buffers and Probe Connectors
Size C
Document Number XXX-XXX
Rev A
Date: Wednesday, November 17, 1999
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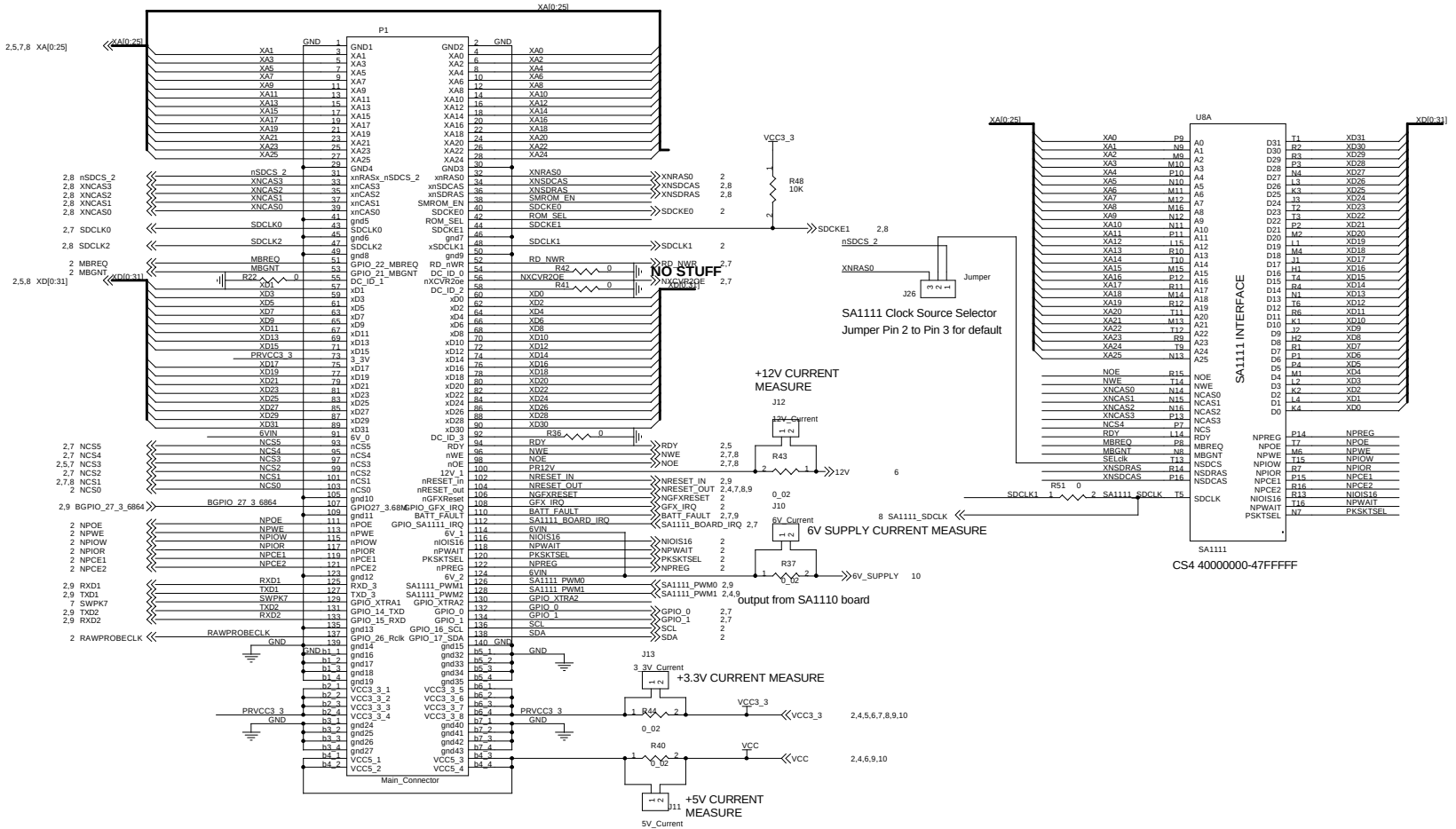

Intel SA1111 Development Module	
Title	MAIN CONNECTOR TO SA1110 BOARD
Size C	Document Number XXX-XXX
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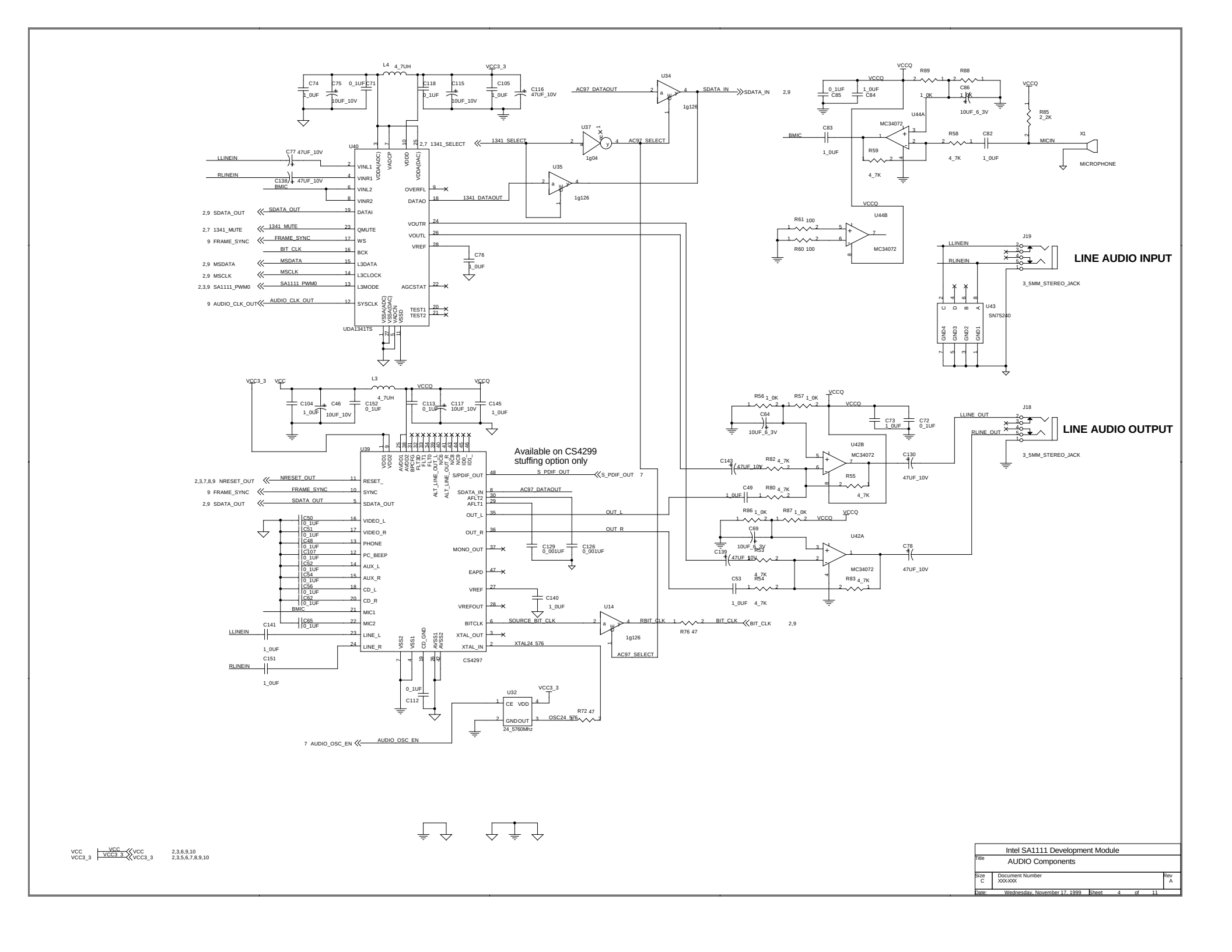
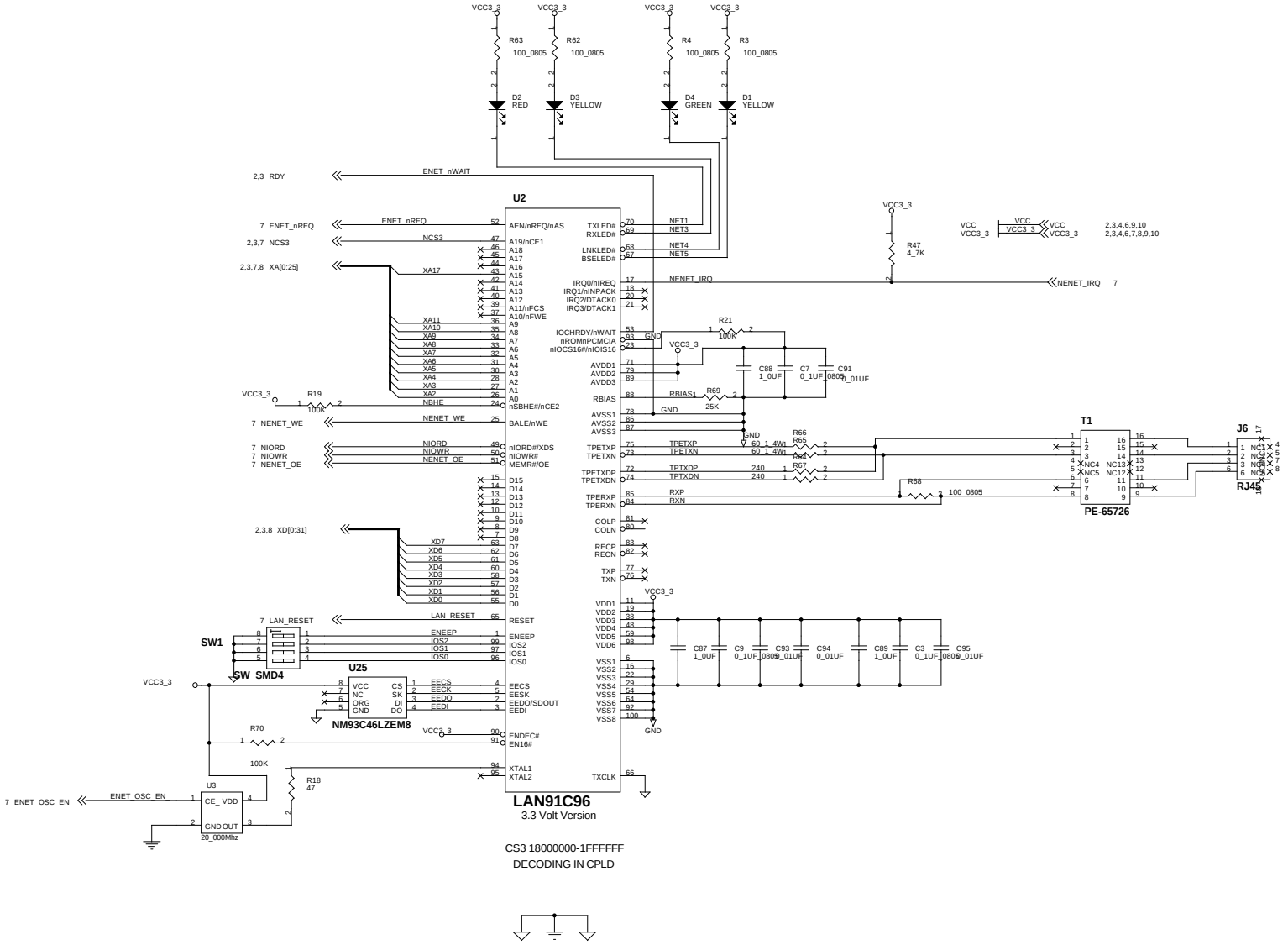
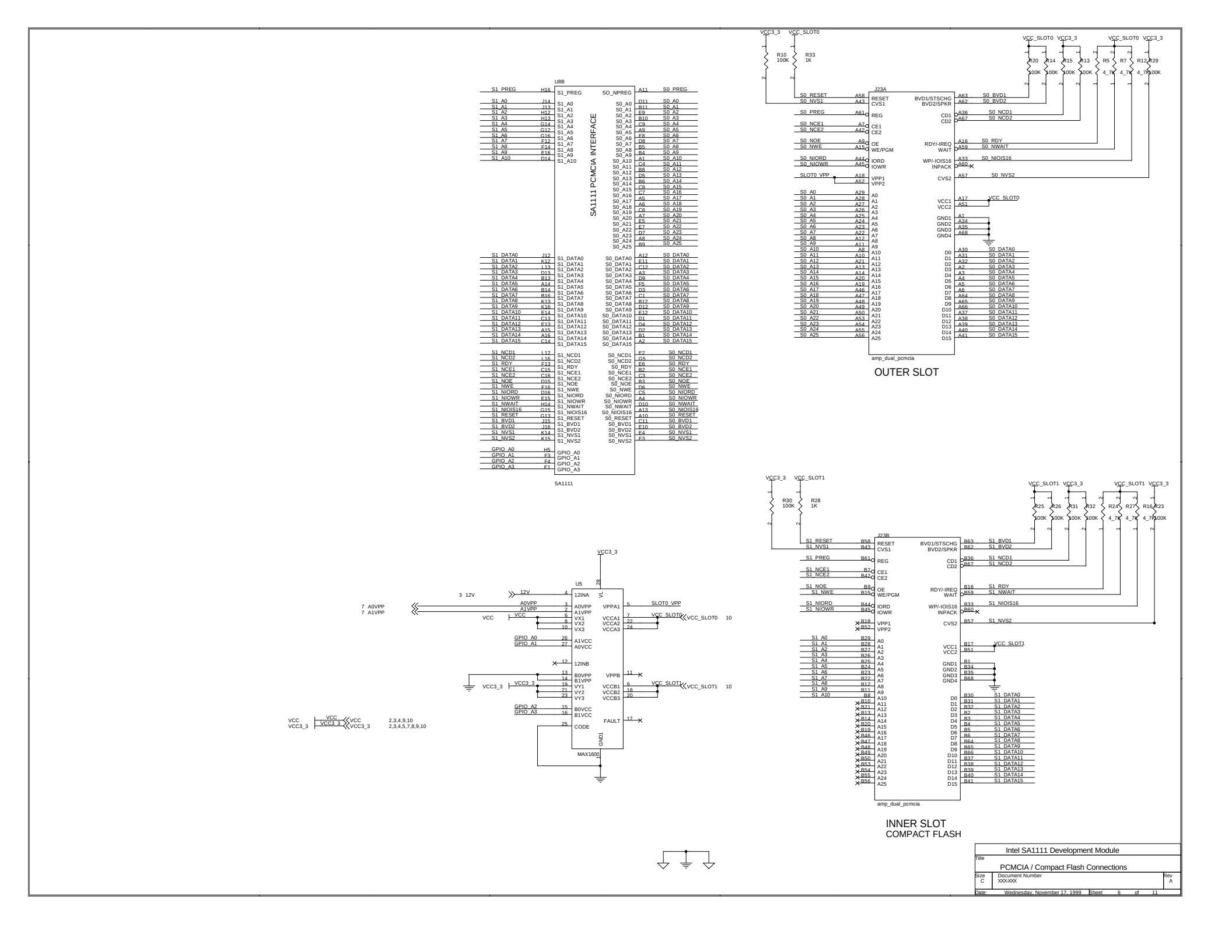
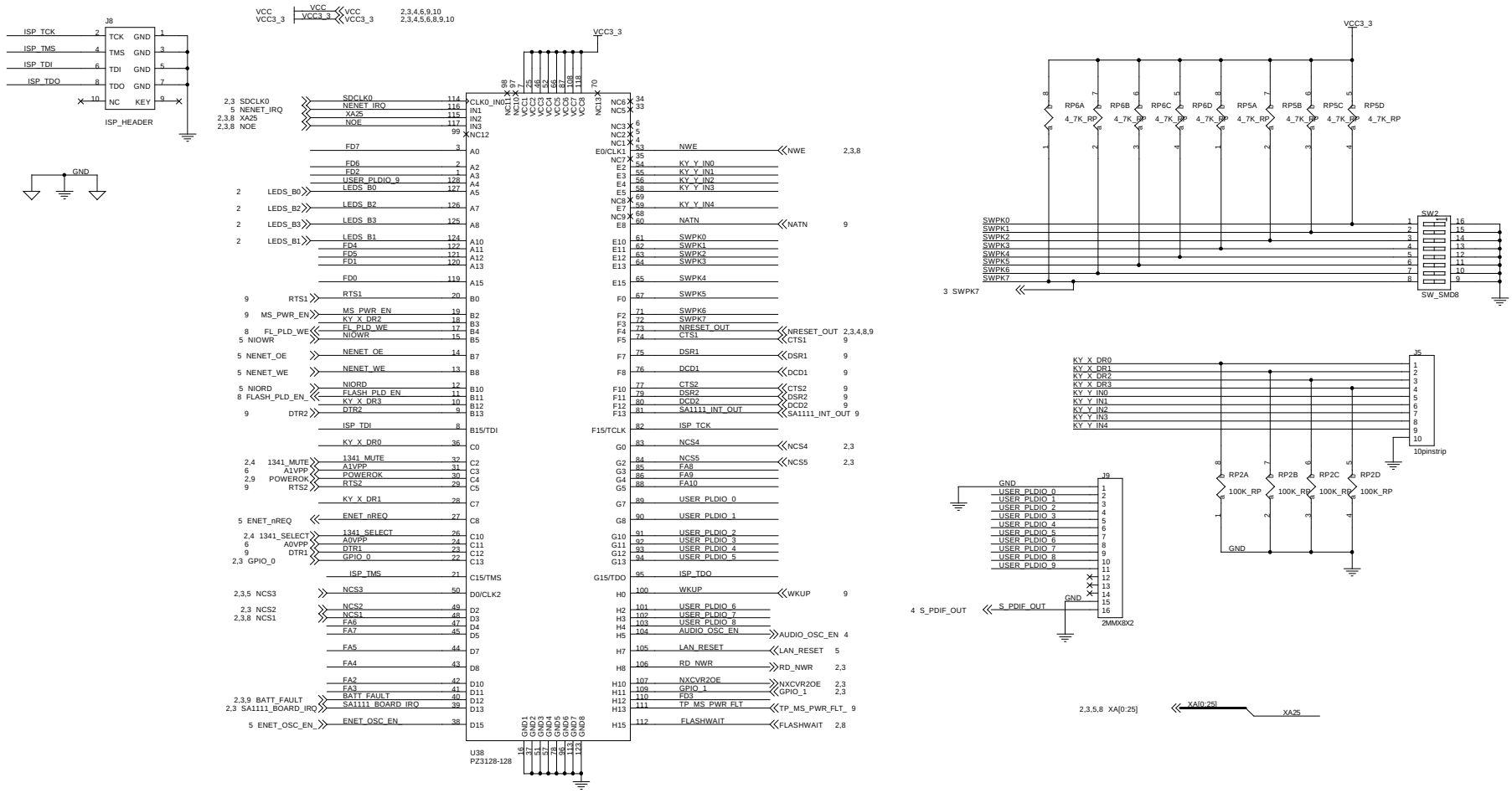
Date: Wednesday, November 17, 1999	Sheet 3 of 11


LINE AUDIO INPUT
LINE AUDIO OUTPUT
Available on CS4299
stuffing option only
UDA1341TS
CS4297
SN75240
MC34072
MICROPHONE
3_5MM_STEREO_JACK
24_5760Mhz
Intel SA1111 Development Module
AUDIO Components
Wednesday, November 17, 1999

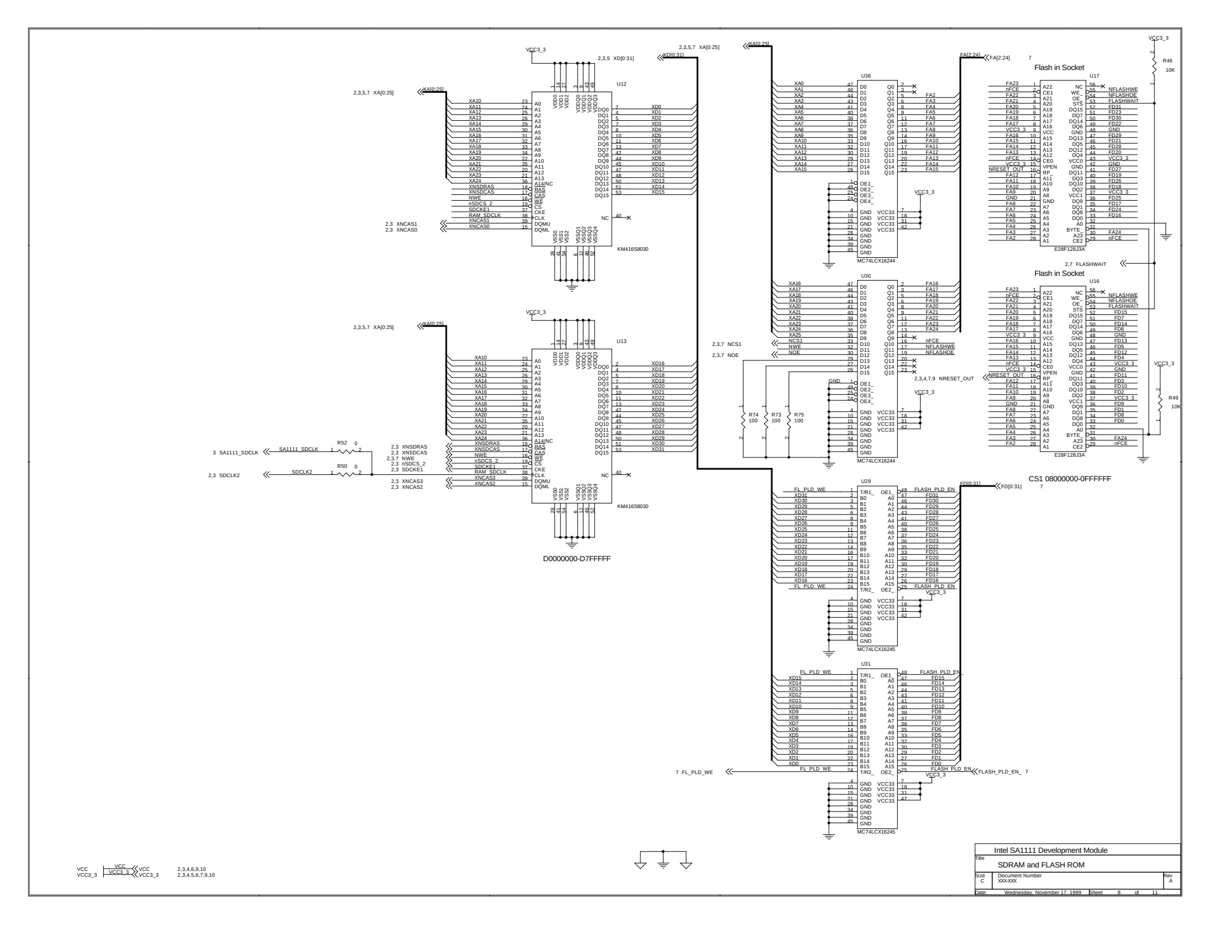


Intel SA1111 Development Module			
Title: Ethernet Interface			
Size: C	Document Number: XXX-XXX		Rev: A
Date: Wednesday, November 17, 1999	Sheet 5 of 11		


U8B
SA1111 PCMCIA INTERFACE
SA1111
OUTER SLOT
amp_dual_pcmcia
INNER SLOT
COMPACT FLASH
U5
MAX1600
J23A
J23B
R10 100K
R33 1K
R30 100K
R28 1K
VCC3_3
VCC_SLOT0
VCC_SLOT1
SLOT0_VPP
Intel SA1111 Development Module
PCMCIA / Compact Flash Connections
Document Number XXX-XXX
Wednesday, November 17, 1999
Sheet 6 of 11



Intel SA1111 Development Module	
Title	PLD Status LEDs Switch and Keypad
Size: C	Document Number: XXX-XXX
Rev: A	
Date: Wednesday, November 17, 1999	Sheet 7 of 11


U12
KM416S8030
U13
KM416S8030
D0000000-D7FFFFF
U36
MC74LCX16244
U30
MC74LCX16244
U29
MC74LCX16245
U31
MC74LCX16245
Flash in Socket
U17
E28F128J3A
Flash in Socket
U16
E28F128J3A
CS1 08000000-0FFFFFF
R74 100
R73 100
R75 100
R46 10K
R49 10K
R52 0
R50 0
Intel SA1111 Development Module
SDRAM and FLASH ROM
Document Number XXX-XXX
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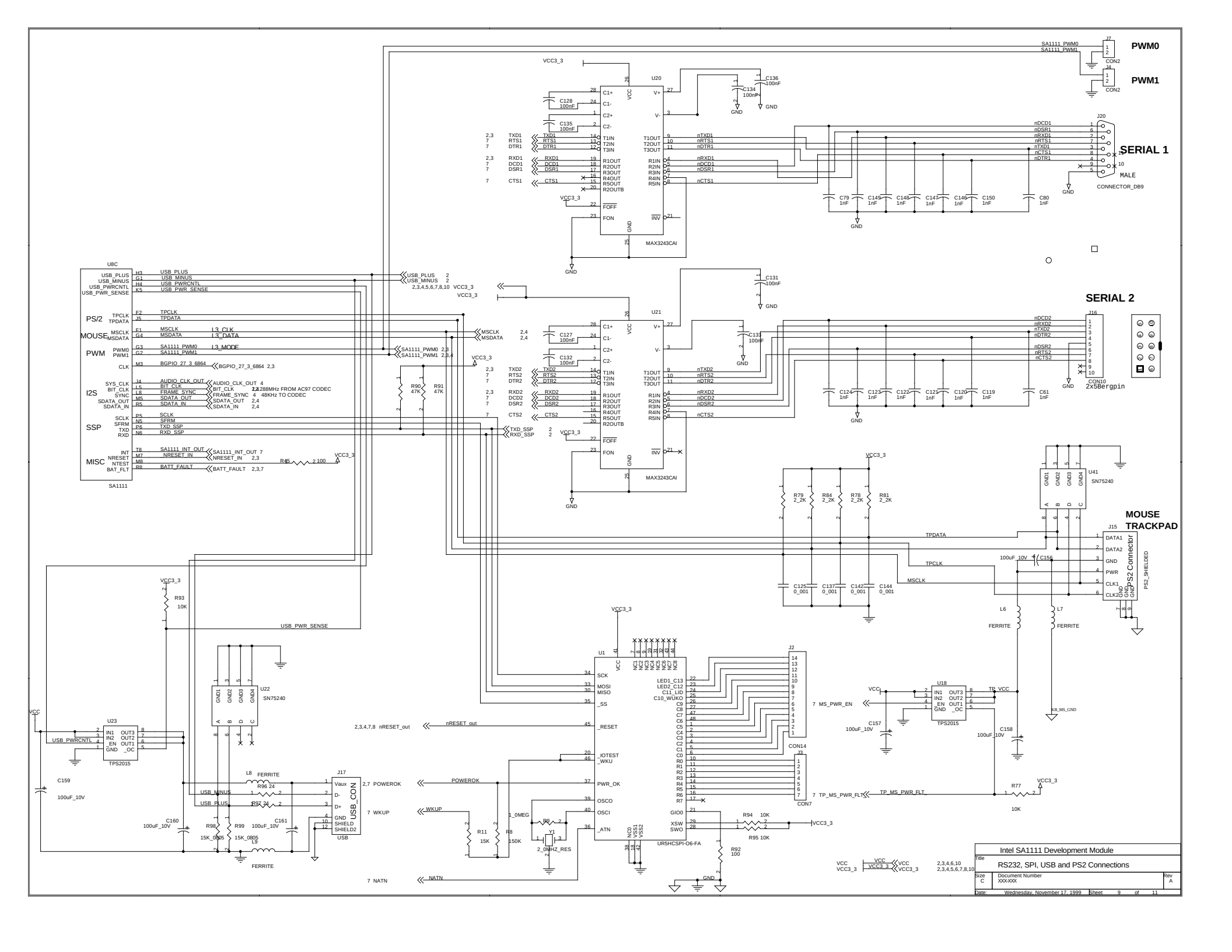
PWM0
PWM1
SERIAL 1
SERIAL 2
MOUSE
TRACKPAD
MAX3243CAI
SA1111
SN75240
TPS2015
USB_CON
CONNECTOR_DB9
2x5Bergpin
PS2_SHIELDED
UR5HCSPI-O6-FA
Intel SA1111 Development Module
RS232, SPI, USB and PS2 Connections
Document Number XXX-XXX
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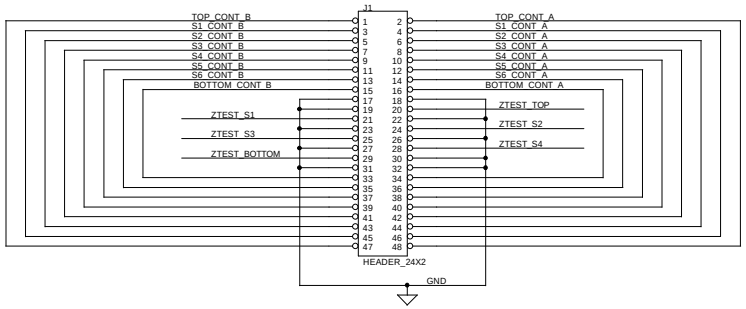


NO STUFF

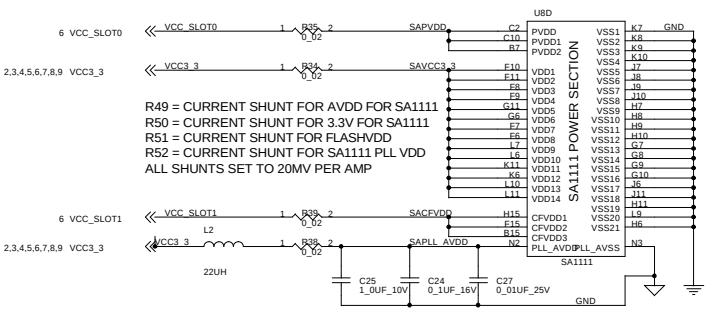
Board test coupon

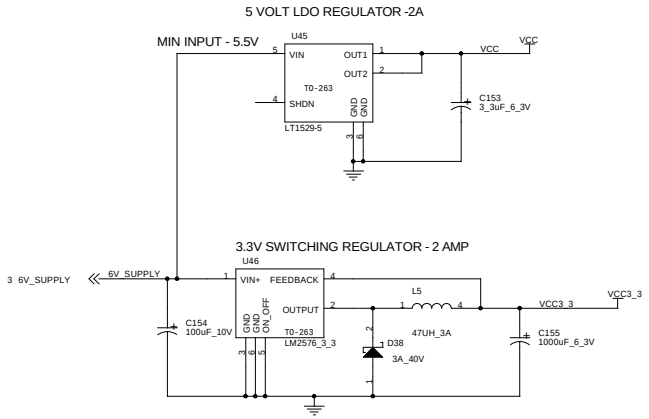
TEST HEADER IS 25 MIL PAD WITH 10 MIL DRILL SPACED 50 MIL SEPERATION. PATTERN IS 20 X 2 PLACED NEAR BOARD EDGE

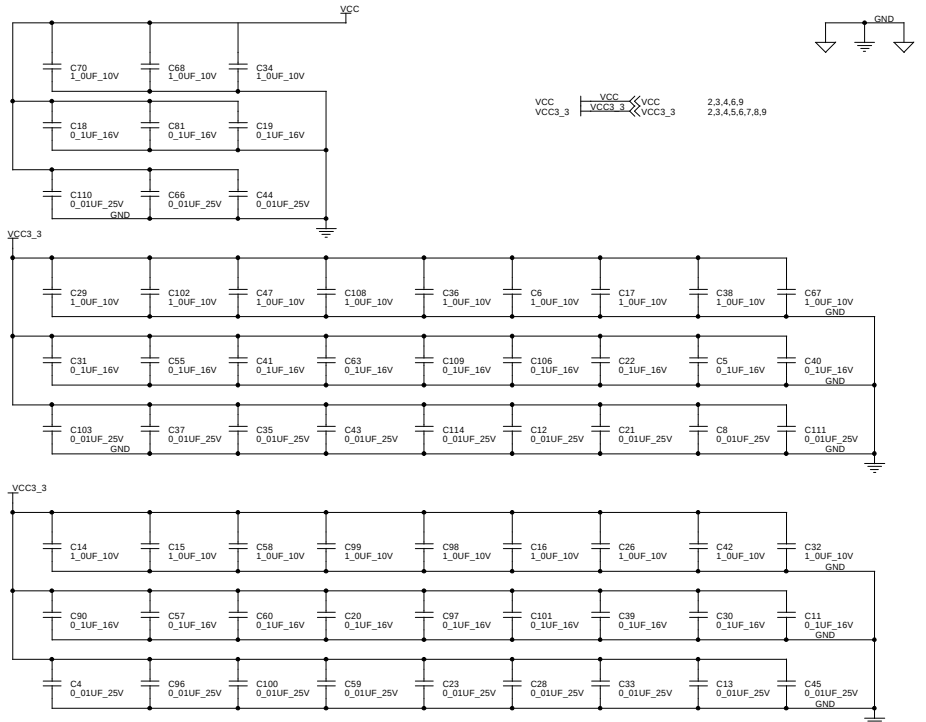
ZTEST_"layer" line set to nominal line widths on each layer - minimum length = 6 Inches. These traces should be surrounded by theiving traces space 40 mils from test trace where possible. Nominal impedence should be 60 ohms +/- 10%.

"LAYER_CONT_A" and "LAYER"_CONT_B Traces are minimum trace width, 5 mil internal, 5 mil top and bottom, and minimum seperation, 5 mil external and internal, that are run around the edge of the board on as long a path as possible. This is an overetch and underetch test.

Power Plane under SA1111 is isolated for power measurement purposes.







Intel SA1111 Development Module		
Title: POWER Supplies, Test Coupon, and MISC		
Size: C	Document Number: XXX-XXX	Rev: A
Date: Wednesday, November 17, 1999	Sheet 10 of 11	