

**HC05L1**  
**EMULATOR MODULE**  
**USER'S MANUAL**

First Edition



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# CONTENTS

CHAPTER 1	INTRODUCTION .....	1-1
1.1	Development Systems .....	1-1
1.1.1	Motorola Modular Development System (MMDS05) .....	1-1
1.1.2	M68HC05EVS Evaluation System (HC05EVS) .....	1-2
1.2	EM Layout .....	1-3
1.3	Connector Pin Assignments (P7 AND P8) .....	1-3
CHAPTER 2	MMDS05 CONFIGURATION AND OPERATION .....	2-1
2.1	Setting L1EM Jumper Headers .....	2-1
2.1.1	Reset Select Header (P4) .....	2-1
2.1.2	Frequency Select Header (P5) .....	2-2
2.1.3	Clock Source Select Header (P6) .....	2-3
2.2	Installing Personality And Help Files .....	2-4
2.2.1	Personality File .....	2-4
2.2.2	Help File .....	2-4
2.3	MMDS05 Limitations .....	2-5
2.4	Remaining System Installation .....	2-5
CHAPTER 3	HC05EVS CONFIGURATION AND OPERATION .....	3-1
3.1	Setting L1EM Jumper Headers .....	3-1
3.1.1	Reset Select Header (P4) .....	3-1
3.1.2	Frequency Select Header (P5) .....	3-2
3.1.3	Clock Source Select Header (P6) .....	3-3
3.2	EVS Limitations .....	3-4
3.3	Logic Analyzer Connector P1 .....	3-4
3.4	Remaining System Installation .....	3-7

**FIGURES**

<b>Figure</b>		<b>Page</b>
1-1	HC05L1 Emulator Module .....	1-4
1-2	Connector P7 Pin Assignment .....	1-5
1-3	Connector P8 Pin Assignment .....	1-6
3-1	Connector P1 Pin Assignment .....	3-5

**TABLES**

<b>Table</b>		<b>Page</b>
3-1	Logic Analyzer Connector P1 Signal Descriptions .....	3-6

This user's manual explains connection, configuration, and operation information specific to the HC05L1EM Emulator Module (hereafter referred to as L1EM). The L1EM makes possible emulation and debugging of MC68HC05L1 microcontroller unit (MCU) based target systems.

The L1EM can be part of two development systems. This chapter describes those systems and explains the layout of the L1EM.

## 1.1 DEVELOPMENT SYSTEMS

Your L1EM can be part of two Motorola development tools: the HC05 Motorola Modular Development System (MMDS05) or the M68HC05EVS Evaluation System (HC05EVS).

### 1.1.1 Motorola Modular Development System (MMDS05)

The MMDS05 is an emulator system that provides a bus state analyzer and real-time memory windows. The unit's integrated design environment includes an editor, an assembler, user interface, and source-level debug. A complete MMDS05 consists of:

- **a station module** -- the metal MMDS05 enclosure, containing the control board and the internal power supply. Most system cables connect to the MMDS05 station module. (The cable to an optional target system, however, runs through an aperture in the station module enclosure to connect directly to the emulator module.)
- **an emulator module (EM)** -- such as the L1EM: a printed circuit board that enables system functionality for a specific MCU. The EM fits into the station module through a sliding panel in the enclosure top. The EM has connectors for the target cable.
- **two logic clip assemblies** -- twisted-pair cables that connect the station module to your target system, a test fixture, a clock, or any other circuitry useful for evaluation or analysis. One end of each cable assembly has a molded connector, which fits into station module pod A and pod B. Leads at the other end of each cable terminate in female probe tips. Ball clips come with the cable assemblies.
- **a 9-lead RS-232 serial cable** -- the cable that connects the station module to the host computer RS-232 port.
- **a 9 to 25-pin adapter** -- a moulded assembly that lets you connect the 9-lead cable to a 25-pin serial port.
- **system software** -- software, on 3-1/2 inch diskettes.

- **MMDS05 documentation** -- an MMDS05 operations manual (MMDS05OM/D) and the appropriate EM user's manual.

As mentioned, your L1EM gives the MMDS05 the ability to emulate the MC68HC05L1 MCU. By substituting a different EM, you can enable your MMDS05 to emulate an MCU of a different series. (Your Motorola representative can explain all the EMs available.)

Chapter 2 explains how to configure and use your L1EM as part of an MMDS05 system. For information about MMDS05 software or the station module, see the MMDS05 operations manual. For layout and configuration information pertaining to a different EM, see the corresponding EM user's manual.

### 1.1.2 M68HC05EVS Evaluation System (HC05EVS)

An HC05EVS is an economical, two-board tool for designing, debugging, and evaluating target systems based on an MC68HC05 MCU. A complete HC05EVS consists of:

- **a platform board (PFB)** -- the bottom board, which supports the emulator module. The platform board has connectors for power and for the terminal or host computer.
- **an emulator module (EM)** -- such as the L1EM: a printed circuit board that enables system functionality for a specific MCU. The EM fits onto the PFB. The EM has connectors for a target cable and a cable to a logic analyzer.
- **an RS-232 serial cable** -- the cable that connects the PFB to the host computer RS-232 port.
- **system software** -- software, on 3-1/2 inch diskettes.
- **HC05EVS documentation** -- an HC05EVS operations manual and the appropriate EM user's manual.

As mentioned, your HC05L1 EM emulates the MC68HC05L1 MCU. By substituting a different EM, you can enable your HC05EVS to emulate an MCU of a different series. (Your Motorola representative can explain all the EMs available.)

Chapter 3 explains how to configure and use your L1EM as part of an HC05EVS system. For information about HC05EVS software or the platform board, see the HC05EVS operations manual. For layout and configuration information pertaining to a different EM, see the corresponding EM user's manual.

## 1.2 EM LAYOUT

Figure 1-1 shows the layout of the L1EM. Jumper header P4 controls the path of a target reset. Jumper header P5 allows you select the frequency of the L1EM on-board clock source. Jumper header P6 lets you select the clock-signal source.

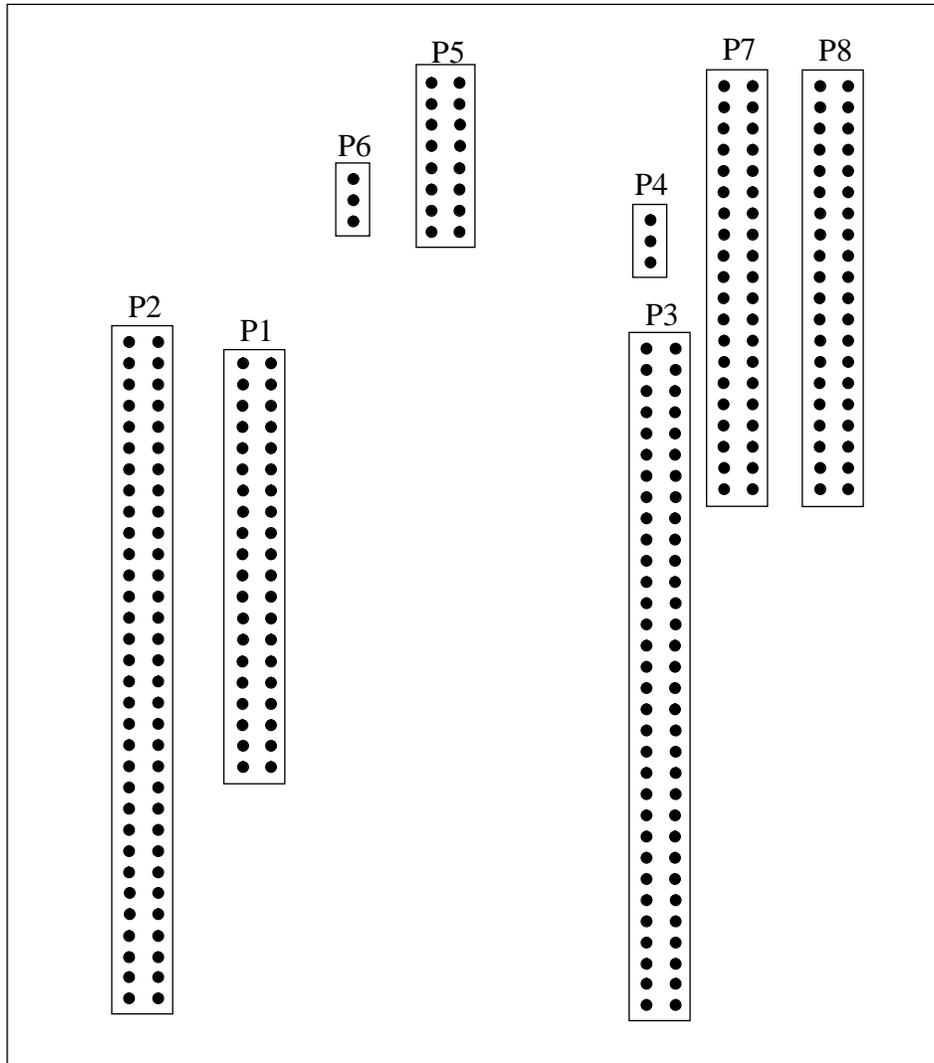
Connectors P7 and P8, for a 64-pin QFP or 56-pin S-DIP cable assembly, (purchased separately from the L1EM.) is the interface to a target system. If you install the L1EM in the MMDS05 station module, the target cable passes through the slit in the station module enclosure. Connector P1 connects to a logic analyzer. (Connector P1 has more significance for an EVS system, as an MMDS05 system includes a bus analyzer.) Expansion header connectors P2 and P3 connect together the EM and the control board (for an MMDS05) or the EM and the platform board (for an EVS).

## 1.3 CONNECTOR PIN ASSIGNMENTS (P4 AND P5)

Figure 1-2 and 1-3 show the pin assignments for connectors P7 and P8. For signal descriptions, consult the technical data book for the specific MCU.

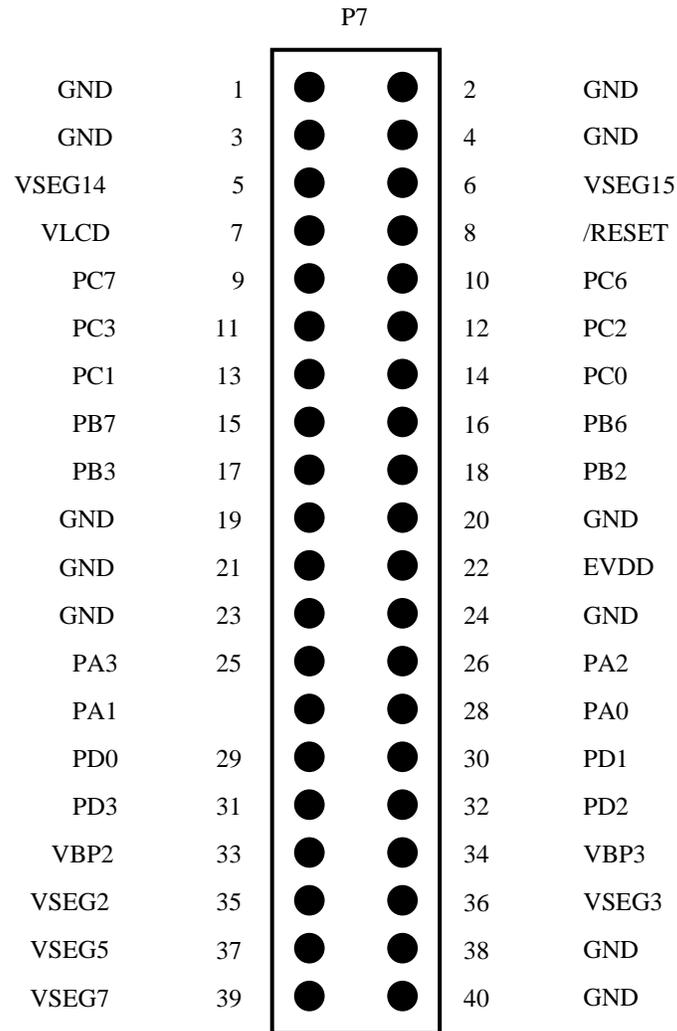
As connector P1 has more importance to an HC05EVS than to an MMDS05, pin assignments for connector P1 are in Chapter 3.

To configure your L1EM for use as part of an MMDS05, follow the instructions of Chapter 2. To configure your L1EM as part of an HC05EVS, follow the instructions of Chapter 3.

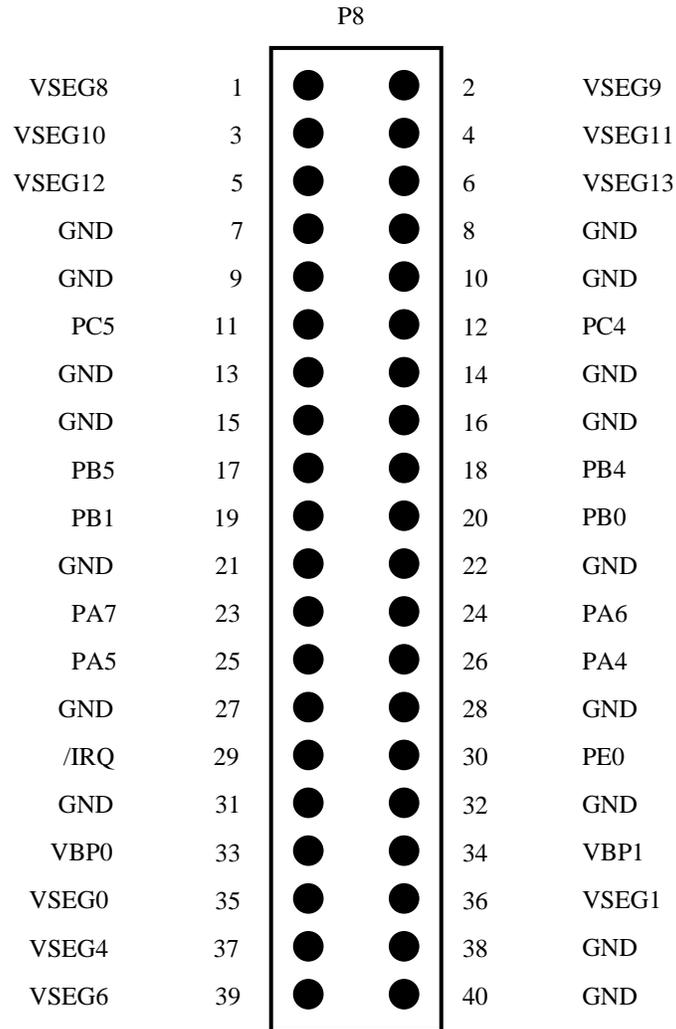


**Figure 1-1. HC05L1 Emulator Module**





**Figure 1-2. Connector P7 Pin Assignments**



**Figure 1-3. Connector P8 Pin Assignments**

This chapter explains how to configure and use your L1EM as part of an MMDS05. For other parts of system installation or configuration, see the MMDS05 operations manual (MMDS05OM/D). (If you will use your L1EM as part of an HC05EVS system, go to Chapter 3 for corresponding information.)

Paragraph 2.1 explains how to set L1EM jumper headers. Paragraph 2.2 explains how to install personality and help files pertaining to the L1EM. Paragraph 2.3 lists limitations on using the L1EM in an MMDS05. Paragraph 2.4 explains remaining system installation.

Note that you can configure an L1EM already installed in the MMDS05 station module enclosure. To do so, switch off station-module power, remove the hatch, then follow the guidance of this chapter.

### CAUTION

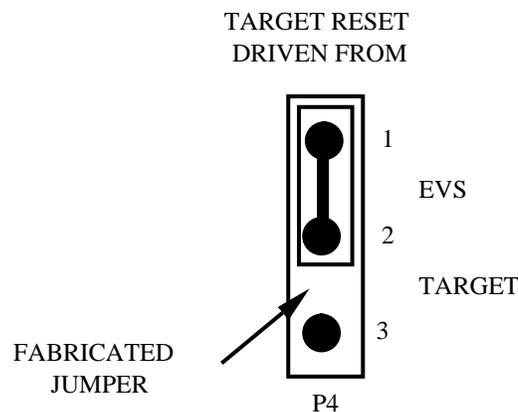
Be sure to switch off power if you reconfigure an installed EM. Reconfiguring EM jumper headers with power left on can damage MMDS05 circuits.

## 2.1 SETTING L1EM JUMPER HEADERS

Your L1EM has three jumper headers, P4, P5 and P6. Paragraphs 2.1.1 through 2.1.3 explain how to configure these headers.

### 2.1.1 Reset Select Header (P4)

Jumper header P4 controls the path of a target reset. The diagram below shows the factory configuration: the fabricated jumper between pins 1 and 2 enables your software to control resets. This is the only correct P4 configuration for an L1EM that is part of an MMDS05.

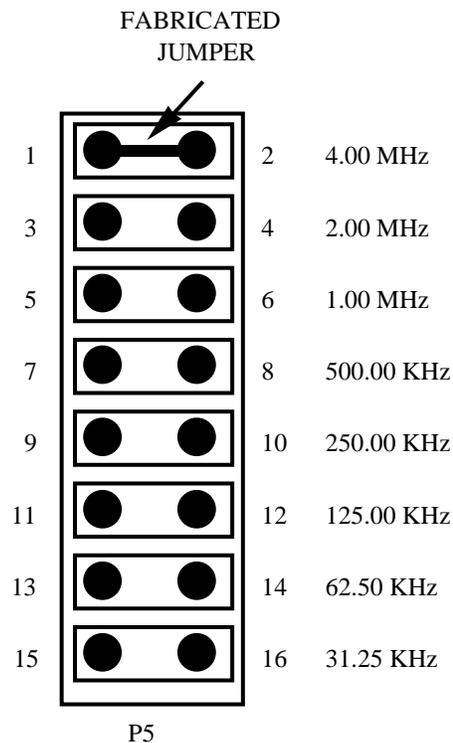


**NOTE**

The P4 pins 1 and 2 configuration is not correct for an L1EM that is part of an MMDS05 system. Such a configuration would interfere with correct operation of the RESETIN and RESETOUT commands.

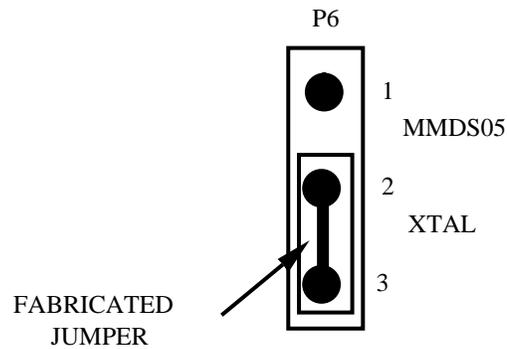
**2.1.2 Frequency Select Header (P5)**

Jumper header P5 determines the frequency of the L1EM clock source. There are eight possible choices of frequencies and the user can select one of them by installing a jumper across the appropriate jumper header pins. The diagram below shows the factory configuration: the factory configuration has a fabricated jumper installed between pins 1 and 2. (i.e. selects the 4.00 MHz clock source.)



### 2.1.3 Clock Source Select Header (P6)

Jumper header P6 determines the source of the clock signal. The diagram below shows the factory configuration: the fabricated jumper between pins 2 and 3 selects the L1EM canned oscillator clock source.



Alternatively, you may use a clock source originating from the control board. To do so, reposition the P6 jumper between pins 1 and 2, then use the MMDS05 OSC command to select a frequency.

## 2.2 INSTALLING PERSONALITY AND HELP FILES

L1EM software, on a 3.5" diskette, consists of these files (where xx denotes a version number):

- 00302Vxx.MEM      Personality file (Each personality file customizes the MMDS05 host software for one or more specific MCUs.)
- 00302Vxx.HLP      HELP windows specific to the personality file.

Note that files 00302Vxx.MEM and 00302Vxx.HLP are specific to the L1EM. You will receive corresponding personality and help files for each additional EM you order.

### 2.2.1 Personality File

Installation of the personality file (00302Vxx.MEM) consists of copying the file from the distribution diskette to a hard disk. Personality files are usually installed in the directory from which the MMDS05 software is executed. If a personality file is not located in that directory, the MMDS05 software displays a window with which the user can search the directory structure to find the correct file.

More than one personality file can be installed; the MMDS05 operating software automatically loads the personality file that corresponds to the currently-connected EM board upon powerup.

### 2.2.2 Help File

Installation of the help file (00302Vxx.HLP) consists of copying the file from the distribution diskette to a hard disk. This file must be installed in the directory from which the MMDS05 software is executed. If it is not located in that directory, no help information pertaining to the MC68HC05L1 MCU will be displayed when the CHIPSINFO command is used.

## 2.3 MMDS05 LIMITATIONS

This paragraph explains the limitations that apply to using your L1EM in an MMDS05 system.

- **Limitation 1. Test Register \$0020:**

Location \$0020 is a test register location when the MC68HC05L1 is running in emulation mode. The user should avoid writing to this location as changing the contents of this register may cause an MCU failure.

## 2.4 REMAINING SYSTEM INSTALLATION.

When you have configured headers P4, P5 and P6, you are through with L1EM configuration:

- To install the L1EM in an MMDS05 station module, remove the panel from the station-module top. Fit together EM connectors P2 and P3 (on the bottom of the board) and control-board connectors P11 and P12. Snap the mounting holes of the EM onto the plastic standoffs. Connect the target cable, if appropriate. Replace the panel.
- If your L1EM already is installed in the station module, reconnect the target cable, (if necessary). Replace the panel.

At this point, you are ready to make remaining cable connections, as necessary, and restore power. For instructions, consult the MMDS05 operations manual (MMDS05OM/D).



This chapter explains how to configure and use your L1EM as part of an HC05EVS. For other parts of system installation or configuration, see the HC05EVS operations manual. (If you will use your L1EM as part of an MMDS05 system, go to Chapter 2 for corresponding information.)

Paragraph 3.1 explains how to set L1EM jumper headers. Paragraph 3.2 lists limitations on using the L1EM in an HC05EVS. Paragraph 3.3 explains pin assignments and signal descriptions for the logic analyzer connector (P1). Paragraph 3.5 explains remaining system configuration.

Note that you can configure an L1EM already installed on the HC05EVS platform board. To do so, disconnect platform-board power, then follow the guidance of this chapter.

### CAUTION

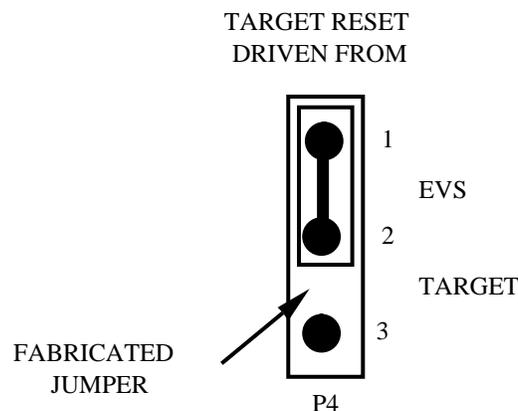
Be sure to disconnect power if you reconfigure an installed EM. Reconfiguring EM jumper headers with power left on can damage HC05EVS circuits.

## 3.1 SETTING L1EM JUMPER HEADERS

Your L1EM has three jumper headers, P4, P5 and P6. Paragraphs 3.1.1 through 3.1.3 explain how to configure these headers.

### 3.1.1 Reset Select Header (P4)

Jumper header P4 controls the path of a target reset. The diagram below shows the factory configuration: the fabricated jumper between pins 1 and 2 enables your L1EM to reset the target when you press the user or master reset switch.

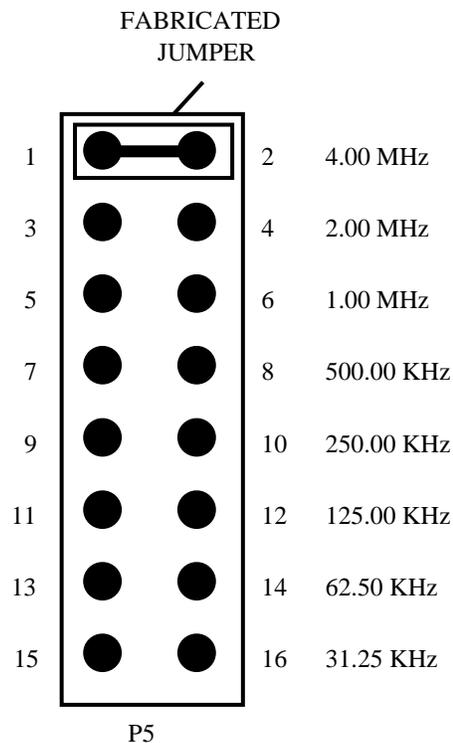


Alternatively, you may enable the target system to reset the L1EVS. To do so:

- Reposition the L1EM P4 jumper between pins 2 and 3, and
- Insert a fabricated jumper into platform-board header J3.

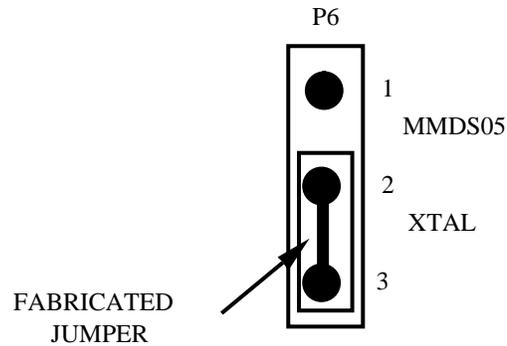
### 3.1.2 Frequency Select Header (P5)

Jumper header P5 determines the frequency of the L1EM clock source. There are eight possible choices of frequencies and the user can select one of them by installing a jumper across the appropriate jumper header pins. The diagram below shows the factory configuration: the factory configuration has a fabricated jumper installed between pins 1 and 2. (i.e. selects the 4.00 MHz clock source.)



### 3.1.3 Clock Source Select Header (P6)

Jumper header P6 determines the source of the clock signal. The diagram below shows the factory configuration: the fabricated jumper between pins 2 and 3 selects the L1EM canned oscillator clock source. This is the only correct configuration for an L1EM that is part of an HC05EVS.



#### NOTE

The P6 pins 1 and 2 configuration is not correct for an L1EM that is part of an HC05EVS system.

This paragraph explains the limitations that apply to using your L1EM in an HC05EVS.

- **Limitation 1. CLI/RTI:**

You may not trace a clear interrupt mask (CLI) or return from interrupt (RTI) instruction with an interrupt enabled and pending, due to MCU interrupt handling. Attempting such a trace causes an interrupt in the monitor map; this forces a software reset of the HC05EVS. User breakpoints remain in the user map as SWI instructions; You must remove such SWI instructions.

- **Limitation 2. Branch:**

Do not trace a conditional branch instruction (such as BRCLR) that branches to itself. As the monitor places an SWI instruction on the object of the branch, the system never would execute the instruction. However, it would appear to you that the instruction had executed. You may enter a G command while the PC points to this type of instruction as long as the instruction is not a breakpoint address.

- **Limitation 3. IRQ/SWI:**

Whenever possible, avoid mixing interrupt requests (IRQs) and user software interrupts (SWIs). This prevents a possible IRQ-SWI timing problem: a concurrent hardware interrupt and SWI could cause an HC05EVS failure that could stop program execution. To recover from such a failure (which occurs very infrequently, statistically), press the master reset switch (SW3).

- **Limitation 4. Memory Map:**

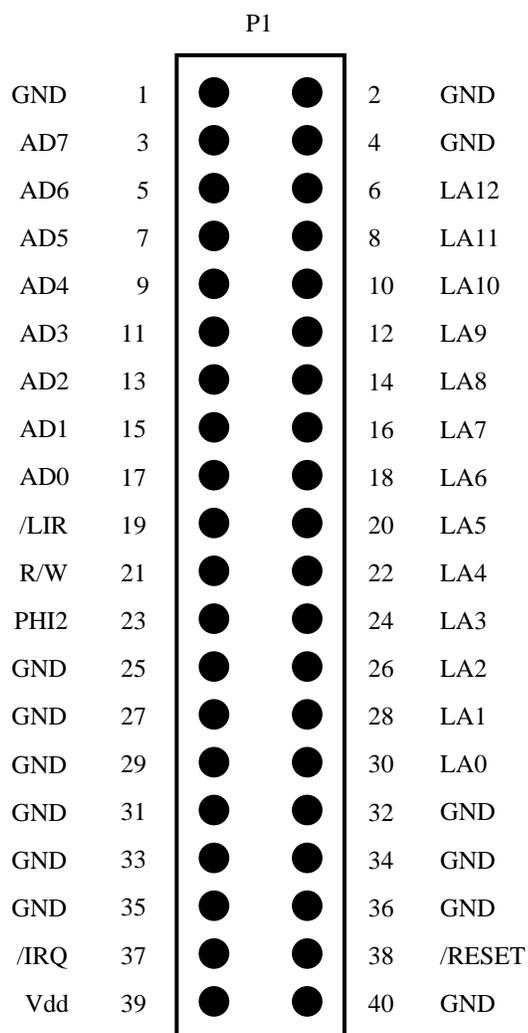
You must be aware of the memory map of the MCU being simulated; be sure to use only valid ROM locations. The HC05EVS does not provide protection to limit user programs to the exact amount of MCU ROM available. (For information about memory maps, consult the technical data book for the MCU.)

- **Limitation 5. Test Register \$0020:**

Location \$0020 is a test register location when the MC68HC05L1 is running in emulation mode. The user should avoid writing to this location as changing the contents of this register may cause an MCU failure.

### 3.3 LOGIC ANALYZER CONNECTOR P1

Figure 3-1 shows the pin assignments for logic analyzer connector P1. Table 3-1 lists signal descriptions for this connector.



**Figure 3-1. Connector P1 Pin Assignments**

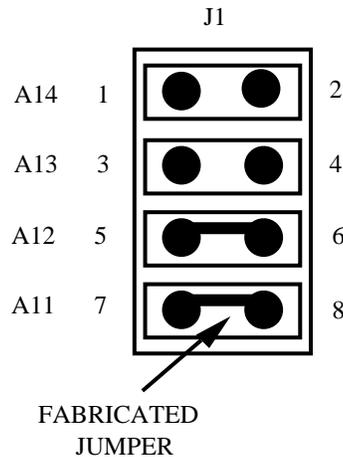
Table 3-1. Logic Analyzer Connector P1 Signal Descriptions

Pin Number	Signal Mnemonic	Signal Name and Description
3, 5, 7, 9, 11, 13, 15, 17	AD7-AD0	<i>Data bus</i> :- MCU bidirectional data bus.
6, 8, 10, 12, 14, 16, 18, 20, 22, 24, 26, 28, 30	LA12-LA0	<i>Address bus</i> :- MCU output address bus.
19	/LIR	<i>Load Instruction Register</i> :- Active-low output signal, asserted when an instruction starts.
21	R/W	<i>Read/Write</i> :- Output signal that indicates the direction of data transfer.
37	/IRQ	<i>MCU Interrupt Request</i> :- Active-low input signal from the target that asynchronously applied an MCU interrupt.
38	/RESET	<i>Reset</i> :- Active-low bidirectional signal for starting an EVS reset.
23	PHI2	<i>PHI2 clock</i> :- Internally generated output clock signal used as a timing reference.
1, 2, 4, 25, 27, 29, 31, 32, 33, 34, 35, 36, 40	GND	<i>Ground</i>
39	Vdd	<i>+5Vdc power</i> :- Input voltage (+5Vdc @ 1A(max)) used by the EVS logic circuits.

### 3.4 REMAINING SYSTEM INSTALLATION

When you have configured headers P4, P5 and P6, you are through with L1EM configuration.

Find jumper header J1 of the HC05EVS platform board. Make sure that two fabricated jumpers are installed, per the diagram below.



To install the L1EM on an HC05EVS platform, fit together EM connectors P2 and P3 (on the bottom of the board) and platform-board connectors P3 and P4. Snap the mounting holes of the EM onto the plastic standoffs.

At this point, you are ready to make remaining cable connections, as necessary, and restore power. For instructions, consult the HC05EVS operations manual.

