

# Passive Delay Lines

**LF SERIES  
DIP PACKAGES**

- 5 to 500 ns delays available.
- Temperature coefficient: 100 ppm/°C maximum.
- Typical distortion: + 10% maximum (preshoot and overshoot only).
- Standard impedance 100 ohms. Also available with 50 ohms, 200 ohms, and other impedances.
- Insulation resistance: 10k meg. ohms minimum at 50 Vdc.
- Military temperature range -55 to +125°C.
- Minimum input pulse width equal to 2.5 x output rise time.

Note — Models available with delays of less than 1 ns are described in product sheet entitled "Pico-Second Delay Lines."

Note — For delay lines qualified to MIL-D-83531, request product sheet entitled "QPL Passive Delay Lines."

## MODEL LF10 10-TAP PASSIVE DELAY LINE (DIP)

TECHNITROL PART NO.	Total Delay (ns)	Tap Delay (ns)	Rise Time (ns-Max.)	Impedance (Ohms) ± 10%	DCR (Ohms Max.)
LF10D1 <sup>[1]</sup>	10	1.0	2.2	100	1.0
LF10E1 <sup>[1]</sup>	12.5	1.2	2.5	100	1.0
LF10F1 <sup>[1]</sup>	20	2.0	4.0	100	1.5
LF10G1 <sup>[2]</sup>	35	2.5	5.0	100	1.5
LF10K1	50	5.0	10.0	100	2.5
LF10M1 <sup>[3]</sup>	100	10.0	20.0	100	5.0
LF10Q1 <sup>[3]</sup>	200	20.0	40.0	100	11.5

Delay Tolerance ± 1.5 ns or 5%, whichever is greater.

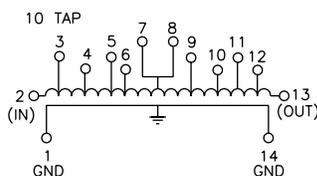
Measured from 50% to 50%.

[1] Contact factory for pinouts and tolerances.

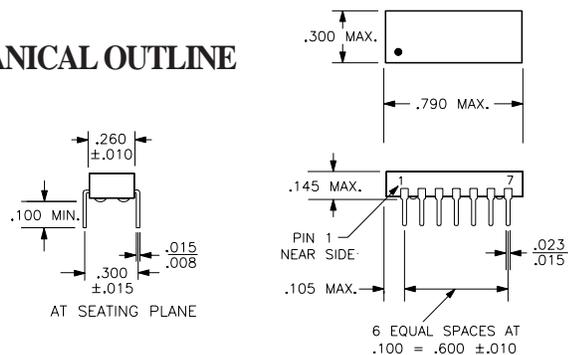
[2] Contact factory for tolerances.

[3] Maximum package height is 0.200 in.

### SCHEMATIC



### MECHANICAL OUTLINE



LF10-15

### Notes

- Pin numbers shown are for reference only and are not necessarily marked on unit.
- Lead material is electro tin plated (alloy 42) or solder dipped.
- All specifications are subject to change without notice.