

SATA Connector with gold fingers facing up (reverse)

* locate as close as possible to SATA connector.

- Select ATAPI DMA method
 Pin [20,18] = 11b: Decode command packet MMC commands
 Pin [20,18] = 10b: Use IOINPIN of Pin 19
 Pin [20,18] = 01b: Use bit 1 in Interrupt Reason
 Pin [20,18] = 00b: Use bit 2 in Features Register

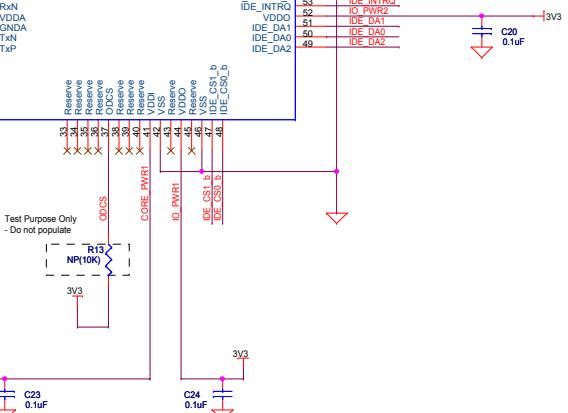
Locate All 0.1uF capacitance as close as possible to SiL3811 pins

Soldering an EPAD to the PCB GND will improve the signal integrity

R14 1K 1%
 REXT
 XTAUCLKI
 C21 18pF
 25MHz
 Y1 18pF
 XTAUO

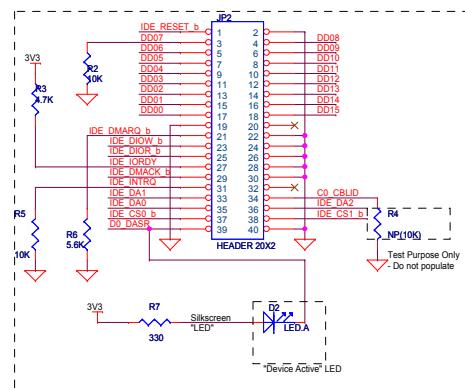
* locate as close as possible to SiL3811 pins.

SiL3811 64pin QFN



Information Subject to Change without Notice.
 Please consult Silicon Image.

For the termination requirement for non-direct attached application, please refer SiL-AN-0147



Revision History

Rev A
 - Initial Released

Silicon Image, Inc.	
1060 E. Arques Ave.	
Sunnyvale, CA 94085	
Title	Sil3811 reference schematics
Size	Document Number
	Sil-SC-0231-A
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