

June 1, 1996 (Version 1.0)

This section describes the following products:

Foundation Series

- Foundation Base System (PC)
- Foundation Base System with VHDL Synthesis (PC)
- Foundation Standard System (PC)
- Foundation Standard System with VHDL Synthesis (PC)

Alliance Series

- OrCAD – Base System (PC)
- OrCAD – Standard System (PC)
- Viewlogic – Base System (PC)
- Viewlogic – Standard System (PC)
- Viewlogic Stand-alone – Base System (PC)
- Viewlogic Stand-alone – Standard System (PC)
- Viewlogic Stand-alone – Extended System (PC)
- Viewlogic – Standard System (Workstation)
- Mentor – Standard System (Workstation)
- Synopsys – Standard System (Workstation)
- Cadence Standard System (Workstation)
- Third-Party Alliance – Standard System (PC and Workstation)

Foundation Series: Foundation Base System (PC)

Base System Includes:

- Schematic editor with library support for XC2000, XC3000/A, XC3100/A, XC4000/E, XC5200 FPGAs and XC7300 and XC9500 CPLDs
- Functional and Timing Simulator
- Core implementation software for FPGAs with device support for XC2000, XC3000/A & XC3100/A up to XC3x42/A, XC4000/E up to XC4003/E, and XC5200 up to XC5204
- Core implementation software for CPLDs with device support for XC7300 and XC9500
- XChecker Diagnostic Cable

Support and Updates Include:

- Hotline Telephone Support
- Access to Xilinx bulletin board
- Apps FAX and E-Mail
- Software Updates if on maintenance
- Online Documentation

Required Hardware Environment:

- Fully compatible PC386/486/Pentium
- MS-Windows 3.1 (minimum)
- MS-DOS version 5.0 (minimum)
- Minimum 70 MB hard disk space
- ISO 9660 type CD ROM drive
- VGA display
- One parallel and two serial ports
- 16 MB of RAM

Package Features - Foundation Base (PC)

Feature	FND Base	FND Std.	FND BaseV	FND Std. V
Libraries and Interface	√	√	√	√
Schematic Editor	√	√	√	√
Simulator (Unlimited)	√	√	√	√
CPLD Devices	√	√	√	√
FPGA Limited ¹	√		√	
FPGA 2K, 3K, 4K, 5K		√		√
Core Implementation	√ ²	√	√ ²	√
Synthesis Tools			√	√
X-BLOX		√		√
XChecker Cable	√	√	√	√
Hotline Support	√	√	√	√

Notes: 1. XC2000, XC3000, up to XC3042/XC3142; XC4000/E up to XC4003/E; XC5200 up to XC5204
 2. XDE Design Editor and Floorplanner not included

Foundation Series: Foundation Base System with VHDL Synthesis (PC)

Base System Includes:

- Schematic editor with library support for XC2000, XC3000/A, XC3100/A, XC4000/E, XC5200 FPGAs and XC7300 and XC9500 CPLDs
- Functional and Timing Simulator
- VHDL Synthesis capability with HDL
- Wizard that makes HDL design entry easier and faster with Xilinx specific templates
- VHDL multimedia tutorial
- Core implementation software for FPGAs with device support for XC2000, XC3000/A & XC3100/A up to XC3x42/A, XC4000/E up to XC4003/E, and XC5200 up to XC5204
- Core implementation software for CPLDs with device support for XC7300 and XC9500
- XChecker Diagnostic Cable

Support and Updates Include:

- Hotline Telephone Support
- Access to Xilinx bulletin board
- Apps FAX and E-Mail
- Software Updates if on maintenance
- Online Documentation

Required Hardware Environment:

- Fully compatible PC386/486/Pentium
- MS-Windows 3.1 (minimum)
- MS-DOS version 5.0 (minimum)
- Minimum 70 MB hard disk space
- ISO 9660 type CD ROM drive
- VGA display
- One parallel and two serial ports
- 16 MB of RAM

Package Features - Foundation (PC)

Feature	FND Base	FND Std.	FND BaseV	FND Std. V
Libraries and Interface	√	√	√	√
Schematic Editor	√	√	√	√
Simulator (Unlimited)	√	√	√	√
CPLD Devices	√	√	√	√
FPGA Limited ¹	√		√	
FPGA 2K, 3K, 4K, 5K		√		√
Core Implementation	√ ²	√	√ ²	√
Synthesis Tools			√	√
X-BLOX		√		√
XChecker Cable	√	√	√	√
Hotline Support	√	√	√	√

Notes: 1. XC2000, XC3000, up to XC3042/XC3142; XC4000/E up to XC4003/E; XC5200 up to XC5204
 2. XDE Design Editor and Floorplanner not included

Foundation Series: Foundation Standard System (PC)

Standard System Includes:

- Schematic editor with library support for XC2000, XC3000/A, XC3100/A, XC4000/E, XC5200 FPGAs and XC7300 and XC9500 CPLDs
- Functional and Timing Simulator (unlimited gates)
- Core implementation software for FPGAs with device support for XC2000, XC3000/A & XC3100/A, XC4000/E and XC5200
- Core implementation software for CPLDs with device support for XC7300 and XC9500
- XChecker Diagnostic Cable

Support and Updates Include:

- Hotline Telephone Support
- Access to Xilinx bulletin board
- Apps FAX and E-Mail
- Software Updates if on maintenance
- Online Documentation

Required Hardware Environment:

- Fully compatible PC386/486/Pentium
- MS-Windows 3.1 (minimum)
- MS-DOS version 5.0 (minimum)
- Minimum 70 MB hard disk space
- ISO 9660 type CD ROM drive
- VGA display
- One parallel and two serial ports
- 16 MB of RAM

Package Features - Foundation (PC)

Feature	FND Base	FND Std.	FND BaseV	FND Std. V
Libraries and Interface	√	√	√	√
Schematic Editor	√	√	√	√
Simulator (Unlimited)	√	√	√	√
CPLD Devices	√	√	√	√
FPGA Limited	√		√	
FPGA 2K, 3K, 4K, 5K		√		√
Core Implementation	√	√	√	√
Synthesis Tools			√	√
X-BLOX		√		√
XChecker Cable	√	√	√	√
Hotline Support	√	√	√	√

Foundation Series: Foundation Standard System with VHDL (PC)

Standard System Includes:

- Schematic editor with library support for XC2000, XC3000/A, XC3100/A, XC4000/E, XC5200 FPGAs and XC7300 and XC9500 CPLDs
- Functional and Timing Simulator (unlimited gates)
- VHDL Syntheses capability with HDL Wizard that makes HDL design entry easier and faster with Xilinx specific templates
- VHDL multimedia tutorial
- Core implementation software for FPGAs with device support for XC2000, XC3000/A & XC3100/A, XC4000/E and XC5200
- Core implementation software for CPLDs with device support for XC7300 and XC9500
- XChecker Diagnostic Cable

Support and Updates Include:

- Hotline Telephone Support
- Access to Xilinx bulletin board
- Apps FAX and E-Mail
- Software Updates if on maintenance
- Online Documentation

Required Hardware Environment:

- Fully compatible PC386/486/Pentium
- MS-Windows 3.1 (minimum)
- MS-DOS version 5.0 (minimum)
- Minimum 70 MB hard disk space
- ISO 9660 type CD ROM drive
- VGA display
- One parallel and two serial ports
- 16 MB of RAM

Package Features - Foundation (PC)

Feature	FND Base	FND Std.	FND BaseV	FND Std. V
Libraries and Interface	√	√	√	√
Schematic Editor	√	√	√	√
Simulator (Unlimited)	√	√	√	√
CPLD Devices	√	√	√	√
FPGA Limited	√		√	
FPGA 2K, 3K, 4K, 5K		√		√
Core Implementation	√	√	√	√
Synthesis Tools			√	√
X-BLOX		√		√
XChecker Cable		√		√
Hotline Support	√	√	√	√

Alliance Series: OrCAD – Base System (PC)

Base System Includes:

- Schematic Interface for OrCAD SDT386+ with library support for XC2000, XC3000, XC3000A, XC3100, XC3100A, XC4000/E and XC5200 FPGAs and XC7000 and XC9500 Series CPLDs
- Functional and Timing Simulation Interface for OrCAD VST386+
- Core Implementation Software for CPLDs and FPGAs with device support for XC2000, XC3000, XC3000A and XC3100, XC3100A up to XC3x42, XC3x42A, XC4000/E up to XC4003/E, XC5200 up to XC5204, XC7300, and XC9500

Note:

- This package does not include the OrCAD SDT schematic capture or VST simulation tools. They must be purchased separately from OrCAD.
- XDE-Xilinx Design Editor is not included.
- This Base Package does not come with a standard one year update contract. Instead, when Xilinx releases a new version of software, customers will be notified and may purchase the Base Update at the listed price.
- The Base Update is only available to licensees of the DS-OR-BAS-PC1 on a one-for-one basis.

Revision Updates Include:

- Latest version software and online documentation
- Only available to customers who have purchased a Base product before.

Required Hardware Environment:

- Fully IBM compatible PC386/486/Pentium
- MS-Windows 3.1 (minimum)
- MS-DOS version 5.0 (minimum)
- Minimum 60 Mbyte hard-disk space
- One 3.5" High-Density floppy disk drive
- VGA display
- One parallel and two serial ports
- 16 Mbytes of RAM for all supported XC3000A and XC4000/E FPGAs
- Mouse

Package Features - OrCAD PC

Feature	Base	Std.
Libraries and Interface	√	√
Schematic Editor		
Simulator (Limited)		
Simulator (Unlimited)		
EPLD Devices	√	√
FPGA Limited ¹	√	
FPGA 2K, 3K, 4K, 5K		√
Core Implementation	√ ²	√
Synthesis Tools		
X-BLOX		√
XChecker Cable	√	√
Hotline Support	√	√

- Notes: 1. XC2000, XC3000, up to XC3042/XC3142;
XC4000/E up to XC4003/E; XC5200 up to XC5204
2. XDE Design Editor and Floorplanner not included

Alliance Series: OrCAD – Standard System (PC)

Standard System Includes:

- Schematic Interface for OrCAD SDT386+ with library support for XC2000, XC3000, XC3000A, XC3100, XC3100A, XC4000/E FPGAs and XC7000 and XC9500 Series CPLDs
- Functional and Timing Simulation Interface for OrCAD VST386+
- X-BLOX Module Generator and Optimizer
- Core Implementation Software for CPLDs and FPGAs with device support for XC2000, XC3000, XC3000A, XC3100, XC3100A, XC4000/E, XC5200, XC7300, and XC9500
- Software Support and Updates for first year

Note:

- This package does not include the OrCAD SDT schematic capture or VST simulation tools. They must be purchased separately from OrCAD.

Support and Updates Include:

- Hotline Telephone Support
- Access to Xilinx Technical Bulletin Board
- Apps FAX and E-Mail
- Software Updates if on maintenance
- Online Documentation
- World Wide Web Access
- Technical Newsletter
- Extensive Application Notes

Required Hardware Environment:

- Fully IBM compatible PC386/486/Pentium
- MS-Windows 3.1 (minimum)
- MS-DOS version 5.0 (minimum)
- Minimum 80 Mbyte hard-disk space
- One ISO 9660 compatible CD-ROM drive
- VGA display
- One parallel and two serial ports
- 16 Mbytes of RAM up to XC4008
- 24 Mbytes of RAM for XC3195, XC3195A, XC4010
- 32 Mbytes of RAM for XC4013
- Mouse

Package Features - OrCAD PC

Feature	Base	Std.
Libraries and Interface	√	√
Schematic Editor		
Simulator (Limited)		
Simulator (Unlimited)		
EPLD Devices	√	√
FPGA Limited	√	
FPGA 2K, 3K, 4K, 5K		√
Core Implementation	√	√
Synthesis Tools		
X-BLOX		√
XChecker Cable	√	√
Hotline Support	√	√

Alliance Series: Viewlogic – Base System (PC)

Base System Includes:

- Viewlogic Schematic Interface library support for XC2000, XC3000, XC3000A, XC3100, XC3100A, XC4000/E, XC5200 FPGAs, and XC7000 and XC9500 CPLDs
- Viewlogic Functional and Timing Simulation Interface
- Core Implementation Software for CPLDs and FPGAs with device support for XC2000, XC3000, XC3000A, XC3100, XC3100A up to XC3x42/A, XC4000/E up to XC4003/E, XC5200 up to XC5204, XC7300, and XC9500

Note:

- This package does not include Viewlogic schematic capture or simulation tools. They must be purchased separately from Viewlogic or Xilinx (see Stand-alone packages).
- Interface and libraries support Workview 6.1, Workview PRO and Office Series.
- XDE-Xilinx Design Editor - not included
- This Base Package does not come with a standard one-year update contract. Instead, when Xilinx releases a new version of software, customers will be notified and may purchase the Base Update at the listed price.
- The Base Update is only available to licensees of the DS-VL-BAS-PC1 on a one-for-one basis.

Revision Updates Include:

- Latest version software and online documentation
- Only available to customers who have purchased a base product before.

Required Hardware Environment:

- Fully IBM compatible PC386/486/Pentium
- MS-Windows version 3.1 (minimum)
- MS-DOS version 5.0 (minimum)
- Minimum 60 Mbytes disk space
- One CD-ROM drive
- VGA display
- 3-Button Serial Mouse
- One parallel and two serial ports
- 16 Mbytes of RAM

Package Features - Viewlogic PC

Feature	VL Base	VL Std.	VLS Base	VLS Std.	VLS Ext.
Libraries and Interface	√	√	√	√	√
Schematic Editor			√	√	√
Simulator (Limited)			√		
Simulator (Unlimited)				√	√
EPLD Devices	√	√	√	√	√
FPGA Limited ¹	√		√		
FPGA 2K, 3K, 4K, 5K		√		√	√
Core Implementation	√ ²	√	√ ²	√	√
Synthesis Tools					√
X-BLOX		√		√	√
XChecker Cable	√	√	√	√	√
Hotline Support	√	√	√	√	√

1. XC2000, XC3000, up to XC3042/XC3142; XC4000/E up to XC4003/E; XC5200 up to XC5204
2. XDE Design Editor and Floorplanner not included

Alliance Series: Viewlogic – Standard System (PC)

Standard System Includes:

- Viewlogic Schematic Interface with library support for XC2000, XC3000, XC3000A, XC3100, XC3100A, XC4000/E, XC5200 FPGAs and XC7000 and XC9500 CPLDs
- Viewlogic Functional and Timing Simulation Interface
- Core Implementation Software for CPLDs and FPGAs with device support for XC2000, XC3000, XC3000A, XC3100, XC3100A, XC4000/E, XC5200, XC7300, and XC9500
- X-BLOX Module Generator and Optimizer
- Software Support and Updates for the first year

Note:

- This package does not include Viewlogic schematic capture or simulation tools. They must be purchased separately from Viewlogic or Xilinx (see Stand-alone packages).
- Interface and libraries support Workview PRO and Office Series.

Support and Updates Include:

- Hotline Telephone Support
- Access to Xilinx Technical Bulletin Board
- Apps FAX and E-Mail
- Software Updates if on maintenance
- Online Documentation
- World Wide Web Access
- Technical Newsletter
- Extensive Application Notes

Required Hardware Environment:

- Fully IBM compatible PC386/486/Pentium
- MS-Windows 3.1 (minimum)
- MS-DOS version 5.0 (minimum)
- Minimum 90 Mbytes hard-disk space
- One ISO 9660 compatible CD-ROM drive
- VGA display
- 3-Button Serial Mouse
- One parallel and two serial ports
- 16 Mbytes of RAM for devices up to XC4008
- 24 Mbytes of RAM for XC3195, XC4010
- 32 Mbytes of RAM for XC4013

Package Features - Viewlogic PC

Feature	VL Base	VL Std.	VLS Base	VLS Std.	VLS Ext.
Libraries and Interface	√	√	√	√	√
Schematic Editor			√	√	√
Simulator (Limited)			√		
Simulator (Unlimited)				√	√
EPLD Devices	√	√	√	√	√
FPGA Limited	√		√		
FPGA 2K, 3K, 4K, 5K		√		√	√
Core Implementation	√	√	√	√	√
Synthesis Tools					√
X-BLOX		√		√	√
XChecker Cable	√	√	√	√	√
Hotline Support	√	√	√	√	√

Alliance Series: Viewlogic *Stand-alone* – Base System (PC)

Stand-alone Standard System Includes:

- Workview Office Schematic Editor with library support for XC2000, XC3000, XC3000A, XC3100, XC3100A, XC4000/E, XC5200 FPGAs and XC7000 and XC9500 CPLDs
- Workview Office Functional and Timing Simulation for designs (limited gates)
- Core Implementation Software for CPLDs and FPGAs with device support for XC2000, XC3000, XC3000A, XC3100, XC3100A, XC4000/E, XC5200, XC7300, and XC9500

Note:

- XDE-Xilinx Design Editor - not included
- This Base Package does not come with a standard one-year update contract. Instead, when Xilinx releases a new version of software, customers will be notified and may purchase the Base Update at the listed price.
- The Base Update is only available to licensees on a one-for-one basis.

Revision Updates Include:

- Latest version software and online documentation
- Only available to customers who have purchased a base product before.

Required Hardware Environment:

- Fully IBM compatible PC386/486/Pentium
- MS-Windows version 3.1 (minimum)
- MS-DOS version 5.0 (minimum)
- Minimum 60 Mbytes disk space
- One CD-ROM drive
- VGA display
- 3-Button Serial Mouse
- One parallel and two serial ports
- 16 Mbytes of RAM

Package Features - Viewlogic PC

Feature	VL Base	VL Std.	VLS Base	VLS Std.	VLS Ext.
Libraries and Interface	√	√	√	√	√
Schematic Editor			√	√	√
Simulator (Limited)			√		
Simulator (Unlimited)				√	√
EPLD Devices	√	√	√	√	√
FPGA Limited ¹	√		√		
FPGA 2K, 3K, 4K, 5K		√		√	√
Core Implementation	√ ²	√	√ ²	√	√
Synthesis Tools					√
X-BLOX		√		√	√
XChecker Cable	√	√	√	√	√
Hotline Support	√	√	√	√	√

Notes: VL = Viewlogic, VLS = Viewlogic *Stand-alone*

1. XC2000, XC3000, up to XC3042/XC3142;
XC4000/E up to XC4003/E; XC5200 up to XC5204
2. XDE Design Editor and Floorplanner not included

Alliance Series: Viewlogic *Stand-alone* – Standard System (PC)

Stand-alone Standard System Includes:

- Workview Office Schematic editor with library support for XC2000, XC3000, XC3000A, XC3100, XC3100A, XC4000/E, XC5200 FPGAs and XC7000 and XC9500 CPLDs
- Workview Office Functional and Timing Simulation for designs (unlimited gates)
- Core Implementation Software for CPLDs and FPGAs with device support for XC2000, XC3000, XC3000A, XC3100, XC3100A, XC4000/E, XC5200, XC7300, and XC9500
- X-BLOX Module Generator and Optimizer
- Software Support and Updates for the first year

Support and Updates Include:

- Hotline Telephone support
- Access to Xilinx Technical Bulletin Board
- Apps FAX and E-Mail
- Software Updates if on maintenance
- Online Documentation
- World Wide Web Access
- Technical Newsletter
- Extensive Application Notes

Required Hardware Environment:

- Fully IBM compatible PC386/486/Pentium
- MS-Windows 3.1 (minimum)
- MS-DOS version 5.0 (minimum)
- Minimum 90 Mbytes hard-disk space
- One ISO 9660 compatible CD-ROM drive
- VGA display
- 3-Button SP Mouse
- One parallel and two serial ports
- 16 Mbytes of RAM for devices up to XC4008
- 24 Mbytes of RAM for XC3195, XC4010
- 32 Mbytes of RAM for XC4013

Package Features - Viewlogic PC

Feature	VL Base	VL Std.	VLS Base	VLS Std.	VLS Ext.
Libraries and Interface	√	√	√	√	√
Schematic Editor			√	√	√
Simulator (Limited)			√		
Simulator (Unlimited)				√	√
EPLD Devices	√	√	√	√	√
FPGA Limited	√		√		
FPGA 2K, 3K, 4K, 5K		√		√	√
Core Implementation	√	√	√	√	√
Synthesis Tools					√
X-BLOX		√		√	√
XChecker Cable	√	√	√	√	√
Hotline Support	√	√	√	√	√

Alliance Series: Viewlogic *Stand-alone* – Extended System (PC)

Extended *Stand-alone* System Includes:

- Workview Office Schematic editor with library support for XC2000, XC3000, XC3000A, XC3100, XC3100A, XC4000/E, XC5200 FPGAs and XC7000 and XC9500 CPLDs
- Workview Office Functional, Timing, and VHDL Simulation (unlimited gates)
- ViewSynthesis – VHDL synthesis with X-BLOX integration and library synthesis support for XC2000, XC3000, XC3000A, XC3100, XC3100A, XC4000/E and XC5200 FPGAs
- X-BLOX Module Generator and Optimizer
- Core Implementation Software for CPLDs and FPGAs with device support for XC2000, XC3000/ XC3100, XC4000/E, XC5200, XC7300, and XC9500
- Software Support and Updates if on maintenance

Support and Updates Include:

- Hotline Telephone Support
- Access to Xilinx Technical Bulletin Board
- Apps FAX and E-Mail
- Software Updates if on maintenance
- Online Documentation
- World Wide Web Access
- Technical Newsletter
- Extensive Application Notes

Required Hardware Environment:

- Fully IBM compatible PC386/486/Pentium
- MS-Windows 3.1 (minimum)
- MS-DOS version 5.0 (minimum)
- Minimum 90 Mbytes hard-disk space
- One ISO 9660 compatible CD-ROM drive
- VGA display
- 3-Button Serial Mouse
- One parallel and two serial ports
- 16 Mbytes of RAM for devices up to XC4008
- 24 Mbytes of RAM for XC3195, XC4010
- 32 Mbytes of RAM for XC4013

Package Features - Viewlogic PC

Feature	VL Base	VL Std.	VLS Base	VLS Std.	VLS Ext.
Libraries and Interface	√	√	√	√	√
Schematic Editor			√	√	√
Simulator (Limited)			√		
Simulator (Unlimited)				√	√
EPLD Devices	√	√	√	√	√
FPGA Limited	√		√		
FPGA 2K, 3K, 4K, 5K		√		√	√
Core Implementation	√	√	√	√	√
Synthesis Tools					√
X-BLOX		√		√	√
XChecker Cable	√	√	√	√	√
Hotline Support	√	√	√	√	√

Alliance Series: Viewlogic – Standard System (Workstation)

Standard System Includes:

- Schematic Interface for Draw with library support for XC2000, XC3000, XC3000A, XC3100, XC3100A, XC4000/E, XC5200 FPGAs and XC7000 and XC9500 CPLDs
- Functional and Timing Simulation Interface for ViewSim
- Core Implementation Software for CPLDs and FPGAs with device support for XC2000, XC3000, XC3000A, XC3100, XC3100A, XC4000/E, XC5200, XC7300, and XC9500
- X-BLOX Module Generator and Optimizer
- Software Support and Updates if on maintenance

Note:

- This package does not include schematic capture or simulation tools. They must be purchased separately.
- Interface supports Workview Office and PRO Series

Support and Updates Include:

- Hotline Telephone Support
- Access to Xilinx Technical Bulletin Board
- Apps FAX and E-Mail
- Software Updates if on maintenance
- Online Documentation
- World Wide Web Access
- Technical Newsletter
- Extensive Application Notes

Required Hardware Environment:

- 50 to 200 MB hard disk space allocated Xilinx designs.
- 32 MB of RAM (minimum)
- Color Monitor
- Swap Space: 140 MB (minimum)
- TCP/IP Software
- CD ROM Drive

Sun-4 Sparcstation Series

- Sun OS 4.1.X
- X-Windows (R3 or R4)
- Open Windows or Motif

HP700 Series

- HPUX 9.0/9.1
- X-Windows (R5)
- HP_VUE 3.0

Recommended Hardware Environment:

- Additional RAM to increase performance

Package Features - Viewlogic W/S

Feature	Std.
Libraries and Interface	√
Schematic Editor	
Simulator (Limited)	
Simulator (Unlimited)	
EPLD Devices	√
FPGA Limited	
FPGA 2K, 3K, 4K, 5K	√
Core Implementation	√
Synthesis Tools	
X-BLOX	√
XChecker Cable	√
Hotline Support	√

Alliance Series: Mentor V8 – Standard System (Workstation)

Standard System Includes:

- Mentor V8 Interface (Mentor Design Architect/QuickSim II Libraries and Interface)
- Core Implementation Software for XC2000, XC3000, XC3000A, XC3100, XC3100A, XC4000/E, XC5200 FPGAs and XC7300 and XC9500 CPLDs
- X-BLOX Module Generator and Optimizer
- Software Support and Updates if on maintenance

Note:

- This package does not include Design Architect schematic capture, or QuickSim II simulation tools. Contact your local Mentor Graphics sales office to purchase these tools.
- AutoLogic synthesis program, libraries and interface are available from Mentor Graphics.

Support and Updates Include:

- Hotline Telephone Support
- Access to Xilinx Technical Bulletin Board
- Apps FAX and E-Mail
- Software Updates if on maintenance
- Online Documentation
- World Wide Web Access
- Technical Newsletter
- Extensive Application Notes

Required Hardware Environment:

- 50 to 200 MB hard disk space allocated Xilinx designs.
- 32 MB of RAM (minimum)
- Color Monitor
- Swap Space: 140 MB (minimum)
- TCP/IP Software
- CD ROM Drive

Sun-4 Sparcstation Series

- Sun OS 4.1.X
- X-Windows (R3 or R4)
- Open Windows or Motif

HP700 Series

- HPUX 9.0/9.1
- X-Windows (R5)
- HP_VUE 3.0

Recommended Hardware Environment:

- Additional RAM to increase performance

Package Features - Mentor W/S

Feature	Std.
Libraries and Interface	√
Schematic Editor	
Simulator (Limited)	
Simulator (Unlimited)	
EPLD Devices	√
FPGA Limited	
FPGA 2K, 3K, 4K, 5K	√
Core Implementation	√
Synthesis Tools	
X-BLOX	√
XChecker Cable	√
Hotline Support	√

Alliance Series: Synopsys – Standard System (Workstation)

Standard System Includes:

- XC3000, XC3000A, XC3100, XC3100A, XC4000/E and XC5200 synthesis library
- Core Implementation Software for XC2000, XC3000, XC3000A, XC3100, XC3100A, XC4000/E, XC5200 FPGAs, and XC7300 and XC9500 CPLDs
- X-BLOX Module Generator and Optimizer
- Works with Synopsys Design Compiler and FPGA Compiler
- Translator from Synopsys to Xilinx XNF
- Software Support and Updates if on maintenance

Note:

- This package does not include Synopsys Design Compiler or FPGA Compiler. These must be purchased separately from Synopsys.

Support and Updates Include:

- Hotline Telephone Support
- Access to Xilinx Technical Bulletin Board
- Apps FAX and E-Mail
- Software Updates if on maintenance
- Online Documentation
- World Wide Web Access
- Technical Newsletter
- Extensive Application Notes

Required Hardware Environment:

- 50 to 200 MB hard disk space allocated Xilinx designs.
- 32 MB of RAM (minimum)
- Color Monitor
- Swap Space: 140 MB (minimum)
- TCP/IP Software
- CD ROM Drive

Sun-4 Sparcstation Series

- Sun OS 4.1.X
- X-Windows (R3 or R4)
- Open Windows or Motif

HP700 Series

- HPUX 9.0/9.1
- X-Windows (R5)
- HP_VUE 3.0

Recommended Hardware Environment:

- Additional RAM to increase performance

Package Features - Synopsys W/S

Feature	Std.
Libraries and Interface	√
Schematic Editor	
Simulator (Limited)	
Simulator (Unlimited)	
EPLD Devices	√
FPGA Limited	
FPGA 2K, 3K, 4K, 5K	√
Core Implementation	√
Synthesis Tools	
X-BLOX	√
XChecker Cable	√
Hotline Support	√

Alliance Series: Cadence – Standard System (Workstation)

Standard System Includes:

- Cadence Interface (Composer and Concept Schematic Libraries and Verilog and RapidSim simulation models and interfaces)
- Core Implementation Software for XC2000, XC3000/A, XC3100/A, XC4000/E, XC5200 FPGAs, and XC7300 and XC9500 CPLDs
- LogiBlox Module Generator and Optimizer
- Software Support and Updates if on maintenance

Note:

- This package does not include Composer/Concept schematic capture, or Verilog/RapidSim simulation tools. Contact your local Cadence sales office to purchase these tools
- FPGA designer (Synergy based top-down FPGA design tool) is available from Cadence.

Support and Updates Include:

- Hotline Telephone Support
- Access to Xilinx Technical Bulletin Board
- Apps FAX and E-Mail
- Software Updates if on maintenance
- Online Documentation
- World Wide Web Access
- Technical Newsletter
- Extensive Application Notes

Required Hardware Environment:

- 50 to 200 MB hard disk space allocated Xilinx designs.
- 32 MB of RAM (minimum)
- Color Monitor
- Swap Space: 140 MB (minimum)
- TCP/IP Software
- CD ROM Drive

Sun-4 Sparcstation Series

- Sun OS 4.1.X
- X-Windows (R3 or R4)
- Open Windows or Motif

HP700 Series

- HPUX 9.0/9.1
- X-Windows (R5)
- HP_VUE 3.0

Recommended Hardware Environment:

- Additional RAM to increase performance

Package Features - Synopsys W/S

Feature	Std.
Libraries and Interface	√
Schematic Editor	
Simulator (Limited)	
Simulator (Unlimited)	
EPLD Devices	√
FPGA Limited	
FPGA 2K, 3K, 4K, 5K	√
Core Implementation	√
Synthesis Tools	
X-BLOX	√
XChecker Cable	√
Hotline Support	√

Alliance Series: Third Party Alliance – Standard System

Standard System Includes:

- X-BLOX Module Generator & Optimizer
- Core Implementation Software for CPLDs and FPGAs with device support for XC2000, XC3000/A, XC3100/A, XC4000/E, XC7300, and XC9500
- Software Support and Updates for the first year

Note:

- Purchase schematic and simulation tools and interfaces and libraries from a Xilinx 3rd-Party Alliance Partner

Support and Updates Include:

- Hotline Telephone Support
- Access to Xilinx Technical Bulletin Board
- Apps FAX and E-Mail
- Software Updates if on maintenance
- Online Documentation
- World Wide Web Access
- Technical Newsletter
- Extensive Application Notes

Required Hardware Environment (PC):

- Fully compatible PC386/486/Pentium
- MS-DOS version 5.0 (minimum)
- MS-Windows 3.1 (minimum)
- Minimum 80 MB hard disk space
- One ISO 9660-type CD-ROM drive
- VGA display (higher resolutions supported)
- One parallel and two serial ports
- 16 MB of RAM for devices up to XC4008
- 24 MB of RAM for XC3195A, XC4010
- 32 MB of RAM for XC4013
- Mouse

Required Hardware Environment (Workstation):

- 50 to 200 MB hard disk space allocated Xilinx designs.
- 32 MB of RAM (minimum)
- Color Monitor
- Swap Space: 140 MB (minimum)
- TCP/IP Software
- CD-ROM Drive

Sun-4 Sparcstation Series

- Sun OS 4.1.X
- X-Windows (R3 or R4)
- Open Windows or Motif

HP700 Series

- HPUX 9.0/9.1
- X-Windows (R5)
- HP_VUE 3.0

IBM RS6000

- AIX 3.2
- X-Windows Support

Package Features - Third Party Alliance

Feature	Std.
Libraries and Interface	√
Schematic Editor	
Simulator (Limited)	
Simulator (Unlimited)	
EPLD Devices	√
FPGA Limited ¹	
FPGA 2K, 3K, 4K, 5K	√
Core Implementation	√
Synthesis Tools	
X-BLOX	√
XChecker Cable	√
Hotline Support	√

