

# **Nisha Firmware Specification**

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MS-190 x4879

*Some Useful Definitions:*

The following is an explanation of the symbols that will be used throughout this document to describe the operation of the various firmware commands.

'<, >': The bracket symbols mean that the information inclosed within them is manditory.

'[, ]': The square bracket symbols mean that the information inclosed within them is optional.

'|' : The vertical bar symbol is used to indicate an alternative or "OR" condition. For example, A|B can be thought of as "Either A or B".

'::=': This symbol is used to indicate a definition or equivelence.

'{, }': Curly brackets are used to denote commemnts.

'+' : The plus sign is used as an addition symbol or logical or'ing.

'\$' : The dolar sign is used to indicate that a value is radix 16 {in other words, the number is in hexadecimal}. Values that are not preceded by '\$' are assumed to be decimal.

'NULL': This key word indicates the empty set, or in some cases the fact that the function whose value is NULL can be ignored. An example is:

Argle\_Bargle ::= <NULL>

Essentially you can forget that Argle\_Bargle exists for this context.

*Command Types:*

Widget commands are broken up into 3 categories:

1. ProFile commands

These commands are used emulate a ProFile mass storage device and provide for downward compatibility.

2. Diagnostic commands

These commands are used to separate the various subfunctions of the drive and provide a means to troubleshoot a Widget without the controller of performing any retrying of it's own.

3. System commands

These commands are used to operate a Widget at it's maximum efficiency. Blocks are transfered logically in a multiple block fashion, up to 255 blocks.

*ProFile Commands:*

Widget is designed to be backwards compatible with the current ProFile Driver, and to that end there exists the three ProFile System commands {Read, Write, and Write\_Verify} within the firmware.

<u>Opcode</u>	<u>Definition</u>
\$00	Read Logical Block
\$01	Write Logical Block
\$02	Write_Verify Logical Block

The three ProFile commands behave in exactly the same fashion as do the corresponding instructions on ProFile, with one small exception: the Read Logical command does not include information concerning Retry Count or Sparring Threshold {however, because of a side effect in the way that the Host/Controller interface was designed, the Host may write as many command bytes to the controller as it chooses. The Controller will only decode the first four.}. The form of each command is:

<\$00|\$01|\$02> <3 bytes of Logical Block Address>

There are two 'special' logical address defined in the ProFile protocol, namely \$FFFFFF {-1} and \$FFFFFE {-2}. Logical address (-1) returns as it's value Device\_ID {as explained under the section titles Diagnostic Commands} and logical address (-2) returns as it' s value Widget's spare table structure in it's raw form.

It should be noted that if *at any time* Widget can not pass it's self test that it will refuse to communicate via logical commands {both ProFile and System type commands}; Widget will respond to Diagnostic commands at all times, however.

The rest of the commands available on Widget are a complete departure from the way that ProFile was implemented. The new form of any command is:

( <Command\_Byte>  
 <Instruction\_Byte>

[Instruction\_Parameter]

<CheckByte> )

Command\_Byte ::= <CommandType\_Nibble + CommandLength\_Nibble>

CommandType\_Nibble ::= <Diagnostic\_Command|System\_Command>

Diagnostic\_Command ::= <\$10>

System\_Command ::= <\$20>

CommandLength\_Nibble ::= <Count of all the bytes in the command string *NOT* including the first one. For example, the command string to read Device\_ID is: ( <\$12> <\$00> <\$ED> ). The commandlength\_nibble in this case is 2.>

System\_Command ::= <Sys\_Read|Sys\_Write|Sys\_WrVer>

Diagnostic\_Command ::= ( <Read\_ID|  
Read\_Controller\_Status|  
Read\_Servo\_Status|  
Send\_Servo\_Command|  
Send\_Seek|  
Send\_Restore|  
Set\_Recovery|  
Soft\_Reset|  
Send\_Park|  
Diag\_Read|  
Diag\_ReadHeader|  
Diag\_Write|  
Auto\_Offset|  
Read\_SpareTable|  
Write\_SpareTable|  
Format\_Track|  
Read\_Abort\_Stat|  
Reset\_Servo|  
Read\_Track|  
Write\_Track> )

Instruction\_Parameter ::= { This value is instruction dependent, and will be formally defined at the same time as the individual instructions }

CheckByte ::= { This byte is the ones-complement of the sum, in MOD-256 arithmetic, of all the bytes in the instruction string *including* the Command\_Byte. }

*Diagnostic\_Commands:*

Widget's personality, or manner in which it behaves in a specific Host environment, can be thought of as having two distinct parts: 1) that portion that is dictated by the hardware and 2) that portion that is controlled by the firmware. As trite as that last statement may seem, the fact remains that the part of Widget that is the hardware is not easily molded to adapt to different conditions. The same is true, but not quite in the same manner, for the firmware: the code is locked in a ROM of some sort and costs a lot to change. How then can Widget's "personality" be changed {on-the-fly} to "adapt" to a new environment? The answer in this case was to architect the firmware in a layered fashion: build the intelligence required to operate Widget in its normal system mode from a pool of discrete, primitive functions; these primitive functions having just one specific task that they are capable of completing. The implication of this architecture is that with very little effort these same primitive functions are available to the Host system.

## Read\_ID

Read\_ID ::= <\$00>

Instruction\_Parameter ::= <NULL>

This diagnostic command requires Widget to deliver to the host some device specific information. The structural layout of the data returned is:

### STRUCTURE Identity\_Block

This identity block is defined by the data structures contained within it; you will note, however, that a comment is given explaining the type of structure for a given element and range of bytes - if the structure is thought of as a linear array of bytes - that include the structure. An example is NameString. It is a 13-character ascii string, and is located in bytes \$0:C.

NameString ::= <Lisa/Nisha2 {13 bytes/\$0:C; Ascii String}>

Device\_Type ::= <\$000110 {3 bytes/\$D:F}>

Firmware\_Revision ::= <{2 bytes/\$10:11}>

Capacity ::= <\$9836 {3 bytes/\$12:14}>

Bytes\_Per\_Block ::= <532 {2 bytes/\$15:16}>

Number\_Of\_Cylinders ::= <610 {2 bytes/\$17:18}>

Number\_Of\_Heads ::= <2 {1 byte/\$19}>

Number\_Of\_Sectors ::= <32 {1 byte/\$1A}>

Number\_Of\_Possible\_SpareBlocks ::= <\$00004C {3 bytes/\$1B:1D}>

Number\_Of\_SpareBlocks ::= <{3 bytes/\$1E:20, range 0..\$4B}>

Number\_Of\_BadBlocks ::= <{3 bytes/\$21:23, range 0..\$4B}>

## Read\_Controller\_Status

Read\_Controller\_Status ::= <\$01>

Every time an operation completes {normally or abnormally} Widget will return Standard\_Status. This allows the Host system to change it's flow of execution based on the state of the value returned in the Status. Normally, Standard\_Status is all that is necessary to ensure continuous operation. In the exceptional case, or when the Host system is emulating the controller's functions, additional information concerning the state of Widget is mandatory: without it the Host simply could not make an optimum choice in deciding a course of action.

Controller\_Status is then a means for the Host system to interrogate Widget further. Each Status {with the exception of Abort\_Status, which is a separate command and is discussed later in this document} belongs to a homogeneous data structure: namely a four byte quantity containing a bit map representing the various exceptional conditions that are available as the first four bytes read from the controller upon completion of the current command.

There are eight status' available to the Host system. The Host requests a specific status by setting the Instruction\_Parameter to the value corresponding to the status needed.

```
IF (Instruction_Byte = Read_Controller_Status)
  THEN Instruction_Parameter ::= (<Standard_Status|
                                Last_Logical_Block|
                                Current_Seek_Address|
                                Current_Cylinder|
                                Internal_Status|
                                State_Registers|
                                Exception_Registers|
                                Last-Seek_Address>)
```

The four byte response to each of the above status requests is of the form:

Status\_Response ::= (<Byte0> <Byte1> <Byte2> <Byte3>)

**Standard\_Status ::= <\$00>**

Byte0 ::= < Bit7: Other than \$55 response from Host  
Bit6: Write Buffer Overflow  
Bit5: {not used}  
Bit4: {not used}  
Bit3: Read Error  
Bit2: No Matching Header Found  
Bit1: Servo Error  
Bit0: Operation Failed >

Byte1 ::= < Bit7: {not used}  
Bit6: Spare Table Overflow  
Bit5: 5 or Less Spare Blocks Available  
Bit4: {not used}  
Bit3: Controller SelfTest Failure  
Bit2: Spare Table has been Updated  
Bit1: Seek Error  
Bit0: Controller Aborted Last Operation >

Byte2 ::= < Bit7: First Status Response since Power-On  
Bit6: Logical Block Number Out of Range  
Bit5: 0 : {not used}>

Byte3 ::= < Bit7: Read Error Detected by Ecc circuitry  
Bit6: Read Error Detected by Crc circuitry  
Bit5: Header timeout  
Bit4: {not used}  
Bit3: 0 : Number of unsuccessful retries {out of 10}>

**Last\_Logical\_Block ::= <\$01>**

**Byte0 ::= {not used}**

**Byte1 ::= <Most Significant Block Address>**

**Byte2 ::= <Next Most Significant Block Address>**

**Byte3 ::= <Least Significant Block Address>**

**Current\_Seek\_Address ::= <\$02>**

Byte0 ::= <Most Significant Cylinder Address>

Byte1 ::= <Least Significant Cylinder Address>

Byte2 ::= <Head Address>

Byte3 ::= <Sector Address>

**Current\_Cylinder ::= <\$03>**

Byte0 ::= <Most Significant Cylinder Address>

Byte1 ::= <Least Significant Cylinder Address>

Byte2 ::= <Head Address>

Byte3 ::= <Sector Address>

**Internal Status ::= <\$04>**

Byte0 ::= <Bit7: Recovery On  
Bit6: Spare Table Almost Full  
Bit5: Buffer Structure is Contaminated  
Bit4: Power reset has just occurred  
Bit3: Current Standard Status is non-zero  
Bit2:1 : {not used}  
Bit0: Controller LED is on>

Byte1 ::= <Bit7: On\_Track  
Bit6: Read Headers after data recal  
Bit5: Current operation is a write operation  
Bit4: Heads are parked  
Bit3: Sequential look-ahead table search  
Bit2: {not used}  
Bit1: Seek\_Complete  
Bit0: Auto\_Offset is ON>

Byte2 ::= {this status is valid ONLY after a ProFile or System Command}  
<Bit7: Seek\_Needed  
Bit6: Head\_Change\_Needed  
Bit5:2 {not used}  
Bit1: Current block is a BAD block  
Bit0: Current block is a SPARE block>

Byte3 ::= <SpareTable\_Type|UserData\_Type>  
SpareTable\_Type ::= <\$08>  
UserData\_Type ::= <\$02>

State\_Registers ::= <\$05>

Byte0 ::= {not used}

Byte1 ::= <Bit7: Ram\_Failure  
Bit6: Eprom\_Failure  
Bit5: Disk\_Speed\_Failure  
Bit4: Servo\_Failure  
Bit3: Sector\_Count\_Failure  
Bit2: State\_Machine\_Failure  
Bit1: Read\_Write\_Failure  
Bit0: No\_SpareTable\_Found>

Byte2 ::= <Bit7: Disk Read/-Write  
Bit6: SioRdy  
Bit5: Msel1  
Bit4: Msel0  
Bit3: Bsy  
Bit2: Cmd  
Bit1: EccError {active low}  
Bit0: Start {active low}>

Byte3 ::= <Bit7: CrcError {active low}  
Bit6: Write\_Not\_Valid {active low}  
Bit5: ServoReady  
Bit4: ServoError  
Bit3: 0 : Current state of the state-machine>

**Exception\_Registers ::= <\$06>**

**Byte0 ::= <Bit7: Read error  
Bit6: Servo error while reading  
Bit5: At least one successful read in last retry sequence  
Bit4: Header Timeout  
Bit3: CrcError or EccError  
Bit2: 0 : {not used}>**

**Byte1 ::= <Bit7 ::= EccError  
Bit6 ::= CrcError  
Bit5 ::= Header Timeout  
Bit4 ::= {not used}  
Bit3: 0 : {number of bad retries out of 10}>**

**Byte2 ::= <Bit7: Write Error  
Bit6: Servo Error while writing  
Bit5: At least one successful write in last retry sequence  
Bit4: Header Timeout  
Bit3: 0 : {not used}>**

**Byte3 ::= {number of bad retries out of 10}**

## **Read\_\_Servo\_\_Status**

**Read\_Servo\_Status ::= <\$02>**

**Instruction\_Parameter ::= <0..8>**

This status command is used to interrogate the Servo Processor in much the same way that Read\_Controller\_Status is used. In fact, the form of the result is the same four byte-mapped quantity.

This command is of the particular value to a diagnostician that is interested in 'scoping-out' the servo subsystem.

A more complete description of the servo commands can be read in the document titled "Widget Servo Functional Objective" written by Jim Reed.

## Send\_Servo\_Command

Send\_Servo\_Command ::= <\$03>

Instruction\_Parameter ::= (<Byte0> <Byte1> <Byte2> <Byte3>)

Normally, the Host will allow the controller to manipulate the servo processor in order to perform useful work. For example, let's suppose that the Host system wishes to move drive's heads from one track to another. Under normal operating conditions the preferred way to perform this task is to use the Send\_Seek command {explained later}. However, the Host has the capability to bypass the controller and direct the servo processor. Indeed, the Host can issue the servo command to position the heads so that the seek is completely transparent to the controller. The implication of this command is that the Host can gain even more control of the system if it so chooses.

A more complete description of the servo commands can be read in the document titled "Widget Servo Functional Objective" written by Jim Reed.

Byte0 ::= <S\_Command + S\_Direction + Hi\_Magnitude>

S\_Command ::= <Offset|  
Diagnostic|  
DataRecal|  
BrakeRelease|  
Access|  
Access\_Offset|  
Home>

Offset ::= <\$10>  
Diagnostic ::= <\$20>  
DataRecal ::= <\$40>  
BrakeRelease ::= <\$70>  
Access ::= <\$80>  
Access\_Offset ::= <\$90>  
Home ::= <\$C0>

S\_Direction ::= <Positive|Negative>

Positive ::= <\$08 {towards inside diameter}>  
Negative ::= <\$00 {towards outside diameter}>

Hi\_Magnitude ::= <0..3 {move heads in multiples of 256}>

Byte1 ::= <Low\_Magnitude ::= 0..255>

{note: Hi\_magnitude, Low\_magnitude, and S\_Direction establish the *relative* distance the heads must move to arrive at the target track}

Byte2 ::= <Offset\_Direction + Auto\_Offset\_Switch + Offset\_Magnitude>

Offset\_Direction ::= <Positive|Negative>

Positive ::= <\$80 {towards outside diameter}>

Negative ::= <\$00 {towards inside diameter}>

Auto\_Offset\_Switch ::= <ON|OFF>

ON ::= <\$40 {assert fine positioning}>

OFF ::= <\$00>

Offset\_Magnitude ::= <0..32>

Byte3 ::= <StatusRquest>

## Send\_Seek

**Send\_Seek ::= <\$04>**

**Instruction\_Parameter ::= ( <HiCyl> <LoCyl> <Head> <Sector> <AutoOffsetFlag> )**

**HiCyl, LoCyl, Head, Sector ::= <\$00..\$FF>**

**AutoOffsetFlag := <ON|OFF>**

**ON ::= <\$01>**

**OFF ::= <\$00>**

Widget's Send\_Seek command allows the Host system to place the heads over any track on the disk. The value of the seek address is sent as the Instruction\_Parameter, and each parameter is a byte in length. For example, for the Host to seek to (Cylinder 1, Head 0, Sector 18) without AutoOffset a seek command would be issued with the following Instruction\_Parameter: (\$0000, \$00, \$12, \$00).

## Send\_Restore

**Send\_Restore ::= <\$05>**

**Instruction\_Parameter ::= <DataRecal|BrakeRelease>**

**DataRecal ::= <\$40>**

**BrakeRelease ::= <\$70>**

The **Send\_Restore** command is used by the Host to initialize the servo processor and to put the heads in a known location. This command is the same as performing a Data/Format Recal except that the controller updates it's internal state to account for the new servo position.

## Set\_Recovery

Set\_Recovery ::= <\$06>

Instruction\_Parameter ::= <ON|OFF>

ON ::= <\$01>

OFF ::= <\$00>

The exception handling characteristics of Widget approximate a binary set: either Widget handles everything, or the Host system does. The command 'Set\_Recovery' is the Host's link with this protocol in that it is through this instruction that the Host can gain control of the media. When Widget comes up after being reset, it assumes control and sets *Recovery* to be ON. The Host system must overtly change this state if it wishes to emulate a different exception handling criteria. Once Recovery is OFF, the controller will always fail in an operation if an exception occurs: the Host *must* assume responsibility for ALL error handling.

## **Soft\_Reset**

**Soft\_Reset ::= <\$07>**

**Instruction\_Parameter ::= <NULL>**

This command instructs the Widget firmware to restart its flow of execution at its initialization point. The results should be the same as a power reset.

## Send\_Park

Send\_Park ::= <\$08>

Instruction\_Paramter ::= <NULL>

When the Host issues a Send\_Park command to the controller the results are that the heads are moved off the data surface and held very near the inside diameter crash stop. The difference between this command and the Send\_Servo\_Command: Home, is that Home is performed 'open-loop' with the crash stop as its reference point, while Send\_Park is an access command to a specific track. The net result is a fairly hefty savings of time.

## Diag\_Read

Diag\_Read ::= <\$09>

Instruction\_Parameter ::= (<Sector><SeqValue>)

Sector ::= (<0..31>)

SeqValue ::= (<NewSector|IncSector><Long>)

NewSector ::= \$80 {selects 'Sector' as the sector to be operated on}

IncSector ::= \$40 {increments the last sector value}

Long ::= \$20 {if Long then the ECC syndrome will be ignored and the checkbytes will be included at the end of the data}

The Diag\_Read command is used to read the block on the disk pointed to by the last seek address. The form of the returned data is exactly the same as that of ProFile\_Read or Sys\_Read in that 4 bytes of Standard\_Status precede the block of data.

## Diag\_ReadHeader

Diag\_ReadHeader ::= <\$0A>

Instruction\_Parameter ::= (<Sector><SeqValue>)

Sector ::= (<0..31>)

SeqValue ::= (<NewSector|IncSector><Long>)

NewSector ::= \$80 {selects 'Sector' as the sector to be operated on}

IncSector ::= \$40 {increments the last sector value}

Long ::= \$20 {if Long then the ECC syndrome will be ignored and the checkbytes will be included at the end of the data}

When the heads are positioned over an unknown location, or when it is suspected that a block's header is shot, it is time to use the Diag\_ReadHeader command. This instruction allows the host to 'suck-up' both whatever information is residing in the block's header field as well as the data from the block. The form of the result is:

Result ::= (<Header {bytes/\$00:05}>  
 <Gap {bytes/\$06:0C}>  
 <Data {bytes/\$0D:21F}>)

Header ::= (<HiCyl> <LowCyl> <HdSct> <-HiCyl> <-LowCyl> <-HdSct>)

HiCyl ::= <Most significant byte of cylinder address>

LowCyl ::= <Least significant byte of cylinder address>

HdSct ::= <Bit7:6 : Head address  
 Bit5:0 : Sector address>

-HiCyl ::= <ones-complement of HiCyl>

-LowCyl ::= <ones-complement of LowCyl>

-HdSct ::= <ones-complement of HdSct>

Gap ::= <\$00>

## Diag\_Write

Diag\_Write ::= <\$0B>

Instruction\_Parameter ::= <NULL>

Sector ::= (<0..31>)

SeqValue ::= (<NewSector|IncSector><Long>)

NewSector ::= \$80 {selects 'Sector' as the sector to be operated on}

IncSector ::= \$40 {increments the last sector value}

Long ::= \$20 {if Long then the ECC checkbytes are to be supplied at the end of the write data}

This instruction allows the Host to write a block of data to the location on the disk pointed to by the last seek address. Diag\_Write is valid for all states that the controller may wind up in, but is recommended that a Send\_Seek command precede the write command to ensure that the correct block will be written.

## **Auto\_Offset**

**Auto\_Offset ::= <\$OC>**

**Instruction\_Parameter ::= <NULL>**

This command is used by the Host to fine-position the heads after they are on-track. The auto\_offset function can also be implemented by using the Send\_Servo\_Command instruction; the difference is that the controller will update some internal information {remember, servo commands are transparent} as well as select the correct head to offset off of {the Widget system uses head 1 only for fine positioning}.

## Read\_SpareTable

Read\_SpareTable ::= <\$0D>

Instruction\_Parameter ::= <NULL>

Reading {and writing} the Widget's sparetable is an absolute must for diagnostic purposes, and if the Host wishes to emulate the controller. The result of this instruction is identical to performing a ProFile\_Read from block -1 {<\$FFFFFE>} and has the form:

```
Result ::= ( <Fence {bytes/$00: 03}>
             <RunNumber {bytes/$04: 07}>
             <Format_Offset {byte/$08}>
             <Format_InterLeave {byte/$09}>
             <HeadPtr_Array {bytes/$0A: 49}>
             <SpareCount {byte/$4A}>
             <BadBlockCount {byte/$4B}>
             <BitMap {bytes/$4C: 55}>
             <Heap {bytes/$56: 185}>
             <InterLeave_Map {bytes/$186: 1A5}>
             <Checksum {bytes/$1A6: 1A7}>
             <Fence {bytes/$1A8: 1AB}>
             <Zone_Table {bytes/$1AC: 1C1}>
             <Fence {bytes/$200: 203}> )
```

Fence ::= ( <\$F0> <\$78> <\$3C> <\$1E> )

RunNumber ::= <32-bit integer>

This integer is incremented once each time the spare table is written to to the disk. Because two copies are kept on the the disk, the RunNumber is used to indicate which is the more recent of the two, should both copies not be updated.

Format\_Offset ::= <0..NumberOfSectors>

Format\_Offset is the number of physical sectors there are from index mark until logical sector 0.

Format\_InterLeave ::= <0..6>

This number is the interleave factor for this disk and is used in calculating where each of the logical sectors are relative to actual sector locations.

HeadPtr\_Array ::= <ARRAY[0..63] of HeadPtr>

HeadPtr ::= <Nil+Ptr>

Nil ::= <\$80 {if Nil the end-of-chain}>

```
Ptr ::= <$00..$7F {address of next element}>
```

A Ptr is a 7-bit structure that 'points' to a specific location within the Heap. To arrive at the actual index value within the Heap, the Ptr must first be multiplied by 4 {the length of each element}.

When a disk is formatted and being written to for the first time, each logical block is assigned the first available physical block on the disk. Therefore you would expect that LogicalBlock(0) would occupy PhysicalBlock(0), L(1) --> P(1), etc. There are instances, however, when a block of data must be relocated to another space on the disk that does not follow the original progression (for example, the original space was defective). In order to 'find' these relocated blocks in the future a record must be kept as to where all these relocated blocks have been put. This record takes the form of 128 linked lists having the form:

```
HeadPtr[n] --> LinkedList[n], where n ::= [0..127]
```

The algorithm for deciding whether or not a logical block has been relocated is to extract bits 10:16 from the LogicalBlockNumber and use it as an index into the HeadPtrArray:

```
IF (HeadPtr[LogicalBlockNumber/bits 10:16].Nil)
  THEN LogicalBlock has not been relocated
  ELSE use HeadPtr[.].Ptr to begin searching the chain for a matching
       element {refer to the structure of ListElement for more detail}
  IF no matching ListElement
    THEN LogicalBlock has not been relocated
    ELSE the element position in the Heap corresponds to the new
         physical block location
```

```
SpareCount ::= <$00..$4B>
```

```
BadBlockCount ::= <$00..$4B>
```

```
BitMap ::= <ARRAY[$00..$4B] of Bits>
```

The bit map is used to keep a record of which spare blocks are occupied.

```
Heap ::= <ARRAY[$00..$4B] of ListElement>
```

```
ListElement ::= (<Nil+Used+Useable+Spr_Type+Data_Type>
                 <Token>
                 <Ptr>)
```

```
Used ::= <$40>
```

```
Useable ::= <$20>
```

```
Spr_Type ::= <Spare|BadBlock>
```

```
Spare ::= <$10>
```

```
BadBlock ::= <$00>
```

```
Data_Type ::= <Data|SpareTable>
```

```
Data ::= <$02>
```

SpareTable ::= <\$08>

Token ::= <Bits 0:9 of LogicalBlock>

InterLeave\_Map ::= <ARRAY[0..31] of [0..31]>

The InterLeave\_Map is used to logical re-interleave the drive so that Widget can be run optimally on any system without having different manufacturing or formatting processes.

Check\_Sum ::= <sum of all bytes in the spare table from the first fence to beginning of this structure, in MOD-65536 arithmetic>

Zone\_Table ::= <ARRAY[0..NumberOfZones] of Zone\_Element>

Zone\_Element ::= <Offset\_Direction+Offset\_Magnitude>

## Write\_SpareTable

Write\_SpareTable ::= <\$0E>

Instruction\_Parameter ::= (<\$F0> <\$7B> <\$3C> <\$1E>)

This command allows the Host to 'force' a new spare table on the controller, and is executed just like any of the other write commands (data, in this case, **MUST** conform to the structure presented in Read\_SpareTable}. The data sent to the controller is written to the two spare table locations on the disk.

## Format\_Track

**Format\_Track ::= <\$0F>**

**Instruction\_Parameter ::= (<Password>)**

**Password ::= (<\$F0> <\$7B> <\$3C> <\$1E>)**

The format command is used to:

1. Operate on the track that is currently beneath the heads - this implies that the Host had best perform a Send\_Seek and Auto\_Offset command prior to formatting a track.
2. New headers will be layed down in every sector of the track.

## **Read\_Abort\_Status**

**Read\_Abort\_Status ::= <\$11>**

**Instruction\_Parameter ::= <NULL>**

Read\_Abort\_Status will return valid data only AFTER the controller has aborted (identified by Standard\_Status.Byte1.Bit0). The form of the result is a 16 byte string, and its contents are the contents of the controller's registers at the time of the abort - with the exception of byte \$0F, which is the value of the Abort taken.

## **Reset\_Servo**

**Reset\_Servo ::= <\$12>**

**Instruction\_Parameter ::= <NULL>**

Reset\_Servo allows the Host to initialize the servo processor without having to power the device down. The controller will automatically reset the Servo, set the baud rate at 57.6K, and check for valid initial conditions.

## Read\_Track

Read\_Track ::= <\$13>

Instruction\_Parameter ::= (<Sector><SeqValue>)

Sector ::= (<0..31>)

SeqValue ::= (<NewSector|IncSector><Long>)

NewSector ::= \$80 {selects 'Sector' as the sector to be operated on}

IncSector ::= \$40 {increments the last sector value}

Long ::= \$20 {if Long then the ECC syndrome will be ignored and the checkbytes will be included at the end of the data}

## Write\_Track

**Write\_Track ::= <\$13>**

**Instruction\_Parameter ::= (<Sector><SeqValue>)**

**Sector ::= (<0..31>)**

**SeqValue ::= (<NewSector|IncSector><Long>)**

**NewSector ::= \$80 {selects 'Sector' as the sector to be operated on}**

**IncSector ::= \$40 {increments the last sector value}**

**Long ::= \$20 {if Long then the ECC checkbytes are to be supplied at the end of the write data}**

This diagnostic command is used mainly to facilitate the Nisha FST program. The entire track (as defined by the last Seek address, and beginning with Sector or Last\_Sector + 1 if NewSector or IncSector is set) is written. Data, however is sent for only the first sector written (implying that the whole track will be written with the same data pattern). This diagnostic command is used mainly to facilitate the Nisha FST program.

*System Commands:*

System commands have been implemented for essentially two reasons:

1. It was important for Widget to add one more check on the CMD/BSY handshake: namely the addition of a checkbyte following the command string.
2. In order to increase the performance of the system without modifying the hardware it was critical to introduce another level of parallelism into the Host/Controller interface. Most of the reads for a specific block on the disk are followed by a read for the next logically sequential block. Therefore the command decoding and checkbyte comparison for all but the first block has been suppressed into a multiblock-type command. The implementation for this added parallelism is to send an extra parameter with the (first) LogicalBlock indicating the number of blocks to be read sequentially.

## **Sys\_Read**

Instruction\_Parameter ::= (<BlockCount> <LogicalBlock>)

BlockCount ::= <\$01..\$FF>

This parameter is the number of blocks to be read that follow sequentially from LogicalBlock. It is assumed that one block (LogicalBlock) will be read.

LogicalBlock ::= <\$000000..009835>

## **Sys\_Write**

Instruction\_Parameter ::= (<BlockCount> <LogicalBlock>)

BlockCount ::= <\$01..\$FF>

This parameter is the number of blocks to be read that follow sequentially from LogicalBlock. It is assumed that one block (LogicalBlock) will be read.

LogicalBlock ::= <\$000000..009835>

## Command Summary

### Profile\_Commands:

Profile\_Read ::= (<\$00> <3 bytes LogicalBlock>)  
Profile\_Write ::= (<\$01> <3 bytes LogicalBlock>)

### Diagnostic\_Commands:

Read\_Id ::= (<\$12> <\$00> <\$ED>)  
Read\_Controller ::= (<\$13> <\$01> <StatusRequest> <CheckByte>)  
Read\_Servo\_Status ::= (<\$13> <\$02> <StatusRequest> <CheckByte>)  
Send\_Servo\_Command ::= (<\$16> <\$03> <CommandRequest> <CheckByte>)  
Send\_Seek ::= (<\$17> <\$04> <SeekAddress> <AutoOffset Flag> <CheckByte>)  
Send\_Restore ::= (<\$13> <\$05> <RecalType> <CheckByte>)  
Set\_Recovery ::= (<\$13> <\$06> <On/Off> <CheckByte>)  
Soft\_Reset ::= (<\$12> <\$07> <\$E6>)  
Send\_Park ::= (<\$12> <\$08> <\$E5>)  
Diag\_Read ::= (<\$14> <\$09> <Sector> <SeqValue> <CheckByte>)  
Diag\_ReadHeader ::= (<\$14> <\$0A> <Sector> <SeqValue> <CheckByte>)  
Diag\_Write ::= (<\$14> <\$0B> <Sector> <SeqValue> <CheckByte>)  
Auto\_Offset ::= (<\$12> <\$0C> <\$E1>)  
Read\_SpareTable ::= (<\$12> <\$0D> <\$E0>)  
Write\_SpareTable ::= (<\$16> <\$0E> <Password> <CheckByte>)  
Format\_Track ::= (<\$16> <\$0F> <Password> <CheckByte>)  
Read\_Abort\_Status ::= (<\$12> <\$11> <\$DC>)  
Reset\_Servo ::= (<\$12> <\$12> <\$DB>)  
Read\_Track ::= (<\$14> <\$13> <Sector> <SeqValue> <CheckByte>)  
Write\_Track ::= (<\$14> <\$14> <Sector> <SeqValue> <CheckByte>)

### System\_Commands:

Sys\_Read ::= (<\$26> <\$00> <BlockCount> <LogicalBlock> <CheckByte>)  
Sys\_Write ::= (<\$26> <\$01> <BlockCount> <LogicalBlock> <CheckByte>)  
Sys\_WrVer ::= (<\$25> <\$02> <LogicalBlock> <CheckByte>)  
  
Password ::= (<\$F0> <\$7B> <\$3C> <\$1E>)

## Abort\_Status\_Variables

There are occasions when the Nisha Controller will detect that something is radically wrong with the Nisha SubSystem, i.e., the ram on board the controller goes on vacation, or the positioning system gives up the ghost, etc. In one of these cases the controller will abort its current instruction and return control to the Host, hopefully with enough information that the Host can make an intelligent decision concerning the state of Nisha.

The Host can read some information concerning the abort that the controller took by requesting Read\_Abort\_Status. This command returns a result that is 20 bytes long: 4 bytes of standard status and 16 bytes of abort status. The contents of the abort status are dependent upon the actual abort taken, and is determined by examining the contents of byte 16: the value of the abort taken.

- \$01: Illegal interface response, or Host Nak  
Byte/\$09: Response byte that caused abort
- \$02: Illegal Ram Bank select  
Byte/\$00: Bank number
- \$03: Format Error: illegal state-machine state  
Byte/\$0A: state of state-machine at time of abort
- \$04: Illegal Rom Bank Select  
Byte/\$00: Bank number
- \$05: Illegal interrupt or DeadMan\_Timeout  
Bytes/\$0A: 0B: Address of routine at time of timeout
- \$06: Format Error: Error while writing sector  
Byte/\$09: Error status from FormatBlock
- \$08: Command Checkbyte Error
- \$09: ProFile or System command attempted while SelfTest Error
- \$0A: Illegal Command
- \$0B: Unrecoverable Servo Error while reading
- \$0C: Sparing attempted on non-existent spare block
- \$0D: Sparing attempted while sparettable full
- \$0E: Deletion attempted of non-existent bad block
- \$0F: Illegal exception instruction
- \$10: Write buffer overflow
- \$11: Unrecoverable servo error while writing
- \$12: Servo status request sent as Servo command
- \$13: Restore Error: Non-Recal parameter  
Byte/\$00: Value of illegal parameter sent
- \$14: Illegal password sent to Write\_SpareTable\_Command
- \$15: Illegal password sent to Format command
- \$16: Illegal format parameters  
Bytes/\$09: 0A: illegal parameters
- \$17: Illegal password sent to Init\_SpareTable\_Command
- \$18: Zero block count sent to System\_Command
- \$19: Write Error: Illegal state-machine state  
Byte/\$0A: State-machine state at time of abort
- \$1A: Read Error: illegal state-machine state  
Byte/\$0A: State-machine state at time of abort

\$1B: ReadHeader Error: illegal state-machine state  
    Byte/\$0A: State-machine state at time of abort  
\$1C: Request for illegal logical block  
    Bytes/\$00:02: logical block number  
\$1D: External Stack overflow  
    Bytes/\$04:07: stack history  
\$1E: Search for SpareTable failed  
\$1F: No sparetable structure found in sparetable  
\$20: Update of sparetable failed  
\$21: Illegal sparecount instruction  
    Bytes/\$09: value of illegal instruction  
\$22: Unrecoverable servo error while seeking  
\$23: Unable to transmit command to servo  
\$24: Unable to receive status from servo  
\$25: Unable to find any headers after DataRecal  
\$26: Servo error after servo reset  
    Byte/\$0A: value of controller status port  
\$27: Servo communication error after servo reset  
\$28: Scan attempted without sparetable  
\$29: Illegal Bank Call  
\$2A: Illegal Bank Return  
\$2B: Illegal Sector value detected in LocateSector  
\$2C: Illefal Sector value detected while remapping  
\$2D: Control/Status register in GA is non-functional  
\$2E: Read gate not active  
\$2F: Read always active  
\$30: Statemachine single step error  
\$31: Data compare mismatch in r/w selftest  
\$32: SpareTable Ptr is addressing out-of-bounds  
\$33: New Spare not found by OverLap