# 6600

CHIPPEWA OPERATING SYSTEM

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or use Comment Sheet located in the rear of this book.

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|--|------|
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### System Tape -

The system tape is loaded from the dead start panel via tape unit 50. This tape contains the operating system and the peripheral and central program libraries. Dead start panel settings are as follows.

```
01
     1410
     7305
02
     0006
03
     7505
O_{\Gamma}
05
     7113
06
     0000
07
     7705
10
     2000
     7705
11
12
     2020
13
     7405
14
     7105
```

The system tape is completely loaded at dead start time. All data is transferred to the channel O disk file. The tape is not referenced during system operation.

The system tape contains a single file of binary records in standard format. There is one file mark at the end of the data. Copies of the system tape may be made by COPYBF in the central library on either tape or cards. Data on the system tape is organized as follows. Each item is treated as one record.

```
Load package.
System display (DSD).
Monitor (MTR).
Central resident (CR).
Peripheral package lLT.
Peripheral package lTD.
Peripheral package 2PC.
Peripheral package DMP.
Peripheral package LBC.
Peripheral package LOC.
Peripheral package PBC.
Peripheral package DIS.
Peripheral package SIM.
(zero length record)
Central library (one record each package)
```

#### Dead Start Load -

The dead start load of the system tape begins with the dead start panel settings on peripheral processor 0. Processor 0 transfers a short program to processor 5 which reads the first record from tape 50 on channel 5. This first record contains the peripheral resident program which is common to all processors and a transient package which loads the remainder of the system tape.

Processor 5 transfers the peripheral resident program to the other nine processors over their respective channels. Processor 5 then reads the next record from the system tape (DSD) and transfers it to processor 9. Processor 5 then reads the third record from the system tape (MTR) and transfers it to processor 0. Processor 5 then reads the next record from the system tape and transfers it to central storage. Processor 5 then disconnects all channels other than 5. This action starts execution in all ten processors. Processor 0 is permanently assigned the job of system monitor. Processor 9 is permanently assigned the job of system display. The other eight processors form a pool for temporary assignment of I/O packages.

Processor 5 reads the system tape to a zero length record. Each record prior to the zero length record is transferred to the disk file on channel 0 as a peripheral package. An entry is made in the Peripheral Library Directory (PLD) as each package is transferred.

Processor 5 then reads the remainder of the system tape and transfers each record to the channel zero disk file as a central processor package. An entry is made in the Central Library Directory (CLD) for each package transferred. Processor 5 then idles in the resident peripheral program until action is requested as a pool PP.

### Equipment Numbers -

Each peripheral equipment is assigned a two digit octal number to uniquely identify the equipment for job assignments. This equipment number is the same as the relative location of the equipment data in the equipment status table. Numbers are assigned as follows.

- 00 Channel O disk file. Ol - Channel 1 disk file.
- 02 Channel 2 disk file.
- O4 Channel 4 card reader.
- 05 Channel 12 card reader.
- 06 Channel 13 card punch.
- 07 Channel 13 line printer.
- 10 Channel 10 display.
- 11 Channel 11 display.
- 30 Channel 3 one inch tape.
- 31 Channel 3 one inch tape.
- 32 Channel 3 one inch tape.
- 33 Channel 3 one inch tape. 40 Channel 4 half inch tape.
- 41 Channel 4 half inch tape.
- 42 Channel 4 half inch tape.
- 43 Channel 4 half inch tape.
- 50 Channel 5 half inch tape.
- 51 Channel 5 halr inch tape. 52 - Channel 5 half inch tape.
- 53 Channel 5 half inch tape.
- 60 Channel 6 one inch tape. 61 - Channel 6 one inch tape.
- 62 Channel 6 one inch tape.
- 63 Channel 6 one inch tape. 70 - Channel 7 one inch tape.
- 71 Channel 7 one inch tape.
- 72 Channel 7 one inch tape.
- 73 Channel 7 one inch tape.

### Central Storage Special Addresses -

Central storage addresses 0000 thru 7777 are reserved for tables and communication areas for the operating system. Special fixed storage locations are as follows.

```
0000
      Zero
0001 RPL, , , . Tesidat pauph Ly Brany
0002 PLD, limit, , , , Pearph. (18. Directy
0003 DFB, input, output, limit, .
0004 FNT, limit, , , . Fice NAME TABLE
0005 EST, limit, , , . GOUIPMENT STORES MAGGE
0006 RSL, limit, , , sestent subscutive Library
0007 CLD, limit, , , . Centar Grany Directry
0010 TRTO, last track, , GO to G3, G4 to G7.7 Mark

0011 TRT1, last track, , GO to G3, G4 to G7.  ASSOCIATED

0012 TRT2, last track, , GO to G3, G4 to G7.
0014 Monitor step control.
0015/0017 Channel status table.
0020 CP zero status.
0023 idle time for central processor.
0024 idle time for peripheral processors.
0026 XJ address for simulator.
0027 P address for simulator.
0030/0037 time and date line.
0040/0052 starting time counts.
0055 temporary storage for monitor.
0056/0057 CP stack indicators.
0060 PP1 communication area.
0070 PP2 communication area.
0100 PP3 communication area.
0110 PP4 communication area.
0120 PP5 communication area.
0130 PP6 communication area.
0140 PP7 communication area.
0150 PP8 communication area.
0160 PP9 communication area.
0170 PPO communication area.
0200 Control point one area.
0400 Control point two area.
0600 Control point three area.
1000 Control point four area.
1200 Control point five area.
1400
      Control point six area.
1600 Control point seven area.
```

#### Control Point Area Information -

(12) millisecond count.

There are seven control points. Each occupies 200 octal locations and includes the following information.

```
000/017 exchange package
020/022 status information
023 central processor running time.
7024 peripheral processor running time
025 PP recall input register
026 sense lights and switches
027 equipment assignments
030/037 last dayfile message.
040/177 control statement buffer.
Word 020 -
(12) status byte (W, X, 1, 2, 3, 4, 5, 6, 7, 8, 9, 0)
(12) error flag.(12) storage move flag.(12) reference address (hundreds).
(12) field length (hundreds).
Word 021 -
(48) job name in display code.
(12) next statement address.
Word 022 -
(12) priority.
(12) message count.
(12) tract count.
(12) time limit (8 sec increments).
(12) operator assigned equipment.
                            MAN DUSEN
Word 023/024 -
(24)
      not used.
(24) second count.
```

### Equipment Status Table -

The equipment status table contains a one word entry for each physical equipment in the system. Format is as follows.

- (12) Control point address.
- (12) Channel number.
- (12) Synchronizer and unit number.
- (12) Equipment type.
- (12) Not used.

Equipment type designations are as follows. (display code)

- (DA) Channel zero disk cabinet.
- (DB) Channel one disk cabinet.
- (DC) Channel two disk cabinet.
- (DS) Display console.
- (CP) Card punch.
- (CR) Card reader.
- (LP) Line printer.
- (MT) 607 magnetic tape.
- (WT) 626 magnetic tape.

The upper bit of the equipment type designator is used as an interlock for equipment which is turned off. This bit is set when the equipment is not available and cleared when the equipment is available.

#### Channel Status Table -

The channel status table occupies 3 words (0015/0017) in resident central storage. These three words are read into monitor peripheral storage for updating and the first 12 bytes in these three words are then associated with the 12 I/O channels. Two psuedo-channels (octal 14 and 15) are used for access to the FNT/FST entries. A cleared byte indicates the channel is not assigned. A processor number indicates the channel assignment.

## File Name Table / File Status Table -

The name and status of all system files are kept in common table structure. This table contains two words per entry. The first word identifies the file and the second word records the status of the file. Formats for each two word entry is as follows.

### First Word.

- (42) file name in display code.
- (6) priority number.
- (6) not used.
- (3) file type (0 = input, 1 = output, 2 = common, 3 = local).
- (3) control point assignment.

## Second Word. (disk file)

- (12) equipment number.
- (12) beginning track.
- (12) current track.
- (12) current sector.
- (12) last buffer status.

# Second Word. (tape file)

- (12) equipment number.
- (24) last block number.
- (12) not used.
- (12) last buffer status.

#### Second Word. (card file)

- (12) equipment number.
- (24) card count this record.
- (12) end of job flag.
- (12) last buffer status.

The buffer status byte in the file status word serves as an interlock for peripheral processor access to the corresponding file data. The last buffer status has an even numerical value while a peripheral package is altering the file data. The last buffer status has an odd value when no peripheral action is involved with the file. Pseudo-channel 14 is requested for access to an existing file status. Pseudo-channel 15 is requested for access to enter a new file name in the table.

### Resident Peripheral Library -

Central storage addresses 10000 thru 13777 are reserved for resident peripheral packages. Each package begins with a control word in the following format.

- (42) package name in display code.
- (18) package length.

The last package is followed by a blank word.

## Peripheral Library Directory -

A directory of peripheral packages located in the disk file library is kept in resident central storage. Each entry in the directory requires one central word. Format of this word is as follows:

- (42) package name in display code.
- (6) sector number.
- (12) track number.

### Resident Subroutine Library -

Central program subroutines which are most frequently used are kept in resident central storage. A program calling for these subroutines may read them directly from this RSL (Resident Subroutine Library) area rather than from the disk file library. Each subroutine in the RSL is preceded by a one word tag in the following format.

- (42) package name in display code.
- (18) package length.

The last package is followed by a blank word.

### Central Library Directory -

Central programs and subroutines which are not stored in the RSL are stored in a disk file library. A directory of these programs and subroutines is kept in central storage (CLD). Each entry in this directory consists of one central word in the following format.

- (42) package name in display code.
- (6) sector number.
- (12) track number.

# Disk File Organization -

Programs, alpha data files, and binary data files are stored in the disk file system in a common structure. The addressing mechanism and file marking system are the same for all types of files.

The physical disk structure of cabinets, head groups, tracks, and sectors, is modified slightly in system addressing. The basic unit for purposes of reserving storage is a half track. This half track unit consists of either the odd or the even numbered sectors in a physical track within a head group. This modification is intended to provide minimum access for a single processor streaming data from a disk record.

A disk file in the logical sense is a named half track (with continuation half tracks) in a single disk cabinet. A disk file may be of any length within the limits of the storage capacity of the disk cabinet. There are 2048 half tracks in a disk cabinet which may be grouped into named files of any length. Each named file must begin at the first sector of a half track.

Each sector in a file contains two control bytes in addition to the 320 data bytes in a sector. The first of these control bytes designates the location of the next sector in the named file. The second control byte designates the number of central words in the current sector. These two control bytes are the first two bytes in a sector.

The first control byte in a disk file sector is coded in one of two formats. If the next sector is in the same half track, the control byte is coded with the next sector number (0000 thru 0077). If the next sector in the named track is the beginning of a new half track, the control byte is coded for the next half track number. (4000 thru 7777). The format for this control byte is as follows.

- (l bit) always set.
- (7 bits) track number.
- (1 bit) odd/even track.
- (3 bits) head group number.

The control bytes for the last sector in a named file are both zero. This sector is read as a file mark. No data can appear in a disk file after the first file mark.

## Track Reservation Tables -

A 2048 bit reservation table is maintained in resident central storage for each disk file cabinet. One bit in the table corresponds to each half track in the disk cabinet. Each table occupies 64 central words and uses the lowest order 32 bits of each word. The highest order 7 bits of the track number specify table address and the lower order 5 bits of the track number specify bit position in the word.

Alpha Files (Tape, Disk) -

Alpha files are stored in packed display code for disk storage and magnetic tape storage. Each line of alphanumeric characters begins at the first byte of a central word and continues two characters per byte to the end of a line of code. The line of coding may be of any length. The last central word used in a line of coding is completed with cleared bytes. A cleared byte is the end of line designation.

Punched Card Formats -

Alpha files - Hollerith code - 80 columns.

Binary files - 15 words per card.

- 7, 9 punch column one.

- word count in column one row 0,1,2,3.

- correction punch in column one row 4.

- check sum modulo 4095 in column two.

Record separator - 7,8,9 punch column one.

File separator - 6,7,8,9 punch column one.

## Resident Peripheral Program -

This program is loaded in each peripheral processor on dead start and is not altered throughout execution of the system. The resident program occupies peripheral addresses 0100 thru 0771. Temporary storage addresses 0000 thru 0017 are used by the resident program. These locations may also be used by a called peripheral package as no continuity is required between executions of the resident program.

Peripheral addresses 0075 thru 0077 are constants for a processor and should not be altered by a peripheral package. These addresses contain the following information.

0075 processor input register address 0076 processor output register address 0077 processor message buffer address

Peripheral Communication Areas -

Central storage addresses 0060 thru 0177 are used for peripheral processor communication. Each processor is assigned a block of eight words. Format within a processor area is as follows.

(one word) processor input register (one word) processor output register (six words) message buffer

The processor input register is cleared when the processor is idling. The monitor processor enters a control word in the input register to call a transient peripheral package to the processor. The resident peripheral program senses the input register entry and searches the RPL (Resident Peripheral Library) for the peripheral package named in the input register. If the named package is not in the RPL the resident program then searches the PLD (Peripheral Library Directory) for the named package. When the package has been located the resident program reads the package into peripheral storage beginning at peripheral address 1000. The resident program then jumps to peripheral address 1000 to begin execution of the transient package.

Format for the input register control word is as follows.

(18 bits) package name in display code

(6 bits) control point number

(36 bits) arguments

The processor output register is entered by a transient peripheral package to communicate with the peripheral monitor. The monitor continually scans the processor output registers and interprets the requests from the transient peripheral packages. The message buffer is used for those monitor requests in which the output register is not sufficiently large. The monitor program clears a processor output register when the requested function has been performed.

The processor input register is not altered during the execution of a transient peripheral package. Upon completion of the transient package a request to the monitor clears the input register and the transient package exits to the resident peripheral program (address 100).

Monitor Calls From Peripheral Processors -

The following control words are interpreted by the monitor processor when entered in a peripheral output register.

0001, 0000,0000,0000,0000 Dayfile message.

The monitor enters a message in the dayfile consisting of a time tag, a job tag, and the contents of the peripheral processor message buffer. The message buffer should contain one line in packed display code.

0002,00NN,0000,0000,0000 Request channel NN.

Assign channel NN to the requesting processor as soon as the channel is available.

0003,00NN,0000,0000,0000 Drop channel NN.

Release the assignment of channel NN from the requesting processor.

0004,0000,0000,0000,0000 Assign PP time.

Update the pp running time at the control point for the requesting processor.

0005,0000,0000,0000,0000 Monitor step control

Shift monitor control to step mode using central address 0014 for control.

0006,00NN,0000,0000,0000 Request track.

Request any unassigned track from track reservation table NN (10,11,12). If a track is available the track number is entered in the first byte of the process message buffer. If a track is not available a cleared byte is returned.

0007,00NN,TTTT,0000,0000 Drop track.

Drop track TTTT from track reservation table NN (10,11,12)

0010,NNNN,0000,0000,0000 Request storage.

Assign NNNN hundred octal words of central storage to the control point of the requesting processor. If a conflict exists the monitor may not assign the requested storage. The requesting processor must sense the control point status to verify the actual assignment. A pause function (17) should preceed further requests for storage.

0011,0000,0000,0000,0000 Release PP.

Release the peripheral processor from the control point. monitor clears the processor input register before resuming this function.

0013,0000,0000,0000 Abort control point.

Abort the job associated with the requesting processor. The requesting processor is responsible for an explanatory message in the dayfile. The monitor clears the PP input register before resuming this function.

0014,TTTT,0000,0000,0000 Time limit.

Enter a job time limit at the associated control point TTTT increments of time. Each time increment is 8 seconds.

0015,0000,0000,0000,0000 Request central processor.

Set the central waiting flag (W) at the associated control point. Search for job priorities to initiate central processor action.

0016,0000,0000,0000,0000 Drop central processor.

Drop central processor execution for the job at the associated control point.

0017,0000,0000,0000,0000 Pause for relocation.

This function allows the monitor to move central storage data for the associated job. The requesting processor should check the reference address for the control point after resume from this function.

> 0020,0000,0000,0000,0000 Request PP.

This function requests the initiation of another peripheral processor. The first word of the message buffer associated with the requesting processor contains the input register data for the new PP. This function is resumed with the PP input register address in the first byte of the message buffer. If no PP is available a blank byte is returned.

0021,0000,0000,0000,0000 Recall central processor.

Restart the central program associated with the requesting PP only if the central recall flag (X) is set.

0022,NNNN,0000,0000,0000 Request equipment

Search the equipment status table for an equipment of type NNNN and assign this equipment to the control point. The monitor responds with the equipment number in the first byte of the PP message buffer if an equipment of the proper type is available. The monitor responds with a blank byte if the equipment is not available.

0023,00NN,0000,0000,0000 Drop equipment.
Drop equipment number NN from the associated control point.

0024,00NN,0000,0000,0000 Request priority.
Assign priority number NN to the control point.

0025,000N,0000,0000,0000 Error exit mode.

Assign exit mode N to the central program at the associated control point.

0027,0000,0000,0000,0000 Toggle simulator.

Toggle the status of the central processor and simulator. The simulator is called from the peripheral library or dropped as required.

0030,000N,0000,0000,0000 Operator drop.

Drop the job at control point N by setting the operator drop error flag.

0031,00NN,0000,0000,0000 Equipment on.

Clear the lockout bit for equipment NN in the equipment status table.

0032,00NN,0000,0000,0000 Equipment off.

Set the lockout bit for equipment NN in the equipment status table.

0033,00NN,000P,0000,0000 Assign equipment.

Assign equipment number NN to control point P and enter the number NN in the control point area for operator assignment.

## Peripheral Program Formats -

Binary versions of peripheral programs contain data in the first word of the program code which controls loading of the package. All peripheral packages in the RPL (Resident Peripheral Library) and PLD (Peripheral Library Directory) are overlays which are called by the resident peripheral programs. These overlays are called once and executed and then discarded when the next overlay is called. All overlays are coded for a particular beginning address in peripheral storage. Overlays which begin at address 1000 in peripheral storage are the basic transient peripheral packages. These transient packages may call equipment driver overlays which begin at peripheral address 2000. The various types of peripheral overlays are identified by name only in the calling formats. Those basic transient packages which may be called by a central program are assigned names beginning with a letter. Those packages which are equipment driver overlays or are special operating system packages are assigned names beginning with a numeral.

The first word of a peripheral package contains the following information.

(18 bits) Package name in display code.

(30 bits) Not used.

(12 bits) Package length in central words.

### Resident Peripheral Library -

The following peripheral overlays are stored in the resident peripheral library.

LAJ - Advance Job.

1BJ - Begin Job.

1DJ - Phase three print.

1LJ - Phase one card load.

2BD - Backspace disk.

2BP - Buffer parameters.

2BT - Backspace tape.

2DF - Drop file.

2DT - Drop tracks.

2EF - Error flag.

2LP - Line printer

2RC - Read cards.

2RD - Read disk.

2RT - Read tape.

2SD - Search dayfile.

2TJ - Translate job card.

2TS - Translate statement.

2WD - Write disk.

2WT - Write tape.

Clo - Circular I/O.

CLL - Call library.

EXU - Execute.

MSG - Message for dayfile.

## Peripheral Library Directory -

The following peripheral overlays are listed in the peripheral library directory.

1LT - Phase one tape load.

1TD - Phase three tape dump.

2PC - Punch cards.

DMP - Dump storage.

LBC - Load binary cards.

LOC - Load octal cards.

PBC - Punch binary cards.

DIS - Display job.

SIM - CPU simulator

## DSD - System Display - Channel 10

This package is loaded in processor 9 on dead start and remains there unaltered throughout execution of the system. This package provides an overall status display for all currently running jobs. The keyboard is used to initiate and control equipment assignment and job progress.

The two console screens may be assigned any combination of two of the following displays.

A - dayfile
B - job status
C - data storage (5 groups of 4 octal digits)
D - data storage (5 groups of 4 octal digits)
E - data storage (5 groups of 4 octal digits)
F - program storage (4 groups of 5 octal digits)
G - program storage (4 groups of 5 octal digits)
H - job backlog

As an example, the following keyboard entry would select H display on the left screen and B display on the right screen.

HB.

The storage displays C through C consist of 4 fields of 8 words each. The fields are designated 0,1,2,3 and may be individually changed to any 8 word group of central storage. The following keyboard entry would change the D display, field 2, to display central storage addresses 001020 through 001027.

D2, 1020.

If D4,1020 were used in the above example the D display would be changed to 4 consecutive fields beginning with address 001020.

Any word in central storage may be modified from this console. An entry in central storage is made in the following format.

1022, 2100 1000 0000 0111 2347.

Spacing is not important. Leading zeros may be dropped in both address and data word. All entries are octal and the digits are right adjusted.

AUTO.

This keyboard entry may be used after a dead start from the system tape to select a mode for automatic job processing. This mode assumes job inputs from punched cards and printer output.

STEP.

This keyboard entry selects a step mode for the operating system monitor. Each processor request is stepped by depressing the keyboard space bar. High speed operation may be resumed by entering a period on the keyboard.

SIM.

This keyboard entry calls a central processor simulator package from the disk file library to a peripheral processor. Subsequent system operation is with a simulated central processor. The simulator may be dropped by repeating the keyboard entry SIM. Repeated entries will toggle the system between simulated and real central processor.

ON42.

This keyboard entry is an example of format for designating to the operating system that a particular equipment is working and ready for automatic monitor assignment to any job requesting this type of equipment. The equipment is designated by a two digit octal number corresponding to the position of the equipment in the equipment status table.

OFF42.

This keyboard entry is an example of format for removing a particular equipment from automatic monitor assignment. The equipment is designated by a two octal digit number corresponding to the position of the equipment in the equipment status table.

TIME. 12.10.03, March 12, 1965.

This keyboard entry permits the operator to enter the correct time and date.

#### 1. READ.

This keyboard entry is an example of format for selecting a punched card job input package. The leading digit designates which control point is to be used for the input package. Jobs are read from punched cards as long as cards are entered in the card reader. All job data is stored in disk files, and the jobs are executed according to the priorities on the job cards.

#### 2. PRINT.

This keyboard entry is an example of format for selecting a printer output package. The leading digit designates which control point is to be used for the print package. Job output files are printed in the order of job priorities.

#### 3. NEXT.

This keyboard entry is an example of format for selecting a control point, as designated by the leading digit, to process job executions. Jobs are called one at a time in order of priority.

#### 4. GO.

This keyboard entry is an example of format for restarting a job which has come to a pause statement. The leading digit must correspond to the control point with the pause statement displayed.

#### 4. DIS.

This keyboard entry is an example of format for calling a job display package (DIS) to a control point as designated by the leading octal digit.

### 4. DROP.

This format drops the job at the designated control point.

#### 4. ASSIGN 53.

This format assigns a particular equipment (53) to a particular control point (4). This method of equipment assignment should be used only when the job is waiting for the equipment assignment as indicated by a REQUEST statement display.

### 7. LOAD.

This keyboard entry selects a magnetic tape job loader package at the designated control point. The package will request operator assignment of a specific tape unit. The tape read on this unit must have input jobs stacked to a double file mark. Such a tape may be prepared by copying card to tape with the library program COPY.

### 5. DUMP.

This keyboard entry selects a magnetic tape dump package at the designated control point. The package will request operator assignment of a specific tape unit. The current backlog of completed output files will be dumped on this tape in the order of job priority. A file mark will be recorded and the tape backspaced to allow repeated dumps on the same tape. The tape may then be listed off line with a tape to printer program.

#### 4. ONSW2.

This keyboard format allows setting of the six sense switches for Fortran programs during execution.

#### 4. OFFSW4.

This keyboard format allows clearing of the six sense switches for Fortran programs during execution.

### 5. DCN11.

This format allows the operator to manually disconnect any I/O channel. The channel number is octal.

### 5. FCN10.

This format allows the operator to manually enter a zero function on any I/O channel. The channel number is octal.

### DIS - Job Display

This package may be called at any time during the execution of a job. This package, when called, stops further automatic advance of the job control cards. The display provided in this package covers only data pertaining to the particular job. The keyboard is used to advance the job control cards and provide any combination of two of the following displays.

A - dayfile
B - job status
C - data storage (5 groups of 4 octal digits)
D - data storage (5 groups of 4 octal digits)
E - data storage (5 groups of 4 octal digits)
F - program storage (4 groups of 5 octal digits)
G - program storage (4 groups of 5 octal digits)

As an example, the following keyboard entry would select A display on the left screen and B display on the right screen.

#### AB.

The storage displays C through G consist of 4 fields of 8 words each. The fields are designated 0,1,2,3 and may be individually changed to any 8 word group of central storage. The following keyboard entry would change the D display field 2 to display central storage addresses 001020 through 001027.

#### D2, 1020.

If D4, 1020 were used in the above example the D display would be changed to 4 consecutive fields beginning with address 001020.

Any word in central storage which lies within the field length of this job may be modified from this console. All addresses are relative to the job reference address. An entry in central storage is made in the following format.

# 1022, 2300 7775 1143 0000 0022.

Spacing is not important. Leading zeros may be dropped in both address and data word. All entries are octal and the digits are right adjusted.

The following keyboard entry formats may be used to alter the contents of the exchange package for the job displayed. If the program is running the operation will stop before the entry is made. The numerical values in the examples below are for illustrating format only.

ENP, 12345.

Enter the value 12345 in the exchange package for the next program address.

ENA3, 665000.

Enter the value 665000 in the exchange package for the A3 register. ENB2, 44.

Enter the value 44 in the exchange package for the B2 register. ENX5, 2223 4000 0000 0000 0200.

Enter the indicated value in the exchange package for the X5 register.

ENEM, 7.

Enter the value 7 in the exchange package for the exit mode. ENFL, 10000.

Enter a field length of 10000 for the exchange package. ONSW3.

This keyboard format allows the operator to set a Fortran sense switch during execution.

OFFSW5.

This keyboard format allows the operator to clear a Fortran sense switch during execution.

The following keyboard entry formats may be used to change the status of a job. The numberical values are for illustration of format only.

ENTL, 200.

Enter a time limit of 200 octal seconds.

ENPR, 5.

Enter a job priority of 5.

DCP.

Drop the central processor and display the exchange area.

RCP.

Request the central processor and begin execution at the next program address.

BKP, 44300.

Breakpoint to address 44300 in the program. This entry begins central processor execution of the program at the next program address and stops execution when the program register reaches address 44300. The contents of address 44300 is cleared while the program is running and is restored when the break point address is reached.

RNS.

Read the next control statement and begin execution.

RSS.

Read the next control statement and stop before execution.

DROP.

Drop the display package and continue execution of the remaining control statements.

HOLD.

Drop the display for later recall by the system display console. This entry holds the job at its present status.

GO.

This keyboard entry restarts a program that has stopped at a pause statement.

# ENS. XXXXXXXXXXXXXXX.

This keyboard format allows the entry of any control statement XXXXXXXXXXXX as if it had been entered on a control card.

# DMP (200,330)

This format dumps storage addresses 200 to 330 in the output file.

# DMP (400)

This format dumps storage from the reference address to address 400.

## DMP.

This format dumps the exchange package in the output file.

#### CIO Circular Buffer I/O Package

Interface with central program -

This peripheral package is called by entering CIO in display code left adjusted in RA+1. The lowest order 18 bits of RA+1 contain the relative storage address (BA) of the circular buffer parameters. These parameters occupy five central words beginning at BA.

- BA. (42 bits) File name in display code left adjusted.
  - (12 bits) not used.
  - (6 bits) buffer status.
- BA + 1. (42 bits) not used. (18 bits) FIRST. First address of circular buffer.
- BA + 2. (42 bits) not used. (18 bits) IN. Next buffer input address.
- BA + 3. (42 bits) not used.
  (18 bits) OUT. Next buffer output address.
- BA + 4. (42 bits) not used. (18 bits) LIMIT. Last buffer address + 1.

If IN = OUT the buffer is empty. Maximum buffer capacity is LIMIT - FIRST - 1. IN and OUT may equal FIRST but may not equal LIMIT.

The six bit buffer status indicates the mode of the buffer and provides an interlock for peripheral package activity. The buffer status has an even numerical value when CIO is called. The buffer status is set to an odd value when the peripheral package has completed the I/O function. Specific status indicators may be obtained by combining the octal digits in the table below.

| st coded read    |
|------------------|
| ete coded read   |
| st binary read   |
| ete binary read  |
| st coded write   |
| ete coded write  |
| st binary write  |
| ete binary write |
|                  |

#### CIO treatment of status codes

(1) Input (001,0X0)

File is read into circular buffer until buffer is filled (001,0X1) or until end record (010,0X1) or file mark (011,0X1). The mode bit in this function request is ignored and mode is determined by the input medium. A file mark response should never occur with data.

(2) Output (001,1X0)

Data in circular buffer is recorded on file for as many complete physical records as available data. No partial end record is made. (001,1X1).

(3) End Record (010,1X0)

Data in circular buffer is recorded on file including a short physical record to mark end of logical record. The last physical record may be of zero length. IN = OUT = FIRST. (010.1X1)

- (4) File Mark (011,1X0)
  - (a) Data in circular buffer; or
  - (b) Last buffer status (001,1XX)

Data in circular buffer is recorded on file including a short physical record to mark end of logical record. Then a file mark is recorded. IN = OUT = FIRST. (011.1X1)

- (c) All others
- A file mark is recorded. IN = OUT = FIRST. (011,1X1)
- (5) Backspace Binary (100,X10)

Back file to end of last record. A file mark is considered as a record in this case. IN = OUT = FIRST. (100,011)

(6) Backspace Coded (100,X00)

Back file one coded line. A file mark is considered as a coded line in this case. The last physical record will be left in the buffer beginning at FIRST. IN and OUT will be adjusted for a one line backspace. (100,001)

- (7) Rewind (101,XX0)
  Rewind the file. IN = OUT = FIRST. (101,0X1)
- (8) Rewind Unload (110,XX0)
  Rewind and unload the file. IN = OUT = FIRST. (110,0X1)

CIO special properties.

- (1) The central program must complete writing a record before requesting a backspace, rewind, or mode change.
- (2) The central program need not complete writing a record before requesting a file mark.
- (3) The central program must complete reading a record before beginning output data to the buffer.
- (4) Binary tapes and coded one inch tapes record 1000 octal word physical records in odd parity. No special control words are added. A zero length physical record is generated by recording a partial word (4 bytes). Coded one inch tapes use packed display code with short word separators.
- (5) Coded half inch tapes record 120 character BCD code in even parity.
- (6) A one inch tape with mixed binary and coded records presents problems if a backspace is used to cross a mode boundary.
- (7) A problem exists in mode change from coded input to output on one inch tape if the file is positioned between two lines of code. No problem exists if the file is positioned before or after a file mark. No problem exists in mode change on binary files.
- (8) A disk file connot be substituted for a tape file if the file has multiple file marks or has data recorded after a file mark.

DMP - Dump Storage.

This peripheral package may be called from a control card or from a DIS console. An octal dump is entered in the OUTPUT file with the central storage address and one data word per line. There are several format variations for calling this package.

DMP.

This format dumps the exchange area into the OUTPUT file. DMP, 3400.

This format dumps from the reference address to the argument address.

DMP (4000,6000)

This format dumps from the first argument address to the second argument address.

LBC - Load Binary Corrections.

This peripheral package may be called from a control card or from a DIS console. Binary corrections are read from the INPUT file and are entered in central storage. If an argument is used in the package call, the binary cards are loaded beginning with that address. If no argument is used, the binary cards are loaded beginning at the reference address. Only one record is read from the INPUT file. If more than one block of data is to be entered more calls must be made.

The two types of format for this package call are as follows.

LBC.

LBC, 2300.

LOC - Load Octal Corrections.

This peripheral package may be called from a control card or from a DIS console. Octal corrections are read from the INPUT file and are entered in central storage. The octal cards which are used for these corrections must be in the following format.

23001 45020 04000 00042 00044

The address must begin in column one. Leading zeros may be dropped in the address. The data word must not begin before column 7. Spacing in the data word is not important but the word must contain 20 digits.

Several formats are allowed in making this package call. The simplest is the following.

LOC.

This call reads all of the correction cards in the next INPUT file record and modifes central storage accordingly.

LOC, 1000.

This call clears central storage from the reference address to the argument address. The correction cards are then read from the INPUT file.

LOC(2022, 3465)

This call clears central storage from the first argument address to the second argument address. The correction cards are then read from the INPUT file.

This package may be called to clear storage only by providing an empty record in the INPUT file.

## PBC - Punch Binary Cards.

This peripheral package may be called from a control card or from a DIS console. This package punches a deck of binary cards directly from central storage. Storage is not modified by this operation.

Several formats may be used in calling this peripheral package.

PBC, 2000.

This format causes a binary deck to be punched from the reference address to the argument address.

PBC(2000,3000)

This format punches a binary deck from the first argument address to the second argument address.

PBC.

This format punches a binary deck using the first word in central storage as a control word for deck length. The deck always begins at the reference address and terminates one address less than that indicated in the lower 18 bits of the first word. This format may be used for punching any central or peripheral program in standard format.

# CLL - Call Central Overlay

This peripheral package calls one or more central overlays to addresses supplied by the calling program. The lowest order 18 bits of the call word in RA+1 contain the storage address (BA) for arguments. The first two words in the argument region contain beginning and limit addresses for the overlays as a group. A list of the overlay names begins in BA+2 and continues to a cleared word.

- BA. (42 bits) not used.
  (18 bits) beginning address for first overlay.

  BA+1. (42 bits) not used.
  (18 bits) limit address for group of overlays.
- BA + 2. (42 bits) name of first overlay in display code. (18 bits) beginning address of overlay (inserted by CLL).
- BA+3. (42 bits) name of second overlay in display code. (18 bits) beginning address of overlay (inserted by CLL).

etc.

CLL searches for the overlays in the order named first in the Resident Subroutine Library (RSL), then in the Central Library Directory (CLD), and then in the assigned job files. As a named overlay is located it is copied into central storage at the next available address. This address is then entered into the argument region with the name of the overlay. If an overlay cannot be located the address is left blank in the argument region. If an overlay exceeds the limit address a 777777 is entered as the argument address. The last overlay is followed by a cleared word. CLL then clears (BA) to indicate completion of the call.

#### EXU - Call and Execute.

This peripheral package is called by entering EXU in display code left adjusted in RA + 1. The lowest order 18 bits of the call word indicates the central storage address for the argument. The argument consists of the name of a central program to be called and executed. This call destroys the calling program and completely replaces it with the called program.

## MSG - Dayfile Message.

This peripheral package is called by entering MSG in display code left adjusted in RA + 1. The lowest order 18 bits of the call word indicates the central storage address for the argument. The argument consists of a sequence of words in display code which are to be entered in the dayfile. The time and job name are automatically inserted before the argument data. The data is terminated by a cleared byte.

Job Control Cards -

A job consists of one or more central programs which are executed with common data files. Job control cards are used to identify the programs and data files and sequence the program executions.

Each job must begin with a job card. This card names the job and provides priority, time limit, and field length information for the job. An example of a job card is as follows.

JOB237, 6, 400, 27000.

In this example the job name is JOB237. Priority is 6. Time limit is 400 octal seconds. Field length is 27000 octal words. Spaces are not important in the job card format except that the job name must begin in column one. The job name must begin with a letter and may have up to 7 characters. The priority is a single decimal digit. A higher priority is represented by a larger number. The time limit may consist of up to 5 octal digits. The octal value in hundreds is approximately the time in minutes. The field length must be in hundreds and cannot exceed 360,000.

The rest of the job control cards must follow the job card in the order of execution. A record separator follows the last job control card. Program and data cards then follow in the order of input requests.

File Names -

All input and output actions of a central program must involve a named file. This file may be a disk file, a magnetic tape file, a punched card file, or a printer output file. The physical unit associated with a file name is controlled by the job control cards and is not a function of the central program coding directly. The operating system provides a common interface between the central program and the peripheral programs which drive the peripheral equipments.

File names must begin with a letter and may have up to 7 characters. There are two special names which are implied with each job. These names are INPUT and OUTPUT. The name INPUT refers to the file from which the job cards were read. This file is the result of loading the card deck for the job. The name OUTPUT refers to the file which ends in printed copy at the end of the job. These two names must be avoided in assigning names to temporary files and other I/O files.

#### Program Calls -

A program is called from a file or the program library by a control card in the following format.

SORT(T1, T2, XLF)

In this example the name of the program is SORT. This program may have been loaded from a file specifically associated with this job, or it may have been compiled from a fortran deck in the input file, or it may be in the general program library. All files associated with the job are searched for this particular name. If no job file has this name the program library is searched for a program of this name. When the program has been located it is read into central core storage. The arguments within the parenthesis are then entered in the program beginning at address 000002. These arguments are names for other files associated with the job and are entered in the forepart of the program in display code left adjusted in each word. The number of arguments must agree with number provided in the program. The execution of the program is initiated at a program address specified in the forepart of the program. (see program format)

If a file name appears for the first time as the argument in a program call it is assumed to be a disk file and is assigned to disk storage at that time.

Special Control Card Formats -

The following control card formats are recognized as special cases and must not be used for the names of programs.

ASSIGN MT, AFILE.

MT= LP

This format assigns any available peripheral unit of type MT to a file name AFILE. This must be the first appearance of AFILE.

#### COMMON BFILE.

This format causes a search for the name BFILE in the list of common files. If a common file is located with this name, and if the file is not being used by another job, it is assigned to this job until dropped. If the file is being used by another job or does not yet appear in the list of common files, this job must wait until the file is available.

If BFILE already appears as a local file name for the job, this format transfers that file to common status and is available to other jobs when dropped by this job.

#### RELEASE BFILE.

This format causes a common file named BFILE currently assigned to this job to be dropped from common status.

#### REQUEST CFILE.

This format requests the operator at the system display console to assign a specific peripheral equipment to this job with the name CFILE. This must be the first appearance of the name CFILE. The job waits for operator action before proceeding.

#### MODE 3.

This format assigns arithmetic exit mode 3 to the job until further specification.

## SWITCH 5.

This format sets switch 5 for reference by subsequent FORTRAN programs.

#### EXIT.

This format is used to separate the control cards associated with the normal execution of the job from a group of control cards to be executed in the event of an error exit. If this control statement is reached by sequential execution of the job it terminates the job. If an error exit occurs in the job before this EXIT card is reached, the control statements following this card are executed.

# Sample Job -

The following job cards are an example of a Fortran load and run job with READ and PRINT statements. The job has a priority of 2, a time limit of one minute, and a field length of 40000 octal words. A Fortran program deck may be declared in either a FORTRAN II or FORTRAN IV mode. The mode is declared in the header card for the main program along with the name and I/O file arguments.

```
JOB236, 2, 100, 40000.
RUN.
789
FORTRAN IV PROGRAM JOHN(INPUT, OUTPUT)
(Fortran program coding)
789
(Fortran data)
6789
```

Sample job -

The following job illustrates a Fortran load and run job with three tape references. The Fortran language TAPE 1 in this example is assumed to be an input tape which the operator loads on a particular unit. The other two tapes are assumed to be scratch tapes which are draw from a tape pool.

```
JOB237, 2, 400, 60000.
ASSIGN WT, TAPE 5.
ASSIGN WT. TAPE 6.
REQUEST TAPE 1.
RUN.
789
FORTRAN II PROGRAM HENRY(INPUT, OUTPUT, TAPE1, TAPE5, TAPE6)

(Fortran program coding)
789

(Fortran data)
6789
```

Monitor Action on Central Programs -

The monitor will act upon a central program in the following cases.

Higher Priority.

If a higher priority program is ready for execution the current program exchanges in favor of the new program.

Arithmetic Exit.

The monitor exchanges to the next lower priority program whenever the central program address becomes zero. An arithmetic exit mode flag is set for this case.

Time Limit.

The monitor exchanges to the next lower priority program if the time limit is reached. A time limit flag is set in this case.

#### Call Peripheral.

A central program calls a peripheral package by entering the package name in display code in the upper 18 bits of RA + 1. The lower 36 bits may contain arguments for the peripheral package. The monitor clears address RA + 1 as soon as the peripheral package is called.

## Recall.

A central program may stop for later recall by entering RCL in RA+1 and looping until RA+1 clears. The monitor will exchange to the next lower priority program and restart this program at a later time (about 250 milliseconds). If the central program has called a peripheral package for I/O action this package may restart the central program when the action is completed.

#### Abort.

A central program may abort the job by entering ABT in RA + 1. This action sets an abort exit flag.

End.

A program is terminated by entering END in RA+1. The monitor then calls a peripheral package to advance the job to the next control statement.

# Central Program Formats -

Binary versions of central programs contain data in the forepart of the program code which controls loading and starting of the program. This data is contained in the first two words of the program code. These words are interpreted by the loading processor and then cleared before beginning execution.

Central program arguments begin in address 000002 of a central program. The arguments are file names which are in display code left adjusted in the word. A set of file names are generally stored in the argument area of a program at the time it is compiled. If arguments are used in a control card at the time the program is called for execution, these arguments are inserted in the program in the argument area and override the original values. If no arguments are used, or if fewer than anticipated arguments are used, the original argument values are effective at run time. The list of argument names at the front of the program must be followed by a cleared word. The first word of program for execution follows this cleared word.

The first word of a binary central program contains the following information.  $\label{eq:total_state} \mathfrak{T}_{\mathbf{q}}$ 

(42 bits) Program name in display code. (18 bits) Program length.

The second word of a binary central program contains the following information.

Execution of a central program begins at an address equal to the number of arguments plus 3.

Resident Subroutine Library -

The following central program subroutines are stored in the resident subroutine library.

ALOG Computes the natural logarithm of a floating point number. The result for a negative argument is indefinite.

ALOG10 Computes the common logarithm of a floating point number. The result for a negative argument is indefinite.

ATAN Computes the arctangent of a floating point number.

COS Calculates the cosine of a floating point number.

DVCHK Divide and check.

END Causes end of file on all circular buffers for the program.

EXIT Terminates a program similar to END. The word EXIT is entered in the dayfile.

EXP Calculates the floating point function E \* \* X.

IBAIEX Computes I \* \* J where both I and J are fixed point.

IFENDF Checks the status of a circular buffer for an end of file condition. IN = OUT and 3X function.

OVERFL Result is set to one if overflow exits and two if no overflow.

PAUSE Loop on recall until operator action. Word PAUSE entered in the dayfile.

RBATEX Computes A \* \* I where A is floating point and I is fixed point.

RBAREX Computes A \* \* B where both A and B are floating point.

REMARK Transmits a message to the system dayfile and displays it on the console display.

Example: CALL REMARK (22H REMARKS HERE IN ENTRY).
22H indicates 22 Hollerith characters in the remarks
(maximum of 40).

SIN Computes the sine of the argument.

SQRT Computes the square root of the argument. A negative argument results in an indefinite result.

SLITE Sense lights. Turn off if zero. Turn on if non zero.

SLITET Respond with one if light was on, two if light was off. Turn off light.

SSWTCH Respond with one if switch is down, two if switch is up.

START Enter START in dayfile.

STOP Enter STOP in dayfile and perform same function as END.

TAN Compute the tangent of the argument.

TANH Compute the hyperbolic tangent of the argument.

TIME Enter the word TIME and hollerith information in the dayfile.

# Library Subroutine Calling Sequences

Arithmetic subroutines in the Resident Subroutine Library may be called by the following calling sequences.

| I. | ALOG   | COS | SQRT |
|----|--------|-----|------|
|    | ALOG10 | SIN | TAN  |
|    | ATAN   | EXP | TANH |

Each of the above subroutines is entered with the address of its single argument in the Bl register and exits with its result in the X6 register. All registers except AO are available and need not be saved.

|                    |  | Example<br>AA = SIN(BB) |   |
|--------------------|--|-------------------------|---|
| Program<br>Address |  |                         |   |
| CSEQ               | I = BB                                 | 6110B                   | Put address of argument in Bl                       |
|                    | R = SIN                                | 0100 SIN                | Return Jump to SIN subroutine                       |
|                    | Y = (AA)                               | 506                     | Store answer from X6 to memory                      |
|                    | •                                      |                         |   |
|                    | Continuation o                         | f main program          |   |
| SIN-3              |  |                         | Subroutine name and length                          |
| SIN-2              |  |                         | Number of arguments                                 |
| SIN-1              | ************************************** |                         |   |
| SIN                | 0                                      | 00                      | Entry/Exit Address                                  |
|                    | •                                      |                         |   |
|                    | P = SIN                                | 02SIN                   | Jump to Entry/Exit Address of Subroutine for return |

to main program

II. The subroutines IBAIEX, RBAIEX, and RBAREX require two arguments. X registers 6 and 7 are used for transmitting the arguments, X6 for the base and X7 for the exponent. These subroutines save and restore all index registers used. The result is left in X6.

|                    |                              | каmple<br>A = BB**СС |   |
|--------------------|------------------------------|----------------------|---|
| Program<br>Address |                              |                      |   |
| CSEQ               | Y = BB                       | 6170 BB              | Set X6 to desired base                                |
|                    | Z = CC                       | 6160 CC              | Set X7 to desired exponent                            |
|                    | R = RBAREX                   | 0100 RBAREX          | Return Jump to RBAREX subroutine                      |
|                    | G = AA                       | 5160 AA              | Store result at AA                                    |
|                    | ·<br>·                       |                      |   |
|                    | Continuation of main program |                      |   |
| RBAREX             | 0                            | 00                   | Entry/Exit  |
|                    | •                            |                      |   |
|                    | P = RBAREX                   | 02                   | Jump to Entry/Exit address for return to main program |

- III. The BACKSP, REWINM, ENDFIL, and IFENDF subroutines receive a single argument via the X6 register and do not use index registers. The calling sequence must place in X6 either:
  - A. The address of the parameter list describing the circular buffer to be referenced, or
  - B. The complement of the address of the file name or variable tape number. File names are left justified and tape numbers are binary and right justified.

If the address of the parameter list is known at the time the calling program is compiled, the calling sequence may place this address in X6 before jumping to the subroutine.

If the address of the parameter list is not known at compile time, the calling sequence must place in X6 the complement of the address of the tape number or file name. When the subroutine senses that X6 holds a complemented address (<0), it determines whether the word at that address is a file name or tape number and, if necessary, forms a file name by inserting TAPE in display code in front of the tape number (also converted

to display code). When the proper file name has been obtained, a masked search is made starting at address 000002 of the program. The address of the parameter list is found in the lower 18 bits of the match.

# Example REWINM 6

When address of parameter list is known:

LIMIT

| When addres        | ss of parameter li | Ist is known:  |   |
|--------------------|--------------------|----------------|---|
| Program<br>Address |                    |                |   |
| CSEQ               | Y = PLIST          | 7160 PLIST     | Set X6 to address of parameter list                         |
|                    | R = REWINM         | 01             | Return Jump to REWINM subroutine                            |
|                    | •                  |                |   |
|                    | Continuation o     | f main program |   |
| REWINM             | 0                  | 00             | Entry/Exit  |
|                    | •                  |                |   |
|                    | P = REWINM         | 0200 REWINM    | Jump to Entry/Exit<br>address for return to<br>main program |
| PLIST              | TAPE6SS            |                | File name in display code and buffer status                 |
|                    | FIRST              |                | First address of buffer                                     |
|                    | IN                 |                |   |
|                    | OUT                |                |   |

Last address of buffer

| When address of parameter list is unknown (va | ariable tape number): |
|---|-----------------------|
|---|-----------------------|

# Program Address

| CSEQ         | Y = TAPENO        | 7160 TAPENO      | Set X6 to address of tape number                 |
|--------------|-------------------|------------------|--|
|              | Y = -Y            | 14606            | Complement address in X6                         |
|              | R = REWINM        | 01               | Return Jump to REWINM subroutine                 |
|              | •                 |                  |  |
|              | •                 |                  |  |
| !            | Continuation of m | ain program      |  |
|              |                   |                  |  |
| REWINM       | 0                 | 00               | Entry/Exit                                       |
|              | •                 |                  |  |
|              | •                 |                  | •  |
|              | P = REWINM        | O2 REWINM        | Jump to Entry/Exit                               |
| ,            |                   |                  | address for return to main program               |
|              |                   |                  |  |
| TAPENO       | 00006             |                  | Tape number (binary)                             |
|              |                   |                  |  |
| RA + 2       | File Name 1       |                  |  |
| RA + 3       | File Name 2       |                  | •  |
| •            |                   |                  |  |
| -            | m. n              |                  | Water 11   |
| RA + n       | TAPE 6 PLIST      | ·                | Matching file name and address of parameter list |
| When address | of parameter list | is unknown (File | name):   |
| CSEQ         | Y = FILENAME      | 7160 FILENAME    | Set X6 to address of file name                   |
|              | Y = -Y            | 14606            | Complement address in X6                         |
|              | R = REWINM        | 01REWINM         | Return Jump to REWINM                            |
|              |                   |                  | subroutine                                       |

FILE NAME TAPE6000..0

IV. The subroutines INPUTB, INPUTC, OUTPTB, AND OUTPTC assume that all registers are available. These subroutines receive arguments via the B registers and are entered at least three times for each data list transferred:

On first entry B1 = 000000, B2 contains the address of the parameter list or the complement of the address of the file name or tape number, and B3 contains the address of the format statement (for the coded routines only).

On intermediate entries Bl contains the address of a data item or the beginning address of an array, and B2 is the array length.

On final entry Bl < 0 indicating that the data list has been exhausted.

## Example

List the data in array AA and variable BB.

## Program Address

| CSEQ | I = 0      | 61100        | Set B1 = 000000                                      |
|------|------------|--------------|--|
|      | J = PLIST  | 61200 PLIST  | Set B2 to address of parameter list                  |
|      | K = FORMAT | 61300 FORMAT | Set B3 to address of format statement                |
|      | R=OUTPTC   | 0100 OUTPTC  | Return Jump to OUTPTC subroutine                     |
|      | I = A      | 61100 AA     | Set Bl to beginning address of array AA              |
|      | J = LENGTH | 61200 LENGTH | Set B2 to length of array AA                         |
|      | R = OUTPTC | 0100 OUTPTC  | Return Jump to OUTPTC subroutine                     |
|      | I = BB     | 6110 B       | Set Bl to address of variable BB                     |
|      | J = 0      | 61200 0      | Set B2 to 000000                                     |
|      | R = OUTPTC | 0100 OUTPTC  | Return Jump to OUTPTC                                |
|      | I = -1     | 6110 -1      | Set B  < 0 to indicate all data has been transferred |
|      |            |              |  |

R = OUTPTC

0100 OUTPTC

Return Jump to OUTPTC subroutine to send E.O.R.

•

Continuation of main program

Central Library Fortran Subroutines -

XLOCF Returns the memory location of a variable name.

INPUTB Binary input.

INPUTC Fortran data input.

OUTPTB Binary output.

OUTPTC Fortran data output.

ENDFIL Write end of file.

REWINM Rewind medium.

BACKSP Backspace medium.

DISPLA Displays a variable name and its numerical value. The value is displayed as an integer if unnormalized and in floating point format if normalized.

Example: CALL DISPLA (5HVNAME, VNAME)

Displays a variable with 5 Hollerith characters (VNAME)

and its numerical value.

RANF Random number generator.

CHAIN Loads a program from the disk file and executes the program.

Arguments transferred from one program to another must be in the common region. All segments to be chained must be compiled with the same file names. The segment name is transferred to the dayfile and displayed prior to execution.

Example: CALL CHAIN (5HLINK2) calls and executes program LINK2.
5H indicates 5 Hollerith characters in the name.

# Central Library Programs -

RUN - Compile and run fortran.

COPY - Copy to double file mark.

COPYCR - Copy coded record.

COPYCF - Copy coded file.

COPYBR - Copy binary record.

COPYBF - Copy binary file.

REWIND - Rewind medium.

VERIFY - Verify two media.

APRAB - Assemble peripheral overlay.

COPYSBF - Copy shifted binary file.

COPYCR(A,B,N) - Copy Coded Record

This central program copies N coded records from a medium A to a medium B. If the third argument is omitted one record is copied.

COPYCF(A,B,N) - Copy Coded File.

This central program copies N coded files from a medium A to a medium B. If the third argument is omitted one file is copied.

COPYBR(A,B,N) - Copy Binary Record.

This central program copies N binary records from a medium A to a medium B. If the third argument is omitted one record is copied.

COPYBF(A,B,N) - Copy Binary File.

This central program copies N binary files from a medium A to a medium B. If the third argument is omitted one file is copied.

COPYSBF(A,B) - Copy Shifted Binary File.

This central program copies a binary file of coded information from a medium A to a medium B, shifting each line one character and adding a leading space.

REWIND(A) - Rewind Medium.

This central program rewinds a tape or disk medium.

BKSP(A) - Backspace Medium.

This central program backspaces a tape or disk medium.

VERIFY(A,B) - Verify Two Media.

This central program reads two files and compares the data word by word to a file mark. If a disagreement is found the program stops. The word from the first file is contained in X1 and the word from the second file in X2. The program may be resumed by advancing the program address and restarting.

COPY(A,B) - Copy to double file mark.

This central program copies from one medium to another until a double file mark is detected. Both media are then backspaced over the last file mark.

#### FORTRAN COMPILER ARGUMENTS

The Fortran compiler, RUN, may be called with up to six arguments. An example appears below, and the arguments are identified in the order of appearance:

RUN(L, 300000, 100000, 3000, AA, BB)

- (1) Compiler mode.
  - G Compile and execute.
  - S Compile and do not execute.
  - P Compile and punch and do not execute.
  - L Compile and list and do not execute.
- (2) Object program field length. (octal)
- (3) Object program common length. (octal)
- (4) Object program I/O buffer lengths. (octal)
- (5) File name for compiler input.
- (6) File name for compiler listable output.

If an argument is entered as zero, or a space, or the list of arguments is shorter than six, an assumed argument is entered by the compiler. Compiler output, except in the G mode, includes a reproduction of the source program, a variable map, and indications of errors detected during compilation. If the G mode is selected, all output is suppressed unless errors are detected, in which case the output is the same as indicated for the other modes. If the L mode is selected, the output will include an octal list of the compiled instructions.

- (1) If the first argument is omitted it is assumed to be G.
- (2) If the second argument is omitted it is set equal to the field length at compile time.
- (3) If the third argument is omitted it is set equal to the amount of common storage required for the main program being compiled.
- (4) The fourth argument specifies the length of the buffers which will be used for each I/O device in the object program. If not specified this argument is assumed to be 2001 octal.
- (5) If this argument is omitted it is assumed to be INPUT.
- (6) If this argument is omitted it is assumed to be OUTPUT.

A copy of the compiled program is always left in disk storage as a binary file with the name of the program as file name. It may be called and executed repeatedly by name.

# FORTRAN PROGRAM HEADER CARDS

Every Fortran program must have a header card in one of the following forms:

PROGRAM NAME (Al, ..., AN)
FORTRANIV PROGRAM NAME (Al, ..., AN)
FORTRANII PROGRAM NAME (Al, ..., AN)

The first two forms cause the program and its subroutines to be compiled in the FORTRAN IV mode. The third form causes compiling in the FORTRAN II mode.

The arguments Al, ..., AN must be the names of all input/output files required by the main program and its subroutines. Although these arguments may be changed at the time the program is executed, they must, at compile time, satisfy the following conditions:

- 1. The file name INPUT must appear if any READ statement is included in the program or its subroutines.
- 2. The file name OUTPUT must appear if any PRINT statement is included in the program or its subroutines.
- 3. The file name PUNCH must appear if any PUNCH statement is included in the program or its subroutines.
- 4. The file name TAPEi, where i is an integer, must appear if a READ INPUT TAPE i, WRITE OUTPUT TAPE i, READ TAPE i, WRITE TAPE i, READ (i,n), WRITE (i,n), READ (i) or WRITE (i) statement is included in the program or its subroutines.
- 5. The file names TAPEil, ..., TAPEik must appear if I is an integer variable name and a READ INPUT TAPE I, WRITE OUTPUT TAPE I, READ TAPE I, WRITE TAPE I, READ (I,n), WRITE (I,n), READ (I) or WRITE (I) statement appears in the program or its subroutines. The integers il, ..., ik must include all values which will be assumed by the variable I. The file name TAPEI may not appear in the list of arguments to the main program.
- 6. The file name TAPEV must appear if V is a symbolic tape unit number and a READ INPUT TAPE V, WRITE OUTPUT TAPE V, READ TAPE V, WRITE TAPE V, READ (V,n), WRITE (V,n), READ (V) or WRITE (V) statement appears in the program or its subroutines

A program is terminated by a single END card.

A provision is made for equivalencing file names at compile time. For example, the header record

PROGRAM NAME (INPUT, OUTPUT, TAPE1 = INPUT, TAPE2 = OUTPUT)

would cause all input normally provided by TAPE1 to be extracted from the INPUT file and all listable output normally recorded on TAPE2 to be transmitted to the OUTPUT file. Equivalenced file names must follow, in the list of arguments, those to which they are made equivalent. Their corresponding argument positions may not be changed at the time the program is executed, although the names of the files to which they are made equivalent may be changed at this time.

#### SUBROUTINE HEADER CARDS

Every Fortran SUBROUTINE compiled independently of a main program which is to use it must have a header card in one of the following forms:

```
SUBROUTINE NAME (A1, ..., AN)
FORTRANIV SUBROUTINE NAME (A1, ..., AN)
FORTRANII SUBROUTINE NAME (A1, ..., AN)
```

The first two forms cause the subroutine to be compiled in the FORTRAN IV mode, and the third form causes it to be compiled in the FORTRAN II mode. The arguments Al, ..., AN have their conventional FORTRAN significance.

A SUBROUTINE is terminated by a single END card.

#### FUNCTION HEADER CARDS

Every Fortran FUNCTION which is compiled independently of a main program which is to use it must have a header card in one of the following forms:

```
FUNCTION NAME (A1, ..., AN)
FORTRANIV FUNCTION NAME (A1, ..., AN)
FORTRANII FUNCTION NAME (A1, ..., AN)
```

The first two forms cause the function to be compiled in the FORTRAN IV mode, and the third form causes it to be compiled in the FORTRAN II mode. The arguments Al, ..., AN have their conventional FORTRAN significance.

A FUNCTION is terminated by a single END card.

#### FORTRAN HANDLING OF BINARY SUBROUTINES

The Fortran compiler, RUN, is capable of processing previously compiled or assembled subroutines which appear on binary cards. If this capability is to be used, then a card with a plus sign (+) in column 1 must immediately follow the END card of the program to be compiled. This card, in turn, must be immediately followed by subroutines on binary cards. The end of the binary subroutine portion of the deck is indicated by a standard end-of-file card.

```
JOB238(5,100,40000)
RUN
789
PROGRAM HENRY (INPUT, OUTPUT)
(Standard Fortran coding)
END
+
(Binary subroutines)
789
(Fortran data)
6789
```

#### ADDITIONAL NOTES ON THE FORTRAN COMPILER

The following remarks concern certain nonstandard features of the compiler;

- 1. Boolean constants in B-type statements must be changed in some cases because of the increased word length of the 6600 or because of the use of display code as an internal code.
- 2. Complex arithmetic is not performed by subroutines.
- 3. Double-precision arithmetic is performed on one-word operands (allowing approximately 15 decimal digits of accuracy), although two memory locations are allocated to double-precision variables.
- 4. The notations ".T." and ".F." may be substituted for the FORTRAN IV constants ".TRUE." and ".FALSE.".
- 5. The compiler requires 30000 octal locations in memory plus a variable-sized area for tables and the binary version of the object program. It is recommended that arrays be assigned to the blank COMMON region since this region is not part of the binary version of the object program as developed at compile time.
- 6. Independently compiled subroutines, or functions, may not use more than 65,536 decimal locations in the COMMON region or more than 65,536 decimal locations locally.
- 7. FORTRAN IV blank COMMON assignments are made after labeled COMMON assignments, i.e., blank COMMON regions are located in higher addresses than labeled COMMON regions. A labeled COMMON region may be considered as a local region of the program or subroutine which establishes it. Hence, data may be entered into it by DATA statements.
- 8. Object-program input/output buffers are assigned to the top of the field in which the program is to run. Blank COMMON assignments are made to the region immediately below the input/output buffers. Neither of these regions is included in the binary version of a program or subroutine which is transmitted to the disk for running or punching.
- 9. FORTRAN II and FORTRAN IV statements which are not inherently incompatible may be intermixed in a program to be compiled in either mode. Inherently incompatible statements are those involving function subroutine references and EQUIVA-LENCE statements causing a reordering of variables in the COMMON region. However, any standard FORTRAN II or FORTRAN IV library or built-in subroutine reference may appear in a program to be compiled in either mode.
- 10. The statement IF(ENDFILE I)N1,N2, where I is a constant or variable, may be used in a program or subroutine.

- 11. If a program is declared to be FORTRAN II, FORTRAN IV, MACHINE, or ASCENTF, this mode is assumed for all subsequent subroutines unless specific subroutines are declared to be of a different mode. If a subroutine is declared to be of a different mode, it will be processed in its declared mode. The subroutine following it, unless again declared to be of a different mode, will be processed in the mode applying to the main program. In other words, after processing any subroutine the compiling mode reverts to the one applying to the main program.
- 12. Indices in DO loops must be less than  $2^{17}$  in absolute value.
- 13. A BLOCK DATA subroutine is not necessary for entering data into a labeled COMMON region, although it may be used if desired.
- 14. An O followed by at least six octal digits is interpreted as an octal number by the compiler and is assigned a logical mode.
- 15. A two-way IF statement is allowed. For example, IF(E)N1,N2 causes branching to statement N2 only if the value of E is +0. E may be of logical, integer, real, double, or complex mode. If its mode is complex, the test is made on its real part.
- 16. Full mixed mode arithmetic is permitted. The mode of an arithmetic expression is determined by the term of highest mode appearing in the expression, where the modes, from highest to lowest, are COMPLEX, DOUBLE, REAL, INTEGER, and LOGICAL. An expression is of logical mode if .NOT., .OR., or .AND. appears as an operator or if it contains a single term of logical mode. In this case, operands may be of any mode. If an operand is complex, a logical operation is performed on its real part.
- 17. If L is a logical variable and E is an expression of any mode, L = E causes a replacement of L by the value of E.
- 18. If V is a variable of any mode and L is a logical expression, V = L causes a replacement of V by the value of L.
- 19. A slash/(/) in a B-type FORTRAN II statement causes a logical difference to be formed on its associated operands.
- 20. If C is a complex variable and E is an expression of any mode except complex, C = E causes a replacement of the real part of C by the value of E and a replacement of the imaginary part of C by +0.
- 21. If V is a variable of any mode except complex and E is a complex expression, V = E causes a replacement of V by the real part of E.
- 22. The logical operators .NOT., .OR., or .AND. may be replaced by .N., .O., or .A., respectively.

- 23. A number with fewer than six digits trailed by an S and appearing as an argument to a subroutine call is interpreted by the compiler as a statement number; e.g., CALL DUMP(100S,200S,0) causes an octal dump of the region between statements 100 and 200 of the program being compiled.
- 24. All working regions of object programs are clear prior to execution, except for blank COMMON regions. Blank COMMON regions are clear at execution time if compilation is in the G mode.

#### FORTRAN HANDLING OF ASSEMBLY LANGUAGE

The Fortran compiler, RUN, is capable of processing programs or subroutines written in assembly language. Such programs or subroutines may be intermixed with regular Fortran programs and subroutines. Each must be organized as follows:

- 1. Header card.
- 2. Fortran cards, if any.
- 3. Declaration cards, if any.
- 4. Instruction cards.
- 5. Constant cards.
- 6. End card.

#### It is noted that

- a) The instruction portion of the deck must be preceded by "0" lines corresponding to control words, arguments, and an exit/entry line.
- b) The constant portion of the deck must be separated from the instruction portion by a card with two periods (..) punched in columns 7 and 8.
- c) The end card is punched as in Fortran, i.e., END in columns 7-9 of a card.
- d) Constants may appear in the instruction portion of the deck provided they are positive and less than 2<sup>54</sup>.
- e) A card with an asterisk (\*) in column 1 may appear anywhere in the deck and is treated as a remark card.
- f) A card with a period (.) in column 1 may appear anywhere in the deck and will cause a page eject at the time the program or subroutine is listed.

#### Header Formats:

Each program or subroutine coded in assembly language must have a header card in one of the following formats:

MACHINE PROGRAM NAME
MACHINE PROGRAM NAME (A1, ..., AN)
MACHINE SUBROUTINE NAME
MACHINE SUBROUTINE NAME (A1, ..., AN)

#### It is noted that

- a) The header information must be punched between column 6 and column 73 of each card used.
- b) Up to 19 continuation cards may be used in any declaration, but an asterisk (\*) must appear in column 6 of each continuation card.
- c) In the forepart of the program or subroutine to be assembled there must be three "0" lines plus one "0" line for each argument Al, ..., AN:

|    | 0   | CONTROL WORD ONE |
|----|-----|------------------|
|    | 0   | CONTROL WORD TWO |
| A1 | 0   | ARGUMENT 1       |
| :  | :   |                  |
| •  | • . |                  |
| AN | 0   | ARGUMENT N       |
|    | 0   | EXIT/ENTRY       |

The first two "0" lines correspond to control information furnished by the compiler, and the last "0" line is unused by a program but is the exit/entry line for a subroutine. The first executable instruction must follow the exit/entry line.

- d) The arguments Al, ..., AN are treated as dummy arguments by the compiler in that they are used only to obtain an argument count to insert in the second control word.
- e) If an assembly-language subroutine is to be referenced by a Fortran program or subroutine, then the assembly-language subroutine must be written assuming that the addresses of the first six arguments, Al-A6, will be transmitted through index registers Bl-B6; addresses of arguments beyond the sixth, A7-AN, will be transmitted into the locations corresponding to A7-AN within the assembly-language subroutines; a return jump will be made to the location following AN.
- f) Function routines, either Fortran-coded, assembly-language coded, or from the system's library, leave results in X6 upon exiting.

# Fortran Formats:

Fortran statements which are allowed in an assembly-language program or subroutine are the following:

COMMON
EQUIVALENCE
DIMENSION
EXTERNAL
DATA

Identifiers appearing in the above statements may be used in subsequent symbolic instructions. Continuation cards must contain an asterisk (\*) in column 6.

#### **Declaration Formats:**

Six tag-associating declarations are allowed in assembly language. These provide for associating alphanumeric tags with constants, with regions reserved locally or in COMMON, and with external subroutines which are subject to reference. Examples and explanations of these declarations follow:

1. CON (C1=25, C2=777B, C3=-6.54E-2)

Causes the constants on the right of the equals relations to be assembled into a storage area and tagged with the identifiers appearing on the left.

2. HOL (H1=ABCDEFGHIJ, H2=1234567890)

Causes the ten-character groups, including spaces, on the right of the equals relations to be converted to display code, placed into a storage area, and tagged with the identifiers appearing on the left.

3. ABS (JJ=100, KK=100B, LL=7777B)

Causes the unsigned values on the right of the equals relations to be assembled into instructions containing the tags on the left in their address fields.

4. RES (K1=10, K2=100B, K3=1000)

Causes local block reservations, where the number of words reserved in each block is the unsigned number to the right of the equals relation and where the beginning of each block is tagged with the identifier on the left.

5. COM (B1=1, B2=300, B3=205B)

Causes blank COMMON block reservations, where the number of words reserved in each block is the unsigned number to the right of the equals relation andwhere the beginning of each block is tagged with the identifier on the left.

6. SUB (SI=SIN, LG=LOG, OUT=OUTPTC)

Causes the subroutines whose names appear on the right of the equals relations to be assembled into memory and tagged with the identifiers appearing on the left.

#### It is noted that

- a) Each tag-associating declaration is punched between column 6 and column 73 of each card used.
- b) Up to 19 continuation cards may be used in any declaration, but an asterisk (\*) must appear in column 6 of each continuation card.
- c) The open parenthesis ( ( ) following CON, HOL, ABS, RES, COM, or SUB may be replaced by any separator if the final closing parenthesis ( ) ) is dropped.

## Instruction Formats:

In the assembly language, operational registers are designated by single-character names as follows:

| S            | =  | XO | 0 = | в0         | A = | A0         |
|--------------|----|----|-----|------------|-----|------------|
| $\mathbf{T}$ | =  | X1 | I = | B1         | B = | A1         |
| U            | =  | X2 | J = | B2         | C = | A2         |
| V            | =  | ХЗ | K = | В3         | D = | A3         |
| W            | =  | X4 | L = | В4         | E = | A4         |
| Х            | == | X5 | M = | B <b>5</b> | F = | <b>A</b> 5 |
| Y            | == | X6 | N = | В6         | G = | <b>A</b> 6 |
| Z            | =  | X7 | Ø = | В7         | H = | A7         |

The letter R is used to specify a return jump, and the letter P is used to specify all other jumps.

Let S represent any of the letters S-Z, I represent any of the letters I-O or the digit O, and A represent any of the letters A-H. Let Q represent a positive integer less than 2<sup>16</sup> or an alphanumeric tag of 2-6 characters. Then the forms of assembly-language instructions, grouped according to functional units required for execution, are as follows:

| Symbolic Form     | Machine Form | <u>Example</u>                             |
|-------------------|--------------|--|
| 0                 | 00xxx        | 0  |
| R=Q               | 01xxK        | R=TAG RJ Q                                 |
| P=Q+I             | 02ixK        | P=TAG+N JP Bi+Q                            |
| P=Q,S=0           | 030jK        | P=TAG,T=0 - ZR Xi Q                        |
| P=Q,S/0           | 031jK        | P=TAG, U/0 - N2                            |
| P=Q,S)0 570       | 032jK        | P=TAG, V) 0 PL                             |
| P=Q,S(0 540       | 033jK        | P=TAG,W(0 ··· NG                           |
| P=Q,S.I           | 034jK        | P=TAG,X.I - · · TR                         |
| $P=Q,S.\emptyset$ | 035jK        | P=TAG,Y.Ø - • • • • •                      |
| P=Q,S.D           | 036jK        | P=TAG,Z.D DF                               |
| P=Q,S.N           | 037jK        | P=TAG, S.N ··· <b>ID</b>                   |
| P=Q,I=I           | 04ijK        | P=TAG,J=K EQ % % & Q                       |
| P=Q,I/I           | 05ijK        | P=TAG,L/M NE                               |
| P=Q,I)I           | 06ijK        | P=TAG,N)Ø GE B: G; Q                       |
| P=Q,I(I           | 07ijK        | P=TAG, I (0 LT                             |
| S=S               | 10ijx        | Y=V &x & x \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ |
| S.L=S*S           | 11ijk        | T.L=WXX Sxi Xj * xk                        |
| S.L=S+S           | 12ijk        | V. L=Y+Z                                   |
| S.L=S-S           | 13ijk        | V.L=U-V                                    |
| S=-S              | 14ijk        | Z=-W Xx                                    |
| S.C=S*S           | 15ijk        | W.C=T*U ~ X≥#x1                            |
| S.C=S+S           | 16ijk        | X.C=V+W - xu+ x3                           |
| S.C=S-S           | 17ijk        | Y. C=S-Z - xu-X;                           |

|                    | Symbolic Form | Machine Form   | Example                                    |
|--------------------|---------------|----------------|--|
|                    | S=S(Q)        | 20ijk          | T=T(24) LX: ik                             |
|                    | S=S(-Q)       | 21ijk          | 17_17 / 10\                                |
|                    | S=S(I)        | 22 <b>i</b> jk | ** ** /**\                                 |
|                    | S=S(-I)       | 23 <b>i</b> jk | V=X(N) Lx' & xx<br>W=W(-M) - Ax & xx       |
|                    | S,I=S-        | 24ijk          | X,J=W Wx: 6j Xx                            |
|                    | S,I=S+        | 25ijk          | Y, K=Z+ - 2 x : 01 Xx                      |
|                    | S,I=S.        | 26 <b>i</b> jk | Z,0=U UXL 6, Xm                            |
|                    | S=I,S.        | 27ijk          | T=K,V Pai bi Xu                            |
|                    | S=*Q          | 43ijk          |  |
|                    | 5 ···Q        | 431JK          | Y=*12 Axi in                               |
| F                  | S.N=S+S       | 30ijk          | 3x7 - V+U=N.T                              |
| FUAL               | S.N=S-S       | 31ijk          | U.N=X-Z                                    |
| Comoto             | S.D=S+S       | 32 <b>ij</b> k | $A \cdot D = A + A - \cdot \mathcal{D}X$ ? |
| Devre              | S.D=S-S       | 33ijk          | W.D=S-U DAL                                |
| * 3                | S.R=S+S       | 34ijk          | X.R=Y+Z Qx                                 |
|                    | S.R=S-S       | 35 <b>i</b> jk | Y. R=Z-T - QX                              |
|                    | 0. K 0 0      | <b>331</b> 1K  | 1.16.2.1 × 16.85                           |
| . <b>.</b>         | S.I=S+S       | 36ijk          | JXI - X+U=I.T                              |
| Meser              | S.I=S-S       | 37ijk          | Z.I=Y-Z Tx:                                |
| * -                |               | -              |  |
|                    | S.N=S*S       | 40ijk          | S.N=X*XFX                                  |
| mant.              | S.R=S*S       | 4lijk          | Y. R=Y*Y ***                               |
| F                  | S.D=S*S       | 42ijk          | T.D=T*U - DXL                              |
|                    | a v a la      |                |  |
| _                  | S.N=S/S       | 44ijk          | Y.N=T/X ~ FX                               |
| Duse               | S.R=S/S       | 45 <b>i</b> jk | Z.R=X/W PXE                                |
| _                  | \$            | 46xxx          | \$ - <b>No</b>                             |
| _                  | S=*S          | 47 <b>ix</b> k | T=*W CXi Xn                                |
| Duce<br>Xy: (Kirk) | - 4           |                | "  |
| XA:CO              | S=(A+Q)       | 50ijK          | T=(C+TAG) - SA; A; K                       |
|                    | S=(I+Q)       | 51ijK          | U=(J+100) - SA; &; +k                      |
|                    | S=(S+Q)       | 52 <b>i</b> jK | Z=(T+30B) - 3A; X:+K                       |
|                    | S=(S+I)       | 53 <b>i</b> jk | T=(T+J) _ 35,SA; X;+3.                     |
|                    | S=(A+I)       | 54ijk          | U=(B+K) - SA; A;+B.                        |
|                    | S=(A-I)       | 55 <b>ij</b> k | V=(C-N) SAL A. 812                         |
|                    | S=(I+I)       | 56ijk          | W=(M+N) $SAiBi+Bn$                         |
|                    | S=(I-I)       | 57ijk          | $X=(L-K) - 3A_i B_i - b_n$                 |
|                    | I=A+Q         | 60ijK          | J=H+TAG _ 3B; 13+K                         |
|                    | I=I+Q         | 6lijK          | K=L+10 56; Bj+k                            |
|                    | I=S+Q         | 62 <b>i</b> jK | L=L+55B - S& x;+k                          |
|                    | I=S+I         | 6 <b>3ij</b> k | M=T+J - 581 X1+BR                          |
|                    | I=A+I         | 64ijk          | N=G+K SB; A;+Br                            |
|                    | I=A-I         | 65 <b>i</b> jk | Ø=A-L 56; A; -Brz                          |
|                    | I=I+I         | 6 <b>6i</b> jk | I=I+J - 56, 35 +3n                         |
|                    | I=I-I         | 67ijk          | J=K-M 56; -6k                              |
|                    | S=A+Q         | 70ijK          | T=G+TAG SX; Aj+K                           |
|                    | S=I+Q         | 71 <b>i</b> jK | U=K+5 . SX, 8,+K                           |
|                    | S=S+Q         | 72 <b>i</b> jK | V=L+15B - 3x1 x1+k                         |
|                    | S=S+I         | 73ijk          | W=X+J 3x' x'+8m                            |
|                    | S=A+I         | 74ijk          | X=A+K - sx; & A+he                         |
|                    | S=A-I         | 75ijk          | Y=C-L - Sto A: -be                         |
|                    | S=I+I         | 76ijk          | Z=M+I . 5 × 2 0 j + 3/2                    |
|                    | S=I-I         | 77ijk          | S=N-0 - 5x2 By-By                          |
|                    |               | <u>-</u> .j    | and Alank                                  |
|                    |               |                |  |

#### It is noted that

a) The arithmetic mode indicators L, C, N, D, R, and I may immediately follow a result register name, i.e., the period (.) in these cases is optional.

TL=X\*Y UC=V+V VN=T/W WD=X+Y XR=S-T

b) In the instructions 02 and 50-77 either term may be dropped, in which case a 0 designation is assembled.

P=K P=TAG T=(J) J=15B U=-K

c) In the instructions 50-54, 60-64, and 70-74 the terms may be interchanged unless Q is a constant.

T=(TAG+L) M=K+B U=L+X

d) In the instructions 50-52, 60-62, and 70-72 the plus sign
 (+) may be replaced by a minus sign
 (-) if Q is a constant.

X=(I-30) J=K-55BY=-1

e) In the instructions 51, 61, and 71 the right member may be an indicated sum or difference of a tag and a constant, in which case the constant must follow the tag.

W=(TAG-35) K=TAG+1 U=TAG+100B

f) In the instruction 51 the parenthesized quantity may be a constant, represented in conventional Fortran form, only if the result register is to receive the machine version of that constant; in this case the address of the converted number is assembled into the instruction.

T=(-1.5E-6) U=(47550516045547B) \* 5 E N D 5 \*

- g) If it is desired to have Q correspond to an octal integer, then the digits in the number must be trailed by a B.
- h) Alternate forms for certain instructions are

| S=S.S   | 11ijk          |
|---------|----------------|
| S=S\$S  | 12ijk          |
| S=-S.S  | 15ijk          |
| S=-S\$S | 16ijk          |
| S=S+S   | 36ijk          |
| S=S-S   | 37ijk          |
| S=S*S   | 40ijk          |
| s=s/s   | 44ijk          |
| A=A+Q   | 50ijK          |
| A=I+Q   | 51ijK          |
| A=S+Q   | 52 <b>i</b> jK |
| A=S+I   | 53ijk          |
| A=A+I   | 54 <b>i</b> jk |
| A=A-I   | 55 <b>i</b> jk |
| A=I+I   | 56ijk          |
| A=I-I   | 57ijk          |
|         |                |

- i) Each instruction must be punched between column 6 and column 73 of a card; no blanks are permitted within the instruction code.
- j) A comment may follow any instruction code, but at least one blank must separate it from the instruction.
- k) An alphanumeric location tag of 2-6 characters may be punched in columns 1-6 of a card containing an instruction; no blanks are permitted within the tag.
- 1) A plus sign (+) in a location field will force the corresponding instruction to the high order positions of a new word.
- m) The instruction 00 is assembled as a full zero word.

#### Constant Formats:

Decimal constants in standard Fortran notation may be specified in assembly-language. Octal constants may be specified by placing a B after the digits of the number.

#### It is noted that

a) A constant must be punched between column 6 and column 73 of a card; no blanks are permitted with the constant specification.

-100.0 500 100B +15.64E-3

b) An alphanumeric location tag of 2-6 characters may be punched in columns 1-6 of a card containing a constant; no blanks are permitted within the tag.

CON1 -150.0 CON2 3.6E10

c) Block reservations of zero words, tagged or untagged, may be made by enclosing the number of words to be reserved in parentheses; the parenthesized quantity must appear between column 6 and column 73 of a card; if a location tag appears on the same card, then it will be associated with the first word of the block.

BK1 (100) BK2 (200B)

# FORTRAN HANDLING OF AN ASCENT SUBSET

The Fortran compiler, RUN, is capable of processing programs or subroutines written in a subset of ASCENT assembly language. Such programs or subroutines may be intermixed with regular Fortran programs and subroutines. Each must be organized as follows:

- 1. Header card.
- 2. Fortran cards, if any.
- 3. Instruction cards.
- 4. Constant cards.
- 5. End card.

#### It is noted that

- a) The instruction portion of the deck must be preceded by lines of coding which produce "O" words corresponding to control words, argument words, and an exit/entry word.
- b) The instruction portion of the deck may contain BSS, BSSZ, and EQU cards. The address field of any such card may contain only a single constant. BSS and BSSZ cards produce zero regions.
- c) The constant portion of the deck may contain BSS, BSSZ, EQU, DPC, BCD, and CON cards. The address fields of these cards may contain only a single constant or ten-character string of the form \*ABCDEFGHIJ\*. DPC and BCD character strings are reduced to display code.
- d) The constant portion of the deck must be separated from the instruction portion by a card with two periods (..) punched in columns 7 and 8.
- e) The end card is punched as in Fortran, i.e., END appears between column 6 and 73 of the card.
- f) Fortran C-type comment cards are not permitted.
- g) Fortran cards may contain COMMON, EQUIVALENCE, DIMENSION, EXTERNAL, or DATA statements. Identifiers appearing in the statements may be used in subsequent symbolic instructions. Continuation cards must contain an asterisk (\*) in column 6.
- h) The header card must be in one of the following formats:

ASCENTF PROGRAM NAME
ASCENTF PROGRAM NAME (A1,...,AN)
ASCENTF SUBROUTINE NAME
ASCENTF SUBROUTINE NAME (A1,...,AN)

- The header information must be punched between column 6 and column 73 of each card used. Continuation cards may be used, but must be designated by an asterisk (\*) in column 6.
- i) Instruction formats are as described in ASCENT programming manuals, but with certain restrictions. An address field may contain an indicated sum of a tag and constant, but not a sum or difference of two tags. Location tags may start in column 1, but may not extend beyond column 6. Instructions may start anywhere beyond column 6, but no card may contain more than one instruction. The PS instruction causes assembly of a full zero word.
- j) Location tags associated with pseudo-operations may start in column 1, but may not extend beyond column 6.
- k) Double-precision and complex "literal constants" are not accepted.
- 1) A minus sign (-) in a location field is not allowed.
- m) An asterisk(\*) in an address field is not allowed.
- n) Except for the header card and the double-period card, separating instructions from constants, upward compatibility from the Chippewa System to SIPROS is achievable by starting location tags of no more than five characters in column 2 and by starting instructions and pseudo-operations in column 11.
- o) All other formats, except those of instructions, which have been described for the Chippewa assembly language acceptable to the compiler may be used; but each such usage introduces an incompatibility between the Chippewa System and SIPROS. If one desires only to replace instruction formats of the Chippewa assembly language by ASCENT formats, but retain all other formats of that language, he may do so.

#### FORTRAN ERROR PRINTOUTS

During a Fortran compilation two-character error prints may follow statements which are incorrect; other such prints may follow the END statement, indicating other types of errors in the program. A list of these two-character error indicators, along with a brief explanation of the type of error which each indicates, follows:

## AC Argument-Count Error

Indicates that the number of arguments in a current reference to a subroutine differs from the number which occurred in a prior reference.

#### AL Argument-List Error

Indicates a format error in a list of arguments.

# AS Assign Error

Indicates a format error in an ASSIGN statement.

#### BC Boolean-Constant Error

Indicates a format error in the designation of a FORTRAN boolean constant in a B-type expression.

## BI Binary-Input Error

Indicates that a subroutine on binary cards following the Fortran program and its Fortran-coded subroutines has an incorrect header card.

#### BO Common-Block-Overflow Error

Indicates that the current requirements for a labeled block of COMMON storage exceed the length of the block as established in a preceding COMMON statement.

#### BX Boolean-Expression Error

Indicates a format error in a B-type boolean statement.

#### CD Duplicate-Common Error

Indicates that a variable currently being assigned to the COMMON region has been previously assigned to this region.

#### CE Common-Equivalence Error

Indicates that two variables assigned to COMMON storage are being improperly equivalenced.

#### CL Call Error

Indicates a format error in a CALL statement.

#### CM Common Error

Indicates a format error in a COMMON statement.

# CN Continuation Error

Indicates that more than nineteen continuation cards appear in succession or that one such card appears in an illogical sequence.

# CO Common-Overflow Error

Indicates that the amount of COMMON storage, required by the main program or specified to the compiler, is less than that required by the current program or subroutine.

# CT Continue Error

Indicates that a CONTINUE statement is missing a statement number.

# DA Duplicate-Argument Error

Indicates that duplicate dummy arguments appear in a function-definition statement.

# DC Decimal-Constant Error

Indicates a format error in the expression of a FORTRAN decimal constant.

# DD Duplicate-Dimension Error

Indicates that a variable currently being dimensioned has been previously dimensioned.

# DF Duplicate-Function-Name Error

Indicates that the function name in the current functiondefinition statement has occurred as the name of a previously defined function.

#### DM Dimension Error

Indicates a format error in a DIMENSION statement.

#### DO Do Error

Indicates a format error in a DO statement.

## DP Duplicate-Statement-Number Error

Indicates that the current statement number has previously appeared in the statement-number field.

#### DS Missing-Do-Number Error

Indicates that references have been made in DO statements to statement numbers which did not appear somewhere in the statement-number field of a card.

#### DT Data Error

Indicates a format error in a DATA statement.

# EC Equivalence-Contradiction Error

Indicates that a variable currently appearing in an EQUIVALENCE statement cannot be equivalenced because of an inherent contradiction in the statement.

#### EF End-of-File Error

Indicates than end-of-file card is detected before the last END card is encountered.

#### EM Exponential-Mode Error

Indicates that the mode of the base or the exponent of an indicated exponentiation process is improper.

#### EQ Equivalence Error

Indicates a format error in an EQUIVALENCE statement.

#### EX Exponent Error

Indicates a format error in the exponent portion of an indicated exponentiation process.

#### FL Function-List Error

Indicates a format error in an EXTERNAL statement or F-type statement.

#### FM Format Error

Indicates a format error in a statement whose type cannot be determined.

#### FN Format-Statement-Number Error

Indicates that a FORMAT statement is missing a statement number.

## FS Format-Specification Error

Indicates a format error in the specification portion of a FORMAT statement.

#### FT Function-Type Error

Indicates a format error in a TYPE statement.

#### GO Go-To Error

Indicates a format error in a GO statement.

#### IF If Error

Indicates a format error in an IF statement.

#### IL Indexed-List Error

Indicates a format error in an indexed list of the current input/output statement.

#### LR Library-Reference Error

Indicates that a reference has been made to a standard library subroutine where more arguments appeared in the reference than are provided for by the subroutine.

#### LN Name-List Error

Indicates a format error or a reference to an array with variable dimensions in a NAMELIST statement.

#### LS List Error

Indicates a format error in an input/output list.

#### MA Misuse-of-Argument Error

Indicates that an argument of the SUBROUTINE or FUNCTION being compiled has been misused in an EQUIVALENCE statement.

#### MC Machine-Constant Error

Indicates an error in the formatting of a constant in a tagdefining line of coding involving the pseudo operations RES, COM, CON, ABS, SUB, or HOL.

#### MD Machine-Duplicate-Tag Error

Indicates that a tag appearing in a tag-defining line of coding involving the pseudo operations RES, COM, CON, ABS, SUB, or HOL has been previously defined.

#### MF Machine-Format Error

Indicates a format error in a tag-defining line of coding involving the pseudo operations RES, COM, CON, ABS, SUB, or HOL.

#### ML Machine-Location-Tag Error

Indicates that a tag appearing in an address field of a machine instruction did not appear in a subsequent location field.

## MO Memory-Overflow Error

Indicates that the compiler field length, as specified on the job card, is too short.

# MR Missing-Subroutine Error

Indicates that subroutines which are not in the standard subroutine library have been referenced by the Fortran program or its Fortran-coded subroutines.

#### MS Missing-Statement-Number Error

Indicates that references have been made to statement numbers which did not appear somewhere in the statement-number field of a card.

## MT Machine-Tag-Definition Error

Indicates a format error in a tag-defining line of coding involving the pseudo operations RES, COM, CON, ABS, SUB, or HOL.

#### NC Name-Conflict Error

Indicates that the name of a SUBROUTINE or FUNCTION conflicts with prior usage of the name.

#### NM Name Error

Indicates an error in formatting of the name (header) card.

# OD Dimension-Statement-Order Error

Indicates that a reference to an array has been made prior to the appearance of the array name in a DIMENSION statement.

#### PN Parentheses Error

Indicates that an unequal number of opened and closed parentheses appear in a statement.

#### RN Return Error

Indicates a format error in a RETURN statement.

## SB Subscript Error

Indicates a format error in a subscript of an array reference currently being processed.

#### SE Sense Error

Indicates a format error in a SENSE statement.

#### SF Short-Field-Length Error

Indicates that the required field length of the program or subroutine being compiled exceeds the specified one.

# SL Subroutine-Storage-Limit Error

Indicates that the compiler field length, as specified on the job card, is exceeded at the time that standard library subroutines are being assembled.

#### SM Statement-Number-Field Error

Indicates a format error in the statement-number field of the current statement.

#### SN Statement-Number Error

Indicates a format error in a position where a statement number should appear.

## SY System Error

Indicates a machine malfunction.

#### TM Too-Many-Arguments Error

Indicates that a subroutine reference has more than sixty arguments or that the program or subroutine being compiled has more than sixty arguments.

#### TY Type Error

Indicates a format error in a TYPE statement.

#### UA Unidentified-Array Error

Indicates that a reference has been made to an array where the array name has not previously appeared in a DIMENSION statement.

#### UE Unidentified-Equipment Error

Indicates a reference to an input/output file which was not listed in the header card of the main program.

# US Unreferenced-Binary-Subroutine Error

Indicates that a subroutine on binary cards following the Fortran program and its Fortran-coded subroutines has not been referenced.

# VC Variable-Name-Conflict Error

Indicates that a variable name appears which conflicts with some other prior usage.

#### VD Variable-Dimensioned-Array Error

Indicates that an array whose dimensions are arguments to the SUBROUTINE or FUNCTION being compiled has been misused.

#### VN Variable-Name Error

Indicates an error in a variable name appearing in the current statement.

#### XF Expression-Format Error

Indicates a format error in the expression currently being processed.

#### XM Expression-Mode Error

Indicates that an expression currently being processed is one in which modes are mixed.

# Error Messages Entered into Dayfile

The following error statements may be entered into the dayfile during execution of a FORTRAN program, or a machine language program using the FORTRAN subroutines.

After each message the program is aborted.

#### Origin

INPUTC - data input subroutine.

INP(FN) FN is the number of the format statement being executed at time of error.

END OF FILE, DATA INPUT. Attempt made to read past file mark.

OUTPTC - data output subroutine.

BCD OUTPUT \*\* UNASSIGNED MEDIUM . Failure to specify a file name during compilation.

NO DEC. PT IN E OR F . No significance specified in E or F conversion.

EXCEEDED RECORD LENGTH . More than 150 characters have been entered as a record.

ILLEGAL FUNCTIONAL LETTER . Illegal format letter.

PAREN. LEVEL N(N( . Format has parenthetical group within a parenthetical group.

For OUTPTC messages, the format number is entered in the line following the message.

INPUTB - binary input.

BIN INPUT \*\* UNASSIGNED MEDIUM.

OUTPTB - binary output.

BIN OUTPUT \*\* UNASSIGNED MEDIUM.

ENDFIL - write end of file.

EOF \*\* UNASSIGNED MEDIUM.

#### CPUASM

The Central Processor Assembler provides a language for writing programs and subroutines that may be executed independently or as a FORTRAN subroutine. The basic features of the language were originally described in Volume I ASCENT of CONTROL DATA 6600 Programming System/Reference Manual.

Assemblies are initiated by calling for CPUASM from the systems library. Punch, list, and execute options are parameters to CPUASM. The binary version is always transferred to the disk file regardless of option parameters or assembly errors. P, L, and X are the three parameters to the assembler:

P - punch binary cards.L - list side by side.X - execute program.

Example of program to be assembled:

JOB,5,3,30000. CPUASM(L,X,P) 7,8,9 PROGRAM NAME STATEMENTS END 6,7,8,9

If the execute option (X) is deleted, the program may still be called by referencing the program via the control card following the assembly.

#### Symbols:

Symbols may be any arrangement of characters up to 10 characters in length. A symbol must begin with an alphabetic letter and must not contain any separators. The special characters used as separators are +, -/\* = \$ and SPACE. Symbols used in address fields may be signed + or - allowing them to be added to one another or to a constant.

#### Constants:

Constants may be either octal or decimal fixed point integers  $\leq 2^{60}$ -1, a symbolic address or a single precision floating point number. Integers are assumed to be decimal unless appended by the letter B. Floating point numbers must have a decimal point and may optionally be followed by an exponent E±N.

#### Field definitions:

The location field starts in column 2 and may be 1-10 characters in length. If a location tag is present the operation code must be separated from it by a space code or must not start prior to column 7 if no location tag is present.

The address field is variable in length and is separated from the operation code. In general, the address field may be any combination of terms, integer and/or symbolic, as long as the constant field for the instruction is not exceeded. Terms in address fields may also be a combination of 1 or 2 A, B, or X registers; if only 1 term is present Bo is assumed as the second term.

Pseudo Operation Codes.

PROGRAM: Defines the job to be a program; the symbol in

the address field is the name of the program as

referenced by the system.

SUBROUTINE: Defines the job to be a subroutine and sets

relocatable bits for later use by the FORTRAN compiler or machine program. The symbol in the address field is the name used to reference the

subroutine.

END: Last card of a program or subroutine.

BSS: Address field defines the length of the block

reservation. Address field may be integer

constant or symbolic constant.

EQU: Equivalences a symbol to another symbol or a

constant.

DPC: Allows the entry of console display codes, the

length being specified by a 2 digit integer, or

the codes between \*'s are entered.

BCD: Converts the characters enclosed by the asterisks

or converts the characters specified by the

beginning 2 digit integer.

CON: Converts each term to a 60 bit constant.

EJECT: Ejects the listing to the top of the next page.

SPACE: Spaces the number of lines specified by the

address field.

# OPERATION CODES

| <u>OP</u> | MNEMONIC | ADDRESS        | REMARKS  |
|-----------|----------|----------------|--|
|           |          |                | .Branch unit                                   |
| 00        | PS       |                | .Program stop                                  |
| 01        | RJ       | K              | .Return jump to K                              |
| 02        | JP       | Bi+ K          | .Jump to Bi+K                                  |
| 030       | ZR       | Xi K           | .Jump to K if Xi=0                             |
| 031       | NZ       | Xi K           | .Jump to K if Xi≠0                             |
| 032       | PL       | Xi K           | .Jump to K if Xi=plus (positive)               |
| 033       | NG       | Xi K           | .Jump to K if Xi=negative                      |
| 034       | IR       | Xi K           | .Jump to K if Xi is in range                   |
| 035       | OR       | Xi K           | .Jump to K if Xi is out of range               |
| 036       | DF       | Xi K           | .Jump to K if Xi is definite                   |
| 037       | ID       | Xi K           | .Jump to K if Xi is indefinite                 |
| 04        | EQ       | BiBjK          | .Jump to K if Bi=Bj                            |
| 04        | ZR       | Bi K           | Jump to K if Bi=BO                             |
| 05        | NE       | BiBjK          | .Jump to K if Bi≠Bj                            |
| 05        | NZ       | Bi K           | Jump to K if Bi≠B0                             |
| 06        | GE       | BiBjK          | Jump to K if Bi≥Bj                             |
| 06<br>07  | PL<br>LT | Bi K           | Jump to K if Bi≥BO                             |
| 07<br>07  | NG       | BiBjK<br>Bi V  | Jump to K if Bi <bi< td=""></bi<>              |
| 07        | NG       | Bi K           | Jump to K if Bi <bo< td=""></bo<>              |
| 10        | BXi      | Хj             | .Boolean Unit<br>.Transmit Xj to Xi            |
| 11        | BXi      | Xj*Xk          | Logical Product of Xj & Xk to Xi               |
| 12        | BXi      | Xj+Xk          | Logical sum of Xj & Xk to Xi                   |
| 13        | BXi      | Xj-Xk          | Logical difference of Xj & Xk to Xi            |
| 14        | BXi      | -Xk            | Transmit the comp. of Xj to Xi                 |
| 15        | BX1      | -Xk*Xj         | .Logical product of Xj & Xk comp. to Xi        |
| 16        | BXi      | -Xk+Xj         | .Logical sum of Xj & Xk comp. to Xi            |
| 17        | BXi      | -Xk-Xj         | .Logical difference of Xj & Xk comp. to Xi     |
|           | •        | •              | .Shift Unit                                    |
| 20        | LX1      | jk             | .Left shift Xi,jk places                       |
| 21        | AXi      | jk             | .Arithmetic right shift Xi,jk places           |
| 22        | LXi      | Bj,Xk          | .Left shift Xi nominally Bi places             |
| 23        | AXi      | Bj,Xk          | .Arithmetic right shift Xi nominally Bi places |
| 24        | NXi      | Bj Xk          | .Normalize Xk in Xi and Bj                     |
| 25        | ZXi      | Bj Xk          | .Round and normalize Xk in Xi and Bj           |
| 26        | UXi      | Bj Xk          | .Unpack Xk to Xi and Bj                        |
| 27        | PXi      | Bj Xk          | .Pack Xi from Xk and Bj                        |
| 43        | MXi      | jk             | .Form mask in Xi,jk bits                       |
| 30        | tv.⁴     | 7 * LV1-       | Add Unit                                       |
| 30<br>31  | FX1      | Xj+Xk          | Floating sum of Xj and Xk to Xi                |
| 32        | FXi      | Xj~Xk          | Floating DR sym of Vi and Vk to Vi             |
|           | DXi      | Xj+Xk<br>Xi-Xk | Floating DP sum of Xj and Xk to Xi             |
| 33<br>34  | DXi      | Xj-Xk          | Floating DP difference of Xj and Xk to Xi      |
| 34        | RX1      | Xj+Xk          | .Round floating sum of Xj and Xk to Xi         |

| <u>OP</u> | MNEMONIC       | ADDRESS        | REMARKS   |
|-----------|----------------|----------------|---|
| 35        | RXi            | Xj-Xk          | .Round floating difference of Xj and Xk to Xi     |
| 36        | IXi            | Xj+Xk          | .Long Add Unit<br>.Integer sum of Xj and Xk to Xi |
| 37        | IXi            | Xj-Xk          | Integer difference of Xj and Xk to Xi             |
|           |                | 5              | .Multiply Unit                                    |
| 40        | FXi            | Xj*Xk          | .Floating product of Xj and Xk to Xi              |
| 41        | RXi            | Xj*Xk          | Round floating product of Xj & Xk to Xi           |
| 42        | DXi            | Xj*Xk          | .Floating DP product of Xj & Xk to Xi             |
|           |                |                | .Divide Unit                                      |
| 44        | FXi            | Xj/Xk          | .Floating divide Xj by Xk to Xi                   |
| 45        | RXi            | Xj/Xk          | .Round floating divide Xj by Xk to Xi             |
| 46        | NO             |                | .No operation                                     |
| 47        | CXi            | Хj             | .Count the number of 1's in Xj to Xi              |
| <b>.</b>  |                |                | .Increment Unit                                   |
| 50        | SAi            | Aj+K           | .Set Ai to Aj+K                                   |
| 50        | SAi            | Aj-K           | .Set Ai to Aj+ comp. of K                         |
| 51        | SAi            | Bj+K           | .Set Ai to Bj+K                                   |
| 51<br>52  | SAi            | Bj-K           | .Set Ai to Bj+ comp. of K                         |
| 52<br>52  | SAi            | Xj+K           | .Set Ai to Xj+K                                   |
| 53        | SAi<br>SAi     | Xj-K           | Set Ai to Xj+ comp. of K                          |
| 54        | SAI            | Xj+Bk<br>Aj+Bk | Set Ai to Xj+Bk                                   |
| 55        | SAi            | Aj-Bk          | Set Ai to Aj+Bk                                   |
| 56        | SAi            | Bj+Bk          | .Set Ai to Aj-Bk<br>.Set Ai to Bj+Bk              |
| 57        | SAi            | Bj-Bk          | Set Ai to Bj-Bk                                   |
| 60        | SBi            | Aj+K           | Set Bi to Aj+K                                    |
| 60        | SBi            | Aj-K           | Set Bi to Aj+ comp. of K                          |
| 61        | SBi            | Bj+K           | .Set Bi to Bj+K                                   |
| 61        | SBi            | Bj-K           | .Set Bi to Bj+ comp. of K                         |
| 62        | SBi            | Xj+K           | .Set Bi to Xj+K                                   |
| 62        | SBi            | Xj-K           | .Set Bi to Xj+ comp. of K                         |
| 63        | SBi            | Xj+Bk          | .Set Bi to Xj+Bk                                  |
| 64        | SBi            | Aj+Bk          | .Set Bi to Aj+Bk                                  |
| 65        | $\mathtt{SBi}$ | Aj-Bk          | .Set Bi to Aj-Bk                                  |
| 66        | $\mathtt{SBi}$ | Bj+Bk          | .Set Bi to Bj+Bk                                  |
| 67        | SBi            | Bj-Bk          | .Set Bi to Bj-Bk                                  |
| 70        | SXi            | Aj+K           | .Set Xi to Aj+K                                   |
| 70        | SXi            | Aj-K           | .Set Xi to Aj+ comp. of K                         |
| 71        | SXi            | Bj+K           | .Set Xi to Bj+K                                   |
| 71        | SXi            | Bj-K           | .Set Xi to Bj+ comp. of K                         |
| 72<br>72  | SXi            | Xj+K           | .Set Xi to Xj+K                                   |
| 72<br>73  | SXi            | Xj-K           | .Set Xi to Xj+ comp. of K                         |
| 73<br>74  | SXi            | Xj+Bk          | Set Xi to Xj+Bk                                   |
| 74<br>75  | SXi<br>SXi     | Aj+Bk          | Set Xi to Aj+Bk                                   |
| 75<br>76  | SXI            | Aj-Bk<br>Bj+Bk | Set Xi to Aj-Bk                                   |
| 77        | SXi            | вј-Вk          | Set Xi to Bj+Bk                                   |
|           | CIL            | את ער          | .Set Xi to Bj-Bk                                  |

#### Error Codes.

The following error codes may appear on the side by side listing:

- P Location symbol has previously been defined.
- U Symbol in address field is undefined.
- C Constant out of range or contains illegal codes.
- I Instruction field not legal.
- M Symbol in address field is multiply defined.
- 0 Address field does not correspond to any valid instruction.

# PAS PERIPHERAL ASSEMBLY LANGUAGE

This assembler is a PPU program which converts PPU symbolic language into PPU absolute language.

The assembler produces a side by side listing and binary cards. Punching of cards is suppressed if an error has been detected in the assembled program. The binary cards punched may be either 80-column binary cards or Chippewa Laboratory standard binary card format, i.e.: Columns 1 and 2 contain a binary card identifier (7-9 punch), number of 60-bit words on the card (15<sub>10</sub> maximum) and a card check sum. Column 80 is a card sequence number. To punch Chippewa Laboratory binary cards, the job cards are:

Job (1, 100, 1000) PAS 789 Program Deck 6789

To bunch standard 80-column binary cards, the job cards are:

Job (1, 100, 1000) PAS, 1000 789 Program Deck 6789

There is no provision for leaving a running version of the assembled program in the computer. The assembled program is available as a card deck and/or a line printer listing.

#### CARD FORMAT:

Card format for this assembler is fixed field except for the remarks card which may be one of two variations.

- (1) Pseudo op "REM" which must appear in the OP field (Cols. 6-7-8) is free fielded thereafter and
  - (2) \* in Column 1, and free fielded thereafter.

The other exception is the "DIS" pseudo op which converts all characters following DIS to display code.

The standard format then, used in all other instances, is as follows:

Col. 1-4 = Location field. Maximum of 4 alpha-numeric characters.

Col. 5 = Blank.

Col. 6-8 = Op field. Three character mnemonic (See Table 1).

Col. 9 = Blank.

Col. 10-13 = Tag field. Maximum of 4 alpha-numeric characters.

There are no provisions for adding a constant to or subtracting a constant from a tag.

Col. 14-19 = Blank.

Col. 20-80 = Comments.

#### **CONSTANTS:**

Numeric constants used in a program will be placed in Columns 10-13 and are considered to be octal. They therefore have a range of 0000 to 7777 in a two address instruction or a range of 00 to 77 in a one address instruction. All constants are right adjusted by the assembler.

Constants may also be in the form of a tag; however, the converted value must also follow the rules for one and two address instructions.

#### OP CODES AND PSEUDO OPS:

These are defined in Table 1.

#### ERROR FLAGS:

There are several errors which will be detected by the assembler and shown as a 1 character/error tag in the left margin of the assembled listing.

Ø = Illegal Op code.

U = Undefined tag has been referenced.

M = Location tag used more than once.

R = Range error. d portion is greater than 77 or on a jump instruction (03-07) location tag is found to be more than  $37_8$  forward or backward.

TABLE 1. MNEMONIC OP CODES AND PSEUDO OPS

Op Codes

| Mnemonic & Octal Code  |  |                          |            |                                       |
|--|--|--------------------------|------------|---------------------------------------|
| Name   | Mnomonia S   |                          | Mnemonic & |                                       |
| ENN 00   Pass   LMI 43   Logical difference ((d))  | 1  | Name                     |            | Name                                  |
| LJM 01  RJM 02  Return jump to m + (d)  UJN 03  Unconditional jump d  Zero jump d  NJN 05  Nonzero jump d  PJN 06  Plus jump d  MJN 07  Minus jump d  LPN 10  Logical difference d  LPN 12  Logical product d  SSN 13  Selective clear d  ADN 16  ADN 21  Add dm  ADC 21  Add dm  CWM 63  Central read from (A) to d  Central write to (A) from d  Central write (d) words  to (A) from m   AJM 64  Fysn 25  Fass  AJM 64  ADJ 64  ADJ 64  ADJ 64  ADJ 65  ADJ 65  AJM 64  ADJ 66  ADJ 66  ADJ 67  ADJ 10  ADJ | Octal Code   | Name                     |            |                                       |
| LJM 01  RJM 02  Return jump to m + (d)  UJN 03  Unconditional jump d  Zero jump d  PJN 06  PJN 06  PJN 06  PJN 06  PJN 07  Minus jump d  SSH 10  SSH 10  SSH 10  SSH 12  Logical difference d  LPN 12  Logical product d  SSN 13  Selective clear d  ADN 16  A | DSN OO   | Pacc                     | LMI 43     | Logical difference ((d))              |
| RIM 02 Return jump to m + (d) UJN 03 Unconditional jump d ZJN 04 Zero jump d PJN 06 Plus jump d NJN 07 Minus jump d SHN 10 Shift d LPN 12 Logical product d SCN 13 Selective clear d ADN 16 Add d SBN 17 Subtract d CRD 60 LDN 16 LO22 Load dm ADN 16 Add dd ADN 16 Add dm ADN 16 Add dd ADN 16 Add dm A | I BN 00  | 1455                     |            |                                       |
| RIM 02 Return jump to m + (d) UJN 03 Unconditional jump d ZJN 04 Zero jump d PJN 06 Plus jump d NJN 07 Minus jump d SHN 10 Shift d LPN 12 Logical product d SCN 13 Selective clear d ADN 16 Add d SBN 17 Subtract d CRD 60 LDN 16 LO22 Load dm ADN 16 Add dd ADN 16 Add dm ADN 16 Add dd ADN 16 Add dm A | T. TM 01   | Long jump to m + (d)     | STI 44     | Store ((d))                           |
| UJN 03 Unconditional jump d ZJN 04 Zero jump d NJN 05 Nonzero jump d PJN 06 Plus jump d MJN 07 Minus jump d MJN 07 Minus jump d MJN 08 Shift d LMN 11 Logical difference d LPN 12 Logical product d SCN 13 Selective clear d ADN 14 Load d SEN 17 Subtract d  LDC 20 Load dm ADC 21 Add dm ADC 21 Add dm ADC 21 Add dm ADC 21 Logical product dm LMC 23 Logical difference dm  PSN 24 Pass EXN 26 Exchange jump RPN 27 Read program address  LDD 30 Load (d) ADD 31 Add (d) SSD 32 Subtract (d) SSD 32 Subtract (d) SSD 32 Subtract (d) SSD 33 Replace add (d) Replace add one (d) SSD 37 Replace add one (d) SSD 37 Replace add (d) ADD 36 Replace add (d) ADD 36 Replace add (d) SSD 37 Replace add (d) SSD 37 Replace add (d) ADD 36 Replace add (d) ADD 36 Replace add (d) ADD 37 Replace add (d) ADD 38 Replace add (d) ADD 39 Replace add (d) ADD 30 Replace add (d) ADD 31 Add (d) ADD 32 Replace add (d) ADD 33 Replace add (d) ADD 34 Replace add (d) ADD 35 Replace add (d) ADD 36 Replace add (d) ADD 37 Replace subtract one (d) ADD 38 Replace add (d) ADD 39 Replace add (d) ADD 30 Replace add (d) ADD 30 Replace add (d) ADD 31 Add (d) ADD 32 Replace add (d) ADD 33 Replace add (d) ADD 34 Add (d) ADD 35 Replace add (d) ADD 36 Replace add (d) ADD 37 Replace add (d) ADD 38 Replace add (d) ADD 39 Replace add (d) ADD 30 Replace add (d) ADD 30 Replace add (d) ADD 31 Add (d) ADD 32 Replace add (d) ADD 33 Replace add (d) ADD 34 Add (d) ADD 35 Replace add (d) ADD 36 Replace |  |                          |            |                                       |
| ZJN 04 Zero jump d NJN 05 Nonzero jump d PJN 06 Plus jump d MJN 07 Minus jump d SSM 52 Subtract (m + (d)) LMN 11 Logical difference d LMN 13 Selective clear d SSN 13 Selective clear d ADM 14 LOAd (d) SSN 17 Subtract d  LDC 20 Load dm ADC 21 Add dm ADC 21 Add dm ADC 21 Add dm ADC 22 Logical product dm LMC 23 Logical difference dm  LMC 23 Logical difference dm  LMC 24 Pass SSN 25 Pass  LDD 30 Load (d) ADM 36 Replace subtract one (m) ADD 31 Add (d) SSD 32 Subtract (d) SSD 32 Subtract (d) ADD 36 Replace add (d) RAD 35 Replace add (d) RAD 36 Replace add (d) RAD 36 Replace add (d) RAD 37 Replace add (d) RAD 38 Replace add (d) RAD 36 Replace add (d) RAD 37 Replace add (d) RAD 38 Replace add (d) RAD 39 Replace add (d) RAD 36 Replace add (d) RAD 37 Replace add (d) RAD 38 Replace add (d) RAD 36 Replace add (d) RAD 37 Replace add (d) RAD 38 Replace add (d) RAD 36 Replace add (d) RAD 37 Replace add (d) RAD 38 Replace add (d) RAD 36 Replace add (d) RAD 36 Replace add (d) RAD 37 Replace add (d) RAD 38 Replace add (d) RAD 36 Replace add (d) RAD 37 Replace add (d) RAD 38 Replace add (d) RAD 39 Replace add (d) RAD 30 Replace  | A Company of the Comp |                          | 1          |                                       |
| NJN 05 Nonzero jump d PJN 06 Plus jump d MJN 07 Minus jump d SHN 10 Shift d LMN 11 Logical difference d LPN 12 Logical product d SCN 13 Selective clear d LDN 14 Load complement d ADN 15 LOR 15 Load complement d ADN 16 Add d SEN 17 Subtract d  LDC 20 Load dm ADC 21 Add dm ADC 21 Add dm LPC 22 Logical product dm LPC 23 Logical difference dm  PSN 24 Pass PSN 25 Pass  EXN 26 Exchange jump RPN 27 Read program address  LDD 30 Load (d) ADD 31 Add (d) SSD 32 Subtract (d) SCN 24 Store (d) RAD 35 Replace add (d) RAD 35 Replace add (d) RAD 36 Replace add (d) RAD 37 Replace add (d) RAD 36 Replace add (d) RAD 37 Replace add (d) ADD 36 Replace add (d) ADD 36 Replace add (d) ADD 37 Replace add (d) ADD 38 Replace add (d) ADD 39 Replace add (d) ADD 31 Add (d) ADD 36 Replace add (d) RAD 35 Replace add (d) RAD 36 Replace add (d) ADD 37 Replace subtract one (d) ADD 34 Add ((d)) ADD 35 Replace subtract one (d) ADD 36 Replace subtract one (d) ADD 37 Replace subtract one (d) ADD 38 Add ((d) ADD 39 Replace subtract one (d) ADD 30 Load (d) ADD 31 Add (d) ADD 32 Replace subtract one (d) ADD 33 Replace subtract one (d) ADD 34 Add ((d)) ADD 35 Replace subtract one (d) ADD 36 Replace subtract one (d) ADD 37 Replace subtract one (d) ADD 38 Add ((d)) ADD 39 Replace subtract one (d) ADD 30 Add ((d)) ADD 31 Add ((d)) ADD 31 Add ((d)) ADD 32 Replace subtract one (d) ADD 33 Replace subtract one (d) ADD 34 Add ((d)) ADD 35 Replace subtract one (d) ADD 36 Replace subtract one (d) ADD 37 Replace subtract one (d) ADD 38 Replace subtract one (d) ADD 39 Replace subtract one (d) ADD 30 Replace subtract one (d) ADD 31 Add ((d)) ADD 31 Add ((d)) ADD 32 Replace subtract one (d) ADD 34 Add ((d)) ADD 35 Replace subtract one (d) ADD 36 Replace subtract one (d) ADD 37 Replace subtract one (d) ADD 38 Replace add (d) ADD 39 Replace subtract one (d) ADD 30 Replace subtract one (d) ADD 31 Add ((d)) ADD 32 Replace subtract one (d) ADD 33 Replace subtract one (d) ADD 34 Add ((d)) ADD 35 Replace subtract one (d) ADD 36 Replace subtract one (d) ADD 37 Replace subtrac | •  |                          |            | Replace subtract one ((d))            |
| PJN 06 Plus jump d Minus jump d Shift  |  |                          |            | i                                     |
| MJN 07 Minus jump d Shift d Sh |  |                          | LDM 50     | Load (m + (d))                        |
| SHN 10 Shift d LMN 11 Logical difference d LPN 12 Logical product d SCN 13 Selective clear d LDN 14 Load d LCN 15 Load complement d ADN 16 Add d SEN 17 Subtract d  CRM 61 Central read from (A) to d CRM 62 Central write (d) words LDC 20 Load dm ADC 21 Add dm LPC 22 Logical product dm LMC 23 Logical difference dm  PSN 24 Pass PSN 25 Pass  EXN 26 Exchange jump RPN 27 Read program address  LDD 30 Load (d) ADD 31 Add (d) SED 32 Subtract (d) SEM 52 Subtract (m + (d)) LMC 33 Logical difference dm  LDC 30 Load dm ADD 31 Add (d) SED 32 Subtract (d) SEM 52 Subtract (m + (d)) LMC 33 Logical difference (m + (d)) Replace add one (m + (d)) Replace add one (m + (d)) Replace add one (m + (d)) Replace add (m + (d)) Replace add one (m + (d)) Replace add one (m + (d)) Replace add (m + (d)) Replace add one (m + (d) Replace add one (m + (d)) Replace add one (m + (d) Replace add one (m + (d) Replace add one (m + (d)) Replace add one (m + (d) Rep |  |                          | ! !        |                                       |
| LMN 11 Logical difference d LPN 12 Logical product d STM 54 Store (m + (d)) SCN 13 Selective clear d LDN 14 Load d LON 15 Load complement d ADN 16 Add d SEN 17 Subtract d  CRD 60 Central read from (A) to d CRM 61 Central read (d) words from (A) to m Central write to (A) from d Central write (d) words to (A) from m  PSN 24 Pass EXN 26 Exchange jump RPN 27 Read program address  EXN 26 Exchange jump RPN 27 Read program address  LDD 30 Load (d) ADD 31 Add (d) SED 32 Subtract (d) LDD 30 Load (d) ADD 31 Add (d) SED 32 Subtract (d) LDD 33 Logical difference (d) SED 34 Store (d) RAD 35 Replace add one (d) RAD 36 Replace add one (d) RAD 37 Replace subtract one (d)  LDI 40 Load ((d)) ADI 41 Add ((d))  LOAD 30 Load ((d)) ADI 41 Add ((d))  LOAD 31 Add ((d)) ADI 41 Add ((d))  LOAD 30 Replace subtract one (d) ADI 41 Add ((d))  LOAD 31 Add ((d)) ADI 41 Add ((d))  LOAD 32 Logical difference (d) ADI 41 Add ((d))  LOAD 34 Replace add one (d) ADI 41 Add ((d))  LOAD 35 Replace add (d) ADI 41 Add ((d))  LOAD 36 Replace add (d) ADI 41 Add ((d))  LOAD 37 Replace add (d) ADI 41 Add ((d))  LOAD 40 Central read from (A) to d Central write (A) ADI 57 Replace add one (m + (d)) ADI 41 ADI 41 Add ((d))  STM 54 Store (m + (d)) STM 54 Store (m + (d) STM 54 Store (m + (d)) STM 54 Store (m + (d) STM 54 Store (m + (d)) STM 54 Store (m + (d) STM 55 Replace add one (m + (d) STM 55 Replac |  |                          | ! (        | · · · · · · · · · · · · · · · · · · · |
| LPN 12 Logical product d SCN 13 Selective clear d LDN 14 Load d LCN 15 Load complement d ADN 16 Add d SEN 17 Subtract d  CRD 60 CRM 61 Central read from (A) to d CRM 61 Central write to (A) from d CRM 63 Central write to (A) from d CRM 63 Central write (A) words from (A) to m CRM 64 Central write to (A) from d CRM 65 Central write to (A) from d CRM 66 Central write to (A) from d CRM 67 Central write to (A) from d CRM 68 Central write to (A) from d CRM 69 Central write to (A) from d CRM 60 Central write to (A) from d CRM 61 Central write to (A) from d CRM 61 Central write to (A) from d CRM 62 Central write to (A) from d CRM 63 Central write to (A) from d CRM 64 Central write (A) words to (A) from m  PSN 24 Pass PSN 25 Pass  LDM 65 Jump to m if channel d inactive Jump to m if channel d full Jump to m if channel d full Jump to m if channel d empty Input to A from channel d Input (A) words to m from channel d OAN 72 OAN 73 OAN 74 Central write (A) OAN 74 OAN 74 DON 75 Disconnect channel d Output (A) words from m on channel d Output (A) words from channel d Output (A) words from CMA OUTPUT (A) words from CMA  |  |                          | : 1        |                                       |
| SCN 13 Selective clear d   |  |                          |            |                                       |
| LDN 14 Load d LCN 15 Load complement d ADN 16 Add d SBN 17 Subtract d  CRD 60 Central read from (A) to d CRM 61 Central write to (A) from d CRM 62 Central write to (A) from d CRM 63 Central write to (A) from d CRM 64 Central write (d) words from (A) to m CWD 62 Central write (d) words to (A) from m  PSN 24 Pass PSN 25 Pass  EXN 26 Exchange jump RPN 27 Read program address  EXN 26 Exchange jump RPN 27 Read program address  EXN 26 Exchange jump RPN 27 Read program address  EXN 26 Exchange jump RPN 27 Read program address  EXN 26 Exchange jump RPN 27 Read program address  EXN 26 Exchange jump RPN 27 Read program address  EXN 26 Exchange jump RPN 27 Read program address  EXN 26 Exchange jump RPN 27 Read program address  EXN 26 Exchange jump RPN 27 Read program address  EXN 26 Exchange jump RPN 27 Read program address  EXN 26 Exchange jump RPN 27 Read program address  EXN 26 Exchange jump RPD 27 Read program address  EXN 26 Exchange jump RPD 27 Read program address  EXN 26 Exchange jump RPD 27 Read program address  EXN 26 Exchange jump RPD 27 Read program address  EXN 26 Exchange jump RPD 27 Read program address  EXN 26 Exchange jump RPD 27 Read program address  EXN 26 Exchange jump RPD 27 Read program address  EXN 26 Exchange jump RPD 40 Load (d)  EJM 63 Input to m if channel d full  Jump to m if channel d full  Jump to m if channel d full  IND 33 Logical difference (d) IAN 70 Input to A from channel d  OAN 72 Output from A on channel d  OAN 73 Output (A) words from m on channel d  ACN 74 Activate channel d  DCN 75 Disconnect channel d  DCN 75 Disconnect channel d  DCN 74 Disconnect channel d  DCN 75 Disconnect channel d  DCN 74 Disconnect channel d  DCN 75 Disconnect channel d  DCN 74 Disconnect channel d  |  |                          | ' 1        |                                       |
| LCN 15 Load complement d ADN 16 Add d SBN 17 Subtract d  CRD 60 Central read from (A) to d CRM 61 Central read (d) words from (A) to m Central write to (A) from d Central write (d) words from (A) to m Central write to (A) from d Central write (d) words to (A) from m  PSN 24 Pass PSN 25 Pass  EXN 26 Exchange jump RPN 27 Read program address  EXN 26 Exchange jump RPN 27 Read program address  EXD 30 Load (d) ADD 31 Add (d) SBD 32 Subtract (d) ILDD 33 Logical difference (d) STD 34 Store (d) RAD 35 Replace add (d) ROD 36 Replace add one (d) SOD 37 Replace subtract one (d)  LDI 40 Load ((d)) ADI 41 Add ((d))  EXN 26 Replace subtract one (d) ADI 41 Add ((d))  FAN 76 Function (A) on channel d ACN 74 Activate channel d FAN 76 Function (A) on channel d   |  | . –                      |            |                                       |
| ADN 16 Add d SBN 17 Subtract d  CRD 60 Central read from (A) to d CRM 61 Central read (d) words from (A) to m Central write to (A) from d Central write (d) words to (A) from m  CRD 62 Central write to (A) from d Central write (d) words to (A) from m  CRD 63 Central write (d) words to (A) from m  CRD 64 Central write to (A) from d Central write (d) words to (A) from m  CRD 65 Central write to (A) from d Central write (d) words to (A) from m  CRD 66 Central read from (A) to d Central read from (A) to d Central read from (A) to d Central read from (A) to m Central write to (A) from d Central vrite to (A) words from (A) to m Central vrite to (A) from d Central vrite to (A) from d Central vrite to (A) words from (A) to m Central vrite to (A) words from (A) to m Central vrite to (A) words from (A) vords from (A) to m Central vrite to (A) words from channel d CN 74 Activate channel d CN 74 Activate channel d CN 74 Activ |  |                          | • 1        |                                       |
| SBN 17 Subtract d CRD 60 Central read from (A) to d CRM 61 Central read (d) words from (A) to m ADC 21 Add dm LPC 22 Logical product dm LPC 23 Logical difference dm  PSN 24 Pass PSN 25 Pass  EXN 26 Exchange jump RPN 27 Read program address  EXN 26 Exchange imp RPN 27 Read program address  EXD 30 Load (d) ADD 31 Add (d) SBD 32 Subtract (d) LIMD 33 Logical difference (d) STD 34 Store (d) RAD 35 Replace add (d) AOD 36 Replace add one (d) SOD 37 Replace subtract one (d) ADI 40 Load ((d)) ADI 41 Add ((d))  LDI 40 Load ((d)) ADI 41 Add ((d))  EXR 60 Central read from (A) to d CRM 61 Central read from (A) to m  From (A) to m CRM 61 Central read from (A) to m Central read from (A) to m CRM 61 Central read from (A) to m Central read from (A) to m CMD 62 Central read from (A) to m Central read from (A) to m CMD 62 Central read from (A) to m Central read from (A) to m CMD 62 Central vrite to (A) from d CWD 62 Central vrite to (A) from d CWD 62 Central vrite to (A) from d CWD 63 Central vrite to (A) from d CWD 62 Central vrite to (A) from d CWD 63 Central vrite to ( | :  |                          |            |                                       |
| LDC 20 Load dm ADC 21 Add dm LPC 22 Logical product dm LPC 23 Logical difference dm  PSN 24 Pass PSN 25 Pass  EXN 26 Exchange jump RPN 27 Read program address  LDD 30 Load (d) ADD 31 Add (d) SBD 32 Subtract (d) LIMD 33 Logical difference (d) STD 34 Store (d) RAD 35 Replace add (d) AOD 36 Replace add one (d) SOD 37 Replace subtract one (d) ADI 41 Add ((d))  LDC 20 Load dm CWM 62 Central read (d) words from (A) to m CWD 62 Central write to (A) from d CWM 63 Central write to (A) from d CWM 63 Central write to (A) from d CWM 63 Central vrite to (A) from d CWM 63 Central vrite to (A) from d CWM 63 Central vrite to (A) from d CWM 63 Central read (d) words from (A) to m CWM 63 Central read (d) words from (A) to m CMM 63 Central vrite to (A) from d CWM 63 Central vrite to (A) from d LDM 65 Logical difference dm  LDM 65 Logical difference in the subtract one (d) LDM 65 Logical difference in the subtract one (d) LDM 65 Logical difference in the subtract one (d) LDM 65 Logical difference in the subtract one (d) LDM 65 Logical difference in the subtract one (d) LDM 65 Logical difference in the subtract one (d) LDM 65 Logical difference in the subtract one (d) LDM 65 Logical difference in the subtract one (d) LDM 65 Logical difference in the subtract one (d) LDM 65 Logical difference in the subtract one (d) LDM 65 Logical difference in the subtract one (d) LDM 66 Logical difference in the subtract one (d) LDM 67 Logical difference in the subtract one (d) LDM 68 Central write to (A) from d LDM 69 Logical difference in the subtract one (d) LDM 69 Logical difference in the subtract one (d) LDM 60 Logical difference in the subtract one (d) LDM 60 Logical difference in the subtract one (d) LDM 61 Logical difference in the subtract one (d) LDM 61 Logical difference in the subtract one (d) LDM 61 Logical difference in the subtract one (d) LDM 61 Logical difference in the subtract one (d) LDM 61 Logical difference in the subtract one (d) LDM 61 Logical difference in the subtract one (d) LDM 62 Logical difference in the subtract one |  |                          | CRD 60     | Central read from (A) to d            |
| LDC 20 Add dm ADC 21 Add dm LPC 22 Logical product dm LMC 23 Logical difference dm  PSN 24 Pass PSN 25 Pass  EXN 26 RPN 27 Read program address  LDD 30 Add (d) SBD 32 Subtract (d) LMD 33 Logical difference (d) STD 34 Store (d) RAD 35 Replace add (d) AOD 36 Replace subtract one (d) ADI 41 ADI 41 Add ((d)) ADI 40 ADI 41 Add ((d)) ADI 41 Add ((d)) ADI 41 Add ((d)) ADI 40 ADI 41 Add ((d)) ADI 40 ACN 74 Activate channel d ACN 74 Activate channel d ACN 76 ADI 40 ACN 76 ACN 76 ACN 76 ACN 77 ACTIVATE TO (A) From A on channel d ACN 77 Activate channel d FAN 76 FUNCTION (A) on channel d  | JUN 17   | baberace a               | l .        |                                       |
| ADC 21 Add dm LPC 22 Logical product dm LMC 23 Logical difference dm  PSN 24 Pass PSN 25 Pass  EXN 26 Exchange jump RPN 27 Read program address  LDD 30 Load (d) ADD 31 Add (d) SBD 32 Subtract (d) LIMD 33 Logical difference (d) STD 34 Store (d) RAD 35 Replace add (d) AOD 36 Replace add one (d) SOD 37 Replace subtract one (d) ADI 41 Add ((d)) ADI 41 Add ((d))  LDI 40 Load ((d)) ADI 41 Add ((d))  ADI 41 Add ((d))  ADI 41 Add ((d))  ADI 41 Add ((d))  CWD 62 Central write to (A) from d CWM 63 Central write to (A) from d CMM 63 Central write (d) words to (A) from m  LAIM 65 Jump to m if channel d full Jump to m if channel d | I DC 20  | ' Load dm                |            |                                       |
| LPC 22 Logical product dm LMC 23 Logical difference dm  PSN 24 Pass PSN 25 Pass  EXN 26 Exchange jump RPN 27 Read program address  LDD 30 Load (d) SBD 32 Subtract (d) LMD 33 Logical difference (d) STD 34 Store (d) RAD 35 Replace add (d) AOD 36 Replace add (d) AOD 37 Replace subtract one (d) LDI 40 Load ((d)) ADI 41 Add ((d))  LMC 23 Logical difference dm  CWM 63 Central write (d) words to (A) from m  AJM 64 Jump to m if channel d active  Jump to m if channel d full  EJM 67 Jump to m if channel d empty  IAN 70 Input to A from channel d Input (A) words to m from channel d Output (A) words from m on channel d ACN 74 Activate channel d ACN 74 Activate channel d DCN 75 Disconnect channel d ACN 76 Function (A) on channel d   |  |                          | CWD 62     |                                       |
| LMC 23 Logical difference dm to (A) from m  PSN 24 Pass  | •  | :                        | ! 1        | Central write (d) words               |
| PSN 24 Pass Pass  EXN 26 Exchange jump RPN 27 Read program address  LDD 30 Load (d) SBD 32 Subtract (d) LMD 33 Logical difference (d) STD 34 Store (d) RAD 35 Replace add (d) RAD 36 Replace subtract one (d) LDI 40 Load ((d)) LDI 40 Load ((d)) LDI 40 Load ((d)) ADI 41 Add ((d))  ADI 41 Add ((d))  PSN 24 Pass  AJM 64 Jump to m if channel d active  Jump to m if channel d full  EJM 67 Jump to m if channel d empty  IAN 70 Input to A from channel d IAM 71 Input (A) words to m from channel d OAN 72 Output from A on channel d OAN 73 Output (A) words from m on channel d ACN 74 Activate channel d DCN 75 Disconnect channel d FAN 76 Function (A) on channel d  | the state of the s | •                        |            |                                       |
| PSN 25 Pass channel d active  IJM 65 Jump to m if  channel d inactive  FJM 66 Jump to m if  channel d full  LDD 30 Load (d)  ADD 31 Add (d)  SBD 32 Subtract (d)  IMD 33 Logical difference (d)  STD 34 Store (d)  RAD 35 Replace add (d)  RAD 36 Replace add one (d)  SOD 37 Replace subtract one (d)  LDI 40 Load ((d))  ADI 41 Add ((d))  Channel d active  IJM 65 Jump to m if  channel d full  EJM 67 Jump to m if  channel d empty  IAN 70 Input to A from channel d  Input (A) words to m  from channel d  OAN 72 Output from A on channel d  OAN 73 Output (A) words from m on  channel d  ACN 74 Activate channel d  DCN 75 Disconnect channel d  FAN 76 Function (A) on channel d  | LINC 23  | Logical difference dim   |            |                                       |
| PSN 25 Pass channel d active  IJM 65 Jump to m if  channel d inactive  FJM 66 Jump to m if  channel d full  LDD 30 Load (d)  ADD 31 Add (d)  SBD 32 Subtract (d)  IMD 33 Logical difference (d)  STD 34 Store (d)  RAD 35 Replace add (d)  RAD 36 Replace add one (d)  SOD 37 Replace subtract one (d)  LDI 40 Load ((d))  ADI 41 Add ((d))  Channel d active  IJM 65 Jump to m if  channel d full  EJM 67 Jump to m if  channel d empty  IAN 70 Input to A from channel d  Input (A) words to m  from channel d  OAN 72 Output from A on channel d  OAN 73 Output (A) words from m on  channel d  ACN 74 Activate channel d  DCN 75 Disconnect channel d  FAN 76 Function (A) on channel d  | DCN 24   | Pagg                     | AJM 64     | Jump to m if                          |
| EXN 26 Exchange jump  RPN 27 Read program address  EXN 26 Exchange jump  RPN 27 Read program address  EXN 26 Exchange jump  RPN 27 Read program address  EXN 26 Exchange jump  RPN 27 Read program address  EXN 26 Exchange jump  FJM 66 Jump to m if  channel d full  EJM 67 Jump to m if  channel d empty  IAN 70 Input to A from channel d  IAM 71 Input (A) words to m  from channel d  OAN 72 Output from A on channel d  OAN 73 Output (A) words from m on  channel d  ACN 74 Activate channel d  ACN 74 Activate channel d  ACN 74 Activate channel d  FAN 76 Function (A) on channel d   | 1  | _                        |            | •                                     |
| EXN 26 Exchange jump Read program address  EXN 27 Read program address  EXN 27 Read program address  EXN 28 FJM 66 Jump to m if channel d full  LDD 30 Load (d)  EJM 67 Jump to m if channel d empty  IAN 70 Input to A from channel d  IAM 71 Input (A) words to m  IAM 71 Input (A) words to m  IAM 72 Output from A on channel d  IAM 73 Output from A on channel d  IAM 74 Output (A) words from m on channel d  IAM 75 Output (A) words from m on channel d  IAM 76 Function (A) on channel d  IAM 77 Output (A) words from m on channel d  IAM 78 Output (A) words from m on channel d  IAM 79 Output (A) words from m on channel d  IAM 70 Input (A) words from m on channel d  IAM 70 Input (A) words from m on channel d  IAM 70 Input (A) words from m on channel d  IAM 70 Input (A) words from m on channel d  IAM 70 Input (A) words to m  IAM 70 Input (A) words from m on channel d  IAM 70 Input (A) words from m on channel d  IAM 70 Input (A) words from m on channel d  IAM 70 Input (A) words from m on channel d  IAM 70 Input (A) words from m on channel d  IAM 70 Input (A) words from m on channel d  IAM 70 Input (A) words from m on channel d  IAM 70 Input (A) words from m on channel d  IAM 70 Input (A) words from m on channel d  IAM 70 Input (A) words from m on channel d  IAM 70 Input (A) words from m on channel d  IAM 70 Input (A) words from m on channel d  IAM 70 Input (A) words from m on channel d  IAM 70 Input (A) words from m on channel d  IAM 70 Input (A) words from m on channel d  IAM 70 Input (A) words from m on channel d  IAM 71 Input (A) words from m on channel d  IAM 71 Input (A) words from m on channel d  IAM 71 Input (A) words from m on channel d  IAM 71 Input (A) words from m on channel d  IAM 71 Input (A) words from m on channel d  IAM 71 Input (A) words from m on channel d  IAM 71 Input (A) words from m on channel d  IAM 71 Input (A) words from m on channel d  IAM 71 Input (A) words from m on channel d  IAM 71 Input (A) words from m on channel d  IAM 71 Input (A) words from m on channel d  IAM 71 Input (A) words from m on c | F3N 23   | lass                     | T.TM 65    | Jump to m if                          |
| RPN 27 Read program address  FJM 66 Jump to m if channel d full  LDD 30 Load (d)  ADD 31 Add (d)  SBD 32 Subtract (d)  LMD 33 Logical difference (d)  STD 34 Store (d)  RAD 35 Replace add (d)  AOD 36 Replace add one (d)  SOD 37 Replace subtract one (d)  LDI 40 Load ((d))  ADI 41 Add ((d))  RPJM 66 Jump to m if channel d full  EJM 67 Jump to m if channel d full  LDM 70 Input to A from channel d input (A) words to m from channel d OAM 71 Output from A on channel d OAM 73 Output (A) words from m on channel d Activate channel d DCN 75 Disconnect channel d Function (A) on channel d Function (A) on channel d   | EVN 26   | Evchange iumn            |            | -                                     |
| channel d full  LDD 30 Load (d)  ADD 31 Add (d)  SBD 32 Subtract (d)  LMD 33 Logical difference (d)  STD 34 Store (d)  RAD 35 Replace add (d)  AOD 36 Replace add one (d)  SOD 37 Replace subtract one (d)  LDI 40 Load ((d))  ADI 41 Add ((d))  Channel d full  EJM 67 Jump to m if  channel d empty  IAN 70 Input to A from channel d  Input (A) words to m  from channel d  OAN 72 Output from A on channel d  OAM 73 Output (A) words from m on  channel d  ACN 74 Activate channel d  DCN 75 Disconnect channel d  FAN 76 Function (A) on channel d   | 1  |                          | F.TM 66    | Jump to m if                          |
| LDD 30 Load (d) ADD 31 Add (d) SBD 32 Subtract (d) LMD 33 Logical difference (d) STD 34 Store (d) RAD 35 Replace add (d) AOD 36 Replace add one (d) SOD 37 Replace subtract one (d)  LDI 40 Load ((d)) ADI 41 Add ((d))  EJM 67 Jump to m if channel d empty IAN 70 Input to A from channel d IAM 71 Input (A) words to m from channel d OAN 72 Output from A on channel d OAM 73 Output (A) words from m on channel d ACN 74 Activate channel d DCN 75 Disconnect channel d FAN 76 Function (A) on channel d  | RPN 27   | Read program address     |            |                                       |
| ADD 31 Add (d) SBD 32 Subtract (d) IMD 33 Logical difference (d) STD 34 Store (d) RAD 35 Replace add (d) AOD 36 Replace add one (d) SOD 37 Replace subtract one (d)  LDI 40 Load ((d)) ADI 41 Add ((d))  ADI 41 Add ((d))  Add (d)  IAN 70 Input to A from channel d Input (A) words to m from channel d OAN 72 Output from A on channel d OAM 73 Output (A) words from m on channel d ACN 74 Activate channel d DCN 75 Disconnect channel d FAN 76 Function (A) on channel d  | T DD 20  | Lond (d)                 | E.TM 67    |                                       |
| SBD 32 Subtract (d)  IMD 33 Logical difference (d)  STD 34 Store (d)  RAD 35 Replace add (d)  AOD 36 Replace add one (d)  SOD 37 Replace subtract one (d)  LDI 40 Load ((d))  ADI 41 Add ((d))  SUB 32 Subtract (d)  IAN 70 Input to A from channel d  IAM 71 Input (A) words to m  from channel d  OAN 72 Output from A on channel d  OAM 73 Output (A) words from m on channel d  ACN 74 Activate channel d  DCN 75 Disconnect channel d  FAN 76 Function (A) on channel d   |  |                          |            |                                       |
| LMD 33 Logical difference (d) STD 34 Store (d) RAD 35 Replace add (d) AOD 36 Replace add one (d) SOD 37 Replace subtract one (d) LDI 40 Load ((d)) ADI 41 Add ((d))  LOGICAL COMPANS AND ADI 41 Add ((d))  LOGICAL COMPANS AND ADI 41 Logical difference (d)  LOGICAL COMPANS AND ADI 41 Input (A) words to m from channel d OAN 72 Output from A on channel d OAM 73 Output (A) words from m on channel d ACN 74 Activate channel d DCN 75 Disconnect channel d FAN 76 Function (A) on channel d  | 3  |                          | TAN 70     |                                       |
| STD 34  RAD 35  Replace add (d)  AOD 36  Replace add one (d)  SOD 37  Replace subtract one (d)  LDI 40  ADI 41  Add ((d))  STD 34  Store (d)  ACN 72  Output from A on channel d  OAM 73  Output (A) words from m on  channel d  ACN 74  Activate channel d  DCN 75  Disconnect channel d  FAN 76  Function (A) on channel d   | į.   |                          | 1 }        | ,                                     |
| RAD 35 Replace add (d) AOD 36 Replace add one (d) SOD 37 Replace subtract one (d)  LDI 40 ADI 41 Add ((d))  RAD 35 Replace add (d) ACN 72 Output from A on channel d OAM 73 Output (A) words from m on channel d ACN 74 Activate channel d DCN 75 Disconnect channel d FAN 76 Function (A) on channel d  | 1  |                          |            |                                       |
| AOD 36 Replace add one (d) SOD 37 Replace subtract one (d)  LDI 40 ADI 41 Add ((d))  Replace add one (d) OAM 73 Output (A) words from m on channel d ACN 74 Activate channel d DCN 75 Disconnect channel d FAN 76 Function (A) on channel d  |  |                          | OAN 72     | i e                                   |
| SOD 37 Replace subtract one (d)  LDI 40 ADI 41 Add ((d))  Replace subtract one (d)  ACN 74 Activate channel d DCN 75 Disconnect channel d FAN 76 Function (A) on channel d   |  | 1 -                      |            |                                       |
| LDI 40 Load ((d)) ADI 41 Add ((d))  Replace Subtlact one (a)  ACN 74 Activate channel d DCN 75 Disconnect channel d FAN 76 Function (A) on channel d   |  |                          | 0.11.75    |                                       |
| LDI 40 Load ((d))  ADI 41 Add ((d))  DCN 75 Disconnect channel d  FAN 76 Function (A) on channel d   | SOD 37   | keptace subtract one (d) | ACN 74     |                                       |
| ADI 41 Add ((d)) FAN 76 Function (A) on channel d  | TDT /0   | Lord ((d))               | 1 1        | ·                                     |
| ADI 41 Add ((d))   |  |                          | !          | · ·                                   |
| SBI 42 Subtract ((d))  |  |                          |            |                                       |
|  | SBT 42   | Subtract ((u))           | THO        |                                       |

# TABLE 1. (cont.)

# Pseudo Ops

IDT = Name of program.

ØRG = Beginning location of assembled program. Will be set to 1000
if no ØRG specified.

SBL = Set beginning address of binary punch out.

BLR = Block locations reserve.

REM = Remarks card. \* in Column 1 is also a remarks card.

EQU = Equates a tag to a numeric constant or a pre-defined location tag.

DIS = Converts the remaining characters to display code and packs them 2 to a word. Word following packed display code is all zeros.

END = Defines the last card of the deck to be assembled.

# PERIPH PERIPHERAL ASSEMBLY LANGUAGE

PERIPH is identical to PAS except that it is run by the CPU rather than a PPU.

# Card Image Records' - Second File Library Tape

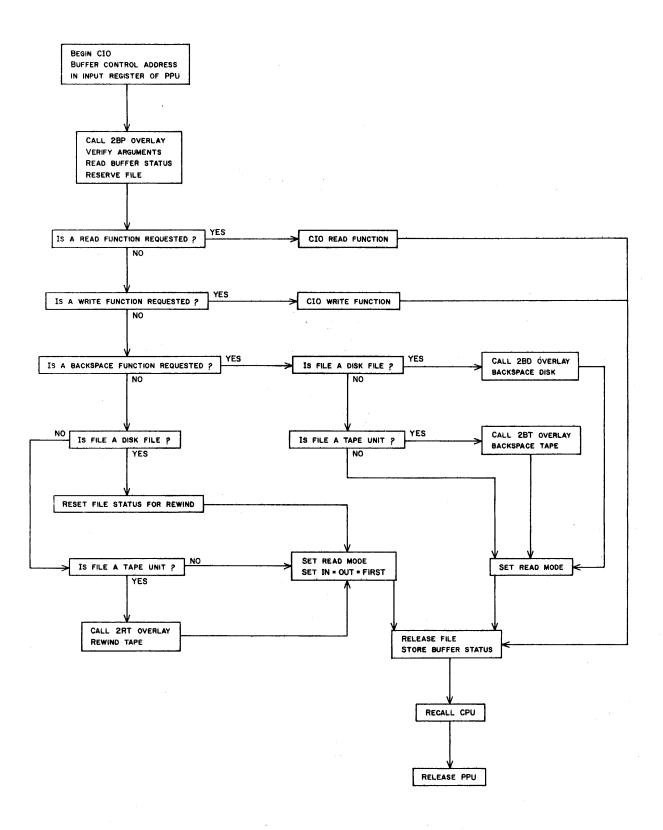
The following routines are listed in card image form in the second file on the library tape. Each routine is entered as one record.

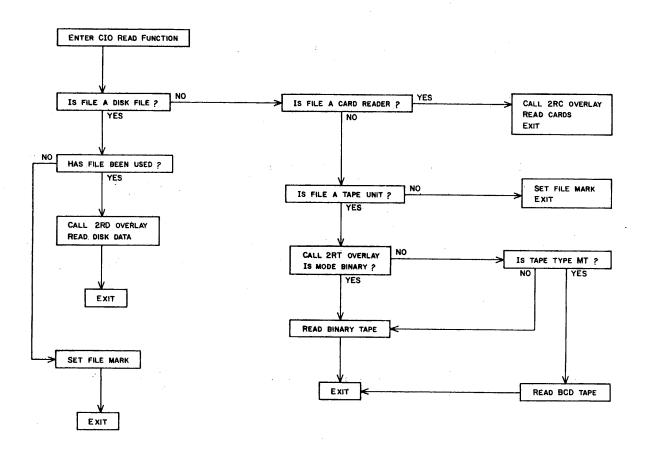
| (00) | Loader | (34)                 | PBC           | ( | (68) | TIME       |
|------|--------|----------------------|---------------|---|------|------------|
| (01) | DSD    | (35)                 | BKSP          |   | (69) | COPYSBF    |
| (02) | MTR    | (36)                 | COPY          |   | (70) | RANF       |
| (03) | CR     | (37)                 | COPYBF        |   | (71) | -          |
| (04) | 1AJ    | (38)                 | COPYBR        |   | (72) | -          |
| (05) | 1BJ    | (39)                 | COPYCF        |   | (73) | _          |
| (06) | 1DJ    | (40)                 | COPYCR        |   | 74)  | . <b>-</b> |
| (07) | 1LJ    | (41)                 | REWIND        |   | (75) | -          |
| (80) | 1LT    | (42)                 | VERIFY        | ( | (76) | _          |
| (09) | 1TD    | (43)                 | APRAB         | ( | (77) | XLOCF      |
| (10) | 2BD    | (44)                 | ALOG          |   | 78)  | INPUTB     |
| (11) | 2BP    | (45)                 | ALOG10        |   | 79)  | INPUTC     |
| (12) | 2BT    | (46)                 | ATAN          |   | 80)  | OUTPTB     |
| (13) | 2DF    | (47)                 | cos           |   | 81)  | OUTPTC     |
| (14) | 2DT    | (48)                 | DVCHK         |   | 82)  | ENDFIL     |
| (15) | 2EF    | (49)                 | END           |   | 83)  | REWINM     |
| (16) | 2LP    | (50)                 | EXIT          |   | 84)  | BACKSP     |
| (17) | 2PC    | (51)                 | EXP           |   | 85)  | DISPLA     |
| (18) | 2RC    | <b>(52)</b>          | <b>IBAIEX</b> |   | 86)  | 007        |
| (19) | 2 RD   | (53)                 | IFENDF        |   | 87)  | RUN        |
| (20) | 2RT    | <b>(</b> 54 <b>)</b> | OVERFL        | • | •    |            |
| (21) | 2SD    | (55)                 | PAUSE         |   |      |            |
| (22) | 2TJ    | (56)                 | RBAIEX        |   |      |            |
| (23) | 2TS    | (57)                 | RBAREX        |   |      |            |
| (24) | 2WD    | (58)                 | REMARK        |   |      |            |
| (25) | 2WT    | (59)                 | SIN           |   |      |            |
| (26) | CIO    | (60)                 | SQRT          |   |      |            |
| (27) | CLL    | (61)                 | SLITE         |   |      |            |
| (28) | DIS    | (62)                 | SLITET        |   |      |            |
| (29) | DMP    | (63)                 | SSWTCH        |   |      |            |
| (30) | EXU    | (64)                 | START         |   |      |            |
| (31) | LBC    | (65)                 | STOP          |   |      |            |
| (32) | LOC    | (66)                 | TAN           |   |      |            |
| (33) | MSG    | (67)                 | TANH          |   |      |            |
|      |        |                      |               |   |      |            |

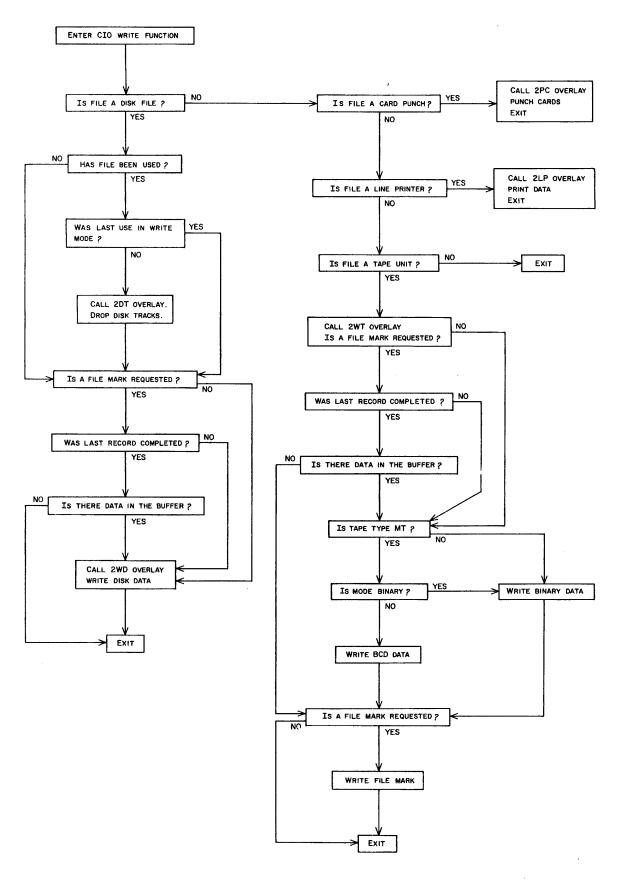
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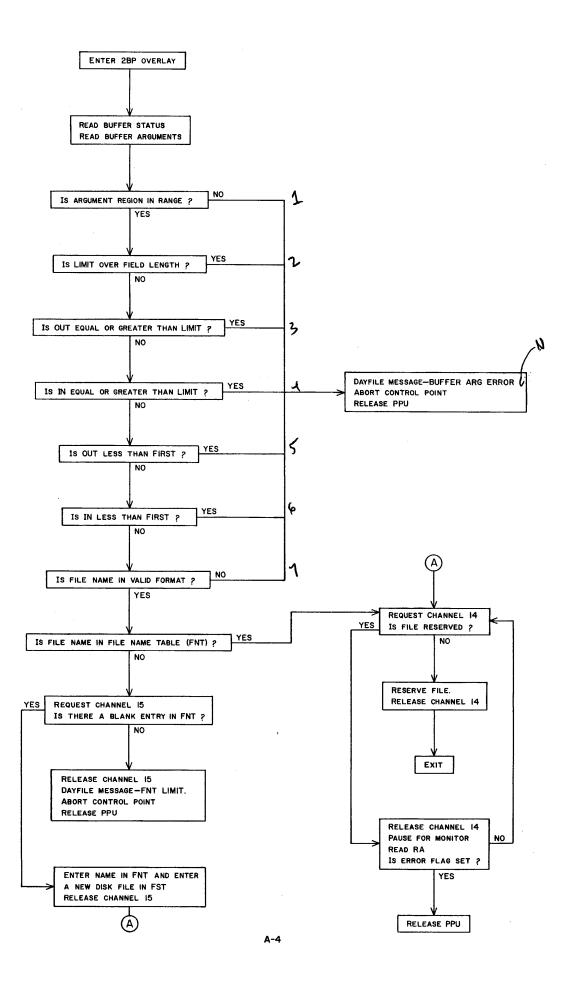
# APPENDIX A

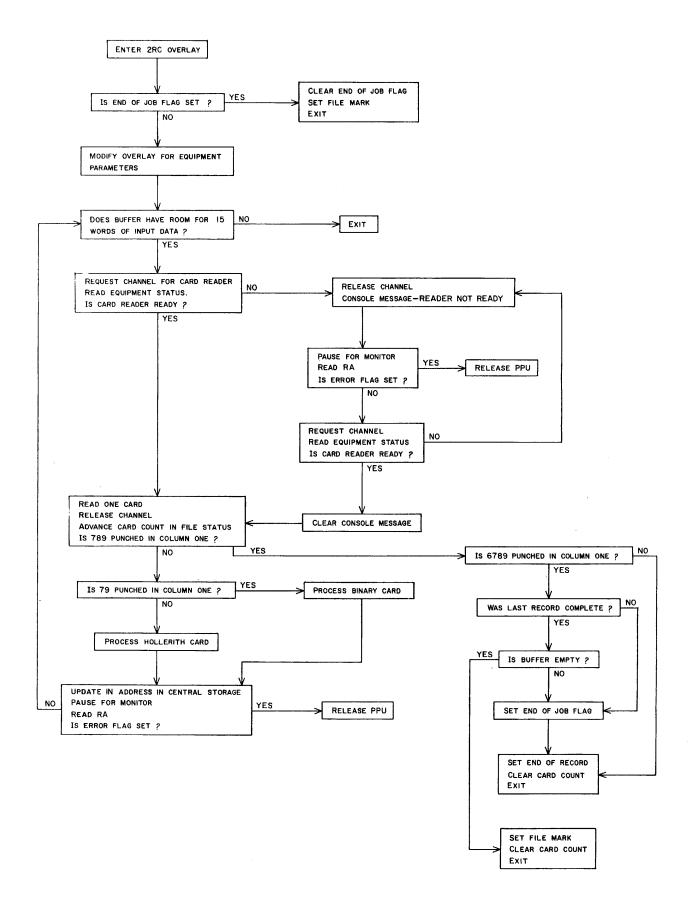
CHIPPEWA OPERATING SYSTEM FLOW CHARTS

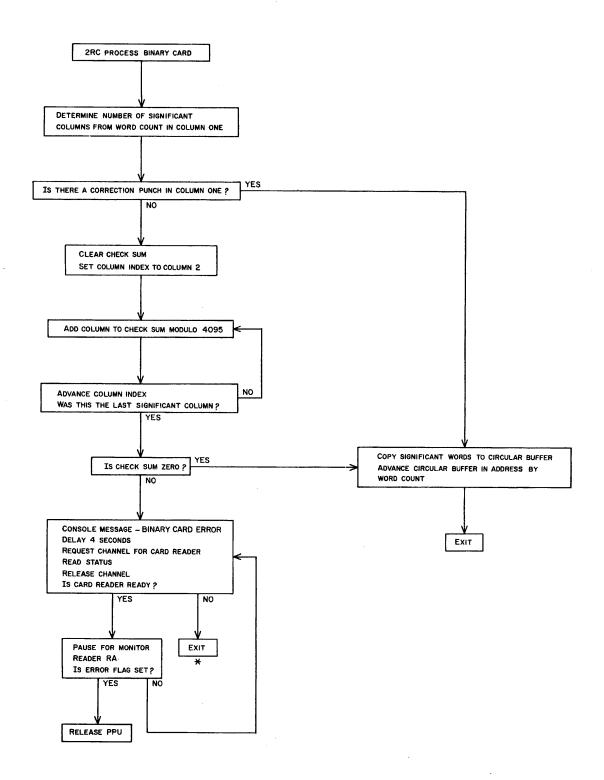




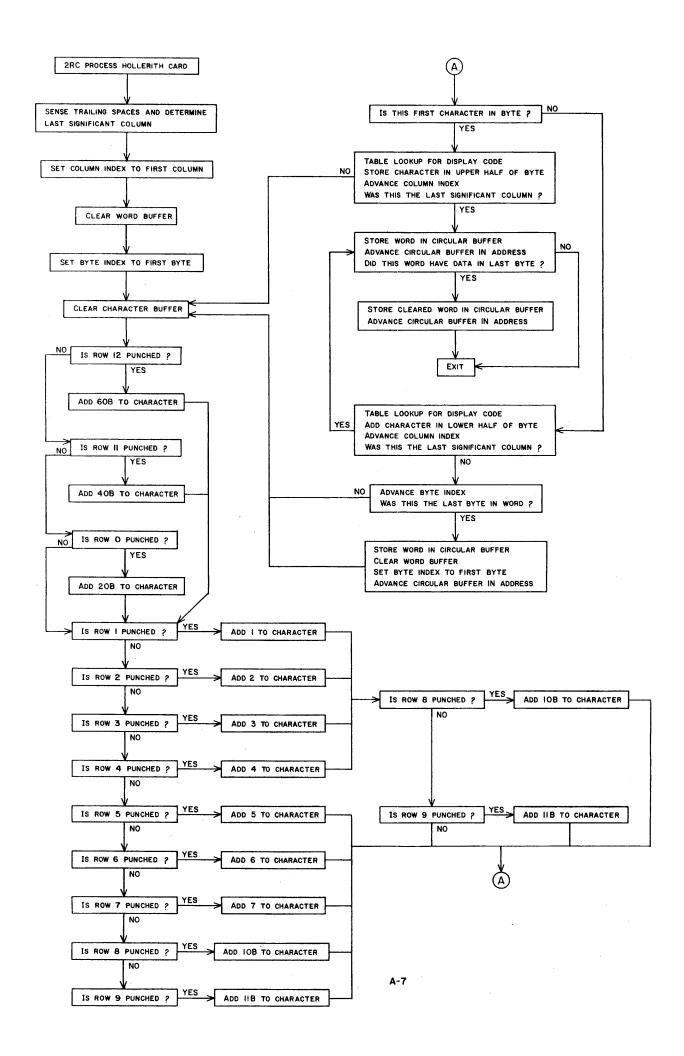


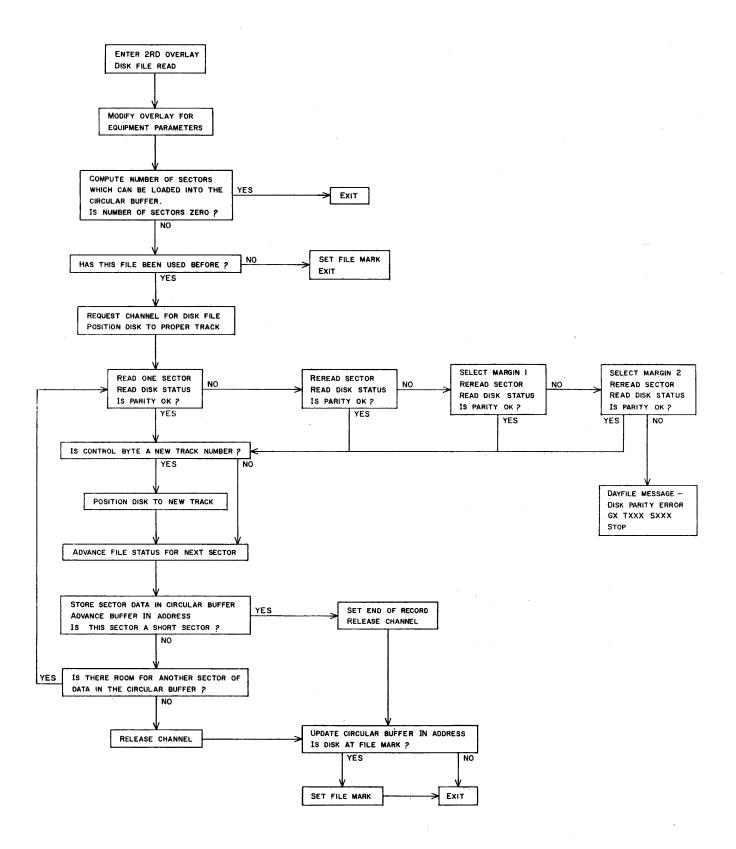


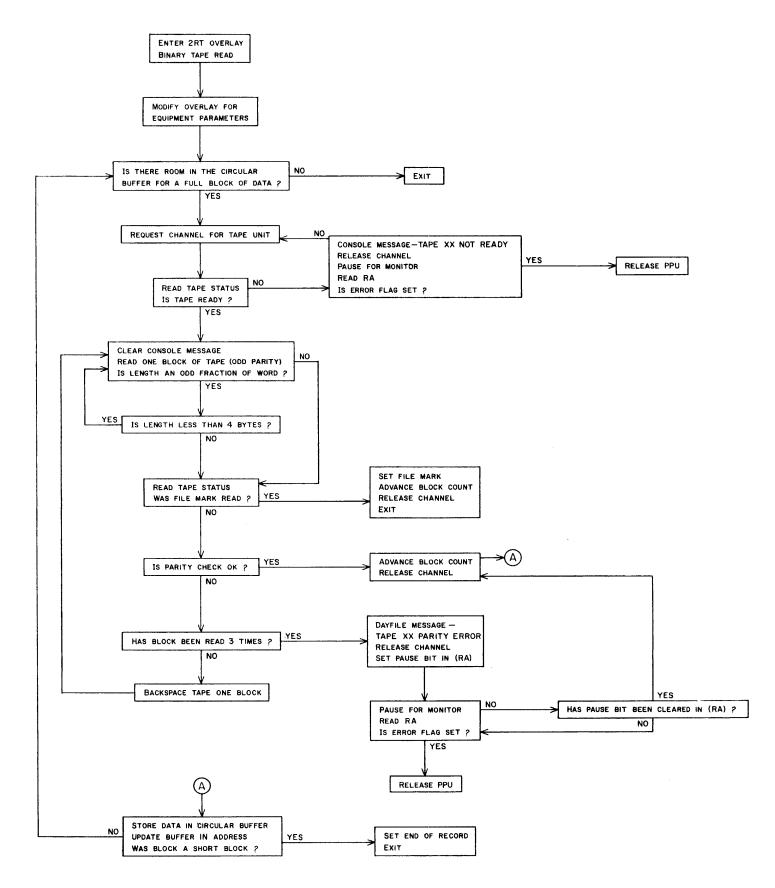


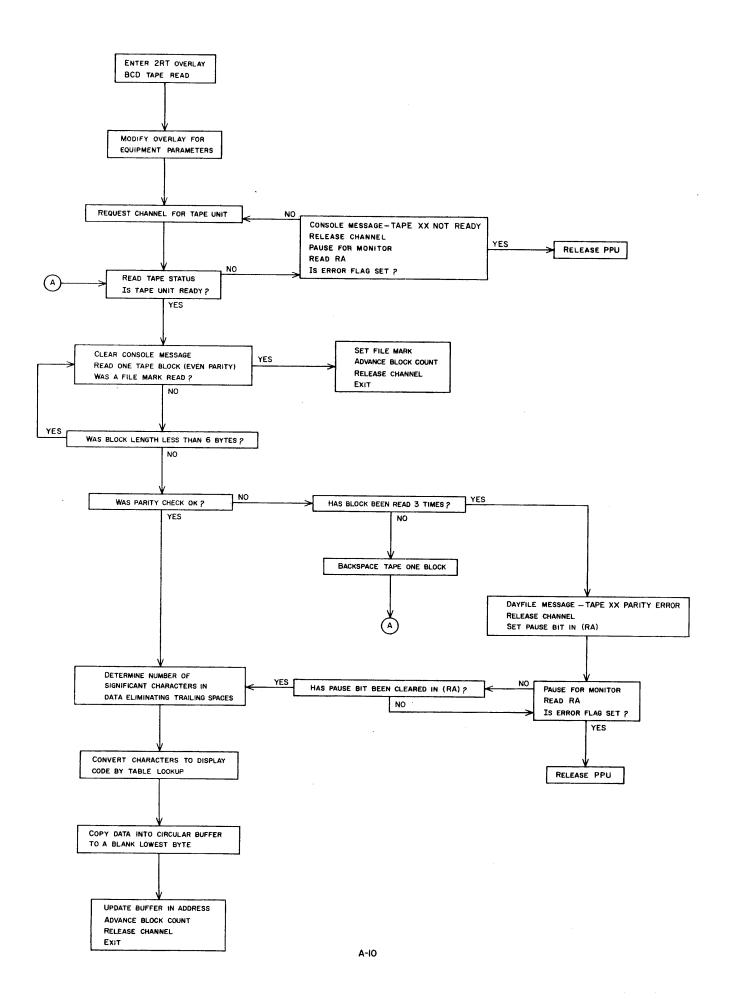


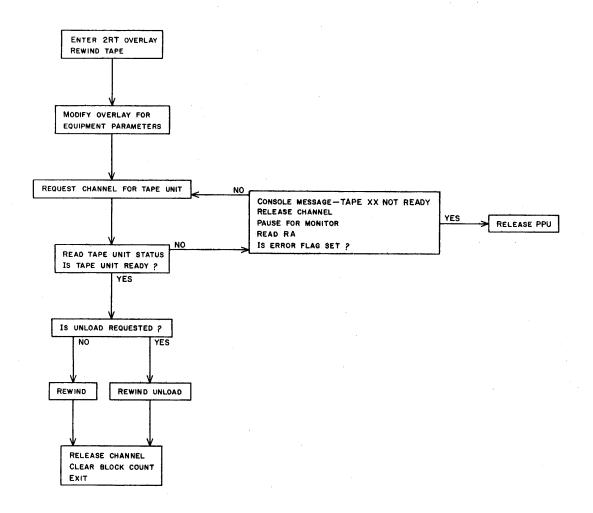
\* THIS PATH PROVIDES AN OPPORTUNITY FOR THE OPERATOR TO REREAD THE FAULTY CARD

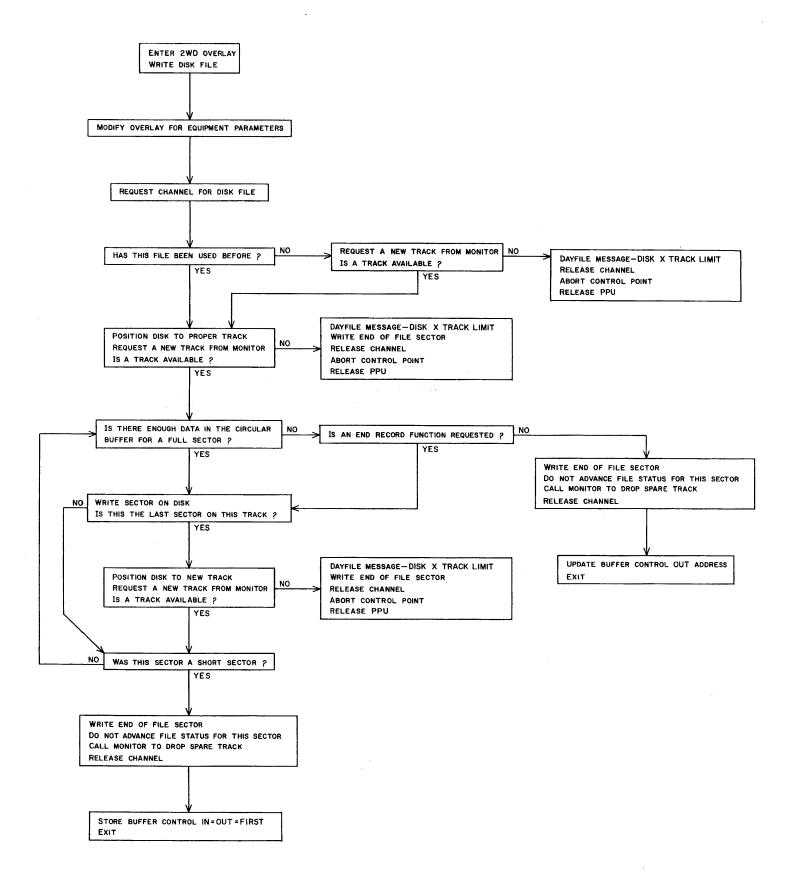


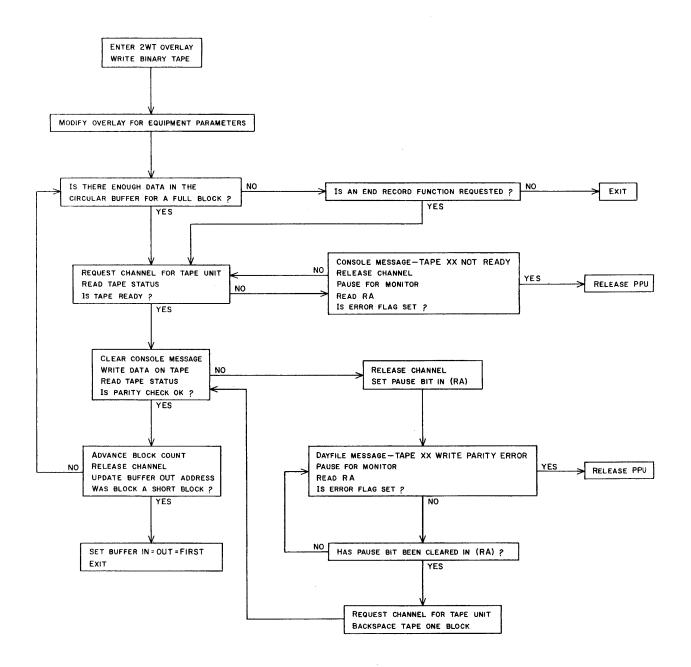


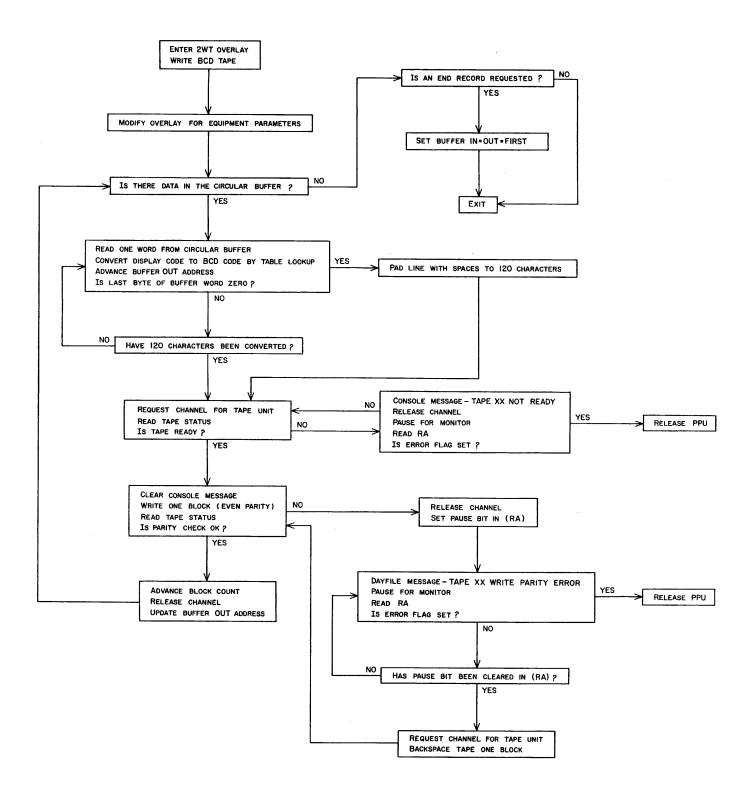


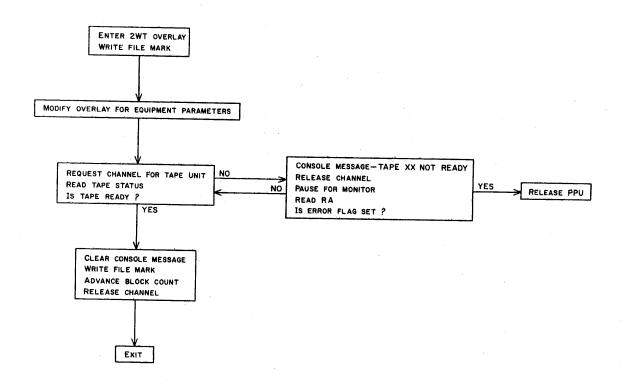


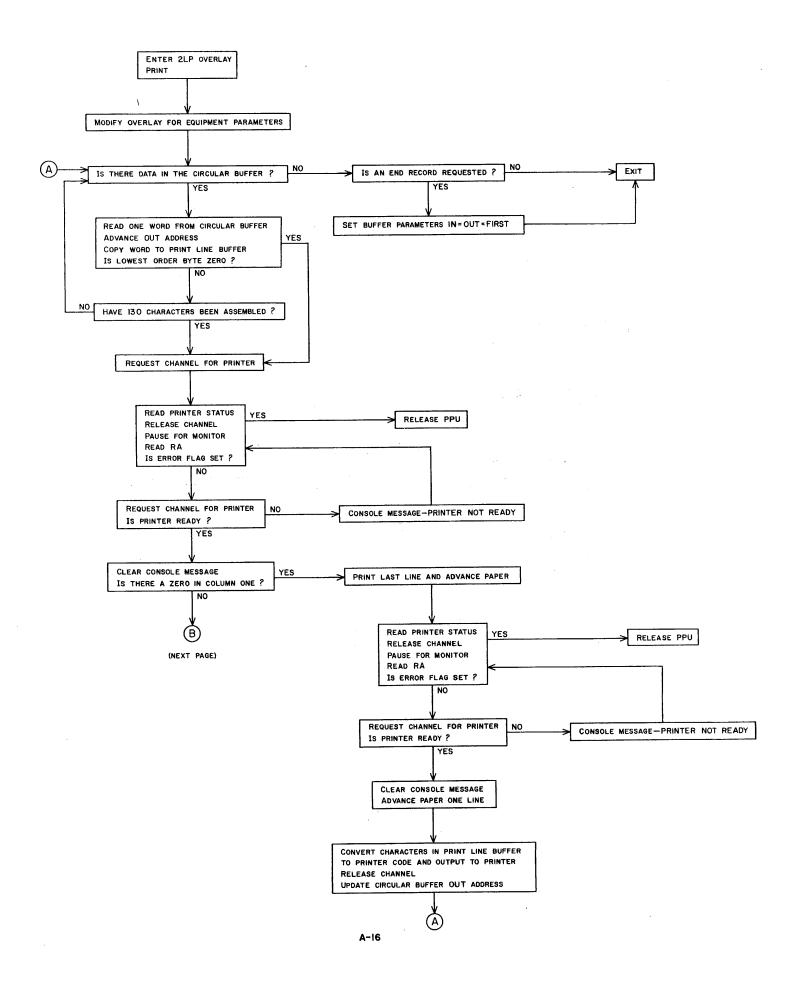


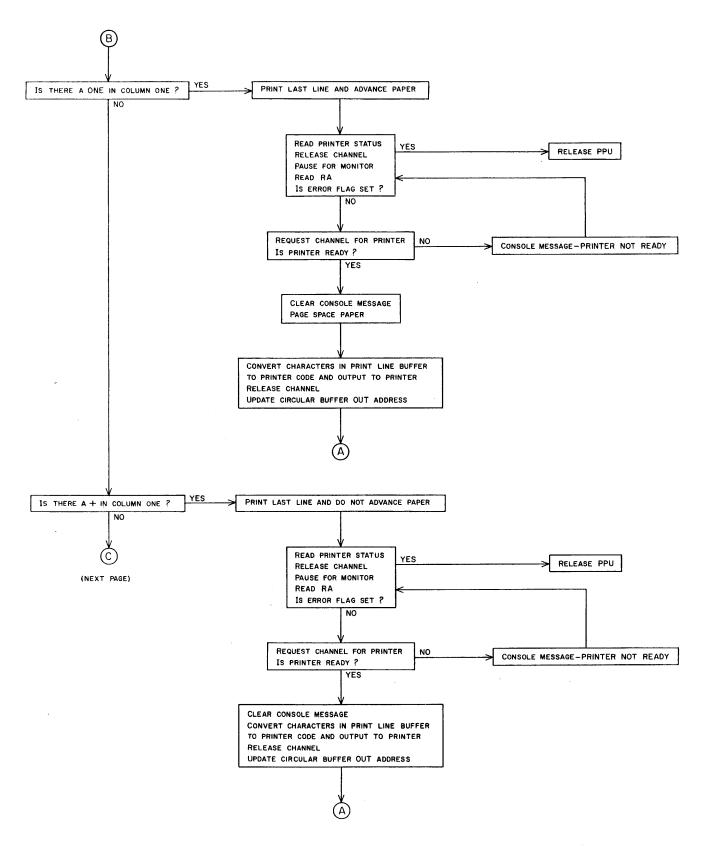




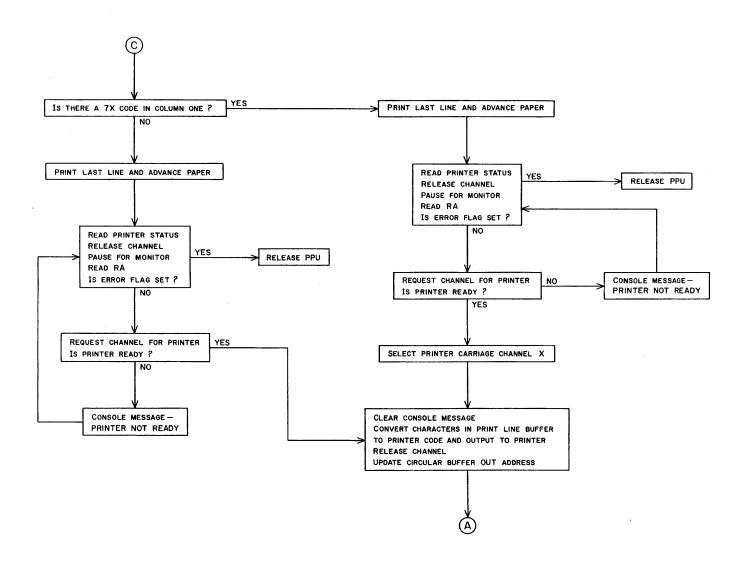


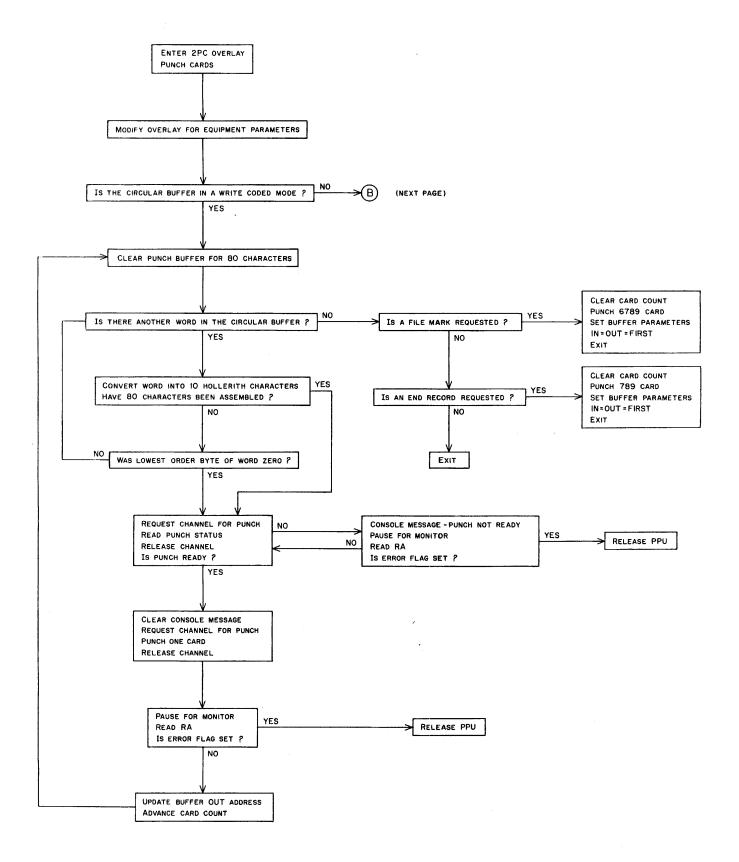


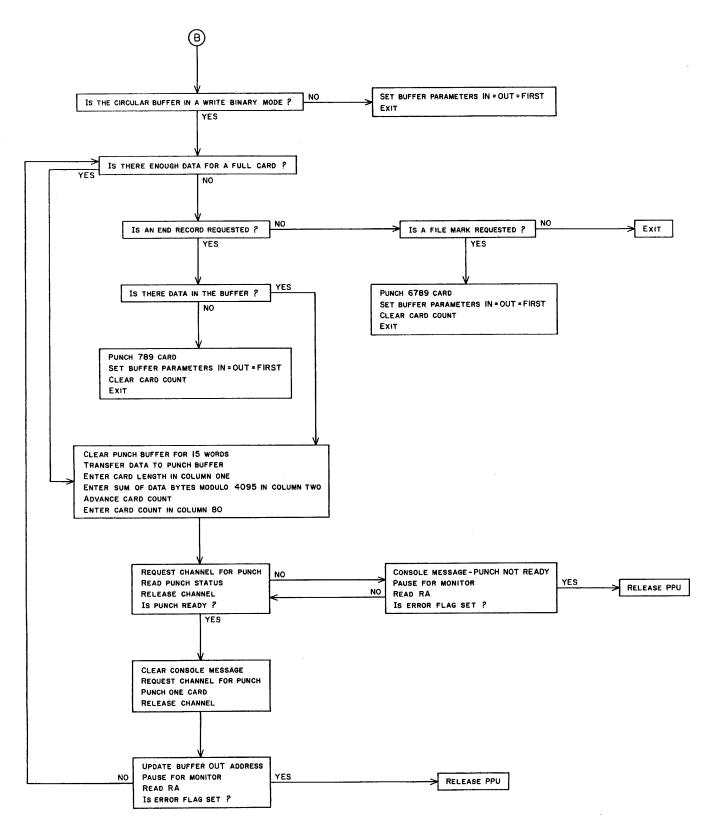


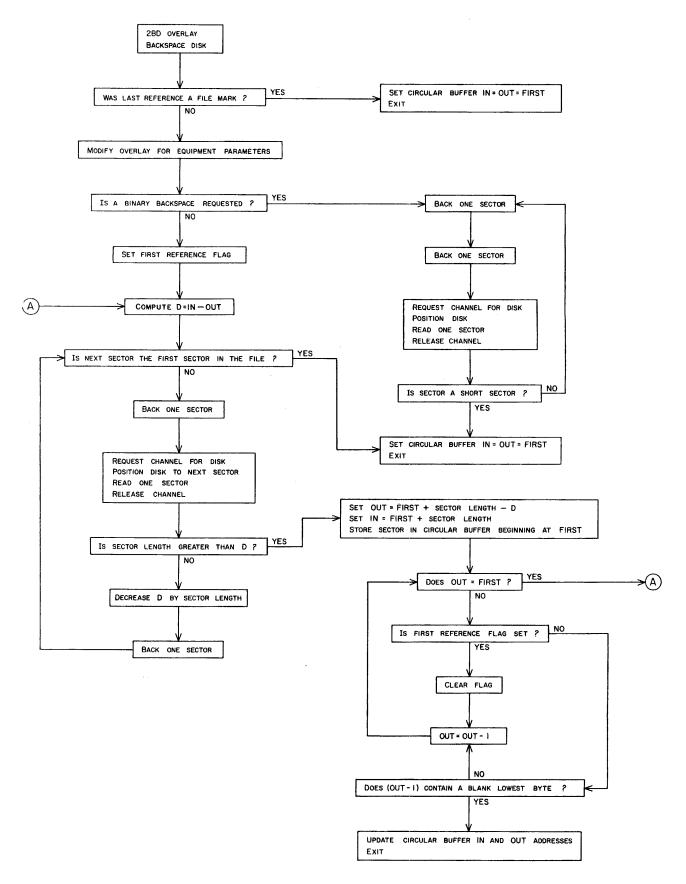


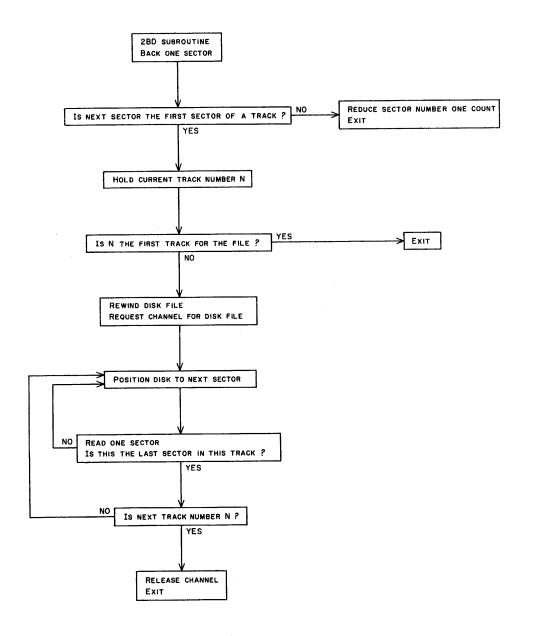
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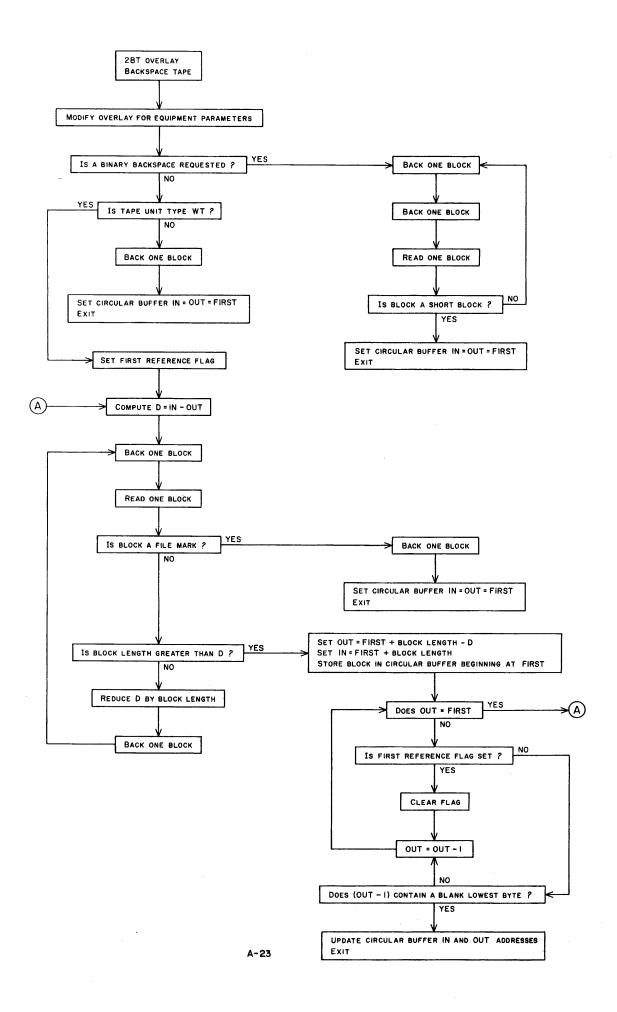


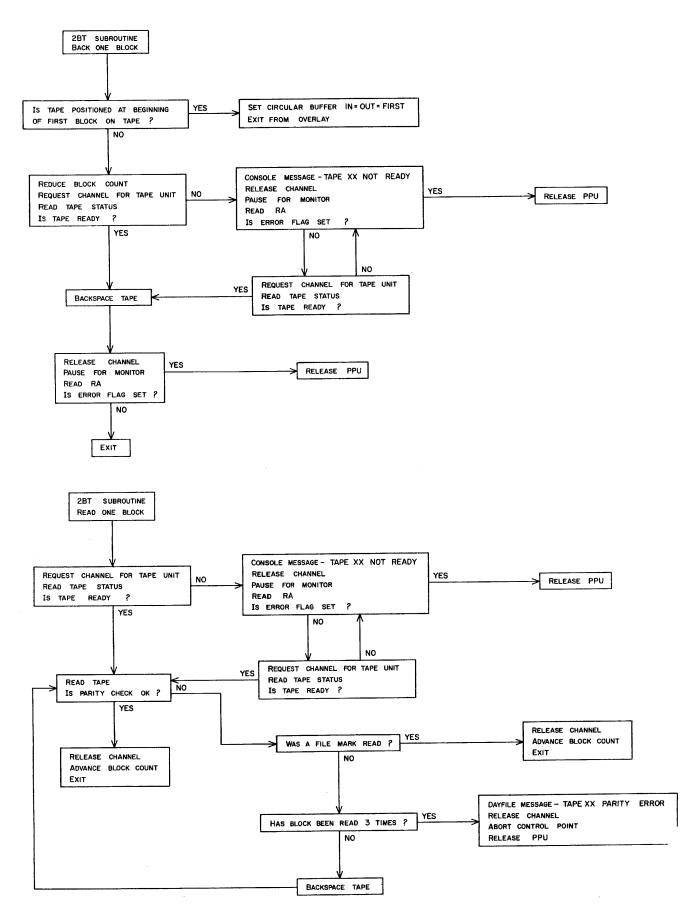


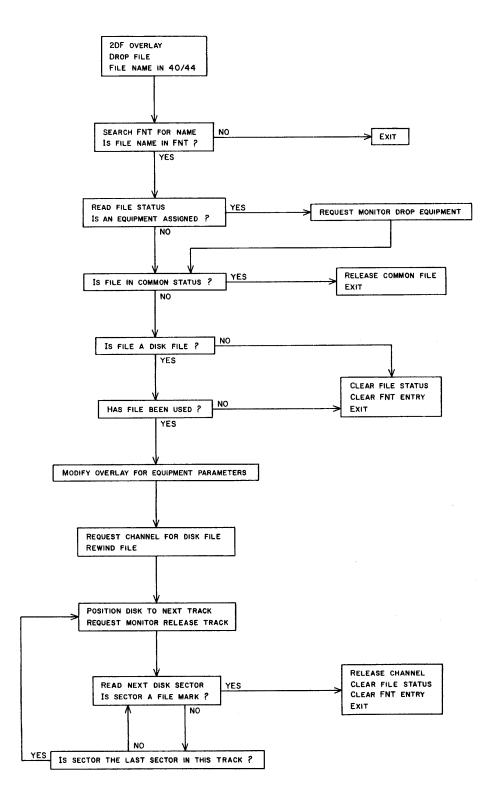


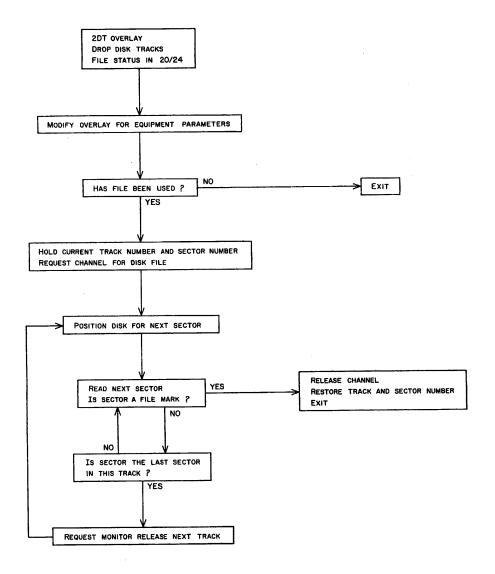


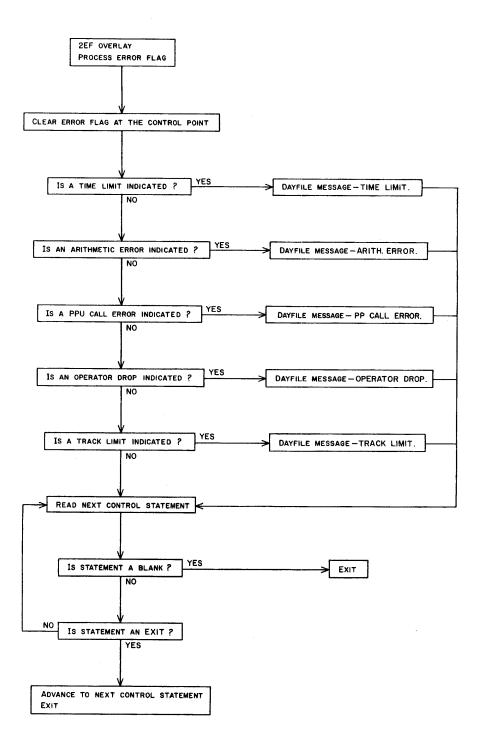


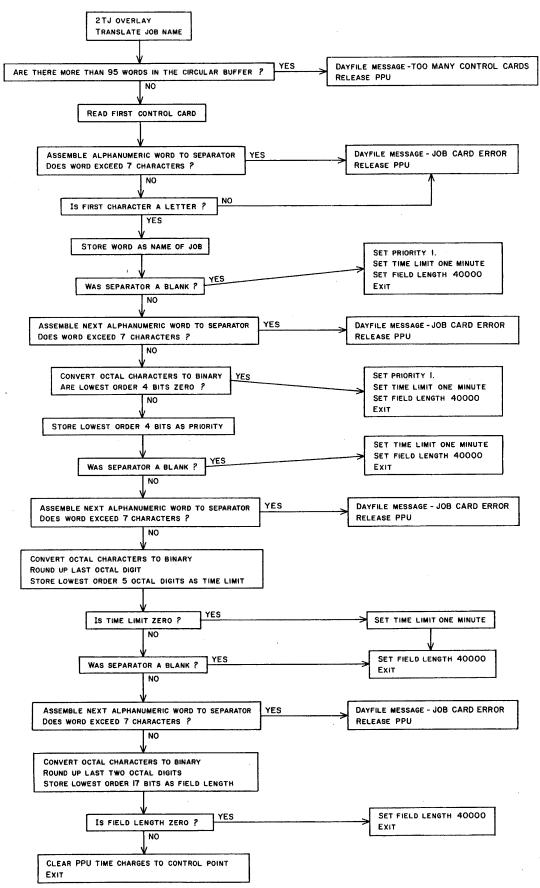


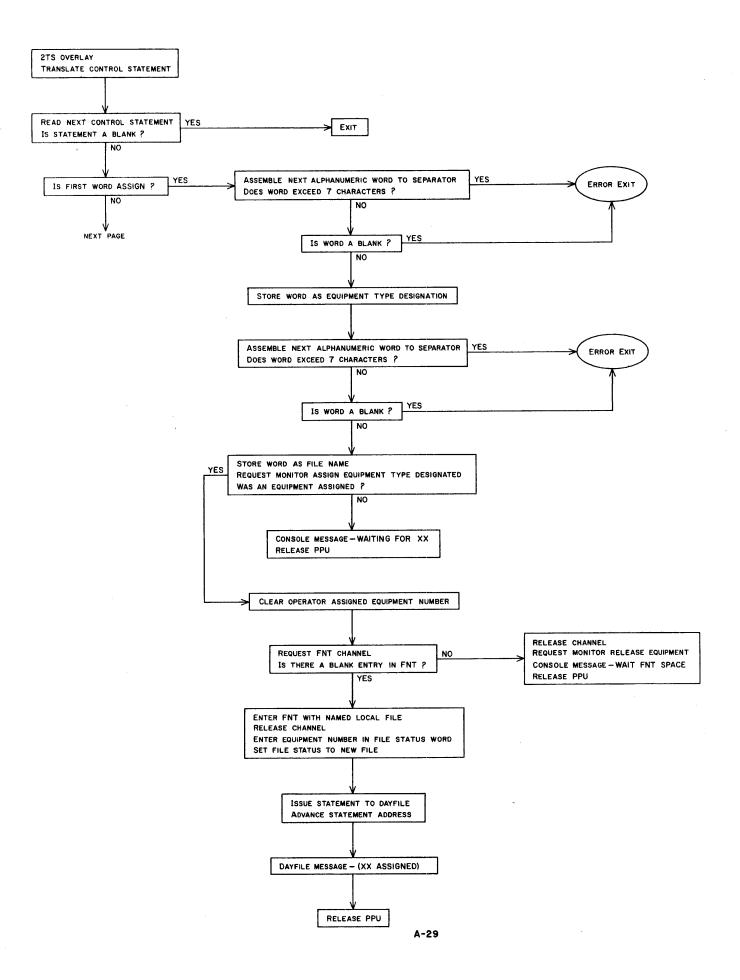


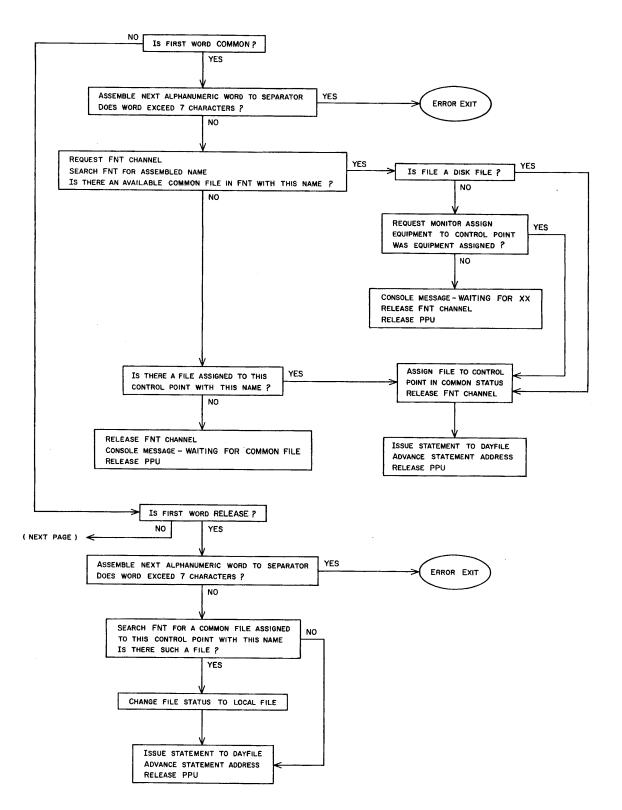


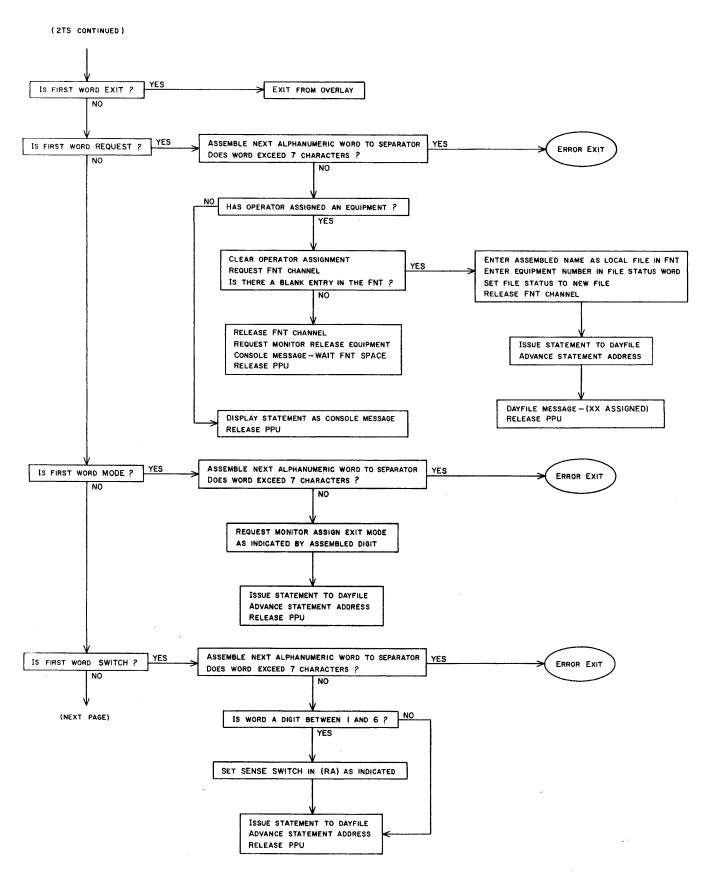


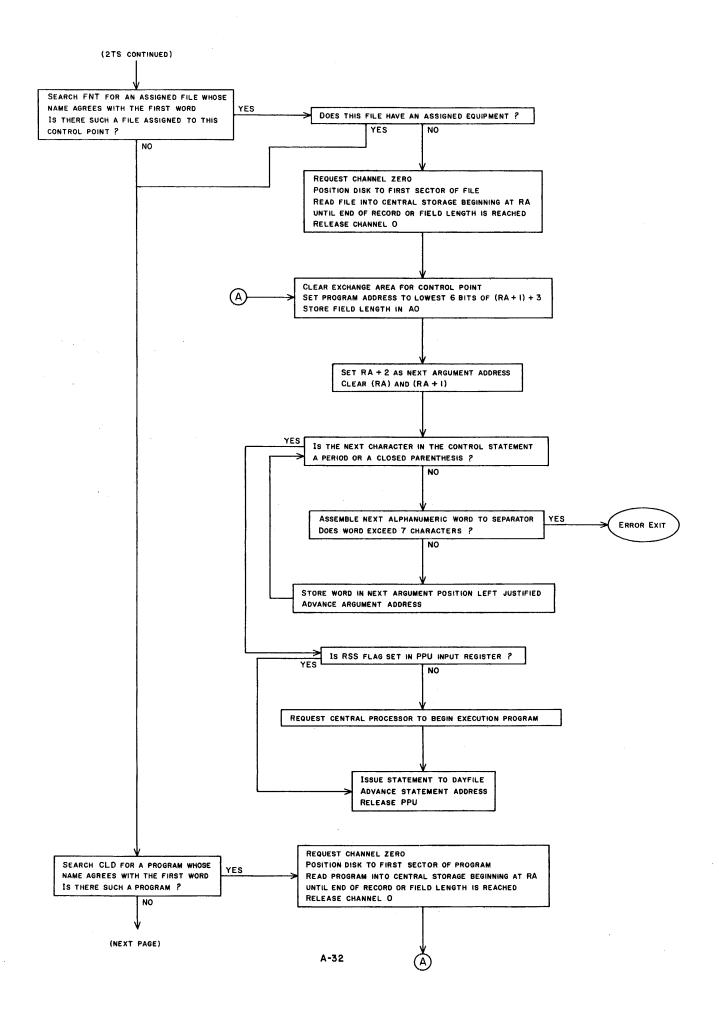


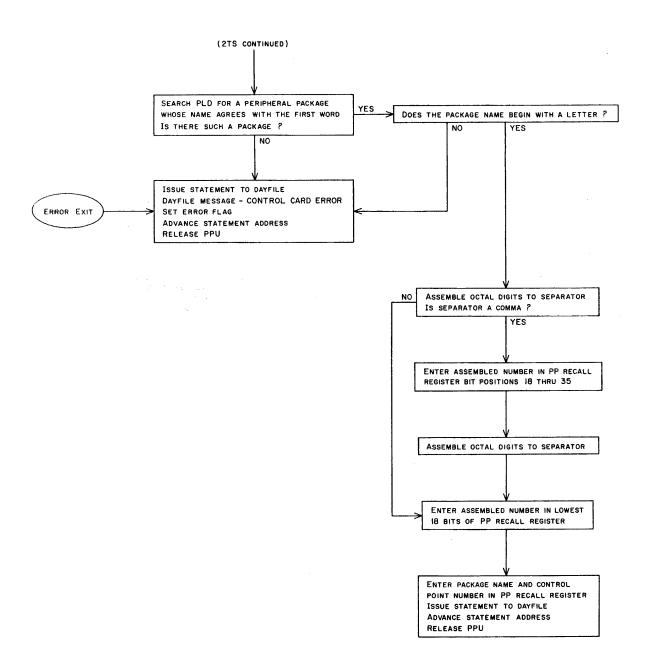


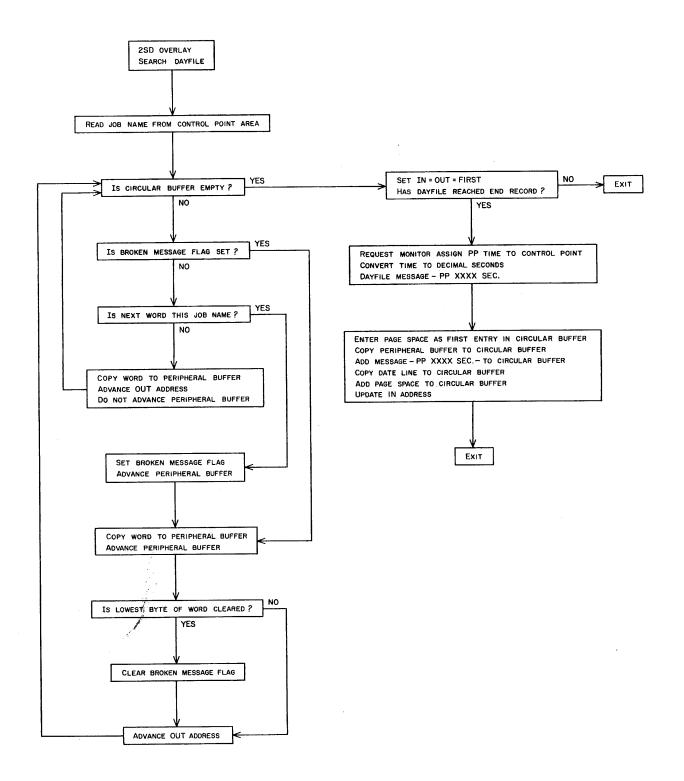


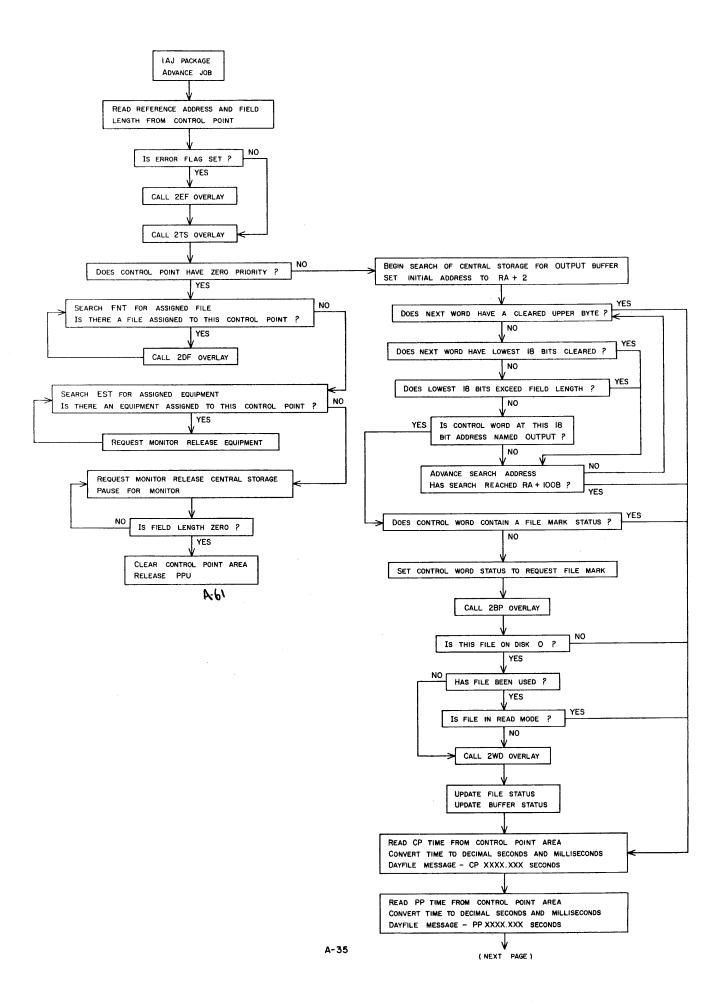


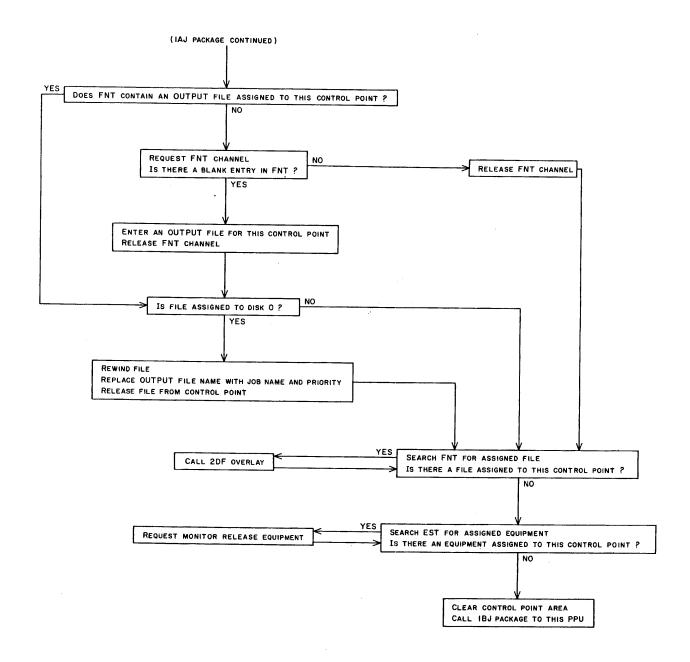


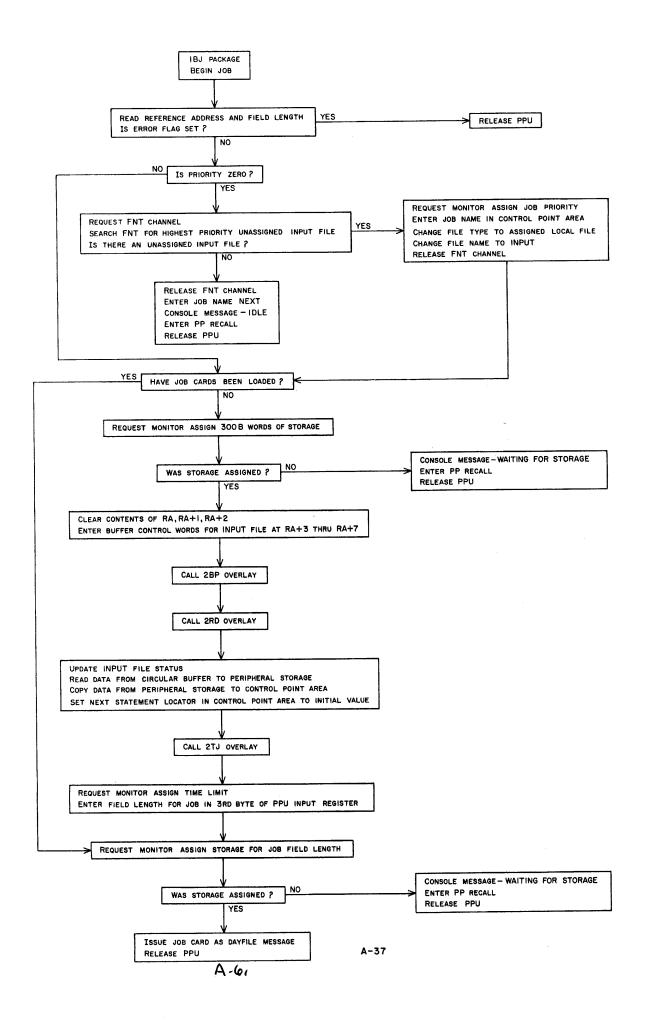


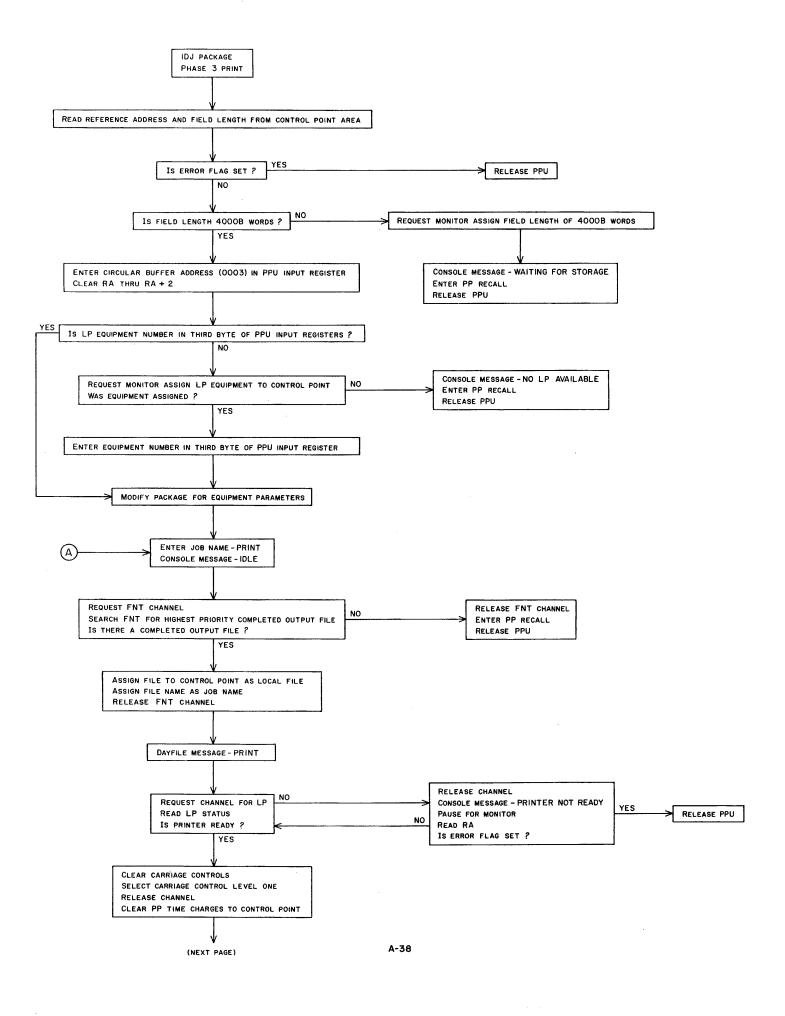


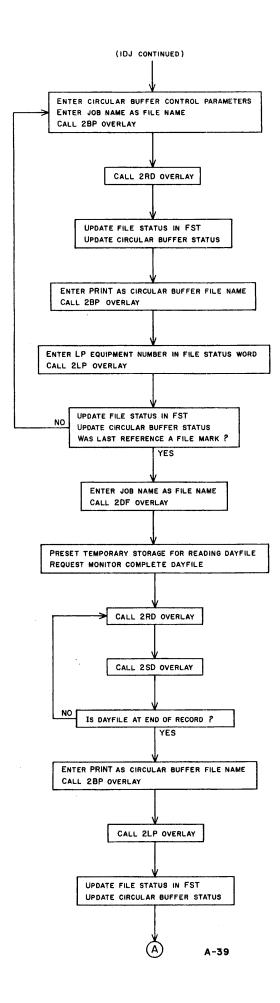


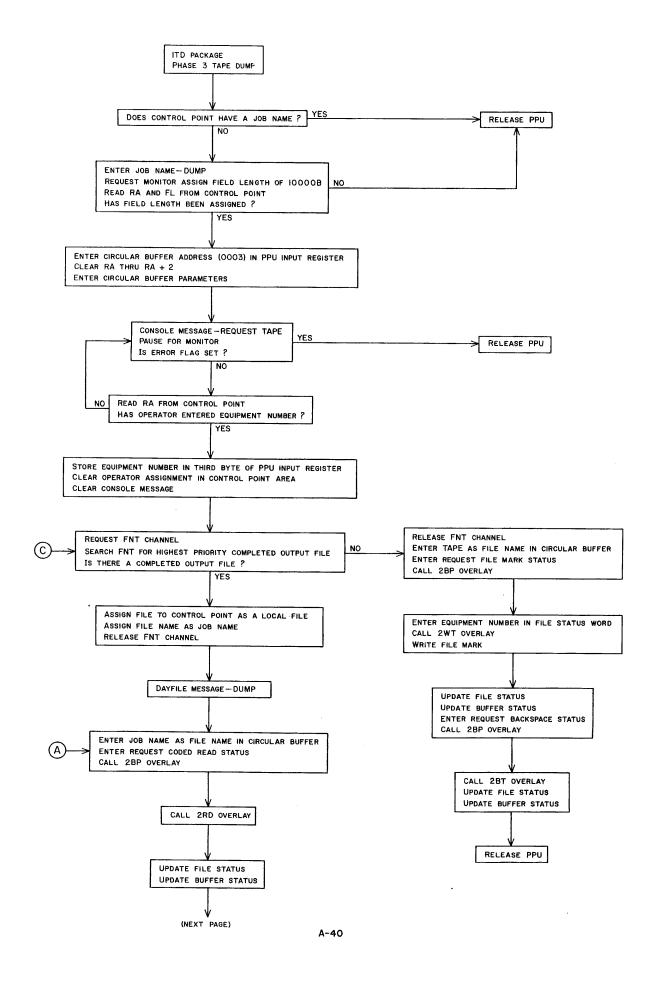


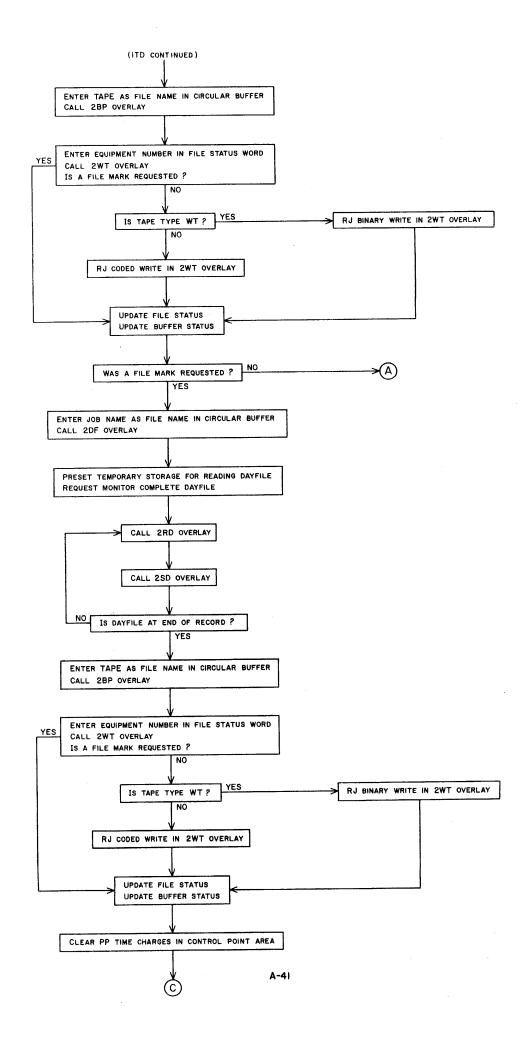


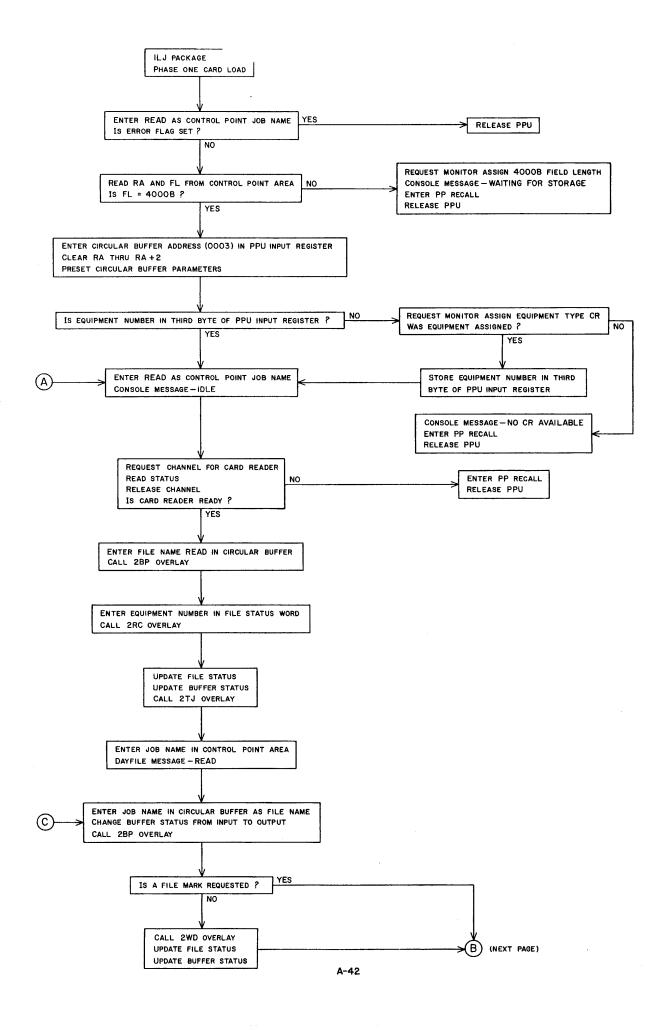


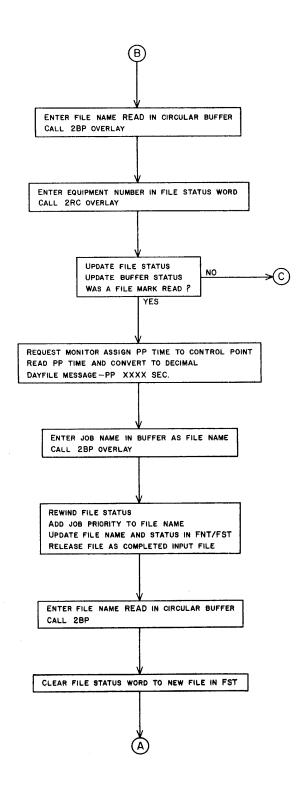


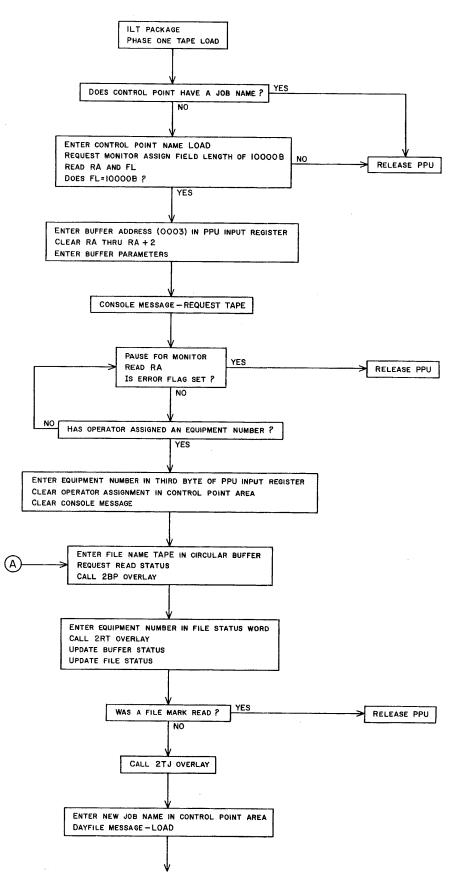


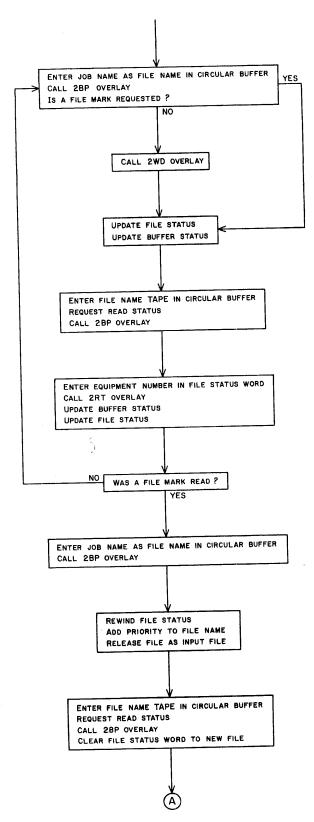




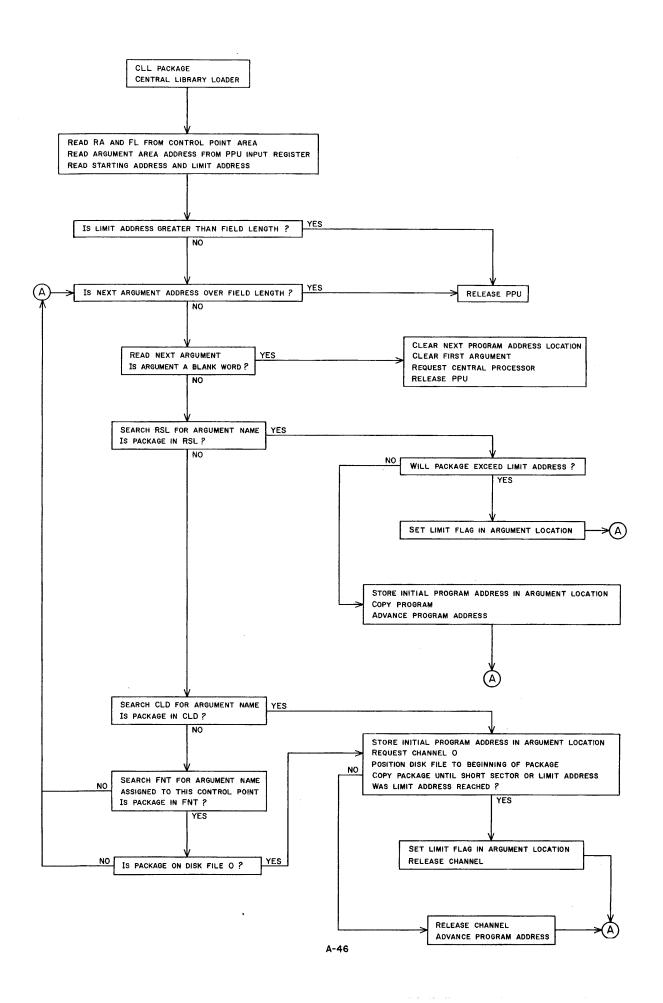


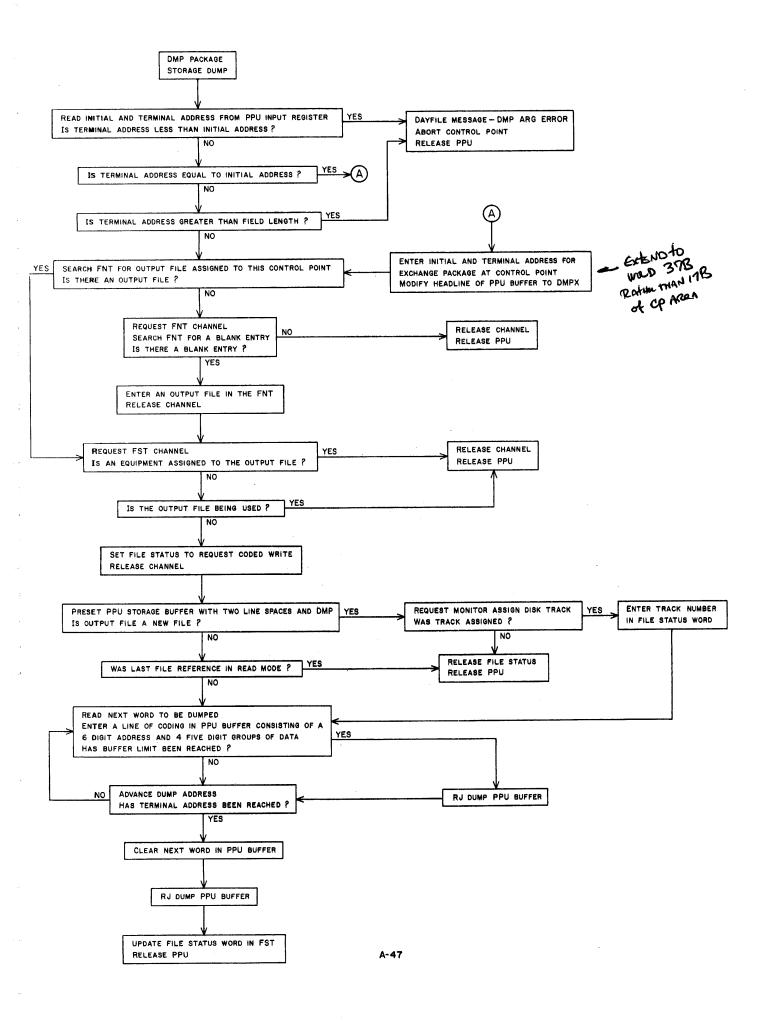


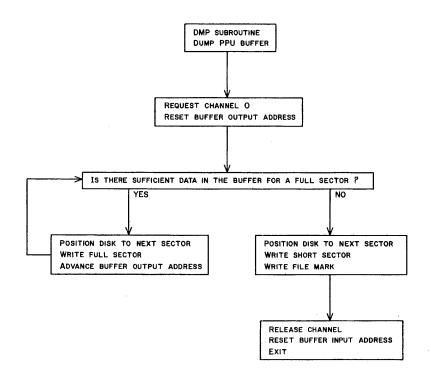


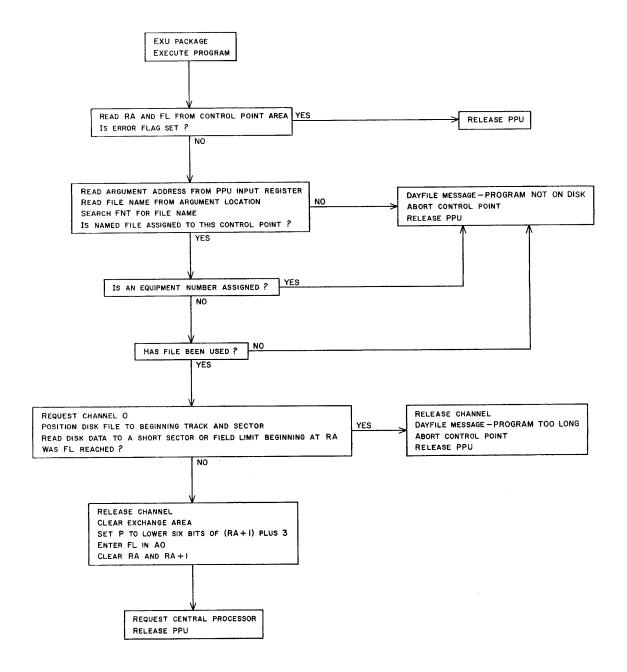


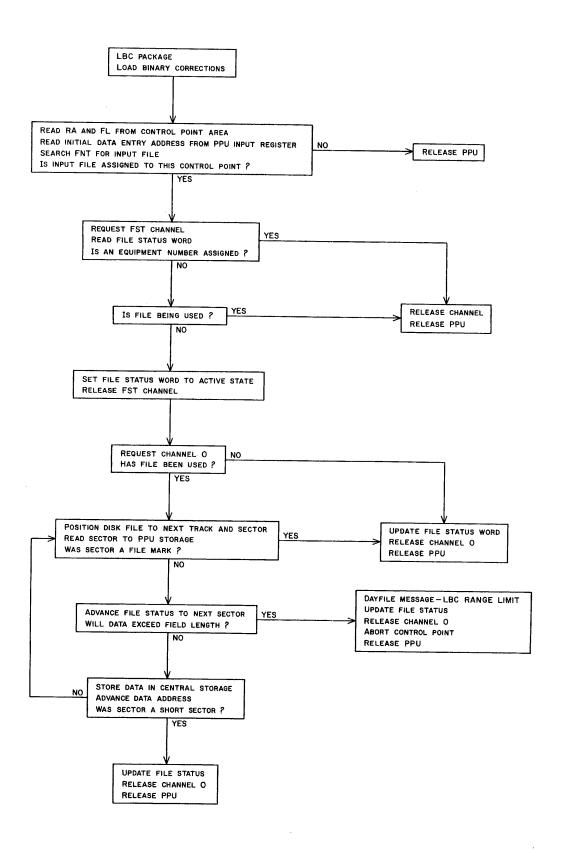
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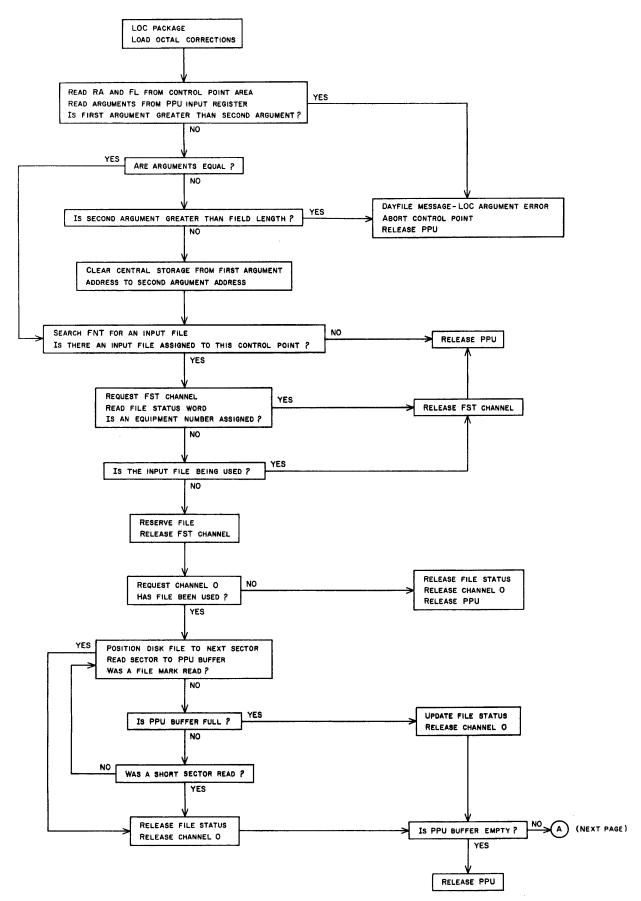


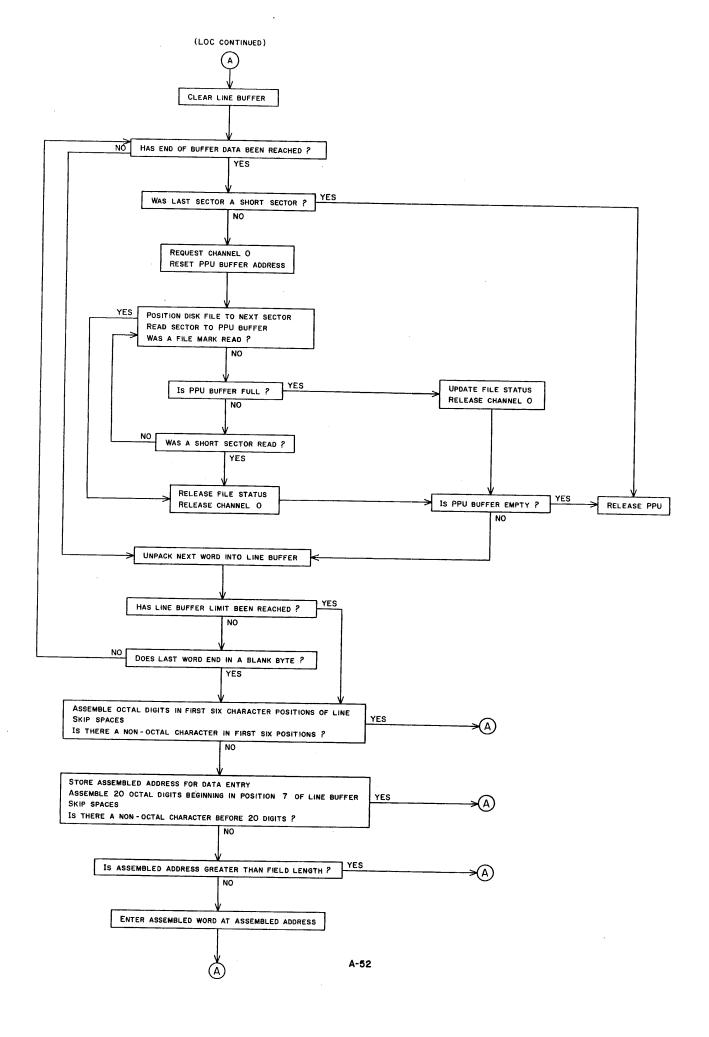


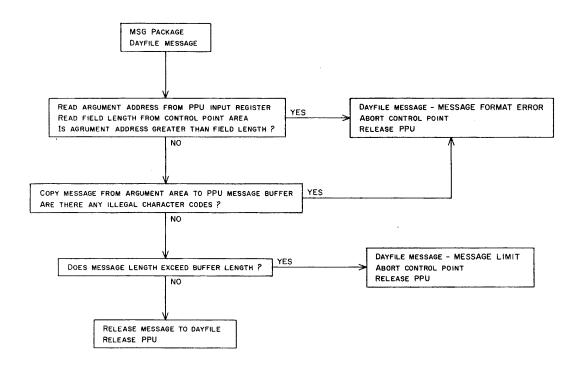


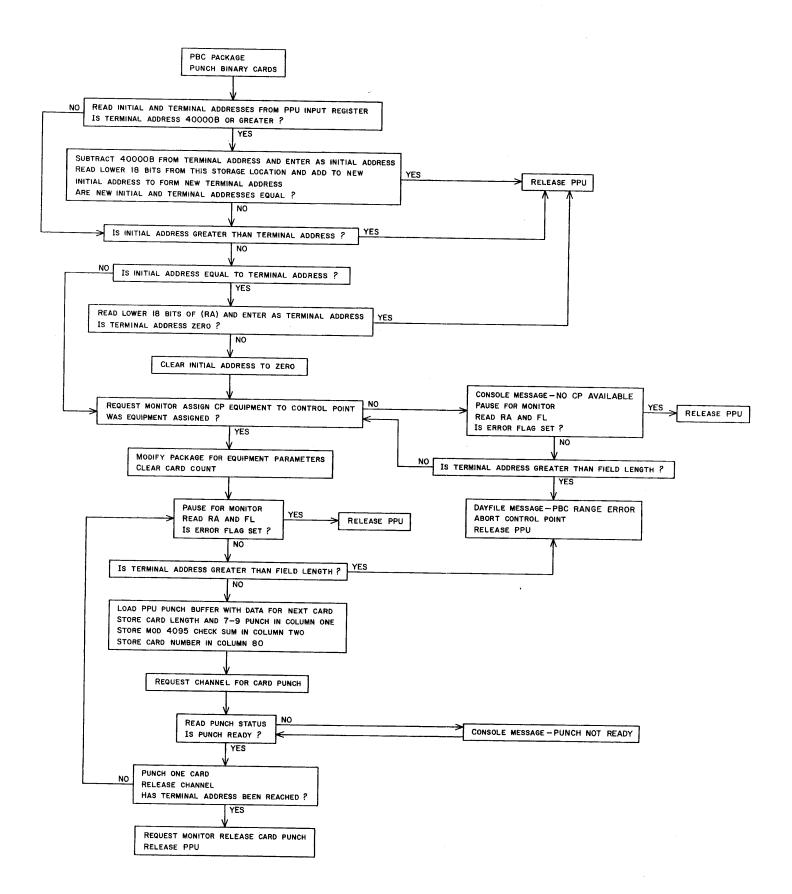


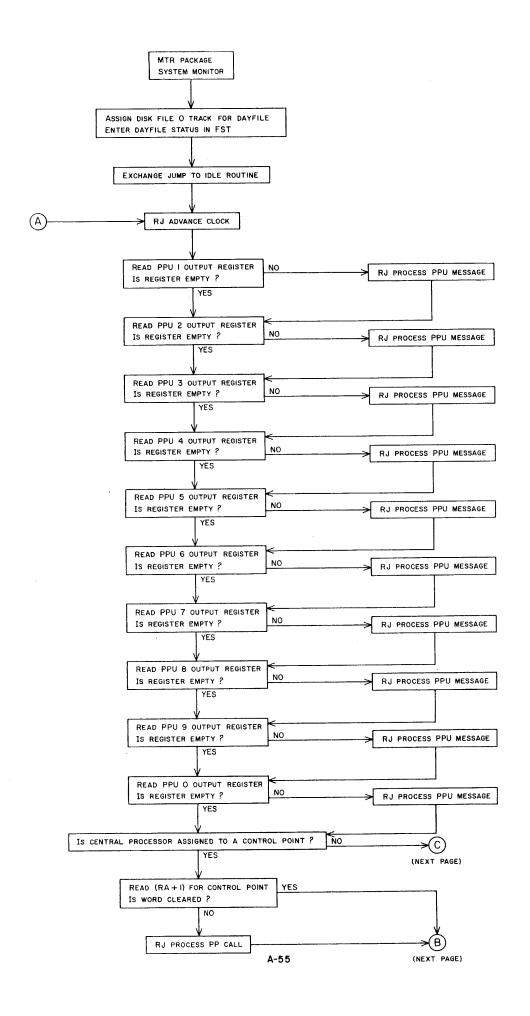


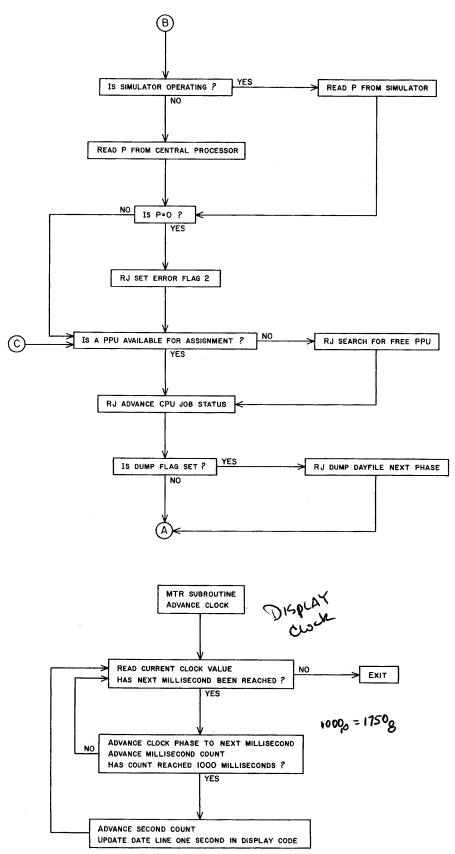




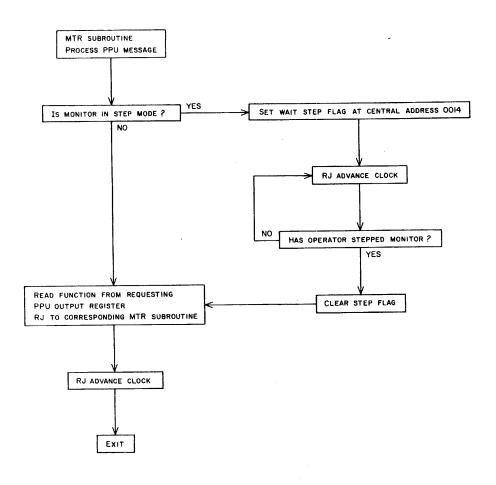


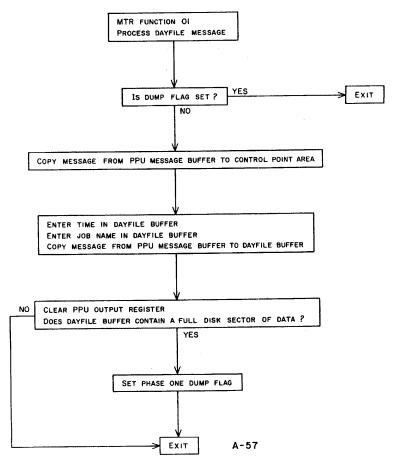


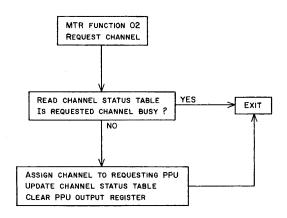


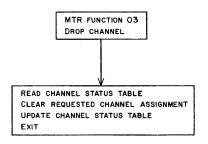


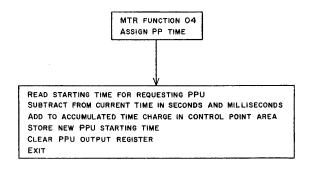
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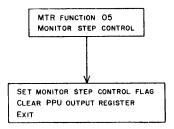


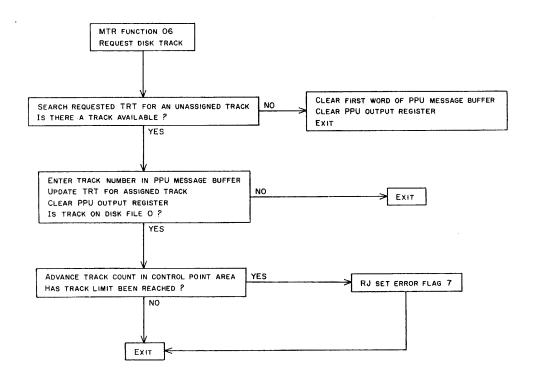


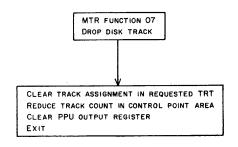


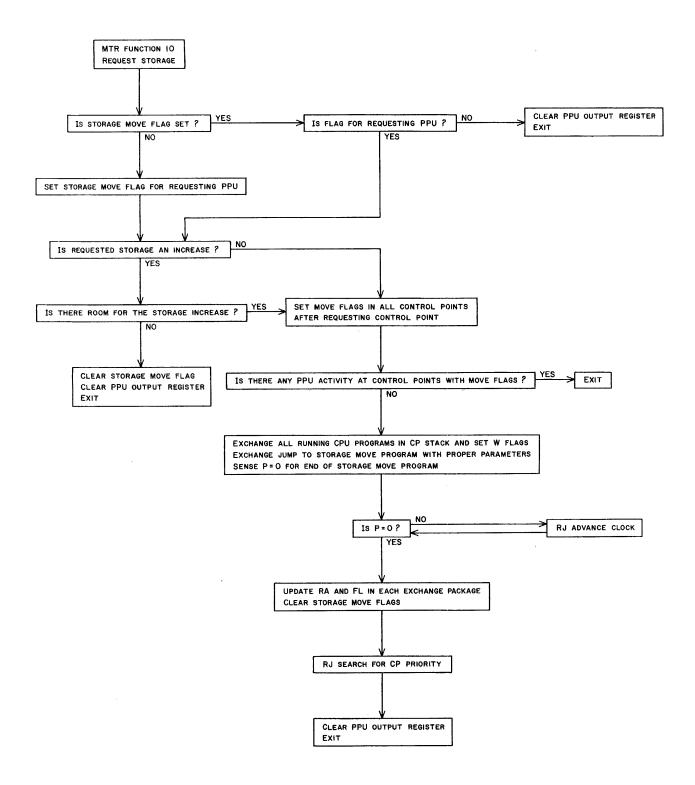


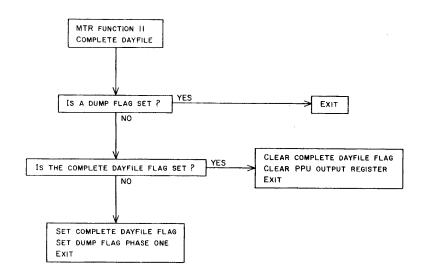


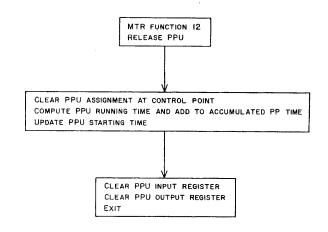


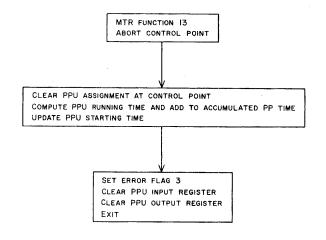


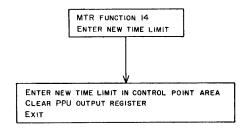


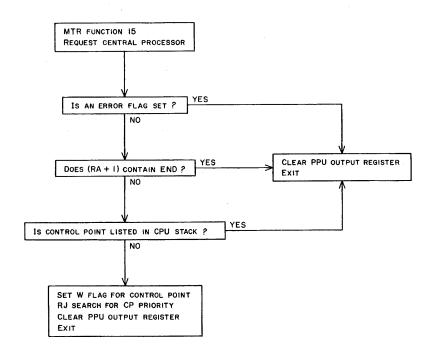


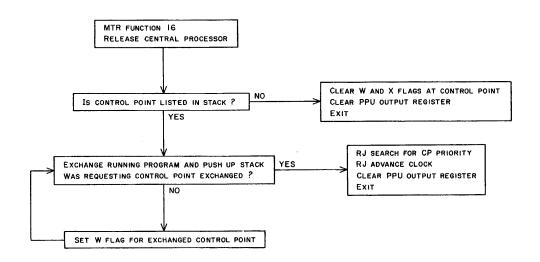


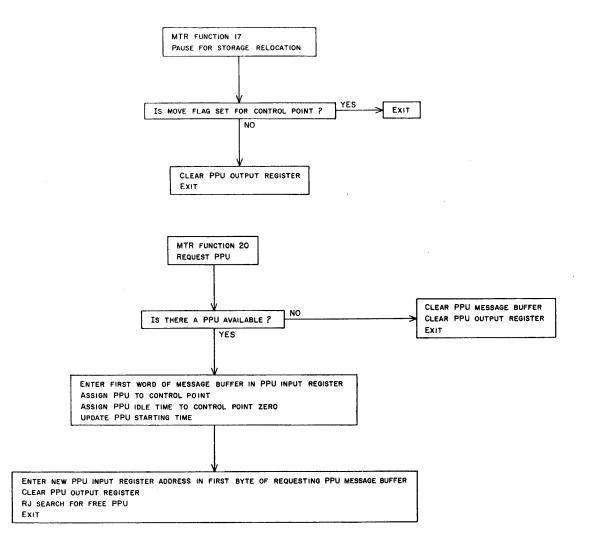


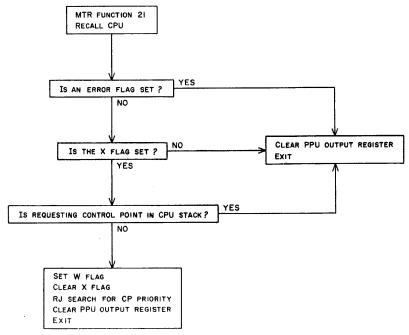


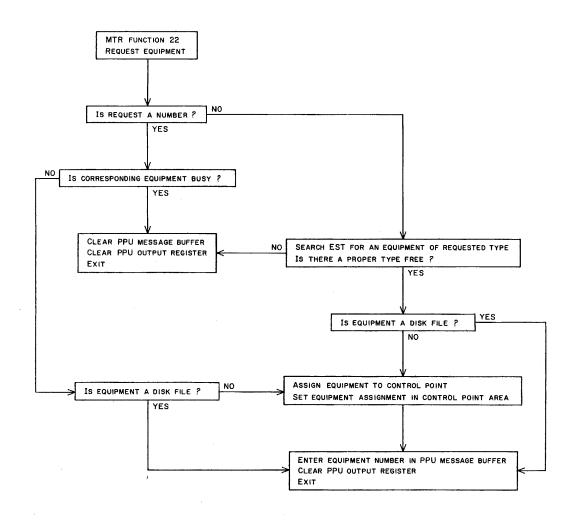


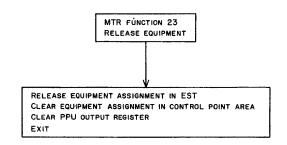


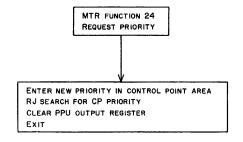


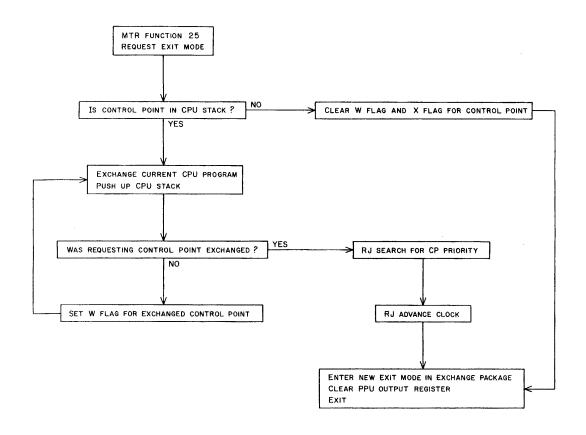


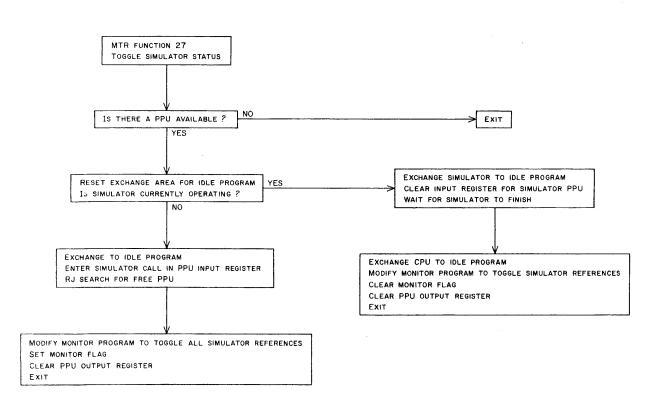


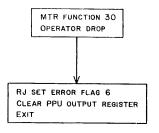


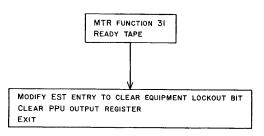


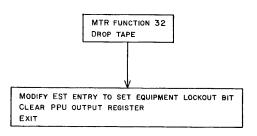


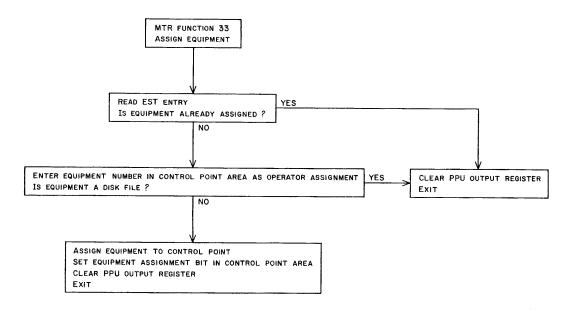


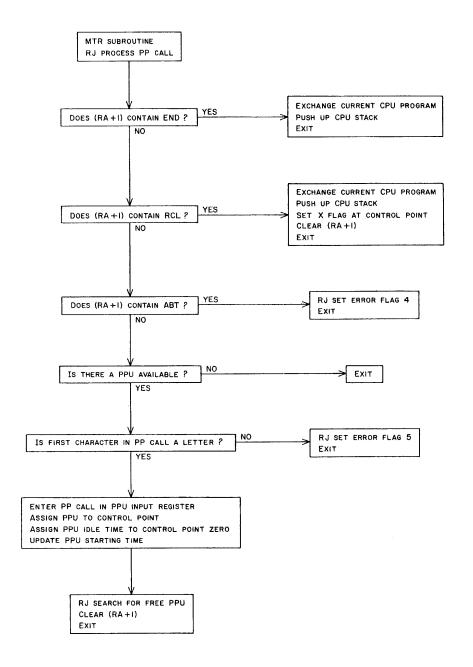


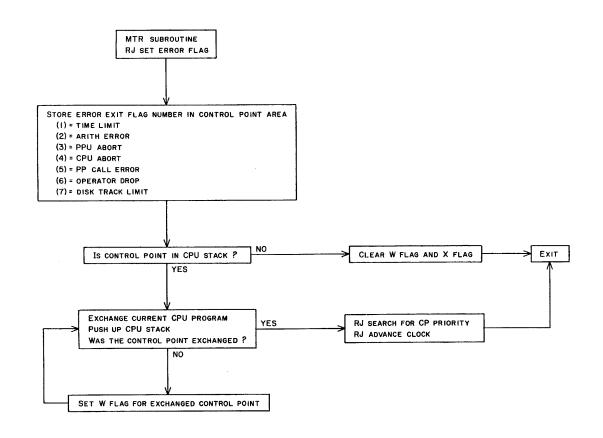


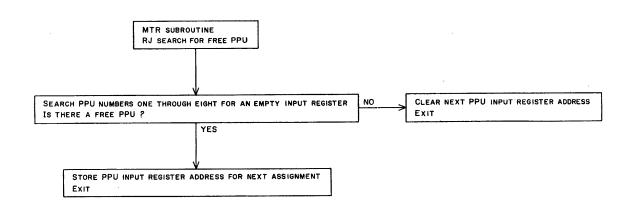


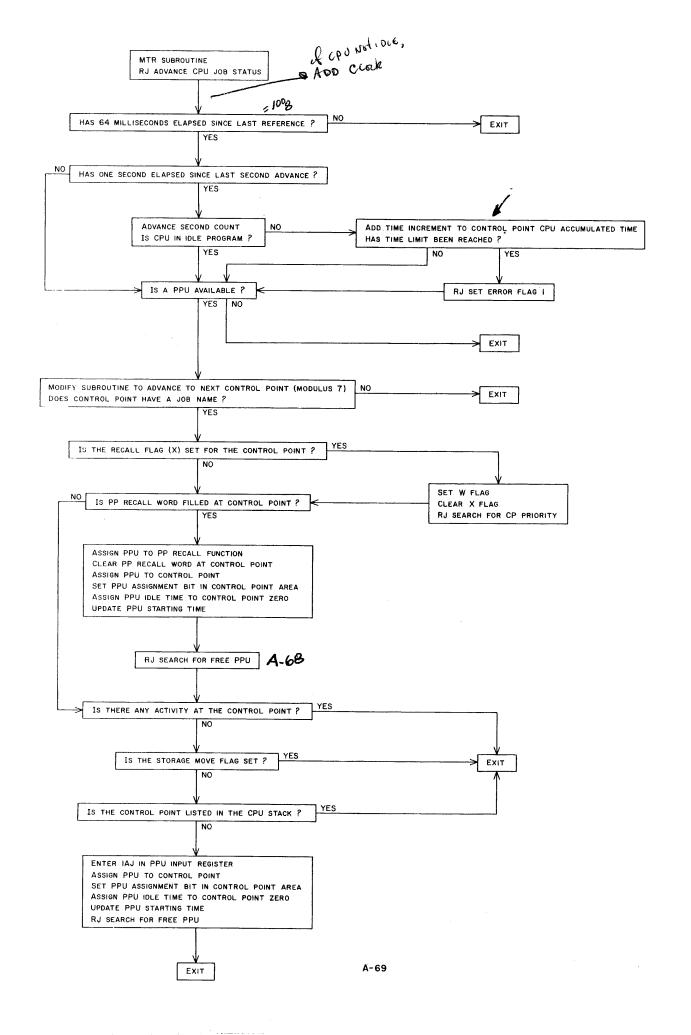


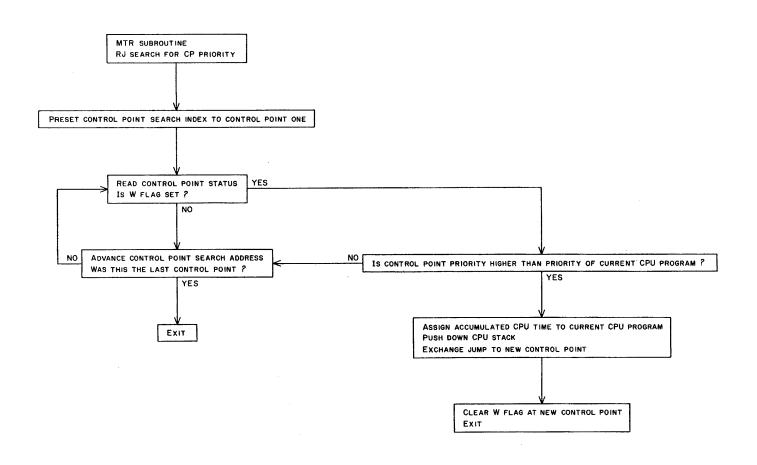


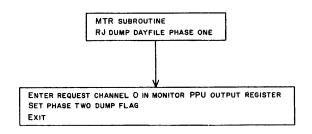


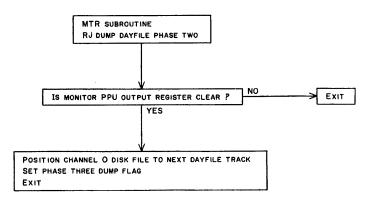


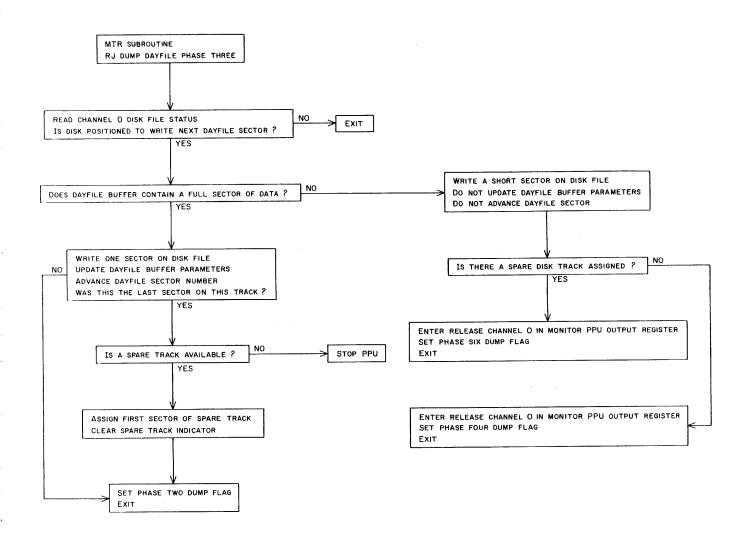


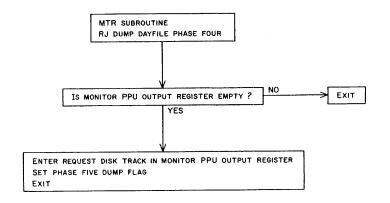


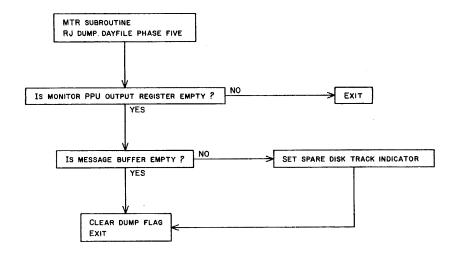


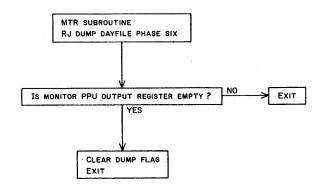






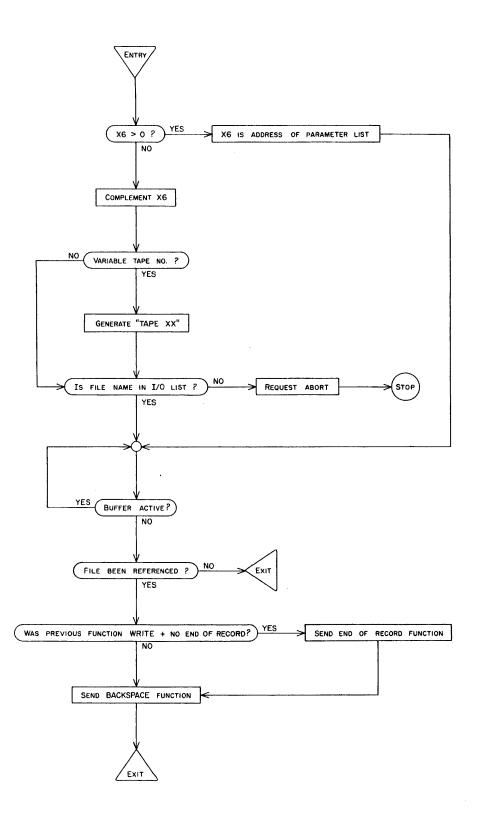


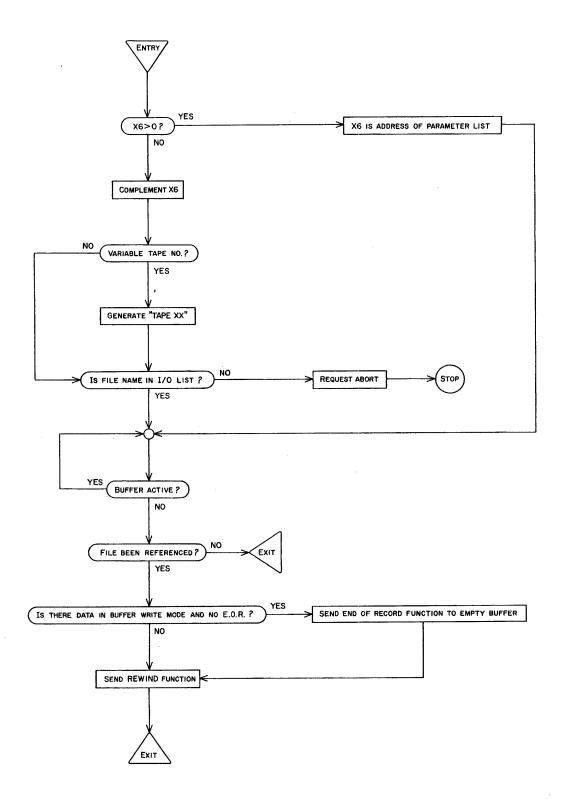


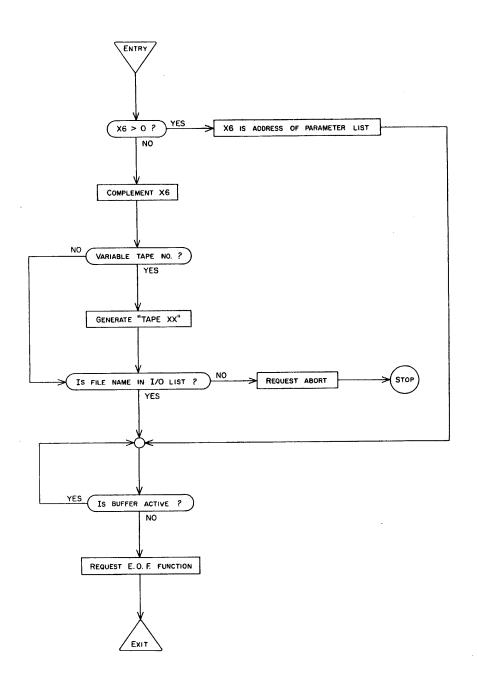


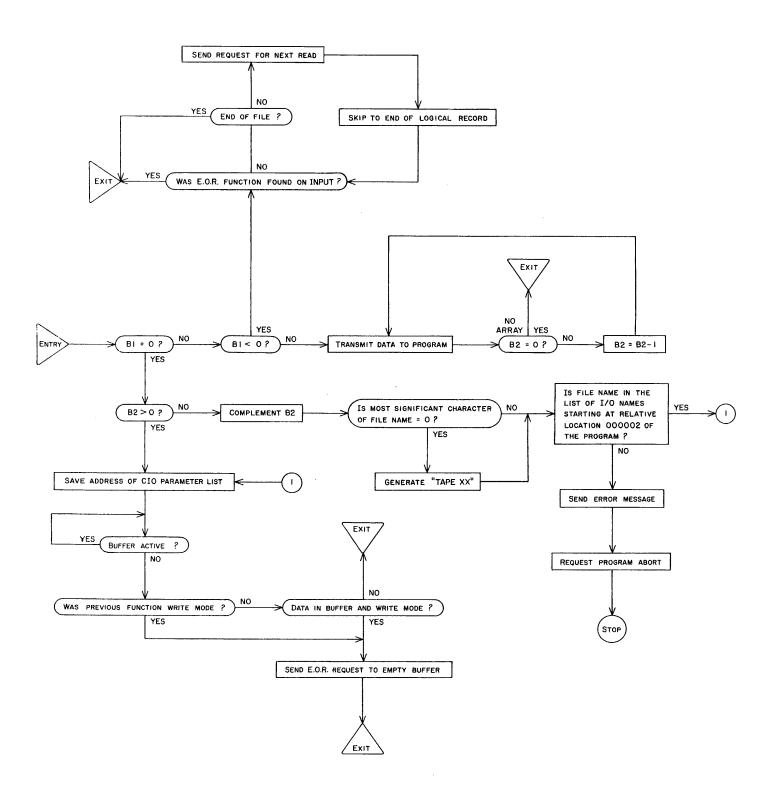
APPENDIX B

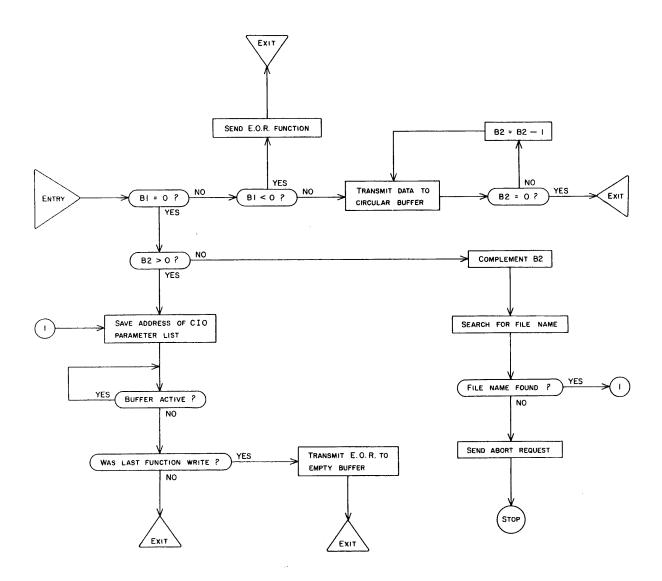
I/O SUBROUTINE FLOW CHARTS





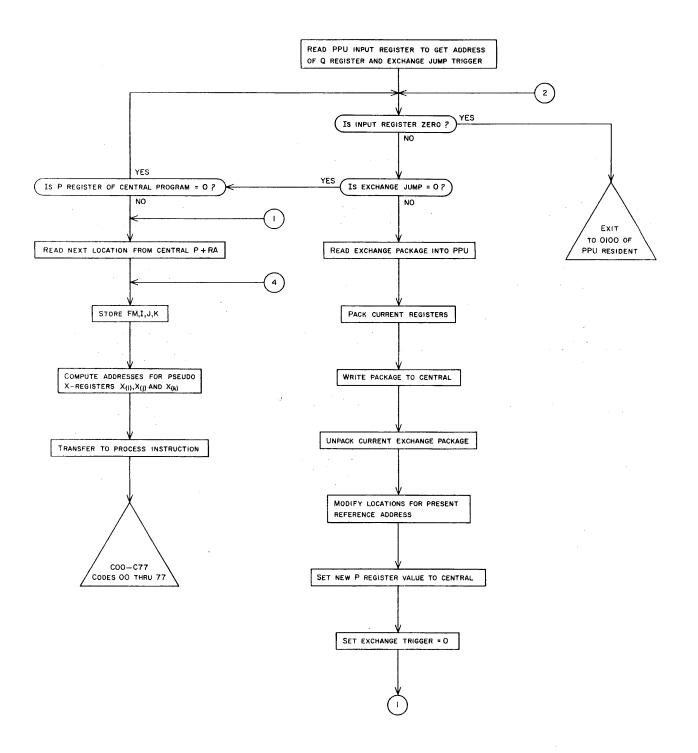


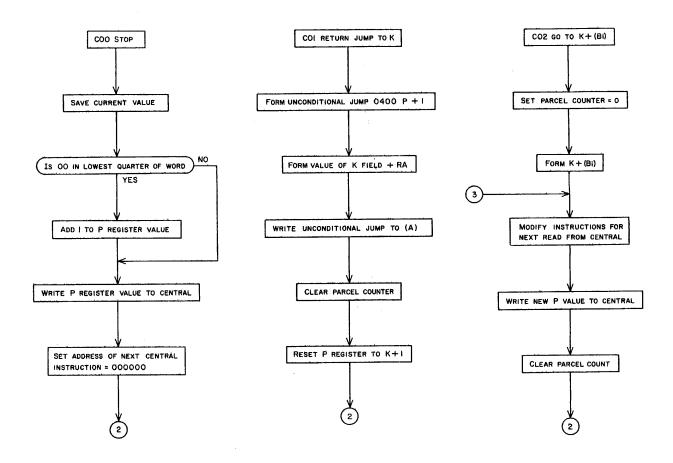


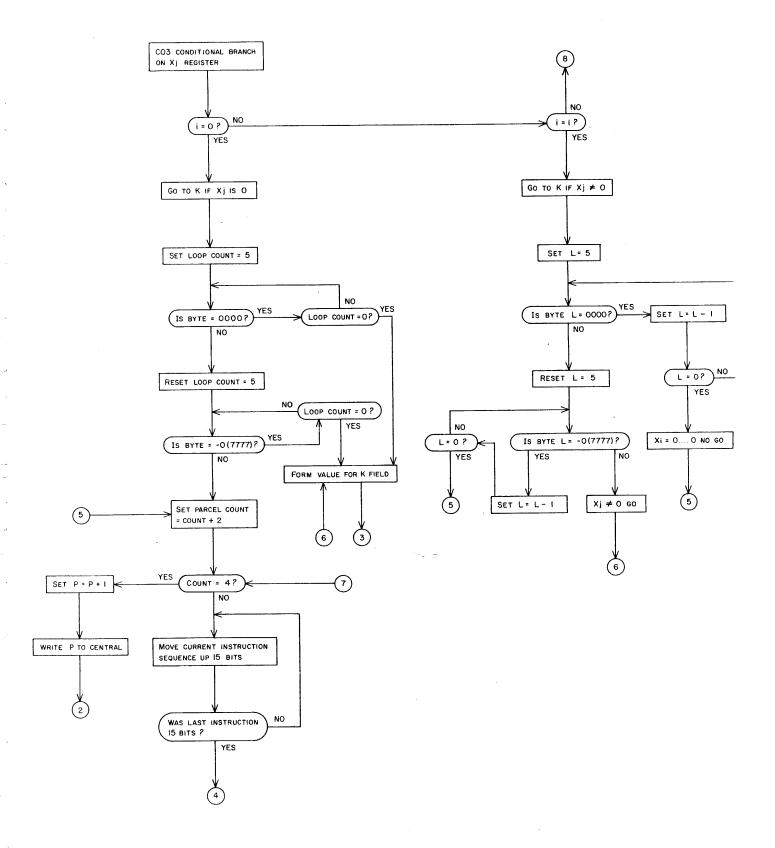


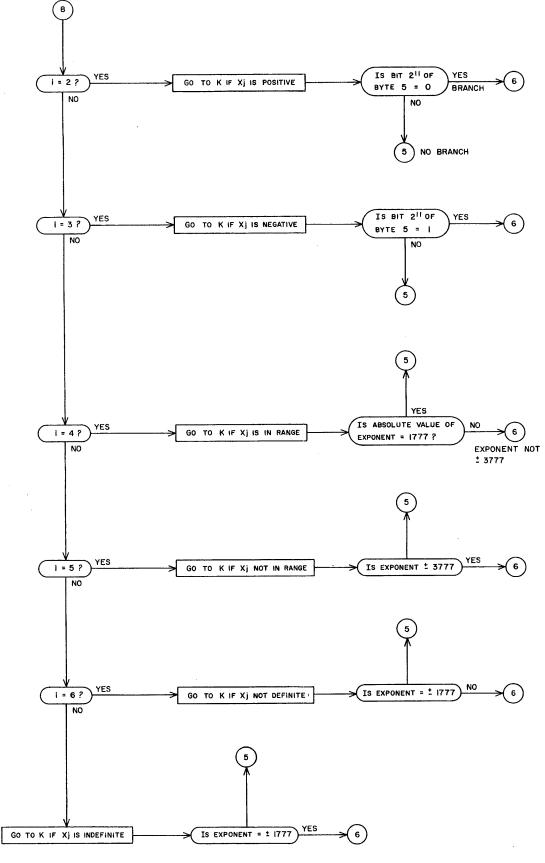
APPENDIX C

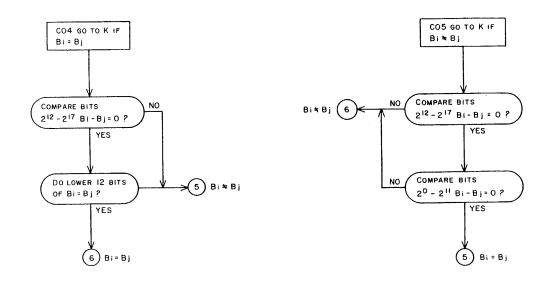
SIM CPU SIMULATOR

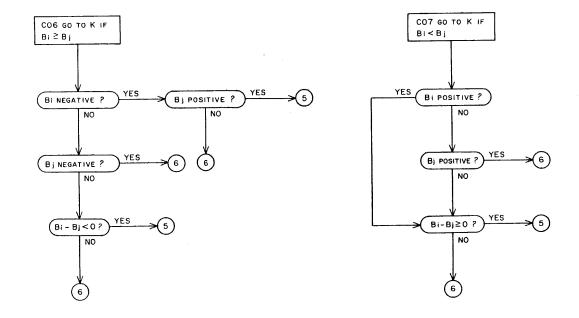


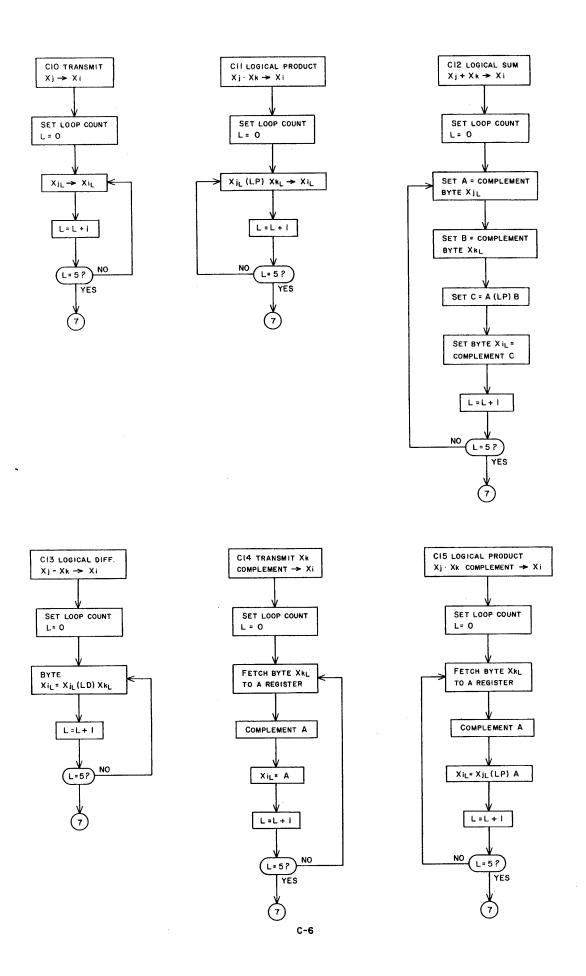


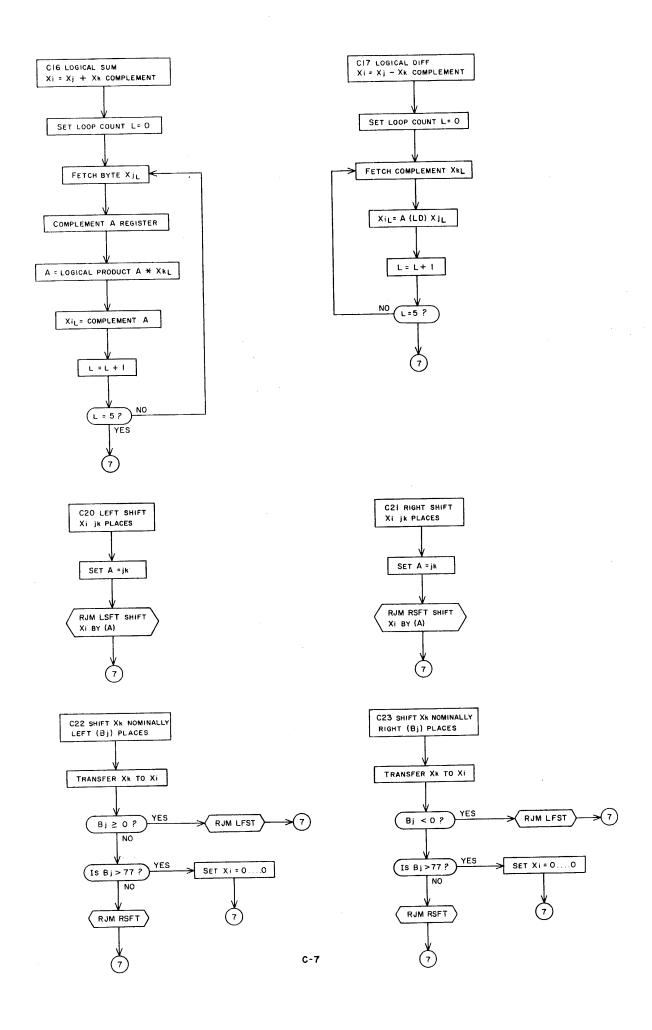


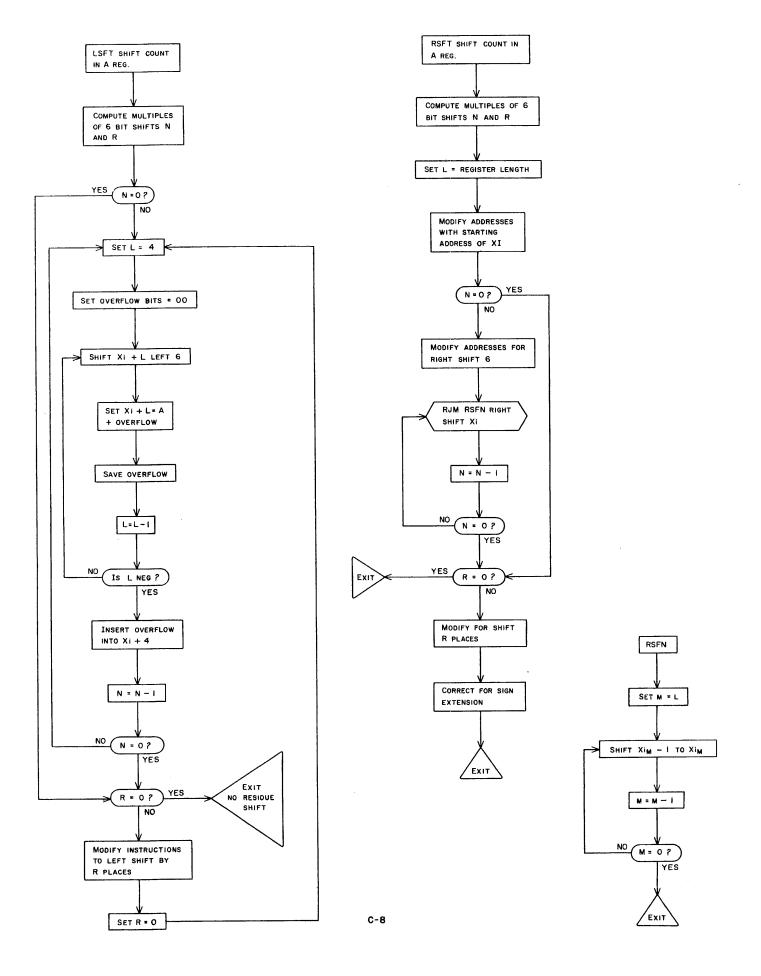


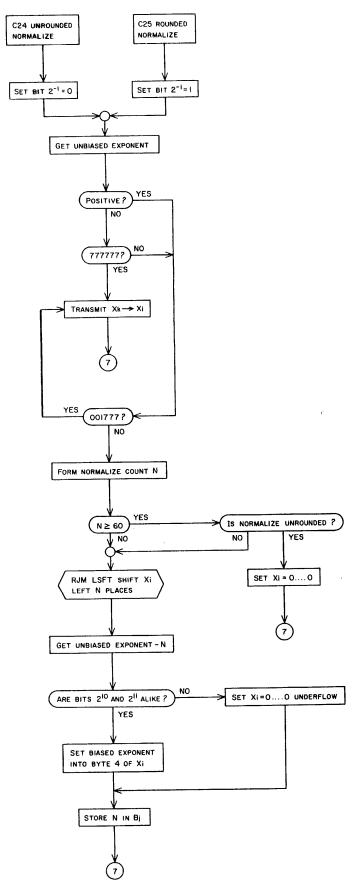


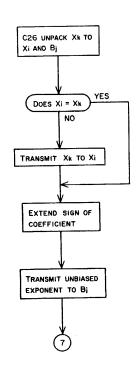


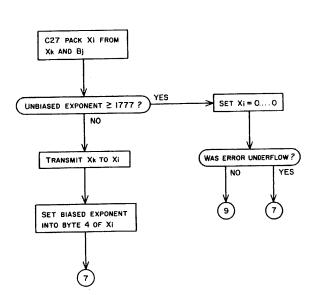


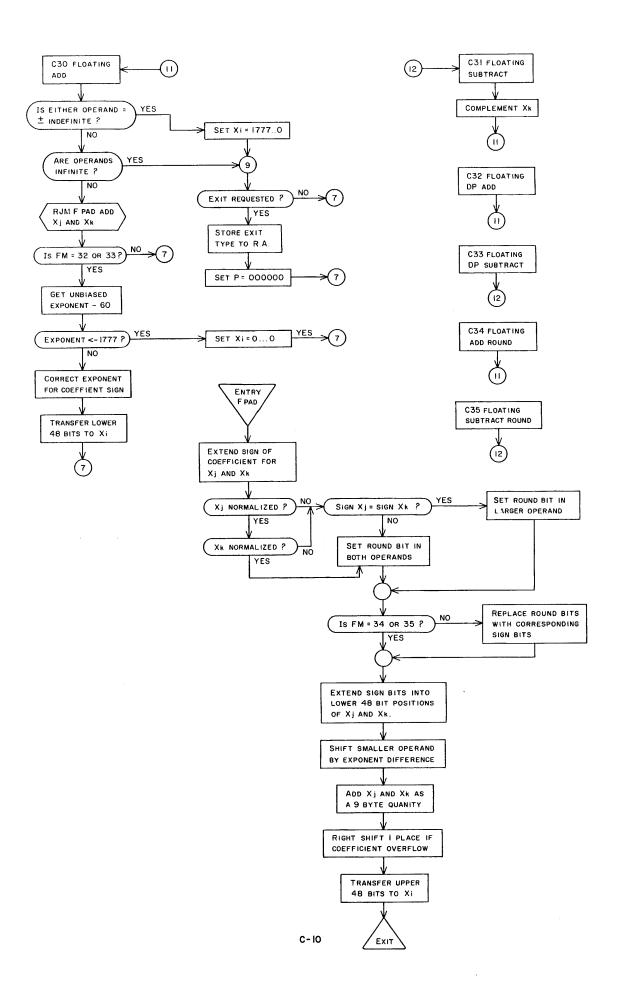


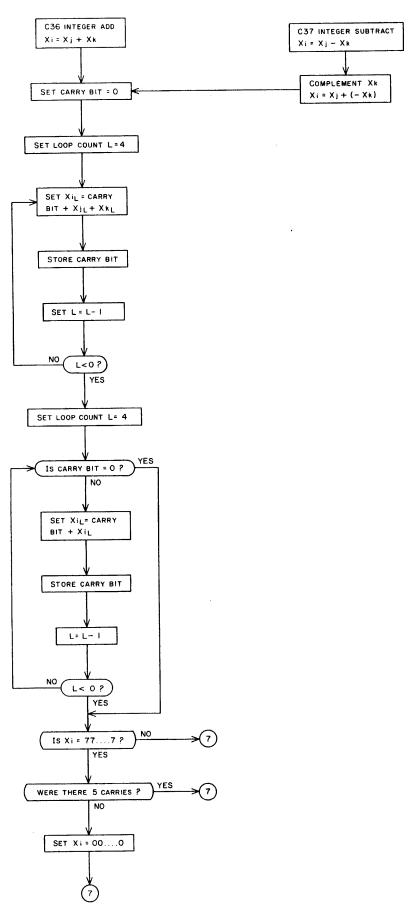


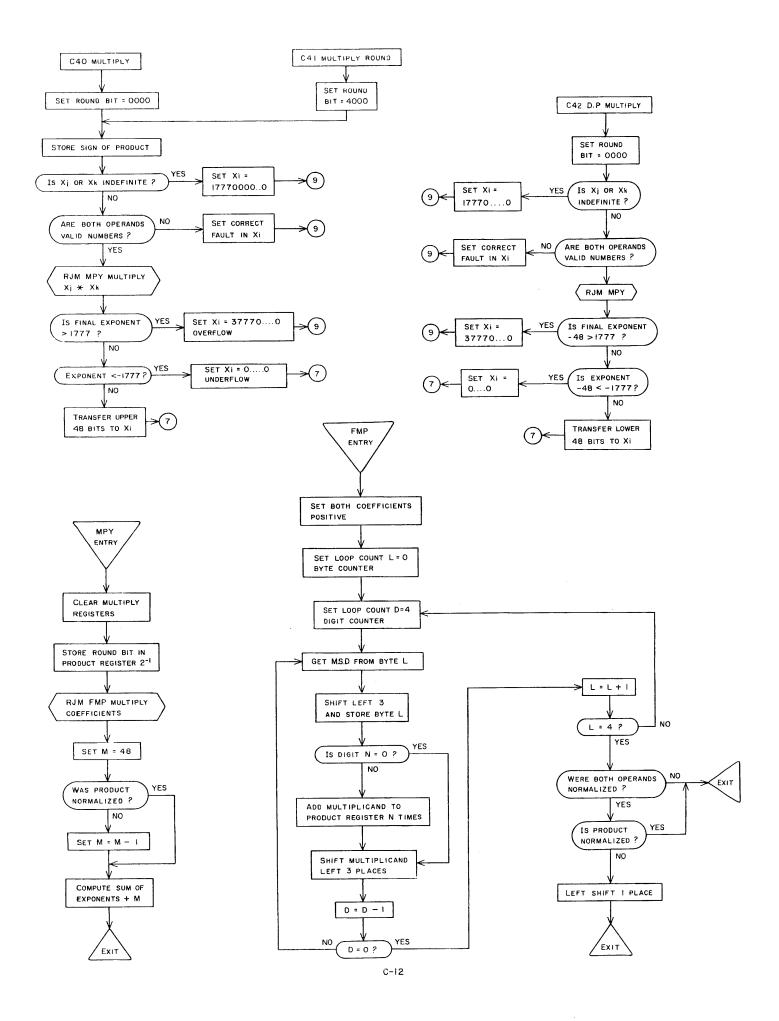


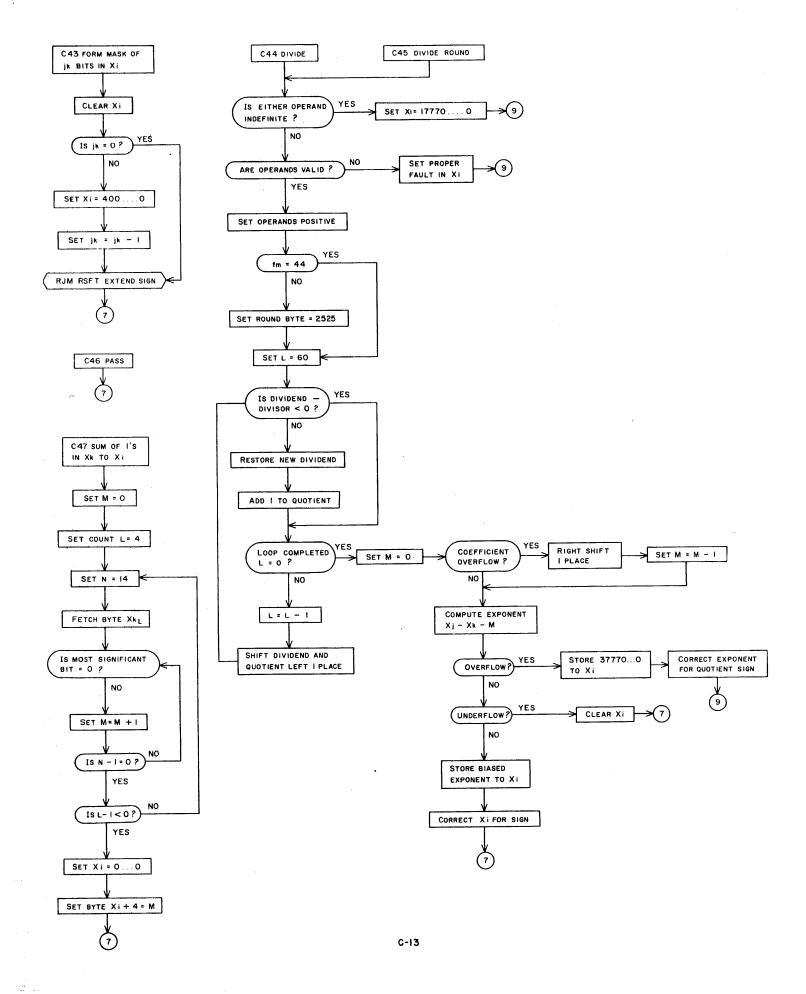


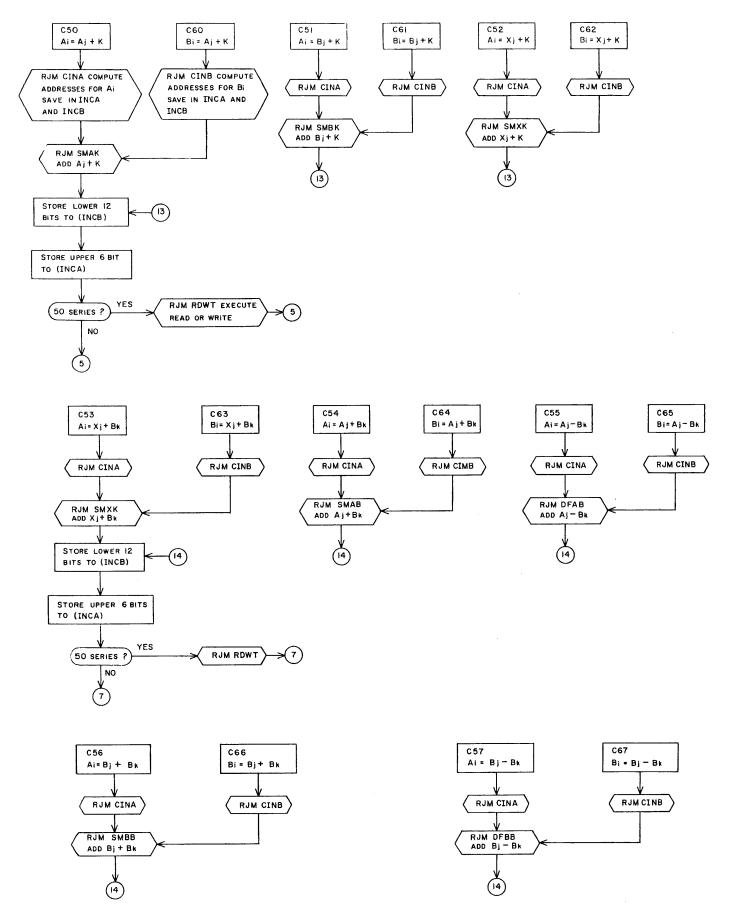


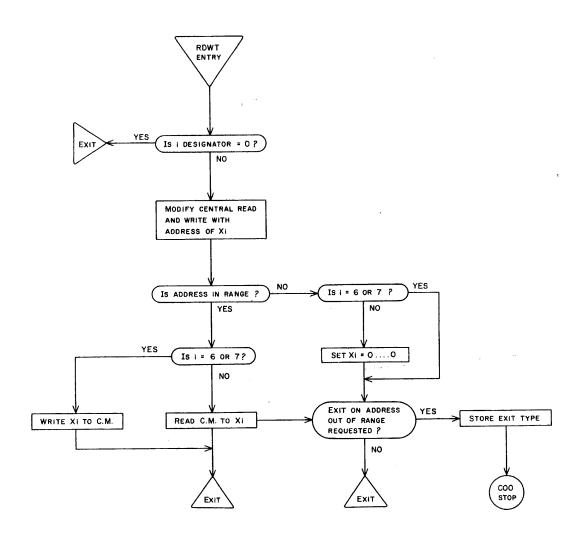


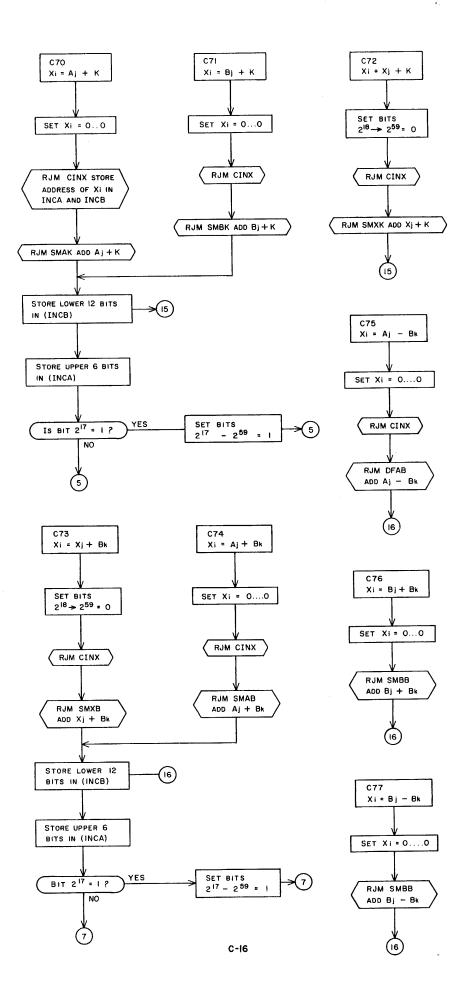












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