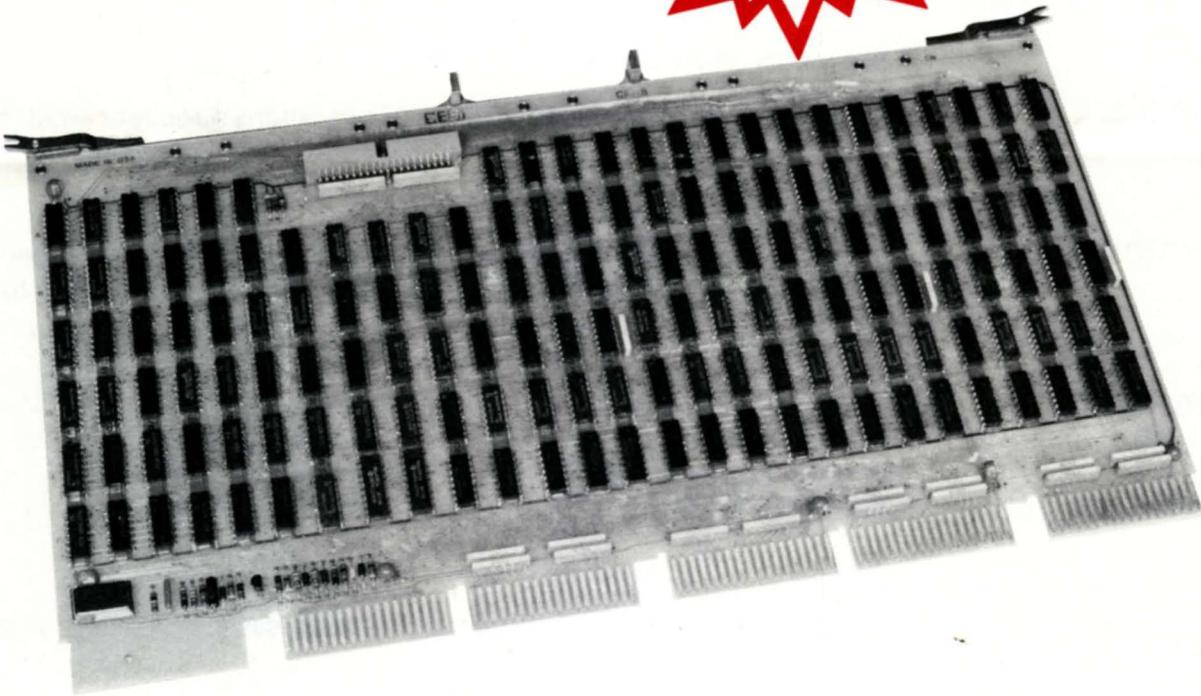


PDP[®]-8 Processor Replacement



DATA SHEET NO. CPU 8782

SYSTEM FEATURES

PDP-8 COMPATIBILITY

The CPU 8 is completely hardware compatible with all DEC[®] HEX-wide OMNIBUS[®] backplanes and software compatible with all PDP-8 software. KK8E speed is supplied with the CPU 8 allowing easy upgrades of KK8A CPU's to KK8E CPU's. The CPU 8 is also hardware compatible with the CESI OMNIPAC 800.

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PERFORMANCE SPECIFICATIONS

TYPE	Single address, fixed word length, parallel transfer programmed data processor.	
WORD LENGTH	12 bits.	
CYCLE TIME	1.2 micro-seconds. Cycle time may be reduced to 900 nano-seconds minimum. Consult factory for correct system configurations.	
MEMORY SUPPORTED	MOS or CORE.	
HARDWARE REGISTERS	2 (AC and MQ).	
AUTO INDEX	8 Auto Index registers per 4K memory field.	
ADDRESSING CAPABILITY	Typically, one instruction may address 256 locations directly or 4096 locations indirectly.	
INSTRUCTION SET	6 memory reference instructions, 20 microprogrammable operate micro-instructions, and 8 input/output transfer instructions for each of up to 64 I/O devices.	
INSTRUCTION EXECUTION TIME	Operate microinstruction: Directly addressed MRI: Indirectly addressed MRI: Auto Indexed MRI:	1.2 micro-seconds. 2.6 micro-seconds. 3.8 micro-seconds. 4.0 micro-seconds or less.
INPUT/OUTPUT CAPABILITY	Programmed data transfer, program interrupt system transfer, and direct memory access.	
SIZE	21.6cm x 40cm (8.5" x 15.75") hex board.	
POWER REQUIREMENTS	+5 VDC @ 2.2 Amps. -12 VDC @ .050 Amps.	

ONE YEAR WARRANTY	CESI unconditionally warrants all its units for one full year and will immediately ship a replacement unit upon notification of malfunction.
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ORDERING	For current information on prices and delivery call (713) 488-8830, or write to our Marketing Department . Quantity discounts quoted upon request.
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