VAXCLUSTER SYSTEM REVISION F1

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VAXCLUSTER SYSTEMS - VAXCLUSTER SYSTEM REVISION F1

TO: FIELD SERVICE UNIT MANAGERS

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SUBJECT: VAXCLUSTER SYSTEMS - VAXCLUSTER SYSTEM REVISION F1

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CHAPTER 1

INTRODUCTION

Over the past quarter the VAXcluster environment has had new products added, and changes implemented to current products. Of significance is the addition of the VAX 8200, VAX 8300, VAX 8500, VAX 8800, and the CIBCL. Currently the VAXcluster revision is E4. With the changes outlined in this document, the revision will become F1.

The remainder of this document provides the detailed information for you to deliver effective service to your customer. Reference Chapter 3 for information on benefits to your customer from upgrading to revision F1.

Maintaining VAXcluster systems at the current revision level is very important to both you and your customer. It allows customers to operate their systems with a minimum amount of interruption caused by known problems, and it means fewer service calls to you. Therefore, please take the time to understand the content of this document and ensure the information is made available to the proper people in your organization.

CHAPTER 2

VAXCLUSTER SYSTEMS REVISION F1

2.1 REQUIREMENTS

The changes required to support VAXcluster systems at revision F1 involve VMS V4.4. New products supported in the VAXcluster systems include the VAX 8200, VAX 8300, VAX 8500, and VAX 8800. The changes/additions detailed here are final and all items have been released.

2.1.1 VAX 8200

The single-processor VAX 8200 is a mid-range VAX with a system performance equal to the 11/780, a price equal to the 11/750, and a footprint equal to the 11/730. The processor interfaces to the new VAXBI Bus and runs full VMS, ULTRIX, and VAXELN. A single CPU provides a performance rating of approximately 1.0 of the 11/780.

The VAXBI Bus has been designed to replace the UNIBUS and will be licensed for use by other vendors. It has a bandwidth of 13.3 MB/sec and will support up to 16 nodes.

A new set of adapters are being developed to interface the BI bus to peripherals and to other interconnect architectures. One of those available at FRS is the UNIBUS Adapter (DWBUA) which will allow initial configurations to use UNIBUS options. It will be phased out in favor of native-BI configurations as more peripheral adapters become available.

The architecture of the BI Bus and the new technology being used in product design have allowed for significant improvements is system reliability and maintainability to be achieved.

2.1.2 VAX 8300

The VAX 8300 is an asymetric dual-processor, mid-range VAX with 1.8 times the performance of a VAX-11/780 on job loads which take advantage of the dual processor. The processor interfaces to the VAXBI Bus and runs full VMS.

2.1.3 VAX 8500

The VAX 8500 is a bounded mid-range performance GENERAL PURPOSE system which features 20MB of memory and two VAXBI channels for I/O. This system will be particularly successful in footprint and cost sensitive applications which can use this performance level. Typical applications include workstation compute engine, factory automation, VAXcluster compute engine, database manipulation, and simulation. Many volume buyers will find the VAX 8500 a cost effective system. It is not expandable to a VAX 8800 System. This system will complete the phase-out of the VAX-11/785 by offering more than twice the performance at a constant price and in the footprint of a VAX750.

2.1.4 VAX 8800

The VAX 8800 is DIGITAL's highest throughput VAX. In compute intensive environments (Scientific, Engineering, Artificial Intelligence and Simulation applications), the VAX 8800 can deliver the throughput of between 2-3 VAX 8600 processors. Two high speed ECL CPU's share up to 32MB of main memory via a 60 MB/s private bus. Up to four VAXBI channels can be connected to this bus, providing the largest total I/O capability of any VAX.

All VAX 8800 kernels include as standard: 32MB memory, hot floating point, BBU, Ethernet port and VAAcluster port in the same footprint as an 8600.

Product Strategy and Objectives:

The VAX 8800 is the first product of a family of products emanating from the Nautilus design effort. The announcement and pricing of the VAX 8800 meets the following strategic objectives:

- * Improve corporate profitability (achieve >60% margins, 1987 and beyond)
- * Improve DIGITAL's competitive posture in TECHNICAL markets vs. both the established "high MIP" vendors as well as the newer "point solution" niche vendors

- * Extend VAX solutions to substantially higher performance markets
- * Protect ongoing revenue stream generated by VAX 8650
- * Establish the "reference plane" consistent with future pricing.

The VAX 8800 will be positioned as DIGITAL's highest performance VAX system for the compute intensive environments. Particular focus will be given to Laboratory, Scientific, Engineering, Simulation and Artificial Intelligence and Commercial batch applications which benefit most from this performance.

2.1.5 VAX/VMS/MicroVMS V4.4

VAX/VMS and MicroVMS Version 4.4 have begun new customer shipments. These are remastered releases and do include new functionality.

This article provides a brief overview of the changes and new features, concentrating on those items which may impact field organizations. For details on most of the new features, see the VMS and MicroVMS V4.4 Release Notes and new and enhanced documentation.

New Features

The following is a brief list of some of the new features of special interest.

- o New CPU support
 - VAX 8200.
 - VAX 8300.
 - VAX 8500.
 - VAX 8800.

VMS V4.4 includes multiprocessing enhancements which are

described in an article in The Buffer. Note that the VAX 8300 and VAX 8800 multiprocessing support is provided through the installation of a product "key" which is only included with VMS/8300 (Q7001) and VMS/8800 (QM001) media kits. Refer

to the article on VMS multiprocessing in The Buffer for more details.

- o New device support
 - DRV11-WA Q-bus version of the DR11-W for MicroVAX II systems.

The V4.4 XADRIVER assumes that the DRVII-WA is at CS Revision Level B and Etch Revision Level D or earlier.

- DMB32 - Backplane interconnect (BI) support for eight asynchronous terminal lines, one line printer, and one 64 KB synchronous line.

The synchronous line is supported by a separate layered product.

- KLESI-B Low-End Storage Interconnect (LESI) controller for the BI.
- CIBCI Computer interconnect (CI) port for BI machines.
- o VAXcluster systems enhancements
 - Clusterwide DECnet node name/address.

Refer to the article on "New Features in DECnet-VAX Version V4.4" in The Buffer for more detail.

- Shared DECnet permanent node database.

Refer to the article on "New Features in DECnet-VAX Version 4.4" The Buffer for more detail.

- Clusterwide MONITOR.

All monitor displays can now be performed for any node in the VAXcluster system. There is also a MONITOR CLUSTER display which provides a brief summary of CPU, disk 1/0, and memory and lock activity on all nodes (up to six) across the VAXcluster system. The disk activity count provides a VAXcluster system aggregate count of disk 1/0 operations per drive for the six most active disks.

Note that MONITOR communicates with remote nodes through DECnet.

Capabilities have been added to allow the system manager to protect layered product images and other files from use by processes on selected nodes through access control lists (ACLs). A new identifier has been added which incorporates the name of the node on which a process is executing. This identifier is of the form SYS\$NODE_nodename. See the Release Notes for hints on using this capability to allow restricting the use of layered products on a common system disk to the subset of nodes which are licensed for the use of the layered product.

- o DIGITAL command language (DCL)
 - New subroutine CALL and GOSUB constructs.
 - Symbol table enhancements.
 - SHOW DEFAULT changes.
- o Batch and print
 - Enhanced handling of forms on print queues.

Includes the ability to specify a default form type for print queues. Thus, a print job specifying no form type will use the printer specific default form, rather than being forced to use the systemwide DEFAULT.

- New /RESTART qualifier.

The queue manager will automatically restart on recovery from a job controller abort. Batch and output queues will be restored to the status that existed prior to the interruption of service.

- User defined I/O routines for symbionts.
- o Record Management Services (RMS)
 - Descending key support.
 - fully shared sequential support (read/write and fixed/variable length sequential files).
 - Hyphens in file names.
- o Run-Time Library (RTL)

The Run-Time Library includes a new package to facilitate programming for DECtalk.

- o Security
 - System services \$GETUAI, \$SETUAI, \$CHECK ACCESS.
 - Allows addition and removal of identifiers from process and/or systems rights list.
 - New ACL editor.

o DEBUG enhancements

- Shareable image debugging.
- New display/window features.

New Documentation

The VMS V4.4 document set is being changed considerably from the V4.2 set. Approximately 9000 pages of new or modified documentation will be included for a page count of approximately 15,000. Some portions of the document set are being considerably reorganized. The Reference Shelf will include 19 binders, three more than V4.2. Some of the Guides are being moved into the Reference Shelf. The Automatic Distribution System (ADS) kits will include the three extra binders.

MicroVMS will have 2000 new and changed pages.

Refer to the article on "VAX/VMS Version 4.4 Documentation Changes" in The Buffer for more detail.

New Release Notes

o Release Notes information

Generally, point release (for example, V4.1, 4.2, 4.3) Release Notes are considered cumulative. That is, everything in all previous Release Notes, as far back as V4.0, applies, unless specifically superseded. The V4.4 Release Notes are intended to supersede all previous Release Notes.

o New upgrade procedure

The VMS V4.4 Release Notes has a completely rewritten section on the upgrade procedure. The new section is complete, especially in the area of various VAXcluster systems upgrade techniques. Please read this carefully.

o Miscellaneous V3 information

Various information taken from the VMS V3 documentation set which was found in the appendixes of the V4.0 Release Notes is now included in various manuals in the V4.4 set.

As a remastered upgrade, V4.4 can be either installed onto a new system disk or applied to upgrade an existing V4.3 or V4.2 system disk.

Also revised, though largely similar to earlier versions, is the upgrade procedure itself, which takes a substantial amount of time. Plan on at least 2 to 3 hours for a VAX-11/780. One portion, in particular, announces that it will take 30 minutes. Do not be disturbed if this section runs for an hour or more.

VAX/VMS V4.4, whether installed or applied as an upgrade to V4.3, includes a mandatory update. The mandatory update must be applied immediately after the installation/upgrade.

A mandatory update is generally planned for most releases involving significant new functionality, especially those with kits involving large media. They are planned in advance; they are not afterthoughts to fix some disastrous problem found at the last minute. They are included in the planning process to allow VMS engineering to fix those problems discovered in the testing of the final kit submitted to the Software Distribution Center (SDC). Generation and testing of the SDC kit takes several weeks, and the planned mandatory update avoids the need to regenerate the entire kit when last minute problems are discovered.

Both the Release Notes section on the upgrade and the installation guides have been revised.

Version 4.4 "Gotchas"

o Mixed V4.3/V4.4 VAXcluster systems

As explained in the Release Notes, VMS V4.4 will coexist with one or more V4.3 nodes in the same VAXcluster system. This is intended only to allow VAXcluster systems to be upgraded gradually, without the need to bring down the entire VAXcluster system for the upgrade. There are restrictions to running in this mixed fashion.

- In a VAXcluster system, V4.4 can coexist with V4.3 for the period of the upgrade. It will not coexist with versions earlier than V4.3.
- MAIL.

The format of the mail file has changed slightly under V4.4. If a user on a V4.3 node attempts to send mail to a user whose mail file has been created by a V4.4 node, an error will result.

- RMS shared sequential files.

As discussed in the Release Notes, the use of shared sequential RMS files is restricted in a mixed VAXcluster system. If you are running a mixed VAXcluster system, you must set SYSGEN parameter VMSD1 on all V4.4 nodes to a value of 1. This informs V4.4 RMS that V4.3 nodes exist in the VAXcluster system, and RMS thus uses a more conservative sharing algorithm. The mixed VAXcluster system algorithm forces the use of a multiblock count of 1, which is less efficient than the normal value. Also, if a V4.3 node finds a multiblock count greater than 1, it returns a RMS\$_FLK (file locked by another user) message. Thus, RMS is protected from data corruption should a V4.4 node not have VMSD1 set to 1.

Remember to set VMSD1 back to 0 when the last V4.3 node is upgraded to V4.4.

o MicroVMS multiuser license kit must be reinstalled

Because MicroVMS V4.4 is packaged as an upgrade that replaces all system executable images, the multiuser license kit must be reinstalled after upgrading your current MicroVMS system to V4.4. If you initially purchased MicroVMS V4.1M or V4.2, the existing multiuser license kit will not install correctly on MicroVMS V4.4. It must be upgraded using a conversion procedure supplied with MicroVMS V4.4.

After completing the installation of MicroVMS V4.4, invoke the conversion procedure from the SYSTEM account with the command:

\$ @SYS\$UPDATE:CVTLICENSE

The procedure will prompt you for a device on which to read your present license kit. It will read the kit, make the necessary changes, and write a new copy of the converted kit, which you may then install using VMSINSTAL.

The conversion is necessary only for multiuser license kits that were shipped with MicroVMS V4.1M and V4.2. License kits shipped with V4.3 and V4.4 do not require conversion.

o Text Processing Utility (TPU)

- The default file type for section files is now .TPU\$SECTION instead of .GBL.
- The format of section files has changed. Thus, users who have created their own section files or tailored the standard files must recompile their section files.

Those creating tailored section files should always keep their modifications in the form of a source file, since TPU may be expected to require recompilations from time to time.

- Section files are now installable as shared images.

VMS Layered Products

Most layered products are unaffected by the upgrade to VMS V4.4. However, a few layered products do require reinstallation, and existing versions of a few layered products are incompatible with VMS V4.4

Products which require reinstallation after the upgrade to VMS V4.4 include:

o VAX-11 RSX

VAX-11 RSX must be reinstalled to reset the alias directory names which are broken by the VMS upgrade.

- o Professional Host Tool Kit
- o VAX Encryption

VAX Encryption must be reinstalled because the base VMS system has a dummy shareable image for Encryption which is linked to facilities, such as BACKUP. The reinstallation of VAX Encryption installs the "real" shareable image for Encryption, plus DCL command and HELP texts.

- o VAX RPG II
- o DECnet-VAX

The DECnet-VAX V4.0 key is still used.

See the SSOT (System Software Order Table) for a listing of the versions of these layered products to be installed. The latest SSOT is available on the EASYNET at SQM::SYS\$SPD:SSOT.mmyy, where mm is the current month and yy is the current year.

The following product versions are known to be incompatible with VMS V4.4. Newer versions must be used instead:

- o VAX ACMS V1.2 Version 1.2 of ACMS must not be used under V4.4 of VMS. If ACMS is to be used, V2.0 must be installed. See important additional information in the Release Notes.
- o VAX LISP/VMS V1.2

- o VAX DECreporter V1.0
- o VAX PSI and PSI Access V3.2
- o VAX DEC/Shell V1.1

Again, see the latest SSOT for the latest information on which versions are applicable.

2.1.6 VAX Volume Shadowing V1.0

This article discusses several aspects of Volume Shadowing, such as its operation, use, installation, and impact on applications and system management.

What Is Volume Shadowing?

VAX/VMS Volume Shadowing is a new VMS product that is designed to help eliminate a dependency on a single disk drive. It performs this function by maintaining several identical copies of a given disk. Several physical disk drives known as "shadow set members" or "member units" are associated with a software created "virtual unit" to form a "shadow set." When the shadow set is first created, the member units are made to contain exactly the same data by a copy process that is described later in this article. All accesses by software to the shadow set are directed to the virtual unit, rather than to the The controller then translates these individual member units. accesses to the virtual unit into accesses to the individual member units. In doing this, read operations can be directed to any member unit that is available in a fashion that optimizes performance. Write operations are automatically performed on all member units to ensure that they all contain exactly the same data. In the event that a member unit fails, the remaining member unit can still supply the requested data, since it contains the same data.

Shadowing is a joint effort between the hierachical storage controller (HSC) 50 or HSC70 storage controller and VMS. As such, the HSC must be present for shadowing to take place. The member units must be a type that is supported by the HSC, such as RA80s, RA60s, and RA81s. Additionally, the member units of a given shadow set must also be the same type of disk drive. For example, a shadow set can not be formed by mixing RA60s and RA81s. All of the member units are accessed through the same HSC whenever they are acting as a shadow set.

Additionally, the proper revision of software is required. The HSC must be at revision V3.00 and VMS must be at revision V4.4. The Volume Shadowing product consists of a manual and product key. The installation of the software key on a VMS V4.4 system turns on the shadowing functionality.

Where Can | Get More Information?

VMS Volume Shadowing Manual, Order No. Al-HI90A-TE

Sales Update, May 5, 1986, Volume 17, No. 22 VAX Volume Shadowing V1.0 Software Product Description (SPD) No. 27.29.xx

Computer Design, "Shadowing Boosts System Reliability," April 1985

What Are the Current Limitations and Restrictions?

The current restrictions are that there can be no more than two members in any given shadow set and that the number of shadow sets is limited to four per HSC (or per pair of HSCs, as in the case where the shadow set members are dual ported). The two-member limit was a very last-minute restriction and, unfortunately, the documentation was already completed when it was imposed. The choices were to delay the shipment of the product until more members could be supported or to ship in a limited form. The later was selected. This means that many of the examples in the documentation show more than two members. A future release of the product is expected to ease this restriction. It must be noted that this restriction is enforced. Attempting to add a third member results in a INCSHAMEM (Incompatible shadow set member) message.

What Is Volume Shadowing Useful For?

The obvious benefits to be gained from Volume Shadowing result from elimination of the dependency on a singe disk to contain data or programs. In accomplishing this, a customer gains the ability to continue uninterrupted operations while a broken or degraded disk is repaired and, subsequently, restored to service. The customer also gains some measure of protection against the sudden or gradual deterioration of a disk that normally results in the occurrence of forced errors that indicate uncorrectable errors have occurred while attempting to read existing data from the disk. This protection results from the shadowing product's ability to access another copy of the same data on another disk drive to replace the unrecoverable data with good data from another source.

There are also some benefits that are not so obvious. When a read operation is requested on a shadowed disk, the HSC is free to optimize the access between any of the shadow set members. This means there are some potential performance benefits to be derived from shadowing whenever the characteristic I/O generated by an application includes a high ratio of reads to writes. A common system disk with secondary page and swap files located on another disk is a good example of this type of application. The performance aspects of shadowing allow more systems to use the same system disk than are otherwise possible. This allows the customer to take advantage of the ease of management aspects of a common system disk while, at the same time, the data availability aspects of shadowing eliminate a potential single point of failure for the VAXcluster System. Another example is a database where the read to write ratio is high.

Yet another benefit is the ability to reduce down time for critical applications while performing backups of data. Prior to the advent of Volume Shadowing, it was necessary to stop all access by an application to a data disk while the disk was being backed up to have complete assurance that the data was in a consistent state. meant that the data was unavailable for a potentially long period of time while the actual backup operation was performed. With Volume Shadowing, it is still necessary to stop application access and dismount the disks being backed up, but since there are multiple copies of the disk, the shadow set can be immediately remounted with all but one member and the application restarted. The disk that was not included in the shadow set can be backed up concurrently with application access to the reformed shadow set. This means that the critical application is only down for as long as it takes to run down activity, dismount the disk, remount the disk, and restart the application.

How Complicated Is Volume Shadowing to Use?

From the user's or application's point of view, as long as the disk is mounted as an ODS2 structure, it is no more complicated to use Volume Shadowing than to use a single disk. The only obvious change is that the name of the disk changes when it becomes shadowed. For example, an RA81 that was called DUA1 may become known as DUS25. The "S" indicates that the unit is a "virtual unit" and a shadow set. The unit number "25" is assigned by the system manager. Once a disk is part of a shadow set virtual unit, it can no longer be accessed by its old physical name. Users and applications writers must use the name of the virtual unit. If they avoided the use of physical device names as was always recommended, this change of names should present no problem.

From the system manager's perspective, shadowing has only slightly more impact. Since the device names change, the system manager should be prepared to listen to the complaints of the people that have ignored all warnings and continued to use physical device names. This aside, all management commands for Volume Shadowing are an extension to the existing commands MOUNT, DISMOUNT, and SHOW DEVICE. There are no new commands to learn. When initially installing Volume Shadowing, the system manager should plan out what disks make sense to shadow, what the virtual unit numbers should be, and be prepared to dismount the disks clusterwide. This is required because the formation of shadow sets is accomplished using the Mount commands and changes name by which the devices are accessed. Users can not create a shadow set that contains physical drives that are mounted somewhere else in the VAXcluster System. Also, when creating a shadowed system disk, users must be prepared to shut down all systems that boot from that disk for the same reason. The system manager should also be prepared to ensure that the latest copy of VMB.EXE is located on the systems console media and must make modifications to the default boot command file. The system manager should also be aware that a significant amount of work is needed for an application to use foreign mounted shadow sets. If such an application exists, then it is probably safe to assume that the disk being used by the application is not a candidate to become a shadow set at this point in time.

From the application designer's perspective, shadowing may range anywhere from no impact to great impact. If the application does not attempt to mount or dismount disks explicitly, then there should be no impact at all, providing that device names are not hard coded into the application. If the application does perform Mounts and Dismounts directly using the existing system services, then shadowing has only a slight impact, since control of shadow sets is given through extensions to the existing \$MOUNT, \$DISMOU, and \$GETDVI system services.

If, on the other hand, the application designer uses foreign mounted disks, then the impact of Volume Shadowing is rather significant. While the Volume Shadowing product does nothing to hinder the use of foreign mounted disks, it also does little to help use them. Control of a foreign mounted disk is still allowed using the normal system services, but several of the features that are provided with ODS2 mounted disks are absent by necessity. These features are the algorithm that selects initial members of a shadow set and assigns copy operations as needed. The applications designer who uses foreign mounted disks must provide the equivalent algorithm. While the Mount system service allows copy operations to be assigned, it is the responsibility of the application to decide what disks to copy to and from. The application must also provide algorithms for any type of error recovery which may be required in the VAXcluster System. This in itself can present a nontrivial design problem.

Volume Shadowing maintains multiple copies of the data contained on a disk transparently to a user application. When a shadow set is initially created, the Volume Shadowing product ensures that all disks contain identical data. This is accomplished by selecting at least one disk to be the initial full member of the shadow set. The Mount command entered by the user specifies one or more physical disks that are potential members of the shadow set. The Volume Shadowing software examines each of these disks and selects the disk or disks that are to become the initial member or members of the shadow set. In the case where a shadow set is initially formed from a nonshadowed disk, there can be only one initial member. This is the disk with a volume label that matches the one specified in the Mount command. Given that two or more of the disks have the same label and that none were previously part of a shadow set, the disk whose name appears first on the command line and has a label that matches the label on the Mount command is chosen to become the first member. In the case where the shadow set previously existed at one time and is now being reformed, several disks may be chosen to be initial members if the shadowing software determines that they all contain exactly the same data. This can be the case when the previous shadow set is properly dismounted.

Whatever the case, the software first selects the initial membership of the shadow set. Once this is accomplished, the software initiates copies from a full member of the shadow set to all members that are determined to be different from the full members. In the process of selecting initial members and adding members with copy operations, all the disks are brought online to the same HSC controller. While these copies are in progress, users and applications may freely access the data that is on the full members. The Volume Shadowing product ensures that any changes made to the full members are also propagated to the members that are undergoing a copy operation.

Once a shadow set is created, additional disks can be added at any time. The Mount command is also used to add a new member to the shadow set. A copy is always initiated from a full member of the shadow set to a newly added member to ensure that both disks contain identical data.

Individual members may be removed from a shadow set at any time using the Dismount command. The shadow set can even function with a single member, but of course there is no data redundancy. It should be noted that the removal of a single disk from a shadow set does not ensure that the removed disk is left in a consistent state. No attempt is made to close open files and write operations may be in progress. The best way to remove a disk from a shadow set and ensure that data is in a consistent state is to dismount the entire shadow set and then remount it minus the member that is to be removed. Since the entire shadow set is properly dismounted, all files are closed.

Both VMS and the HSC play a key role in the total product. During shadow set formation, VMS decides whether or not disks contain identical information. In doing this, it selects the initial members of the shadow set and assigns the proper copy operations to the remainder.

When shadow sets are formed, dissolved, or simply have their membership changed; VMS marks the disks that are currently in the set with a shadow generation number. This ensures that each of the member disks in the set that contain identical data can be clearly distinguished from disks that are not in the set or from disks that are undergoing copy operations.

Also, since the restoration of an image backup creates a disk that has the same data as the source disk but located in different places because of compression, a mechanism was developed to allow the shadowing product to distinguish between the two disks. This is accomplished by storing a Backup Revision Number on the disk. This number is changed by VMS BACKUP during the restoration process to allow the restored disk to be distinguished from the source disk. When given a choice between the restored disk and the source disk, VMS chooses the restored disk to become the initial member of the shadow set. Note that the STANDALONE BACKUP that existed prior to VMS V4.4 does not implement this feature. You must rebuild STANDALONE BACKUP after the installation of VMS V4.4, just as the VMS V4.4 Release Notes suggest.

VMS also plays a key role in failure recovery. When an HSC is rendered inoperative, VMS attempts to rebuild the shadow set on the same or an alternate HSC. This is accomplished by locating as many member units as can be found and then reconstructing the shadow set from them. If the drives are dual ported between two HSCs, the shadow set is rebuilt on the alternate. If only one HSC is present, the shadow set is rebuilt on the HSC when it is finally restored to service, unless mount verification has timed out.

The HSC, on the other hand, is called on to ensure that disks containing dissimilar data are made to contain the same data. VMS may instruct the HSC to copy from one disk to another, but the HSC does the actual copy internally to itself. If a read operation is directed to the shadow set while a copy is in progress, the HSC satisfies that read from any of the full members, and it is free to optimize the access in any way that it deems appropriate. If a write is directed to the shadow set, the HSC ensures that both the full members and the sources of a copy contains the same data when the copy is complete. The actual write command does not complete until all members of the shadow set are written.

The HSC is also called on to repair disks that deteriorate as a result of uncorrectable errors. For example, if an uncorrectable error occurs in a block on a disk that is a full member of a shadow set,

then the HSC accesses the same block on another full member to obtain a copy of the good data. It then copies the data onto the disk that originally had the problem. Of course, if no full members are available to supply the corrected data, the HSC must ensure that all drives contain identical data and that the forced error indicator is set for that block on all drives.

What Are the Limitations of Volume Shadowing?

While Volume Shadowing can be used as tool to help perform backups of data, it does not in any sense replace the need for those backups, nor does it eliminate the need for proper transaction journaling. For example, if a write operation with incorrect data is directed to the shadow set because of a software or hardware error, the Volume

Shadowing product correctly performs its function and ensures that all members of the shadow set contain the same incorrect data.

What Considerations Should Be Made with Regards to Performance?

For optimal performance, it is recommend that each member of a shadow set be located on a different disk data channel module (K.sdi) in the HSC. This is because each disk data channel can only perform one data transfer at a time. Using separate disk data channels allows parallel transfers on multiple disks. This is especially important to write performance where the same data must be written on each member of the shadow set. Instead of doing the writes sequentially, they can be performed in parallel. In the case of read performance, several disks can be read at the same time at different locations. Also, reads to one disk can be overlapped with writes to another if permitted by file locking operations. Note that the assumption of a gain in read performance assumes that enough reads are outstanding at the same time to permit parallel transfers.

Before predicting the effect of shadowing on performance, one must understand the ratio of reads to writes and the number of commands that are outstanding at the same time. This is demonstrated by the following conclusions extracted from a study of shadow set performance by the VMS Performance Group.

- o For most systems, optimal performance is obtained with a two-member shadow set split across K.sdi channels.
- o The best shadow set performance relative to a single RA81 for a typical system is in an environment with a high read I/O to write I/O ratio, little seeking, two shadow members on separate K.sdis, and sustained disk queues of greater than O. Performance is typically 70 to 100% better than a single RA81 in the same environment.

o The poorest shadow set performance relative to a single RA81 for a typical system is in an environment with a high write 1/0 to read 1/0 ratio, little seeking, two or more shadow set members on the same K.sdi, and sustained disk queues less than 1. Performance for a two-member shadow set is typically 30% below a single RA81 in the same environment. Performance degrades further for shadow sets with more than two members.

What Can Be Done Regarding Troubleshooting Failures?

As always, one of the best things to do when faced with a problem is to get an accurate description of the problem. This includes a detailed description of the sequence of events and/or operator actions that resulted in the failure. The best description is one that allows reproduction of the problem, since the first step to solving a problem is finding a reliable way of reproducing it. Things to look for when faced with a problem with Volume Shadowing are as follows:

Console Printouts from All Systems

On VAX nodes, some of the more interesting messages are in the form of %SYSTEM-I-MOUNTVER. These messages describe when copy operations start and stop. They also indicate when mount verification aborts. In general, the OPCOM messages are a restatement of the MOUNTVER messages and, as such, can generally be ignored.

On the HSC nodes, the consoles may describe errors that occurred on the disk or controller at the time of the problem.

Operators Logs

Operators logs contain the OPCOM messages that result from the MOUNTVER messages and, as such, can be useful if the console printouts are unavailable.

Error Logs

There are at least two reasons to look at the error log:

- o Many problems result from a disk error. This information can be found in the error logs.
- o Many of the actions taken by VMS with regards to shadowing occur in the context of mount verification. When VMS is faced with an error condition that it can not handle and is not severe enough to warrant crashing the system, it aborts mount verification. This results in a nonfatal INVDSKCONFG bugcheck and an entry in the error log. The values in RO, RI, and R2, that are displayed in the bugcheck entry are significant and can be used to isolate the problem. A chart located in module DUMNTVER.LIS of DSDRIVER can be used to interpret these values. Save the error log for analysis by the appropriate people. Nonfatal bugchecks can also be converted to fatal errors and generate crash dumps when the SYSGEN parameter BUGCHECKFATAL is set to a 1.

Crash Dumps

If an error is so severe that VMS can not recover or does not trust the integrity of the system, a fatal bugcheck is generated. For shadowing, this is a INVDSKCONFG bugcheck and, as was the case with the nonfatal version previously mentioned, the values in RO, Rl, and R2 may be significant. Save the crash dump for analysis. It is beyond the scope of this article to give a detailed method for analyzing the crash dumps.

What Are the More Obvious Things to Look for When Troubleshooting?

Problems when booting from a shadowed system disk:

- o Is the latest version of VMB on all of the consoles? For a quick check, it should be at least 55 blocks in length. The older VMBs do not support booting from a shadowed system disk.
- o Is the SYSGEN parameter VMS7 set to a 1? This causes DSDRIVER, the disk class driver that contains shadowing support, to be loaded rather than DUDRIVER, the disk class driver without shadowing support. Attempting to boot from a shadowed system disk with VMS7 set to a 0 results in a DISKCLASS bugcheck when the normal disk class driver, DUDRIVER, is instructed to perform operations on a shadowed system disk. For a quick check, conversationally boot and check the value of the VMS7.

FOR INTERNAL USE ONLY

o Is the shadowing key installed? The system bugchecks when the nonshadowing disk class driver is instructed to perform operations on a shadowed system disk while booting. For a quick check, see if SYS\$SYSTEM:DSDRIVER.EXE exists on the system. If the shadowing key was not installed, this file is not present.

Problems with quorum disks and shadow sets:

A shadow set or a disk that is a member of a shadow set can not be used as a quorum disk. Attempting to do so means that the quorum file is not used and that the votes that would be contributed by the quorum file are not present. If a quorum file is required, it must be located on a nonshadowed disk. Also, a quorum file need not be located on the system disk so one may have a shadowed system disk and still have a quorum disk, as long as they are different disks.

Problems when booting VMS V4.3 in the presence of Volume Shadowing:

VMS V4.3 hangs when booting from an HSC that has virtual units on it because of VMS V4.4 systems running shadowing. To prevent this hang, copy SYS\$SYSTEM:DUDRIVER.EXE from the VMS V4.4 system to the VMS V4.3 system.

Problems after installing shadowing:

- o Was the system rebooted after the installation of the key to load the proper shadowing driver?
- o Was the SYSGEN parameter VMS7 set to a 1 to cause the proper driver to be loaded? Also, was MODPARAMS.DAT edited to reflect the new value of VMS7?

Operational problems:

Are members not being found when a shadow set is formed during a system boot? Since the time when RA-type disks were first supported on HSC controllers, it states that it can take some number of seconds to configure the devices. Site specific startup command files should take this delay into account when mounting disks and wait as needed for the drive to be configured.

NOTE

Do not use an F\$GETDVI to determine if a virtual unit exists, as can be done with a physical unit, before mounting it. This is because the virtual unit never exists until after the first time it is mounted.

Configuration problems:

- Attempting to add a third member to a shadow set results in an INCSHAMEM error. Attempting to exceed the number of shadow sets supported by the HSC results in an REMRSRC (Insufficient system resources at remote node) error. Note that while the supported limit of shadow sets is currently four, the HSC, at the time of this writing, actually allows eight shadow sets to be formed before returning an error when its limits are exceeded. These limits are expected to be raised in a future version of the product.
- o Are all the disks that form a shadow set dual ported between the same two HSCs? Not doing so can cause problems during shadow set formation and controller failover.
- o Are the configuration restrictions being exceeded by mounting more than the maximum supported number of shadow sets on a combination of two HSCs? If one of the HSCs fails, some of the shadow sets can not be formed on the other because the limits of a single HSC is exceeded.

Quick Reference of Selected Topics for the System Manager

Command to mount a single FILES-11 volume:

```
$ MOUNT DJS23: -
$_ /SHADOW = ( $1$DJA9:, $1$DJA5: ) -
$ MYVOLUME DISK$MYVOLUME
```

Command to mount a FILES-11 volume set:

```
$ MOUNT DUS23: -

$_ /SHADOW = ( $1$DUA9:, $1$DUA5: ), -

$_ DUS51: -

$_ /SHADOW = ($1$DUA12:, $1$DUA3:) -

$ WORK1,WORK2 WRKD$
```

Qualifiers to the Mount command:

/NOCOPY Qualifier - Provides protection against user error by only creating a shadow set from the disks that do not require copy operations.

/CONFIRM Qualifier - Provides the greatest control of interactive mounts. VMS interrogates all volumes and presents a summary of actions that are performed. The user can say yes or no to the entire mount request.

/CLUSTER Qualifier - Shadow sets can be mounted clusterwide or on a subset of VAXcluster nodes, such as nonshadowed disks.

Shadow set membership appears the same to every node in the VAXcluster System that has it mounted. The shadow set can only be accessed from nodes which have the Volume Shadowing license applied.

Dismounting a Shadow Set:

DISMOUNT \$2\$DUS1 - Dismounts the shadow set on a single node.

DISMOUNT/CLUSTER \$2\$DUS1 - Dismounts the shadow set clusterwide.

Adding and removing members from a shadow set:

To add the disk \$2\$DUA7: to the shadow set \$2\$DUS1:

MOUNT/SYSTEM \$2\$DUS1:/SHADOW=\$2\$DUA7: VOL_NAME DISK\$VOL_NAME

When adding a disk to a shadow set, the Volume Shadowing software updates the shadow set membership on every node that has it mounted.

To remove the disk \$2\$DUA7: from the shadow set \$2\$DUS1:

DISMOUNT \$2\$DUA7:

When removing a disk from a shadow set, the Volume Shadowing software removes the disk from shadow set membership on every node that has the shadow set mounted.

Mounting previous members outside a shadow set:

Former shadow set members mounted outside of a shadow set are write locked by default by VMS V4.4. This behavior can be overridden by using the MOUNT /OVERIDE=SHADOW_MEMBERSHIP. The action of this qualifier is to zero the shadow generation number and ensure that the volume is always the target of copy when added back into a shadow set.

Restoring shadow sets from backup:

Volumes restored by VMS BACKUP from /IMAGE backup have the backup revision number updated by VMS V4.4 BACKUP so that the restored copy is clearly different from the source copy of the disk.

Considerations with rolling upgrades where VMS Versions 4.3 and 4.4 are mixed in the same VAXcluster System:

Since VMS V4.3 backup does not provide these protective mechanisms, do not mount previous shadow set members with VMS V4.3 and be sure to rebuild STANDALONE BACKUP on system disk and console media after the VMS V4.4 installation.

FOR INTERNAL USE ONLY

VMS V4.3 hangs when booting from an HSC that has virtual units on it because of VMS V4.4 systems running shadowing. To prevent this hang, copy SYS\$SYSTEM:DUDRIVER.EXE from the VMS V4.4 system to the VMS V4.3 system.

2.1.7 KNOWN VOLUME SHADOWING PROBLEMS IN VMS VERSION 4.4

The VAX/VMS Volume Shadowing product is starting to ship to customers. As with most first release products, Volume Shadowing has some bugs. Based on experience at customer field test sites, it is believed that these bugs will not cause very many complaints. However, if enough things go wrong at the same time, system crashes and/or other forms of confusion will almost certainly occur.

Therefore, this document has been prepared to make support groups aware of the known problems and to inform you that the support strategy for Volume Shadowing is business as usual utilizing standard escalation processes that exist in each geography. Escalation of problems into corporate support groups is also business as usual.

In addition, a copy of the volume shadowing software that will be distributed to VMS V4.5 field test sites has been packaged in a SPR installation kit, and is available to you for distribution to customers. The field test V4.5 shadowing software corrects the most significant problems found in V4.4. Exactly which problems are corrected by the V4.5 shadowing software will be indicated in the problem descriptions which follow. However, please remember that the V4.5 software is basically field test quality software. Although it is known to be superior to the V4.4 software in many respects, it has not yet been tested by real customers.

The V4.5 SPR installation kit goes by the VMSINSTAL name VMSSHD V94.4. It can be found in BULOVA::SYS\$PUBLIC. The kit is actually composed of three files:

VMSSHD944.A; 1 VMSSHD944.B; 1 VMSSHD944.C; 1

One very important warning about the field test V4.5 shadowing software kit: The kit installs a protected copy of the shadowing software and renames all existing copies of DSDRIVER.EXE to DSDRIVER.V44EXE. Because of this, installation of the field test V4.5 shadowing software kit MUST be followed by reinstallation of the volume shadowing license key. In addition, sites that are shadowing their system disk should read Section 4.4 of the VAX/VMS Volume Shadowing Manual before installing the field test V4.5 shadowing software kit. (Note: The message printed by the kit indicates that Section 4.3 should be read; this is incorrect). After the kit and the license key have both been installed, all nodes must be rebooted.

It is realized that this represents a significant amount of disruption for customers. Until better software licensing tools are available, however, we must distribute volume shadowing software updates in this way so that unlicensed customers do not obtain free use of the product simply by installing an appropriate update kit.

FOR INTERNAL USE ONLY

Remember, there is a limit of two members per shadow set. This limit is enforced by the VMS shadowing software. The V4.5 field test shadowing software provided on the kit described above does NOT alter the two members per shadow set limit.

PROBLEM LIST FROM VMS ENGINEERING

1. Symptoms

While processing a shadow copy operation, probably during shadow set failover attempt, the system crashes with an "INVALID EXCEPTION" bugcheck. The current stack is the interrupt stack and the IPL is 8. Examination of the dump shows the failing instruction is a "MOVL (R1), R1" at DSDRIVER+5Bxx, and R1 contains a small positive number or zero.

Fixed in 4.5

Yes.

Cause

A bad branch destination that is taken only when an attempt to remove a shadow set member fails because that member is the target of a copy operation.

Workaround

None.

2. Symptoms

When a shadow set that is undergoing a merge copy operation loses the member that is the source of the copy data, sometimes the member that is the target of the copy will be incorrectly promoted to a full shadow set member. When this happens, the shadow set hangs in mount verification until MVTIMEOUT expires.

Fixed in 4.5

Yes.

Cause

This is caused by a subroutine call that leaves member generation information unprotected in registers that the subroutine sometimes corrupts.

Workaround

Dismount and remount the shadow set. The member that was previously acting at the source of the copy operation is still valid and should be used to perform a full copy operation when the shadow set is remounted.

3. Symptoms

After a shadow set is dismounted, attempts to examine one or both of the member drives with the SHOW DEVICE command cause an access violation and stack dump on the terminal that executed the command. A later attempt to use the drive that causes SHOW DEVICE to take an access violation will crash the system with a "DISK CLASS DRIVER DETECTED FATAL ERROR" bugcheck. The bugcheck occurs at DSDRIVER+4Cxx and the bugcheck instruction is followed by a "CMPB #11, OA (RO)".

Fixed in 4.5

Yes.

Cause

If an HSC dissolves a shadow set but otherwise remains operational and mount verification executes an IO\$_PACKACK for that shadow set, the IO\$_PACKACK function incorrectly clears the UCB\$V_LCL_VALID flag for the shadow set virtual unit. This is a time-bomb. When the shadow set is dismounted, this time-bomb prevents proper cleanup of the shadow set members information in the I/O database. The residual information in the I/O database causes \$HOW DEVICE to take the access violation and the future mount command to crash the system.

Note

An HSC does not usually dissolve a shadow set without instructions from VMS. The only instance where this can happen is reduction of the shadow set to zero working members. This can be caused by a hardware failure of a shadow set member that is acting as the source for a copy operation or hardware failures in both shadow set members.

Workaround

Do not attempt to build a shadow set using a drive that causes SHOW DEVICE to take an access violation. Reboot the system at the first convenient opportunity.

4. Symptoms

Shadow sets timeout (or abort) mount verification long before MVTIMEOUT has expired. Examination of the error log shows a non-fatal "INVALID DISK CONFIGURATION" bugcheck at the time that the shadow set aborted mount verification. The non-fatal bugcheck entry shows RO=1A4 and R1=4. If BUGCHECKFATAL is set to 1, the system crashes.

Fixed in 4.5

Yes.

Cause

A shadowing I/O operation performed by mount verification resulted in an error that mount verification considers to be fatal, even though it is a perfectly reasonable error under the circumstances. If one VAXcluster host fails to properly handle a shadow set failure, many of the other VAXcluster hosts are likely to trip over this bug.

Workaround

Keep BUGCHECKFATAL set at its default value, O. Dismount, check for problem number 3, and remount shadow sets that abort mount verification.

5. Symptoms

Attempts to mount a non-shadowed volume report the "INCONSISTENT SHADOW SET MEMBER" error. This has been reported exactly twice during the 6 months plus life of the bug. Both times the error occurred on a BACKUP command executed as a part of trying to build VMS layered product kits. Theoretically, occurrence of this error is not limited to systems that have volume shadowing installed.

Fixed in 4.5

Yes.

Cause

This is caused by an incorrect register reference in the disk class driver's IO\$_PACKACK processing.

Workaround

None.

6. Symptoms

Normally the HSC is expected to remove disks with serious problems from a shadow set. If however a failing disk can cause the HSC to crash or be host cleared, there is nothing to prevent that disk from being added back into the

shadow set when it is rebuilt on the alternate HSC. This can then cause that HSC to crash or be host cleared. Note that this problem exists even without shadowing. Since almost the entire disk must be accessed when rebuilding a shadow set, this problem tends to be aggravated by shadowing.

Fixed in 4.5

No. Additional MSCP architecture work plus implementation by both the HSC and VMS is required to prevent this problem.

Cause

The exact cause for the failure to remove broken disks from a shadow set is unknown to VMS.

Workaround

Remove the disk from service and get it repaired.

7. Symptoms

Actual, functional members are removed from a shadow set during a failover. After an HSC fails, the VMS waits up to 45 seconds from the time that it finds the first previous member of a shadow set until if finds all previous members, or gives up. When this timer expires, the shadow set is reconstructed with those members that were found. Members that were not found are removed from the shadow set. Sometimes a disk will take more time than VMS waits to become available on the alternate HSC, resulting in its removal from the shadow set.

Fixed in 4.5

Yes and no. This problem tends to aggravate problem 2 (described above) under multiple failure conditions. Problem 2 is fixed in V4.5. However, the root problem -- failure of drives to become available on the alternate HSC -- requires changes in the HSC and/or the drives themselves.

Cause

The exact cause for disks not becoming available on their alternate path in a timely fashion is unknown to VMS. The current time-out times for the HSC/Drive is 255 seconds, according to the specifications.

Workaround

Add the removed disk back into the shadow set using the MOUNT utility either manually or automatically from a batch job.

8. Symptoms

Problems combining processors running Volume Shadowing with processors running versions of VMS prior to V4.4 in the same VAXcluster. The problems are exhibited as a hang when booting a processor running an early version of VMS from an HSC where a shadow set exists or as a heavy load being placed on an HSC where a shadow set exists from continuous disk unit polling by the early version of VMS. This problem has also been seen causing a high rate of receiver ready collisions on RA60s.

Cause

The older version of the disk class driver loops continuously, polling for disk unit status, when it encounters a virtual unit upon establishing a connection to the HSC.

Fixed in 4.5

No. The problem exists in the early versions VMS and can not be fixed by a new version.

Workaround

For VMS 4.3 systems -- the disk class driver, DUDRIVER.EXE, from the VMS 4.4 system can be used on the VMS 4.3 system. The Volume Shadowing Reference Manual, section 4.7, describes a procedure to accomplish this.

Note

Coexistence of V4.3 and V4.4 systems on a single CI is supported only during the "rolling upgrade" process. Most customers should not start using shadow sets until the "rolling upgrade" process is complete.

For VMS 4.2 and below -- Versions of VMS prior to V4.3 are not supported in the same VAXcluster as VMS 4.4. Please note that this problem is independent value of the SYSGEN parameter VAXCLUSTER. That is, systems with VAXCLUSTER=O (not a recommended setting) also exhibit the problem.

2.1.8 CIBCI

The CIBCL is a VAX CI (Computer Interconnect) port for DIGITAL's new bus, the VAXBI. It will provide VAX8200/8300 and VAX8800/8500 systems that utilize the VAXBI with an interim means of connecting to a VAXcluster. The CIBCL is an interim product providing these systems with a CI connection.

The CIBCI is the fifth implementation of the VAX-11 CI Port Architecture, the first four being the CI780, CI750, HSC50, and the HSC70.

The CIBCI totals five modules. They are two newly designed modules, BI adapter control (BAC) and BI adapter data (BAD), and the existing CI750 port processor (CDP), CI packet buffer (IPB), and CI LINK (ILI) modules. Like the CI750, the CDP, IPB, and ILI modules will be contained in a dedicated 10.5" rack mountable box.

The CIBCI initially shipped on some VAX 8800 systems at revision Al. Currently, the latest supported revision is Bl. Since updates to the hardware are happening between diagnostic releases, there is a possibility that you will end up with some "out of rev" diagnostics on your site. The two charts below show the compatibility between old and new CIBCI hardware and diagnostics.

(CIBCI		Al		Bl			
T1017 T1018 L0400 L0101 L0100	(BAD) (CDP) (IPB)	E	31 31,C1 41		C1,D1 C1 B1,C1 J1 D1	Ì	•	

The diagnostics listed under hardware revision Al were shipped in conjunction with CIBCI hardware.

HARDWARE	REVISION	A 1	1	B 1		
- EVCKA		 1.5		1.6	·	
- EVCKB		1.4		1.4		
- EVCKC		1.4		1.4		
- EVCKD		1.12		1.12		
- EVCKE		1.9		1.9	(minor	problem described below)
- EVCKF		1.6		1.6		·
- EBSAA		9.1		10.0	*	
- EZSAA		9.1		9.1		
- EVGAA		3.5		3.5		
- EVGAB		3.5		3.5		

When running revision Al CIBCI's, the diagnostics listed under Al run without error. If you receive a CIBCI with hardware at Bl you will experience difficulties when attempting to run some of the diagnostics listed under hardware rev Al. Here is a list of the problems.

FOR INTERNAL USE ONLY

Supervisor problems - If you have a T1017 at rev C1 or D1 you will find out that the VAX8200/8300 supervisor EBSAA rev 9.1 will not work with the revision 5 BIIC installed into the C1BC1. You will need EBSAA revision 10.0.

When using supervisor EBSAA rev 10.0, diagnostic EVCKA rev 1.5 fails test 18 on a VAX8200/8300 EVCKA rev 1.6 solves this problem.

When running EVCKE rev 1.9 on a VAX8800/8500 it fails test one (1) approximately once every 5 passes.

All of the corrected diagnostics with the exception of EVCKE, will be available in diagnostic release 24. The date of SDC submission for release 24 is 19-MAY-1986.

This is explained, along with other CIBCI anomalies, in the July VAXSTUFF.

2.2 SUMMARY OF REQUIRED CHANGES

The following summarized the revision level of the major components for upgrading to VAXcluster Revision F1:

ITEM	REVISION LEVEL	MFG. Break-In *****
VMS	V4.4 FOR ALL CPU NODES	05/86
C1750	El (HARDWARE), V7.0 (MICROCODE)	07/15/86
C1780	F1 (HARDWARE), V7.0 (MIRCOCODE)	07/15/86
нѕс50	EI (HARDWARE), V3.0 (MICROCODE)	07/86
нѕс70	B1 (HARDWARE), V3.0 (MICROCODE)	07/86
11785	REVISION 02	10/18/85
TA81	REVISION B1	03/01/86

The following provides a summary of the required FCO EQ kits for revision F1:

OPT ON	FCO NUMBER	FCO NUMBER	EQ KIT NUMBER	AVAILABILITY ******
C1750	C1750 REV E1	C1750-E1-1-004	EQ-01421-01	07/15/86
C1780	C1780 REV F1	C1780-F1-1-005	EQ-01422-01	07/15/86

The above EQ kits are available through Stockroom 17.

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2.3 CONTACTS FOR ASSISTANCE

Any questions concerning information contained in this document should be directed to:

US AREA - REGIONAL VAXCLUSTER CORE GROUP MEMBER (ref. list in Appendix A.1)

EUROPE - ATHOL HARRISON, 41-22-969191, GVAO4::HARRISON

GIA - STEVE EDDLESTON, AKOVO4::EDDLESTON, DTN 244-6679, 1-617-264-6679

CHAPTER 3

ADDITIONAL INFORMATION

3.1 VAXCLUSTER SYSTEMS OPERATING WITH MULTIPLE VERSIONS OF VMS

VAXcluster Rolling Upgrade Policy

It is essential that customers upgrade their VAXcluster to the latest revision as soon as possible. We recognize that some customers may have to run mixed revisions while they work to install one revision across all VAXcluster nodes. During the period in which they run mixed revisions, new functionality provided by the latest release will not be supported.

This position is taken based on the inability to provide complete regression testing for all components that could possible be included in the VAXcluster System. For more information see 2.1.5 VMS V4.4.

3.2 CUSTOMER BENEFITS IN UPGRADING TO REVISION F1

The VAXcluster verification testing for the new BI-based processors, the VAX 8200, the VAX 8300, the VAX 8500 and the VAX 8800, is now complete. With the completion of this testing, VAXcluster Engineering announces full support for the broadest range of processing and storage power within a common architecture in the industry. From the 0.6 MIPS VAX-11/750 to the 12 MIPS VAX 8800, from DEC's oldest VAX technology to our newest, this support gives your customer more flexibility, capacity and growth potential within a single system management domain than ever before.

FEATURES AND BENEFITS FOR VAXcluster RELEASE F1

o Extremely broad capacity range - VAXcluster customers may now configure incremental growth patterns without the need to overconfigure processor power.

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- o Include BI components in the VAXcluster System The new system bus technology on the newly supported VAXes in the VAXcluster provides better interfaces into the system than ever before.
- o VAX/VMS V4.4 VMS V4.4 provides your customer with several VAXcluster System level benefits. These include
 - VAXcluster DECnet node name and addressing. This allows a VAXcluster to act as a single node address within a local area network, or wide area network, enhancing the single system management domain of VAXclusters.
 - VAX/VMS MONITOR utility All monitor displays can now be performed for any node in the VAXcluster System from any other node in the system. The new MONITOR CLUSTER display provides a summary of CPU, disk I/O and memory and lock activity on up to six VAX nodes across the entire VAXcluster System.
 - Access Control Lists (ACLs) ACLs allow your customer's system manager to control access to shared files, layered products and programs, giving an additional level of control over secured or licensed files in the VAXcluster System.

VAX DIAGNOSTIC RELEASE-24 CONSOLE MEDIA AND C1780/C1750/C1BC1 MICROCODE CHANGE

!!!!!!!!!! IMPORTANT WARNING !!!!!!!!!!!

Starting with VAX Diagnostic Release #24 (15-JUL-86), all VAX CONSOLE Media for all VAX CPU types will have C17*0/C1BC1 Microcode V7.0 as the default "C1780.BIN" C1 microcode file. This version (V7.0) of C1780.BIN is not compatible with older C1780s or C1750s containing V3.0 C1 microcode PROMS (used with V3.0-V5.0) on the Revision "H1" L0101 Packet Buffer module.

!!!! CI WILL NOT BOOT !!!!

THEREFORE, THESE NEW VAX CONSOLES WILL NOT BOOT CI7xO/CIBCIS RUNNING V3.0, V4.0, OR V5.0 CI MICROCODE (V3.0 LO101 PROMS), WITHOUT COPYING V5.0 CI780.BIN FILE FROM OLDER CONSOLE MEDIA, OR OTHERWISE UPGRADING LO101 TO REVISION "JI" (V7.0 PROMS: DEC P/N 23-***F3, WHERE *** = 296-301) VIA CI FCOS.

As of 15-JUL-86, all new C1780s and C1750s will begin shipping with Revision "J1 or K1" L0101 (V7.0 PR0Ms) modules, coinciding with Diag. Release #24 distribution from SDC: thus the need for use of V7.0 C1780.BIN as default. Field C1750/C1780s will upgrade to V7.0 microcode via FCOs included in this document to be released JUL/AUG-86: C1780-F1-1-005 Rev. "F1"; C1750-E1-1-004, Rev. "E1".

VAX780/785/750: There typically is no need to upgrade CONSOLE (i.e., use REL-24 console) until a CPU-processor or C17x0 microcode upgrade FCO is being performed. These FCOs would provide details for managing C1780.BIN. However, if REL-24 CONSOLE media must be used with Rev-"H1" L0101 (V3.0 CI microcode PROMs), then V5.0 C1780.BIN must be copied from old CONSOLE Media with EXCHANGE in order to boot CI. Various Tech-Tips, VAXSTUFF articles, and older C17x0 FCOs detail C1780.BIN "EXCHANGE copying" and C1780.BIN file version identification.

VAX8600/8650: These CONSOLEs are routinely updated from UPDATE Magtape, which in Rel-#24 will contain V7.0 C1780.BIN as default, and C1780_V50.BIN and C1780_V60.BIN for use with Rev-"H1" (V5) or Rev-"HH1" (V6) L0101 if necessary. Procedures for using V5/V6 C1780.BIN are in VAX86*O CONSOLE RELEASE Notes, as well as HPS-CSSE VAX86*O CONSOLE/MICRO-DIAG BLITZ and memo to be included with Rel-24 CONSOLE media.

VAX8200/8300/8500/8800: Most field CIBCIs have been upgraded and all new CIBCIs have shipped with Rev. "JI" L0101 (V7.0 PR0Ms) modules since APR-86, and will not be affected by the default CI microcode version change. Please note from previous BLITZes that VAXclusters with BI-based VAXs (8200, 8800, etc.) should already have all CI7x0/CIBCIs upgraded to V7.0 CI microcode.

3.4 REV 7 MICROCODE BENEFITS

C1780 / C1750 / C1BC1 MICROCODE VERSION V7.0 BENEFITS

Version V7.0 of CI microcode was completed in FEB-86, ECO'd into CIBCI in APR-86, and pre-released for VAXclusters with severe CI problems since APR-86. Several hundreds of clustered VAXs have been running V7.0 since APR-86. V7.0 fixes several major CI errors caused by CI microcode, each problem of which is described in detail below. V7.0 also implements a "variable SANITY TIMER", to vastly improve VAXcluster detection and transition time for a "hung or halted" CPU. Incidental changes in V7.0 are:

- Removal of the CI "Maintenance commands", to simplify Engineering support and create additional patch space;
- Removal of MESSAGE & DATAGRAM FREE QUEUE caching, so avoid VMS memory loss when CI_PORT is re-initialized;
- Cl Microcode listing documentation clean-up to improve Engineering microcode supportability.

VARIABLE CI7XO/CIBCI "SANITY TIMER"

This is the most significant of all the V7.0 changes to most customers, especially those not currently having CI Errors. The CI Sanity Timer, formerly fixed at 99 seconds, is a mechanism for the CI_Port to "watchdog" VMS and the CPU: in the event of a software or hardware "halt or hang", the CI_Port will shut itself down preventing its automatic response to other node's CONFIGURATION POLLER "REQUEST IDENTIFICATION" commands. Thus, formerly, a 99-second period was required for other nodes to detect that a given CPU-node had abnormally stopped; it should be noted that normal VMS Shutdown and BUGCHECK operations circumvent this 99-second timeout.

With the new "Variable Sanity Timer", starting with VMS-4.4, VMS will use the effective value of VMS SYSGEN parameter "PASTIMOUT" (on CI_ports running V7.0 only) to control the CI Sanity Timout period: by default, this is ten (10) seconds. Please refer to VMS-4.4 Release Notes on "PADRIVER CHANGES" for more information.

CI_PORT ERRORS FIXED IN V7.0 MICROCODE

1. CI_PATH ARBITRATION TIMEOUT (ARB_TO) EXTENDED: The CI microcode formerly allowed insufficient time (1.5 ms.) for the CI Link Module (L0100) to arbitrate for the CI_path, resulting in PATH ARB_TO errors on large, busy VAXclusters. VMS will automatically recover from this error during the next VAXcluster PAPOLLINTERVAL (typically 5 seconds), thus never impacting VAXcluster availability.

This error was primarily aggravated by weaknesses in the L0100 CI architectual implementation, which is currently under ECO review, that caused frequest CI_Node collisions on the CI_path. This timeout is now extended to 26.6 milliseconds, eliminating the ARB_TO error even in the most rigorous testing. Note however that the L0100 problems on very large VAXclusters may still cause inexplicable PATH Losses, or Virtual Circuit closures, requiring special CI configuration treatment: refer to VAX FOCUS PRODUCT REPORT.

- 2. CI BUFFER DATA-TRANSFER-SERVICE ERRORS/LOSSES: A problem with the CI microcode's caching (in CI Local Store) of the CI_PORT BUFFER DESCRIPTOR TABLE entries, would occasionally allow use of a "stale" BUFFER_DESCRIPTOR entry in transferring a memory buffer to/from a disk or another node. This error may only occur on VAXclusters with 3 or more HSC50s, and more that 12 MIPs (Mega-Instructions-per-Second) of CPU power, and only under rare aggravated conditions involving CI_path or Virtual Circuit closures. The net result could be as severe as transfer of the wrong memory buffer (typically only 1-2 pages) to a disk file, with no CI hardware or VMS Error Log indications of a problem. Another related readily visible CI_Port error to this problem is the CI_BUFFER_LENGTH_VIOLATION "Response Packet" error status.
- In V7.0 CI microcode, this problem is fixed by additional BUFF_DESC_CACHE integrity checks, through use of a "Valid Bit" and an improved cache tagging scheme.
- 3. CI BUFFER LENGTH VIOLATION PACKET STATUS-ERROR: Refer to CI Microcode problem #2, above. This error would result in a CI_Port re-initialization, of which VMS-4.* only allows 50 retries prior to CI_Port shutdown.
- 4. CI_PORT MISCELLANEOUS ERROR TYPE=50000 (IQRE): The CI_Port "Internal Queue Retry Expired (IQRE)" Miscellaneous Error will cause VMS CI_Port re-initialization, of which only 50 are allowed before CI_Port shutdown. The cause was CI microcode mis-handling of a "Send Message" command failure that would trigger CI Virtual Circuit closure. This error is obvious in the VMS Error Log.
- 5. CI7xO "LOCAL STORE PARITY ERROR": The CI microcode will incorrectly handle messages/datagrams "apparently" received from CI_Node numbers higher than #15 (decimal), that might be accidentally received (with good CRC) due to CI_Path noise. Such messages cause referencing an area in CI Local Store that has not been initialized, resulting in CI "Local Store Parity Errors". This event triggers a VMS "CI PORT ERROR BIT SET" Error Log entry, and CI_Port re-initialization. This CI microcode problem is fixed in V7.0 CI microcode.

NOTE: There are also other legitimate hardware causes for the CI7xO LOCAL STORE PARITY ERROR, published in VAX FOCUS PRODUCT REPORT, that should also be investigated. 6. CI750 "READ-LOCK-TIMEOUT" MEMORY SYSTEM ERROR: The CI750 may intermittently exhibit "Read-Lock-Timout MEMORY SYSTEM ERROR" with heavily loaded VMS applications using the "BBSSI or BBCCI" machine instructions on a VAX-11/750. This CI error is seen in the VMS Error Log on VAX-11/750 systems as "CI Port Error Bit Set", and will cause CI_Port re-initialization. Another CI error which may appear from the same problem is "CI750 DATA STRUCTURE ERROR TYPE-'B' OR TYPE-'C'".

The problem lies within the VAX-11/750 CPU microcode for the BBSS1 / BBCC1 instructions, and is not due to a C1750 design problem. The cause is that the BBSS1 or BBCC1 may be interrupted after starting an "interlocked memory read" when the last instruction byte lies across a VAX page-boundary. This BBSS1/BBCC1 problem is fixed in Version "099" of VAX-11/750 CPU microcode, first released on VAX750 CONSOLE Media TU58 #41, P/N BE-T204K-ME in VAX DIAGNOSTIC RELEASE #23.

3.5 VAX-11/750 CPU MICROCODE VERSION "099" BENEFITS

Version "099" of VAX-11/750 CPU microcode was initially distributed with EVNDX VAX DIAGNOSTIC RELEASE # 23 on VAX750 CONSOLE Media (TU58 #41), Part Number BE-T204K-ME, within file "PCS750.BIN". The current VAX750 CONSOLE Media, Part Number BE-T204L-ME of DIAGNOSTIC RELEASE #24, is required with Revision "F1" within this VAXcluster REVISION MANAGEMENT DOCUMENT. Therefore, this new VAX750 CONSOLE update will be the first to officially upgrade CPU microcode to Version "099", which until now had only been documented in the VAX FOCUS PRODUCT REPORT.

A brief description of changes will be included here.

- 1. BBSSI / BBCCI INSTRUCTION "INTERLOCK READ" HANG (Also C1750 "READ-LOCK-TIMEOUT"/R.L.T.O. MEMORY SYSTEM ERROR): The C1750 may intermittently exhibit "R.L.T.O. MEMORY SYSTEM ERROR" with heavily loaded VMS applications using the "BBSSI or BBCCI" machine instructions on a VAX-11/750. The problem lies within the VAX-11/750 CPU microcode for the BBSSI/BBCCI instructions in that they may be interrupted after starting an "interlocked memory read" when the last instruction byte lies across a VAX page-boundary. This BBSSI/BBCCI problem is fixed in Version "O99" of VAX-11/750 CPU microcode, first released on VAX750 CONSOLE Media TU58 #41, P/N BE-T204K-ME in VAX DIAGNOSTIC RELEASE #23.
- 2. DIVP INSTRUCTION "SYSTEM HANG": The VAX750 microcode can hang in a loop when executing the DIVP under these unusual conditions Non-longword aligned Stack Pointer; Stack Pointer within 16 bytes of Page Boundary; FIRST-PART-DONE (FPD) bit set in PSL; and a pending UNIBUS Interrupt. This problem was predominantly seen with DIBOL Version 2.1 and earlier. It is fixed in Version 099 of CPU microcode.
- 3. "CALLS" INSTRUCTION: An incorrect "Reason Mask" may be delivered if an ACCESS-VIOLATION occurs when CALLS is used on a "write-protected" stack.

- 4. "EDITPC" INSTRUCTION: This instruction may incorrectly post a RESERVED OPERAND FAULT with rare, although legitimate operand strings. This is an obscure problem only seen in VAX Architectual testing.
- 5. "INTERRUPT / EXCEPTION" HANG WITH INVALID SCBB: If SYSTEM-CONTROL-BLOCK-BASE points to a non-existant memory location, a CPU interrupt or exception would result in an infinite microcode loop and hung CPU, until version 099 of VAX750 CPU microcode.

3.6 VAXCLUSTER REGISTRATION IN COLORADO

The CSC in Colorado provides a remote support service which plays an integral role in the servicing of VAXcluster Systems. As such, their ability to screen a VAXcluster System call quickly means quicker service to the customer, a more effective program, increased level of service to the Branch, and overall a more positive relationship between DEC and the customer. The registration information is stored in the comments field in the customer record on the HOST system in Colorado. During call screening the CRR becomes aware of this information which causes the CRR to undertake certain actions which normally would not be done - verification that the node is in a VAXcluster System, if an intermittent or undefined problem will send the call to the VAXcluster Support team immediately.

All VAXcluster Systems under Field Service contract in the US are to be registered with Colorado. Additionally, as new CPU's are added to the configuration, they too must be registered.

In order to register the VAXcluster System with Colorado, follow the steps listed here:

- * CALL THE CSC IN COLORADO 800-525-6570
- * ASK FOR VAX DATA ENTRY
- * YOU WILL BE ASKED FOR CERTAIN INFORMATION SUCH AS:
 CUSTOMER NAME
 LOCATION
 VAXCLUSTER CONFIGURATION
 SYSTEM SERIAL NUMBERS OF THE CPU'S, HSC'S

3.7 VAXCLUSTER WALL CHART KIT

VAXcluster Wall Chart: Your System At-A-Glance

DIGITAL's all new VAXcluster Wall Chart is a visual tool that helps you manage a VAXcluster System more efficiently and productively. Designed to give you a quick representation of the components of the VAXcluster System, the VAXcluster Wall Chart provides information on:

o CPUs

o Terminal servers

o HSCs

- o VAXcluster consoles
- o Disk drives
- o Terminals
- o Tape drives
- o Optional items you may want to include

The VAXcluster Wall Chart can be ordered through DECDirect (1-800-258-1710) part number EK-VCLWC-PK FOR \$250. This kit will be available August 25, 1986.

The VAXcluster Wall Chart has multiple uses. You can use the Wall Chart to make system management and operation easier, since the operations staff can see the most up-to-date representation of your VAXcluster System. Or, you can use the VAXcluster Wall Chart as a planning tool. You can do your software planning and management using the Wall Chart. You can also use the VAXcluster Wall Chart to plan for a new VAXcluster System, or to plan an upgrade of an existing VAXcluster System.

Service personnel who need quick access to information also benefit from the VAXcluster Wall Chart. The Wall Chart provides such information as:

- o Node names and numbers
- o Disk allocation class
- o System ID
- o Root directories
- o VMS version

furthermore, the VAXcluster Wall Chart can be used as a computer room display for customers and as a tool to show both information and needs to upper management.

The VAXcluster Wall Chart contains:

- o A white 35" X 45" wallboard
- o Vinyl labels for CPUs, HSCs, disk drives, tape drives, terminal servers, VAXcluster consoles. terminals, and blanks for optional display items
- o Clear plastic envelopes with insertable display cards for mounting on the front of devices in the computer room with information coinciding with that on the wall chart
- o Black vinyl letters and tape
- o A precision edge knife
- o A zippered portfolio for storing unused pieces

All vinyl labels can be easily positioned and repositioned on the chart so you can display your most current VAXcluster System configuration. The labels are simply peeled off and then pressed back into place. This means you can move the labels as often as you need, or want to. In addition, identification of cluster devices in the computer room is made easier when you mount the plastic envelopes with the insertable display cards. The kit contains adequate components to display a 16 node VAXcluster System.

3.8 TA78/81 SUBSYSTEM PROBLEMS

For some time now, there has been an investigation into the problems seen with DSA type tape subsystems. Most notably, the TA78 has seen the problems wherein it is sent off line, the formatter is declared inoperative and VMS BACKUP cannot 'restart', when it encounters excessive errors.

The intent here is to outline the errors that we have identified, the causes and cures that have been designed and the status of ongoing investigation.

The problems can be broken into four different problems, of which three have been fixed by different sections of the system.

- 1. Blank Tapes: a problem was discovered in the handling of blank or degaussed tapes. This error would cause the subsystem to hang and/or the HSC would declare the formatter inoperative.
- 2. Position Lost: a problem usually encountered when attempting to mount or initialize a tape (not necessarily blank). This would spawn a protocol error and result in the formatter being declared inoperative.
- 3. Restart: under VMS BACKUP, when excessive errors occurred, the restart option did not work.
- 4. Offline: under DSA, a tape drive cannot be taken off line without a problem. The HSC will declare the drive went offline without request. This is an unresolvable situation today.

What follows is a short dissertation on each of these problems.

1.) TS78 Microcode

PROBLEM

During the initialization process, it was discovered that an optimized rewind command was causing a problem between the HSC and the TS78. At the start of initialization, the tape attempts to read to determine if any valid data is resident on the tape. On occasion, it would attempt to read some random data, which it could not (of course) complete reading. At this point, the BOT flag had not yet been cleared and the position count is still zero, as nothing had been read, although the drive was down tape some distance. The rewind command optimization looked at the BOT flag, and seeing it set, would ignore the rewind, assuming it unnecessary at this time. Nothing else could happen in the drive, as everything else looks for the actual BOT status in the formatter buffer.

SOLUTION

The TS78 microcode has been re-written to un-optimize the rewind command for this situation. This was first tested at the University of Wisconsin, September 1985 and has been used in many sites since then.

SCHEDULE

It is now available in M8972 modules of rev. D1 and as a FC0 (#TA78-I-003/EQ #1419-01).

2.) HSC Code

PROBLEM

When a tape drive sets the 'Position lost' bit, it then sends a request to the HSC for 'Reposition for Termination'. The HSC would then clear the error bits in the TS78 and pass the failure back to VMS.

NOTE: This is the key to the failure. When the HSC cleared the error bits in the TS78, it DID NOT clear the position lost bit.

VMS would then issue a Set Unit Characteristics command to the TS78, via the HSC, with a Format byte of zero. The assumption from VMS was that the density that was being used was wrong.

When the HSC issued the SUC command to the TS78, the TS78 would reject the command because the Position lost bit was set.

At this point the HSC would retry the command twice with the same results and then declare the TS78 INOPERATIVE.

SOLUTION

The fix for this problem, in the HSC software, was to clear the Position lost bit during error recovery for the Reposition for termination thus allowing the subsequent SUC command to succeed.

SCHEDULE

HSC V3.00 has been sent 13 May 86 to SDC and release is expected by mid July. The numbers are: QX926-HG for HSC50 and QX927-H7 for HSC70.

3.) VMS BACKUP Utility

PROBLEM

Problem description and background

If, in the course of writing a save set to tape, the BACKUP or Standalone BACKUP Utility encounters bad media, or other excessive hardware-related or media-related errors, the utility will provide an opportunity for operator intervention in the backup operation.

If the output volume is the first volume in the backup operation, only QUIT and CONTINUE are available as recovery options. If, however, the output volume is some subsequent volume in the backup operation, then RESTART is also available. RESTART causes BACKUP or standalone BACKUP to restart the backup operation at the beginning of the current save set volume.

With version VMS 4.4 and earlier, if more than a threshold number of errors were detected on the output tape and the operator wished to select the RESTART option, the operator had to remove the tape from the drive before replying to the BACKUP prompt. On tape drives attached to an HSC this caused the tape drive to disappear from the controller's table of valid devices. In this event, the entire backup operation had to be restarted.

SOLUTION

With the installation of the SPR kit, the utility unloads the current tape from the drive as soon as the RESTART option is taken and then prompts for a replacement volume. It is important that the operator NOT load the new tape until the utility has prompted for it, after the RESTART option is taken.

After the SPR kit is installed, a new standalone BACKUP kit will have to be built using the STABACKIT command procedure, as documented in section 2.8.2 of the System Manager's Reference Manual.

NOTE: that a new method is required to recover from tape errors.

- 1. Answer 'YES' to BACKUP's restart prompt. This must be done BEFORE unloading the tape.
- 2. BACKUP will then unload the tape for you and prompt for a new tape to be loaded.
- 3. Load the new tape and answer yes to the prompt.

SCHEDULE

VMS patches to Backup have been received in SHR and CXO. All testing is complete in these labs and these patches are available to the test sites. When field testing is complete, these patches will be available to the field.

The VMS patches for V4.3 and V4.4 will be available on or about July 15,1986 subject to acceptance testing. They will be locate on the following node; CSSE32::sys\$public:TAP043 for V4.3 and CSSE32::SYS\$PUBLIC:TAP044 for V4.4.

4.) Drive Off Line

This situation occurs when a drive is taken off line or when an unexpected event occurs (loss of vacuum, bumping switch,etc). The system cannot recover from this situation. There has been a meeting between Tapes Engineering and VMS Engineering and three possible solutions have been proposed. These will be investigated and the best will be chosen and implemented. Meetings are ongoing to discuss further investigations of these alternatives.

SUMMARY

Although each of these is a fully independent item and none requires either of the others to be installed, it is recommended that all three fixes be installed to ensure that the tape subsystem is up to the highest level it can be.

HSC code is planned to be released to the field by mid July and the patches to VMS Backup will be released by VAXWorks after the test sites have reported that sufficient time has been logged and the patches are good. This should also occur around mid July.

APPENDIX A

ADDTIONAL INFORMATION

A.1 US AREA VAXCLUSTER CORE GROUP CONTACT LIST

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MID ATLANTIC RE	GION			
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 Phone
 Node
 Mail/stop
 Role

 DTN
 PK02-1/M21
 MGR

 Paul Houlihan
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 PK02-1/M21
 TECH

WESTERN REGION

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Software Technical Backup Unit:

Kelly Green DTN 521-2211 10378::GREEN WR03/T8 TECH

A.2 FCO SHEETS

	+- FCO 1	-+ 0	e1 f20
FIELD CHANGE ORDER	N.	mber: C1750-E1-1-004	
incorporates ECO LOI FCO upgrades LOIO1 P	rofit C1750s to Revision Le O1-MK009. Field spares sto Part Rev to "J1" and C1780 M s no longer restricted.	ck should also be upg	raded.
"internal queue retr "BUFFER LENGTH VIOL. BUFFER ambiguity by	Numerous Functional Errors y error"). 2. Need a vari ", potential DATAGRAM FREE c checking "valid" bit when BE_TO" V6.0 Cl_ucode Cluster	able sanity timer. 3 Q corruption and theo using cached BUFF_DES	. Fix
	owing locations should conta		u
Location Part #	Location Part #		
E114 23-296F3			
E 174 23-299F 3	E154 23-300F3	E134 23-301F3	
Compatibility/Prerec	uisite FCO:	Est. Time to Ins 1 hour per node	tall:
Special Tools or Tes	FCO Parts Informat		N
Order by Qu	uantity: Part Number:	Description:	
FCO Kit#: EQ-01421-01	uantity: Part Number: See Page -2-	besci iption.	
EQ-01421-02			
EQ-01421-03 FA-04706-01			
EQ Kit Variation Syst	em/Option Applic: N/A		
	Approvals		
CSSE Engineer	F.S. Product Safety	F.S. Logistics	
Bob Brassard		Ed Duggan	
CSSE Manager	F.S. Microfiche Libraries	Affected Population	•
Jan Sicard	1.3. MICIOTICHE LIBRATIES	Arrected Topulation	•
Jan Sicard	VAX		
ESD&P Micropub.	V 7/1/2	Initial Kitting:	
Fred Therriault		5. 5 5511191	
Revision:		Hardcopy Publicatio	n:
FCO Release Date		Parts Availability:	

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FCO C1750-E1-1-004

PAGE 2 OF 20

FCO KIT CONTENTS:

	Quantity	Part Number	Description	
EQ-01421-01	(1 EA)	23-296F3-00	Microcode ROM	
	(1 EA)	23-297F3-00	Microcode ROM	
	(1 EA)	23-298F3-00	Microcode ROM	
	(1 EA)	23-299F3-00	Microcode ROM	
	(1 EA)	23-300F3-00	Microcode ROM	
	(1 EA)	23-301F3-00	Microcode ROM	
	(1 EA)	36-19208-01	Wire Marker "1"	
	(1 EA)	36-19210-10	Wire Marker "J"	
	(1 EA)	FA-04706-01	FCO Document	
EQ-01421-02	(1 EA)	BB-FG70B-DE	Magtape	

NOTE: Magtape with 1 save-set "CIDO2O.A" containing CI78O.BIN @ V7.0, CI78O/CI75O functional (EVGAA/B) and Repair-level (EVCGA/B/C/D; ECCGA..E; EVCKA..F/CIBCI) diagnostics, 4 Supervisors (E*SAA), and EVXCI CI_Exerciser files.

Directory of CIDO20.A save-set is included ****

in APPENDIX-I of this FCO. *****

EQ-01421-03 (1 EA)

)

BB-F104A-ME

Magtape

NOTE: Magtape with only C1780.BIN file @ V7.0 for Self-Maint. customers without Diagnostic License.

FA-04706-01 (1 EA)

FCO Document

							
 	FCO	Level of Urgency ++ ++	Page1 0f_20				
FIELD CHANGE ORDER	3	Number: C178	0-F1-1-005				
incorporates ECO LO FCO upgrades LO101 F	trofit C1780s to Revi 101-MK009. Field spa Part Rev to "J1" and is no longer restrict	res stock should a C1780 MICROCODE to	lso be upgraded.				
Problem/Symptom: 1. Numerous Functional Errors ("arbitration Timeout", "internal queue retry error"). 2. Need a variable sanity timer. 3. Fix "BUFFER LENGTH VIOL.", potential DATAGRAM FREE_Q corruption and theoretical BUFFER ambiguity by checking "valid" bit when using cached BUFF_DESC. 4. "Sanity Timer/ARB_TO" V6.0 Cl_ucode Cluster hang problem.							
	E95 23-29	rt#Location 7F3E76	Part # 23-298F3				
Compatibility/Prered	quisite FCO:	,	ime to Install: per node				
Special Tools or Tes	st Equipment: See	Page -3-					
	FCO Parts I	nformation					
Order by Qu FCO Kit#:	uantity: Part	Number: Des	cription:				
EQ-01422-01 See Page -2- EQ-01422-02 EQ-01422-03 FA-04707-01							
EQ Kit Variation Sys	tem/Option Applic: N	/A					
	Approvals						
CSSE Engineer Bob Brassard	F.S. Product Safe	ty F.S. Lo Ed Dugga	gistics n				
CSSE Manager Jan Sicard	F.S. Microfiche Lib	raries Affected	Population:				
ESD&P Micropub. Fred Therriault	VAX	Initial K	itting:				
Revision:] 	Hardcopy	Publication:				
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FCO C1780-F1-1-005

PAGE 2 OF 20

FCO KIT CONTENTS:

	Quantity	Part Number	Description	
EQ-01422-01	(1 EA)	23-296F3-00	Microcode ROM	
	(1 EA)	23-297F3-00	Microcode ROM	
	(1 EA)	23-298F3-00	Microcode ROM	
	(1 EA)	23-299F3-00	Microcode ROM	
	(1 EA)	23-300F3-00	Microcode ROM	
	(1 EA)	23-301F3-00	Microcode ROM	
	(1 EA)	36-19208-01	Wire Marker "l"	
	(1 EA)	36-19210-10	Wire Marker "J"	
	(1 EA)	FA-04707-01	FCO Document	
EQ-01422-02	(1 EA)	BB-FG70B-DE	Magtape	

NOTE: Magtape with 1 save-set "CIDO20.A" containing C1780.BIN @ V7.0, C1780/C1750 functional (EVGAA/B) and Repair-level (EVCGA/B/C/D; ECCGA..E; EVCKA..F/CIBCI) diagnostics, 4 Supervisors (E*SAA), and EVXCI CI_Exerciser files.

***** Directory of CIDO20.A save-set is included **** **** in APPENDIX-I of this FCO. ***

EQ-01422-03 (1 EA)

BB-F104A-ME

Magtape

NOTE: Magtape with only C1780.BIN file @ V7.0 for Self-Maint. customers without Diagnostic License.

FA-04707-01 (1 EA)

FCO Document

A.3 VAXCLUSTER REVISION CONTROL DOCUMENT

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4.0	VAX-11/780 SYSTEM and OPTION CHARTS	88
5.0	VAX-11/782 SYSTEM REVISION CHARTS	94
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8.0	VAX 8300 SYSTEM REVISION CHARTS	98
9.0	VAX 8600 & VAX 8650 SYSTEM REVISION CHARTS	99
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1.0 INTRODUCTION

This document provides information concerning the compatible revision levels for all the components of a VAXcluster configuration. Revision information for hardware, software, firmware and diagnostics will be noted.

This is the sixth official release of the document. It will be updated as required and released via microfiche through the VAX Library updates and the Speed Bulletin. It is also available over the ENET in the following world access directory on VOLKS:

VOLKS::CLUSTER\$REV:CLUSTER.REV

The Revision Management Document for clusters is noted first, followed by individual option revision charts for the VAX CPUs, HSC50/70, UDA50, and disk and/or tape drives that are supported in a VAXcluster configuration.

There is some compatibility information noted; however, more detailed information on the CPUs and options is available in separate documentation. These documents, as well as how to obtain them, are noted in Section 21.0, Revision Documentation.

Read the NOTES Section (22.0) for important information relative to this update.

2.0 VAXCLUSTER REVISION MANAGEMENT DOCUMENT

This section contains a copy of the RM document that is maintained by the VAXcluster Engineering Office.

This document has been signed off by the appropriate groups.

Any discrepancies between the official document and this one will be noted with an (*) asterisk.

OPTION NAME | VAXcluster (TM) |

CONTROL PROCEDURE AND CONTACT

ECOs to VAXcluster components will be reviewed and a determination of whether the VAXcluster revision level should be changed will be made in accordance with general ECO procedures. Contact VAXcluster Program Office for change requests by contacting the Responsible individual as indicated below.

INITIAL RELEASE OF REV F1 - 30-MAY-1986
INITIAL RELEASE OF REV E4 - 24-FEB-1986
INITIAL RELEASE OF REV E3 - 04-OCT-1985
INITIAL RELEASE OF REV E2 - 04-APR-1985
INITIAL RELEASE OF THIS DOCUMENT - 04-JAN-1985

REV F	DRAWN: PAT McGRATH	DATE: 5/30/86	d i g i t a l		
DATE 5/30/86	 CHECKED:		TITLE VAXcluster REVISION MANAGEMENT DOCUMENT		
	RESP.ENG: KEN BAKER	DATE: 5/30/86	SHEET 1 OF 22 Document Number		
S T 0 R	MFG. ENG.: DICK GOLEMBESKI	DATE: 5/30/86	Document Number		
	FIELD SERVICE: LARRY GOELZ	DATE: 5/30/86 	SIZE CODE NUMBER REV K RM CLUSTER-V-O F 		

FOR INTERNAL USE ONLY

OPTION NAME VAXcluster (TM)							
BRIEF DESCRIPTION OF VAXcluster REVISION LEVELS	REV	ECO #					
A1 = 11780, 11782, C1780, VMS V3.1 B1 = Add HSC50, RA81, RA60, UDA50, VMS V3.4 C1 = Add 11785, C1750, VMS V3.6 D1 = Add VMS V4.0, update revision levels of most components E1 = Add DSA tape (TA78) and VMS V4.1 E2 = Add VAX8600 and updates to revisions of other components E3 = Add TA81 (new product); VMS 4.2; updates C1750 to REV D1 AND C1780 TO REV E1; HSC50 REV D1	N/A A B	N/A N/A N/A N/A TWOO1,1A MROO2,2A					
E4 = Add VAX8650 VMS V4.3 HSC70 F1 = Add VAX8200; VAX8300; VAX8500; VAX8800; CIBCI; updates	E	MROO4					
C1750 to Rev El and C1780 to Rev Fl; VMS V4.4.	F	MR005					
TITLE SHEET 2 OF 22							
MANAGEMENT DOCUMENT Document Number	· · · · · · · · · · · · · · · · · · ·						
	RE\ F	/ / 					

•	TION Xclus		(TM)							
GEN	ERAL	INFO	RMATI	ON:						
	the 1 time VAXc1	ates as p uste	t rev ossib	ision le. tems	as so The re funct	oon as eason ional	s practi for thi ity inte	cal and	eir VAXcluster s d over as short e have attempted during upgrades	a period of to maintain
			•							
1	TITLE SHEET 3 OF 22									
	VAXcluster REVISION MANAGEMENT DOCUMENT Document Number									
d	 	g	 i	 t	a	 1	SIZE K	CODE RM	NUMBER CLUSTER-V-O	REV F

OPTION NAME VAXcluster (TM) Rev F1 included the following: Release REV F1 May 30,1986 The F1 VAXcluster Revision permits the addition of VAX8200, VAX8300, VAX8500, VAX8800, and CIBCI. If the VAXcluster contains any of these processors. take note of the following requirements: All C1750s, C1780s, CIBCIs and Console Media in the VAXcluster must be upgraded to the minimum specified revision levels. All VAX hosts must use VMS Version 4.4 minimum. No restriction as to number or mix of the above BI processors in a VAXcluster. TITLE SHEET 4 OF 22 VAXcluster REVISION MANAGEMENT DOCUMENT Document Number | SIZE | CODE | NUMBER | REV 1 | K | RM | CLUSTER-V-O | F

OPTION NA VAXcluste		R E V	E 2	 E3	 E4	 F1
PART NO.	DESCRIPTION					
11/750	VAX-11/750 Computer Sys 	stem	58,78, 	58,78 	58,78 	58,78
BE-T204?-ME	TU58 #41 VAX 11750 CONS 	OLE	H		J	L
11/780	VAX-11/780 and VAX-11/7 Computer Systems 	782	7	7 	7 	7 7
AS-T213?-ME	RXL VAX 11780 STANDARD	CONSOLE	G	Н 	н] J
AS-T215?-DE	RX41 VAX 11780 EUROPE R CONSOLE 	RD	G		J 	К
AS-T216?-DE	RX4 VAX 11780 REMOTE CO	NSOLE	H	 	J	к
11/785	VAX-11/785 Computer Sys 	tem	00,01	02	02 	02
AS-T793?-ME	RX1A VAX 11785 CONSOLE		D	F	F	н
NOTES:	R E V I S I	H E C C C C C C C C C	W W O O O 2 2 A	M R O O 3	 M R O O 4	 M R O O
	N 	Y D A T	12/	10/ 04/ 85	2/ 24/ 86	5/ 30/ 86
d i g		REVISIO	Xcluster N MANAGEM OCUMENT	ENT	 SHEET 5	OF 22

OPTION NA VAXcluste							R E V		E 2	E 3	E4	Fl
PART NO.	DESCF	RIPT	I ON									
VAX8600	VAX8600 	Com	puter	Sys	tem			 	F1	Н	н	Н,Ј
 BC-T987?-ME	 VAX8600 	CON	SOLE-	- NO	DIA	G PACH	(В	E	F	F
 BC-T989?-DE	 VAX8600 	CON	SOLE-	 -W/D	 I A G	PACK		 	В	E	F	F
BB-T990?-DE	VAX8600	MAG	TAPE					 	N/A	N/A	N/A 	F
BB-FF58?-DE	VAX8600, 8600-6 8650-1	.0 P	ACKS	T0 6	5.2	TAPE		 	N/A	N/A	N/A 	B
								 			<u> </u> 	
NOTES:					R E V I S I	H I S T O R	E C O N O	T W O O 2	T W O 2 A	M R O O 3	M R O O 4	M R O O S
					N 	Y	 D A T E		4/ 12/ 85	10/ 04/ 85	2/ 24/ 86	5/ 30/ 86
d i g		 a	 1	 T T 	LE	REVIS	SION	M	uster Anagemi Ment	ENT	 SHEET 6	OF 22

	PTION AXclu			1)						R E V		E 2	E3	E 4	FI
Ρ/	ART N	10.	 [ESCI	RIPT	ON									
VAX8	3650		VAX8	8650	Comp	oute	r Sys	stem			 	N/A	N/A	В	B,C
BC-F	G45?	-ME	 VAX8 	8650		SOLE- PACI		O I AG	•		 	N/A	N/A	N/A	В
BC-F	 G47?	-DE	 VAX8 	 8650	CONS	SOLE	-W/D	 I AG .			 	N/A	N/A	N/A	В
 ВВ-F	G48?	-DE	 VAX8 	8650	MAGT	TAPE	FOR	1.2	PACK			N/A	N/A	N/A	В
 BB-F	F 58?	-DE	860	0-6	/VAX8 .0 P/	ACKS	T0 6	5.2	TAPE			N/A	N/A 	N/A 	B
			 						•						
			 									• • • • · · ·	 		
N01	ΓES:		·			• •• ••		R E V I S I O	H I S T O R	E C O N O	 T W 0 0	T W O 2 A	M R O O O O O O O O O O O O O O O O O O	 M R O O	M R O O O
								N - 	Y	 D A T E		4/ 12/ 85	10/ 04/ 85	2/ 24/ 86	5/ 30/ 86
d	 	g	 i	t	 a		 T	LE	REVI	SION	M.	uster ANAGEMI MENT	ENT	 SHEET 7	OF 22

OPTION NA VAXcluste		R E V	 E2	E 3	 E4 	Fì
PART NO.	DESCRIPTION					
VAX8200	VAX8200 Computer System		N/A	N/A 	· N/A	Α
VAX8300	VAX8300 Computer System		N/A	N/A 	N/A	A
BL-FG81?-ME	VAX8200/8300 CONSOLE 		N/A	N/A 	N/A	В
BB-FG87?-DE	 VAX8200/8300 COMPLETE DIAG (1600 BPI MT) 		N/A 	 N/A 	N/A 	В
VAX8500	VAX8500 Computer System		N/A	N/A 	N/A	Α
VAX8800	VAX8800 Computer System		N/A	N/A 	N/A 	B1
BT-ZMAAD-C3	VAX8500/8800 CONSOLE MEDIA		N/A	N/A	N/A 	V22C
ZM920-C3	 VAX8500/8800 DIAGNOSTIC SET 		 N/A 	 N/A 	 N/A 	V22C
NOTES:	R E H V I S S T I O O R N Y	E C O N O	 T T W W O O O 2 2 A	 M R O O 3	 M R O O 4	M R O O
		D A T E	4/ 12/ 85	10/ 04/ 85	2/ 24/ 86	5/ 30/ 86
d i g	.	SION	cluster MANAGEMI	ENT	 SHEET 8	OF 22

OPTION NA VAXcluste				R E V	 E2	E3	E4	Fl
PART NO.	DESCRIPTION							
C1750	VAX-11/750 Adapter Interconnect (CI)	to C	omput	er	C1 	D1	D I	ΕΊ
C1780	SBI Adapter to Com Interconnect (CI)	puter			D1 	E1	E 1	FI
CIBCI	BI Adapter to Comp	uter			N/A 	N/A	N/A 	В
	<u> </u>							
			-,- -					
NOTES:		R E V S O	H I S T O R	E C O N O	 T T W W O O O 2 2 A	M R O O 3	M R O O 4	M R O O
		N	Y	D A T E	4/ 12/ 85	10/ 04/ 85	2/ 24/ 86	5/ 30/ 86
d i g		TLE		SION	cluster MANAGEM CUMENT	ENT	 SHEET 9	OF 22

OPTION NA VAXcluste		1)						R E V		E 2	E3	E 4	Fl
PART NO.		ESC	RIPT	ION									
HSC50	Comp				onned ontro				 	C1,C2	DI	DI	Dl
HSC70	Comp					 t-b	 ased	Disk	 	N/A	N/A	 A1	A 1
	 	· -							 		 		
	. <u></u> -								 		 	 	
	· <u></u> -								<u>-</u> 		<u>'</u> 	<u></u> 	<u></u>
NOTES:						R E V I	н ! s т	C 0	 T W O O	T W O 2	 M R O	 M R O	M R 0
						I 0 N	0 R Y	N 0 	2 	A .	3 	4 	5
								D A T E		4/ 12/ 85	10/ 04/ 85	2/ 24/ 86	5/ 30/ 86
d i g		t	 a	 1	T T 	 -E	REVI	SION	M	uster Anagemi Ment	ENT	 SHEET 10	OF 22

OPTION VAXclus			M)						R E V		E 2	E3	E 4	F1
PART N	0.		DESC	RIPT	1 ON									
RA60	 - - !		-Mby noval		SA D	isk	Driv	 e			A5	A5,A7	A5,A7	A6,A7
RA80		121- (Win	-Mby	te D ster	SA D	isk	Driv	e			во	BO	% SEE NOTES	% SEE NOTES
RA81			-Mby			isk	Driv	e			E3,F H,J1	E3,F, H,J1	% SEE NOTES	% SEE NOTES
									~~~					
NOTES: % = NO RE	EV L	_EVE!	L RE	STRI	стіо	NS	R E V   S   O N	H I S T O R Y	E C O N O	T   W   O   O   2	T W O 2	M   R   O   O   3	M   R   O   O   4	M   R   O   O   5
							14	ı	D A T E		4/ 12/ 85	10/   04/   85	2/   24/   86	5/   30/   86
d   i	g	       	     t	       a	     	TIT	LE	REVI	SION	M	uster Anagemi Ment	ENT	      SHEET 1	1 OF 22

OPTION VAXclu		тм)						R   E   V	   	E 2	E3	E4	F1
TA78	TA	PE SU	BSYST	TEM					     	B1	Bl	B1	B1
TA81	TA   	PE SU	BSYS	TEM					     	N/A	Αl	A1	A 1
									r   				
				·					   				 
NOTES									   			   	   
NOTES:						R E V   S   O N	H I S T O R	E C O N O	T   W   O   O   2	T W O 2 A	M   R   O   O   3	M   R   O   O   4	M   R   O   O   5
						N     	Y	D A T E		4/ 12/ 85	10/   04/   85	2/   24/   86	5/   30/   86 
d   i	g   i	     t	     a		T T      	LE	REVI	SION	M	uster NAGEM MENT	ENT	      SHEET 1:	2 OF 22

OPTION NA VAXcluste					R   E   V		E 2	E 3	E4	Fl
PART NO.	DESCRIPT	ION								, 100 tar 100 tar 60 tar 60
VAX/VMS	VAX Virtual  System	Memory	Oper	ating		     	4.1A	4.2	4.3	4.4
						   	·			
						<u>-</u>				
NOTES:			R   E   V   S   O	H S T O R Y	0	T W O O 2	T W O 2 A	M R 0 0 3	M R O O 4	M R O O 5
			N   	۲	D A T E		4/ 12/ 85	10/ 04/ 85	2/   24/   86	5/ 30/ 86
d   i   g	i t a	TIT	TITLE   VAXcluster   REVISION MANAGEMENT   DOCUMENT				ENT	      SHEET 1:	3 OF 22	

OPTION NAME VAXcluster (TM) Release REV E4 REV E4 INCLUDED THE FOLLOWING: February 24, 1986 Release of VMS version 4.3 Release of VAX8650 Release of HSC70 INOTE: Please install all ECO'S to insure that the VAXcluster is at the highest possible rev at all times to reduce the possibility of incompatibility of SOFTWARE, uCODE, and HARDWARE which may cause failures. It is essential that Customers upgrade their VAXcluster to the latest revision as soon as possible. We recognize that some Customers may have to run mixed revisions while they work to one revision across all VAXcluster Nodes. During the period in which they run mixed revisions, new functionality provided by the latest release is not supported. If either a VAX8650 or a HSC70 is added to a VAXcluster Rev E4 is required throughout the cluster. The minimum acceptable VAX8650 rev is specified; later rev's, where available, should be used. C17XX New revision of the LO101 - Rev "HH1. This revision contains REV 6 Roms and must be used with C1780.BIN REV 6. These should ONLY be installed at customer sites that are having any of the following problems: 1. Buffer Length Violations 2. Arbitration Timeout Errors 3. Internal Queue Retry Errors 4. JATAGRAM FREE Q corruption LO101 REV "HH1" will NOT ship from manufacturing in CI7XX options and will ONLY be available to field service via EQ kit to upgrade customers having problems TITLE SHEET 14 OF 22 VAXcluster REVISION MANAGEMENT DOCUMENT |Document Number NUMBER | SIZE | CODE | REV | RM | CLUSTER-V-O |

OPTION NAME VAXcluster (TM) Release REV E3 REV E3 INCLUDED THE FOLLOWING: October 04, 1985 HSC50 has been UPGRADED to D1 The addition of the TA81 (requires HSC50 rev D1 and VMS 4.2) VMS version 4.2 The tape restriction has been eased. Now 8 tapes instead of 6 are allowed per HSC50 in any combination. The block transfer size restriction on transfers to tapes has been eliminated. CONSOLE MEDIA have been upgraded to include new microcode for C1750 and C1780. There is no change to the hardware ROM'S. The VAX 11/785 and the VAX8600 have had many changes made to their console uCODE. Refer to the appropriate revision matrix for complete details of the problems and fixes. INOTE: Please install all ECO'S to insure that the VAXcluster is at the highest possible rev at all times to reduce the possibility of incompatibility of SOFTWARE, uCODE, and HARDWARE which may cause failures. It is essential that Customers upgrade their VAXcluster to the latest revision as soon as possible. We recognize that some Customers may have to run mixed revisions while they work to one revision across all VAXcluster Nodes. During the period in which they run mixed revisions, new functionality provided by the latest release is not supported. TITLE SHEET 15 OF 22 VAXcluster REVISION MANAGEMENT DOCUMENT |Document Number NUMBER | SIZE | CODE | REV RM | CLUSTER-V-O

OPTION NAME VAXcluster (TM)	   				
Release REV E2 APRIL 12, 1985	REV E2 INCL	UDED THE	FOLLOW	VING:	
	VAX8600 to	the VAXo x8600, 1	luster.	ermits the addit . If the VAXclute of the follow	uster
		ust be u		d Console Media d to the minimum	
	Other hos	ts may ι	use VMS	use VMS Version Version 4.1 (m is preferred in	inimum) or
	must be a	t microd	ode lev 780, 11/	(8600 or VAX 11, /el 5 (minimum) /782, 11/785 sys	. UDA's
	  -  -	,			
TITLE		 	SHE	T 16 OF 22	
VAXcluster REVISI MANAGEMENT DOCUME		Documen	nt Numbe	er	
d   i   g   i	t   a   1	SIZE   K	CODE RM	NUMBER CLUSTER-V-0	REV F

OPTION NAME VAXcluster (TM) RELEASE REV E1 REV ET RELEASE INCLUDED THE FOLLOWING: MARCH 26, 1985 The El revision of the VAXcluster adds DSA tape functionality to the VAXcluster. Initial support for the TA78 tape is provided by VMS 4.1. Newer revisions of the C1750 and C1780 microcode are also available in this same time frame. These newer versions are not required by cluster revision El but the customer is encouraged to upgrade to these newer versions as they become available. The change to the newer revisions is accomplished by using the latest revision of the CPU console medium. The versions which provide this update are the following: CPU MODEL CONSOLE RESULTING CI REVISION VAX 11/750 BE-T204H-ME C1750 REV. C1 VAX 11/780 AS-T213G-ME C1780 REV. D1 AS-T216H-ME C1780 REV. D1
AS-T215H-ME C1780 REV. D1
VAX 11/785 AS-T793D-ME C1780 REV. D1 SHEET 17 OF 22 TITLE VAXcluster REVISION MANAGEMENT DOCUMENT | SIZE | CODE | NUMBER | REV a | 1 | K | RM | CLUSTER-V-O |

OPTION NAME VAXcluster (TM) RELEASE REV DI | REV DI RELEASE INCLUDED THE FOLLOWING: |DECEMBER 20, 1984 The D1 release adds VMS Version 4.0 to the VAXcluster. Use of the VMS Version 4.0 requires the HSC50, C1750, and C1780 to be raised to the minimum revision levels indicated in the Revision Matrix. Uniform application of the latest revision throughout the VAXcluster is preferred. TITLE SHEET 18 OF 22 VAXcluster REVISION MANAGEMENT DOCUMENT |Document Number | SIZE | CODE | NUMBER | REV | t | a | 1 | K | RM | CLUSTER-V-O | F

OPTION NAME VAXcluster (TM)										
RELEASE REV C1  DECEMBER 20, 1984	REV C1 RELEASE INCLUDED THE FOL	LOWING:								
	C1750 and 11/750 Microcode are contained partially on hardware ROM's and partially on the Console Cassette. It is important to use corresponding revision levels the C1750, 11/750, and Console cassette as shown in to Cluster Revision Matrix. A local disk is required for 11/750 for revisions of the Console Cassette earlier than BE-T204F-ME.									
	If SYE version 3.6 is used on a C1750 a "C1780". The version of C1750 is SYE version 4.1 which V3.5.	f SYE which recognizes the								
!     	Minimum UDA50 controller microcode versions are: Level 3 - for 11780, 11782, 11785 Level 5 - for 11750									
	Refer to the "VAX/VMS Guide to details.	System Management" for								
	The required minimum revision 1 processor are as follows: 11750 revision 5 - with no UDA50 p Under no conditions can revision	50 present present								
	The required minimum revision lare as follows:	levels for the 11750 PCS								
	11750 revision 5 - PCS revision 11750 revision 7 - PCS revision	. <b>.</b> .								
  TITLE   VAXcluster REVISI	SHEET 19	9 OF 22								
MANAGEMENT DOCUME	Ŧ.									

OPTION NAME VAXcluster (TM)	
RELEASE REV B1 DECEMBER 20, 1984	REV B1 RELEASE INCLUDED THE FOLLOWING:
	The RA60 is supported with HSC50 Rev B2 or greater. RA60 revision level A5 (minimum) is required for RA60s in dual-port configurations only, otherwise Rev A4 (minimum) is permitted.
	Dynamic dual porting of Massbus disks requires the installation of existing ECOs. Contact Colorado CSSE for more details.
TITLE VAXcluster REVISI MANAGEMENT DOCUME	1

OPTION NAME VAXcluster (TM) RELEASE REV A1 REV AT RELEASE INCLUDED THE FOLLOWING: DECEMBER 20, 1984 | -----11/780 and C1780 Microcode is contained partially on hardware ROM's and partially on Console Media. It is important to assure that corresponding revisions are used. In all cases, the latest set is preferred. Minimum acceptable revisions are shown in the VAXcluster Revision Matrix. SHEET 21 OF 22 TITLE VAXcluster REVISION MANAGEMENT DOCUMENT |Document Number | SIZE | CODE | NUMBER REV 1 K RM CLUSTER-V-O t a

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#### 3.0 VAX-11/750 & VAX-11/751 SYSTEM REVISION CHART

(Refer to Option Revision sections for module revision information.)

11750 SYSTEM REV SID	40/48	50/58	60/68	70/78
KERNEL	40/48	50/58	60/68	70/78
RH750 FP750 KU750 DR750 DW750 KC750	01 01,02 00 01 00,01,02 00,01 N/A	01 01,02 01 01 01,02 01 B1-E1	01 01,02 01 01 01,02 01 B1-E1	01 02 01 01 02 01 B1-E1
DIAGNOSTIC RELEASE * (Minumum)		18	18	18
VMS RELEASE:	3.X,4.X	3.X,4.X	3.X,4.X	3.X,4.X
** CPU MICROCODE	094	096,097	098	098,099

#### Notes:

____

o VMS Version 4 backwards compatible to hardware Rev 40/48 in non-C1750 systems.

VMS V 4.0 requires minimum hardware Rev 50/58 (i.e. L0008) and minimum ucode Rev 98 FOR C1750 SYSTEMS with no UDA50's.

11/750 systems with UDA50s and CIs must be at Rev 7 prior to VMS V4.0 upgrades and UDA50 must be at Rev 5.

- o UDA50A Rev 5 refers to ucode Version 5.
- o Minimum rev for C1750 is B1. It is recommended that all C1750s be upgraded to the current rev.
- * Minimum release of diagnostics to support C1750 option revision.
- ** 11750 CPU with a C1750 must have 4MB of physical memory.

CPU Microcode version "099" (CMT099) corrects problems with the following instructions: BBSSI/BBCCI (causes CI750 Read-Lock-Timout MEMORY SYSTEM ERROR), CALLS, EDITPC, DIVP, INTERRUPT/EXCEPTION HANGS WITH INVALID "SCBB".

CHART 1

11/750 RE	V		40	50	60	70	70
KA750							Α
MODULE	SLOT	ETCH			<u> </u>	1 1	
L0002	2	C D	CS=C,C1   MR=C,D   CS=D   MR=D	CS=C, C1   MR=C, D   CS=D, E   MR=D, E	CS=C, C1   MR=C, D   CS=D, E   MR=D, E	CS=C,C1     MR=C,D     CS=D,E     MR=D,E	CS=C,C1 MR=C,D CS=D,E MR=D,E-E2
L0003	3	C D	CS=C1, C2   MR=E, F   CS=E, F   MR=E, F	CS=C2   MR=F   CS=F   MR=F	PR=H1     PR=H2	į	PR=J1,K1    PR=J2,K2   K3
L0003-YA	3	Α					PR=A1
L0004	4	D E	CS=H   MR=H, J   CS=J   MR=J	CS=J1  MR=K  CS=K  MR=K	PR=L 1     PR=L2	PR=M1,N1    PR=M2,N2	PR=P1    PR=P2
L0005	5	D	CS=E  MR=H	N/A  N/A	N/A  N/A	N/A   N/A	N/A
L0008-YA	5	В		PR=A1-	PR=A1-B2   B2	PR=A1-B2	PR=A1-B2
L0008-YB	5	В		PR=D2	PR=D2	PR=D2	PR=D2
M9313	28 A-B	В	CS=A  MR=A,B	CS=A  MR=A,B	CS=A	CS=A	CS=A
TU58-XA			F-K	F-K .	F-K	F-K	F-K
TU58-XB (54-13489	, ,	E F	F1-F4   H-L1	F1-F4   H-L1	F1-F4   H-L1	F1-F4   H-L1	F1-F4  H-L1
CONTROL PANEL (54-1379	    5)	С	CS=C  MR=C,D	CS=C  MR=C,D	CS=C  MR=C,D	CS=C   MR=C,D	CS=C  MR=C,D 
BACKPLN (70-16486	, ,		WL=B	WL=B	WL=B	WL=B	WL=B

Key: E = Etch Rev; CS = Circuit Schematic Rev; MR = Module Rev;
--- PR= Part Rev; WL = Wirelist Rev

CHART 2

11/750 RI	EV		48	58 =======	68	78   	78
KA750			1		<u> </u>		Α
MODULE	SLOT	ETCH			1		
L0002	2	C D	CS=C,C1   MR=C,D   CS=D   MR=D	CS=C, C1   MR=C, D   CS=D, E   MR=D, E	CS=C,C1  MR=C,D  CS=D,E  MR=D,E	MR=C,D CS=D,E	CS=C,C1 MR=C,D CS=D,E MR=D,E-E2
L0003	3	C D	CS=C1, C2   MR=E, F   CS=E, F   MR=E, F	CS=C2  MR=F  CS=F  MR=F	PR=H 1     PR=H2 	PR=J1,K1    PR=J2,K2,   K3	PR=J1,K1    PR=J2,K2   K3
L0003-YA	3	Α					PR=A1
L0004	4	D E	CS=H  MR=H,J  CS=J  MR=J	CS=J1  MR=K  CS=K  MR=K	PR=L 1     PR=L 2	PR=M1,N1    PR=M2,N2	PR=P1
L0005	5	D	CS=E  MR=H	N/A  N/A	N/A  N/A	N/A   N/A	N/A
L0008-YA	5	В	   	PR=A1-	PR=A1-B2   B2	PR=A1-B2  	PR=A1-B2
L0008-YB	5	В		PR=D2	PR=D2	PR=D2	PR=D2
M9313	28 A-B	В	CS=A  MR=A,B	CS=A  MR=A,B	CS=A	CS=A	CS=A
TU58-XA			F-K	F-K	F-K	j F-K	F-K
TU58-XB (54-1348)		E F	F1-F4   H-L1	F1-F4   H-L1	F1-F4   H-L1	F1-F4   H-L1	F1-F4  H-L1
CONTROL PANEL (54-1379	   5)	С	CS=C  MR=C,D 	CS=C  MR=C,D	CS=C  MR=C,D 	CS=C MR=C,D	CS=C  MR=C,D
BACKPNL (70-16486			WL=C	WL=C	WL=C	WL=C	WL=C

Key: E = Etch Rev; CS = Circuit Schematic Rev; MR = Module Rev;
--- PR= Part Rev; WL = Wirelist Rev

## KC750 OPTION REVISION DESCRIPTION

KC750 Revisi	on			00		01		02		03		04
Module	SLOT	ETCH			1							
L0006     	•			MR=*2 MR=B MR=C		MR=*3 MR=B1 MR=D						

### RH750 OPTION REVISION DESCRIPTION

RH750 Revi	sion	1	00	1	01	1	02		03		04
MODULE	SLOT	ETCH		1							
L0007	7,8 or 9     	C   D	CS=A MR=A	İ	CS=A1 MR=B CS=B, MR=B,				,		

## FP750 OPTION REVISION DESCRIPTION

FP750 Revi	sion		1	00	01		02	 	03	1	04
MODULE	SLOT	ETCH	]					   			
L0001	1	D     E		MR=B	MR=C	, D	PR=E2     PR=E1				

### KU750 OPTION REVISION DESCRIPTION

KU750 Revis	i on		00	01	0	2	03	04
	SLOT	ETC	1					
5413865-C	attaches	C	CS=B  MR=C	   N/A				
L0008-YB	5	A		PR=C2	1			
G&H Microcode			M198A  (2.0)	T909A 				

#### DW750 OPTION REVISION DESCRIPTION

DW750 Revision | 00 | 01 | 02 | 03 | 04 |

MODULE | SLOT | ETCH | | | | | |

L0010 | 10 | C | CS=B | PR=C1 | PR=E1, | | | | |

| MR=B | F1 | |

#### DR750 OPTION REVISION DESCRIPTION

-----

DR750 Revis	sion		1	00	1	01	١	02		03	1	04	
	SLOT	ETCH			1								
L0014		A		CS=A MR=A		PR=B	1						
DR750 Microcode				Г326А	7	7326A							

### KA750 OPTION REVISION DESCRIPTION

		+
ision		A
SLOT	ETCH	
2	C   D	CS=C,C1   CS=D,E
3	C   D	PR=J1,K1     PR=J2,K2,K3
3	A	PR=A1
4	D   E	PR=P1     PR=P2
5	B	PR=A1-B2
5	B	PR=D2
		WL=C
	SLOT  2 3 3	SLOT  ETCH  2

### C1750 OPTION LEVEL REVISION CHART

C1750	00	B1	C 1	D1,(**DD1)	E1
L0100	D1 (B2*)	D1(B2*)	D1,(B2*)	D1, (B2*)	D1,(B2*)
L0101	Fl	H1	HI	Hl,**HHl	11
L0009	A1,A2	A1,A2	A1,A2	A1,A2	A1,A2
L0400	B1	B1	B1,C1	Bl,Cl	B1,C1
Microcode					1
C1780.BIN	Rev 2.0	Rev 3.0	Rev 4.0	Rev 5	Rev 7
BE-T204*-M	E-F	G	Н	  **Rev 6.0	
Back plane	B1,C1	B1,C1	B1,C1	B1,C1	B1,C1
POWER SUPPL	Y A	Α	A	Α	

^{* (}NOTE) "B2" is being marked on "B" etch and is equal to "D1".
** (NOTE) Rev 6.0 uCODE and the "HH1" module are only used if you need to install FCO CI750-DD1-I-004 making the CI750 Rev DD1.

### 4.0 11780 SYSTEM REVISION CHART

11780-	01	02	2 A	03	3A	04	05
KA780- MS780A- MS780C- -or-	00,01 01 01	01,02 01 01	02,2A 01,1A 01,1A	03 01,1A 01,1A	03 01,1A 01,1A	04 01,1A 01,1A	04 01,1A 01,1A
MS780E- -or-						00	00
MS780H- DW780-	01	01	01,14	01,14	01,1A	00 01,1A	00 01,1A
RH780- FP780- C1780- DR780	01 01	01 01	01,1A 01,1A	01,1A 01,1A	02,03 01,1A	02,03 01,1A 01,C1 01	02,03 02 01,C1 01
11780-	06	6a		07			
KA780- MS780A- MS780C- -or-	05 01,1A 01,1A		,1A,1B	06 01,1A,1B 01,1A,1B			
MS780E- -or-	00	00		00			
MS780H- DW780-	00 01,1A	00 1B,C1 01	,1A,1B,C1	00 01,1A,1B	,C1(D1)		
RH780- FP780- MA780- KE780A-				B1, (C1) O2 O1	 AB1)		
KU780A- C1780- DR780	01,C1 01	01 01	,C1	01 F1 01			

KA780 OPTION LEVEL REVISION CHART

KA780-	00	01	02	2 A	03
M8218	C, (D)	C,D	C,D	C,D	C,D
M8219	B, (C)	B,C	B,C	B,C	B,C
M8220	A, (B)	A,B	A,B	A,B,(C)	A,B,C
M8221	A, (B)	A,B	A,B	A,B	A,B
M8222	A, (B)	A,B	A,B	A,B	A,B
M8223	(A)	A	Α	Α	Α
M8224	(B)	(c)	С	(D)	D
M8225	A, (B)	A,B	A,B	A,B	A,B
M8226	B, (C)	B,C	B,C	B,C	B,C
M8227	A,B,(C)	A,B,C	A,B,C	A,B,C	A,B,C
M8228	A,B,(C)	A,B,C	A,B,C	A,B,C	A,B,C
M8229	A, (B)	A,B	A,B	A,B	A,B
M8230	B, (C)	B,C	B,C	B,C	B,C
M8231	A,B,(C)	A,B,C	A,B,C	A,B,C	A,B,C
M8232	· (A)	A	A	A, (B)	A,B
M8233	A, (B)	A,B	A,B	A,B	A,B
M8234	(A)	Ā	A	A	A
M8235	E1,(F)	(H)	Н	H,(J)	(H1),(K)
M8236	(C)	С	С	C,(D)	C,D
M8237	*, (A)	ж, Д	*,A	*, Д	*,A
M9043 7013628	ж, (A)	*, A	∜*, A	*, А	*, Д
WIRELIST	(D)	D .	D	D	D

## KA780 OPTION LEVEL REVISION CHART (cont.)

KA780	04	05	5A	06
M8218	C,D,E	C,D,E	C,D,E	C,D,E
M8219	B,C,D	B,C,D	B,C,D	B,C,D
M8220	A,B,C	A,B,C	A,B,C	A,B,C
M8221	A,B	A,B	A,B	A,B
M8222	A,B,C	A,B,C,D	A,B,C,D	A,B,C,D
M8223	A,B	A,B,C	A,B,C	A,B,C
M8224	D	D	D	E,F
M8225	A,B	A,B	A,B	A,B
M8226	B,C	B,C	B,C	B,C
M8227	A,B,C	(B1,D)	Bl,D	B1,D
M8228	A,B,C,	(B1,D)	B1,D	Bl,D
M8229	A,B	A,B	A,B	A,B
M8230	B,C	B,C	B,C	B,C
M8231	A,B,C	A,B,C	A,B,C	B,C,
M8232	A,B	A,B	A,B	A,B
M8233	A,B,C	A,B,C	A,B,C	A,B,C
M8234	A,B	A,B	A,B	A,B
M8235	(J1),(L)	Jl,L	J1,L	H3,M
M8236	C,D	C,D	C,D	C,D
M8237	*,A	ж <b>,</b> Д	*,Д	*,A
M8238				A
M9043 7013628	. * <b>,</b> A	*,A	ж, Д	*,A
WIRELIST	D	D	E-K1	E-K1

MS780A OPTION LEVEL REVISION CHART

MS780A-	01	1 A	18
M8211	(c)	C,(D)	C,D,E
M8212	(A)	A,B,C	A,B,C,D
M8213	(D)	D, (E)	D,E
M8214	(c)	C	C, (D)
7013625	J. (4)	A. A	
WIRELIST	*, (A)	<b>%</b> ,Д	*,A

#### MS780C OPTION LEVEL REVISION CHART

MS780C-	01	1 A	18
M8210 M8212 M8213 M8214	(*), (A) (A) (D) (C)	*,A,(B) A,(B),(C) D,(E) C	*,A,B,(C) A,B,C,(D) D,E C,(D)
7013625	<del>-</del> .	-	Εl
WIRELIST	*, (A)	% <b>,</b> А	<b>አ</b> አ

**NOTE: Since we are now implementing part revision on the backplane, we will no longer track the wirelist revision.

#### MS780E OPTION LEVEL REVISION CHART

MS780E-	00
M8373	B
M8375	C
M8376	D1,E

Backplane 70-19729-00 A

#### MS780H OPTION LEVEL REVISION CHART

MS780H-	00	
M8374 M8375 M8376	A,A1,A2 C D1,E	
Racknlane		

Backplane 70-19729-00 A

## DW780 OPTION LEVEL REVISION CHART

DW780-	01	1.4	1 B	Cl	D 1
M8270	(c)	C, (D)	C,D	C,D	C,D
M8271	B,(C)	C, (D)	C,D,(E)	F1,F2	F1,F2
M8272	A,B(C)	A,B,C,(D)	C,D	C,D	C,D
M8273	(B)	B, (C)	B,C	B,C	(11)
M9042	(A)	A	A	A	A
M9044	A,(B)	B, (C)	B,C	B,C	B,C

#### RH780 OPTION LEVEL REVISION CHART

RH780-	01		1 A		02		03
M8275 M8276 M8277 M8278	C, (D) B, (C) C, D, (E) C, (D)		D, (E) B, C E C, D		D,E B,C E C,D		D, E (D) (F) C, D
M9041 7013627 WIRELIST	*, (A) D		<b>%, А</b> D		*, A (E)		ж, А Е
RH780 -	04		AAl	AB1	-		
M8275 M8276 M8277 M8278	D,D1,E, (E),(E1) (H) C,D,(E)		D-H E-E 1 H C-F	(J1,J2 E,E1 H C-F	)		
M9041 7013627	*, <b>A</b> (H)		*,A E	E			
RH780 -	В1	(C1)					
M8274 M8275 M8276 M8278	A 1 D-H E-E 1 C-F	A1 (J1,J2) E,E1 C-F					
M9041 7013627	*,A H	*,A H				**********	

### FP780 OPTION LEVEL REVISION CHART

FP780-	01	1 A	02
M8285	*, (A) B, (C) (B) (*) (C)	*,A	*,A
M8286		B,C,(D)	B,C,D
M8287		B,(C)	B,C
M8288		*	*
M8289		C,(D)	(C1,E)

### MA780 OPTION LEVEL REVISION CHART

MA780-	00	01	
M8210 M8212-YA M8258 M8259 M8260 M8261	A,B,C D A A,B,C A,B B	A,B,C D B D A,B	
M9045	Α	Α	
WIRELIST:			
MA780A: 70-16518	A	A	
MA780C:	A	A	

## KE780A OPTION LEVEL REVISION CHART

KE780A-	00
FPLA	23-065C6

## KU780A OPTION LEVEL REVISION CHART

M8238	(A)
KU780A-	00

### DR780 OPTION LEVEL REVISION CHART

DR780-	00	01
M8296	ж	*
M8297 M8298	* (A),(B)	* А.В
M8299	(B),(C)	וֹם
MICROCODE	T392A	T392B
M9046 WIRELIST:	*	y'c
70-16896	A	A

NOTE: ECO M8297-TW002 for DR780 was never signed and has been canceled. The DR780 option remains at Rev "01".

### C1780 OPTION LEVEL REVISION CHART

C1780	[C]	01	E1, (**EE1)	[F]
L0100	D1,(B2*)	JD1, (B2*)	D1,(B2*)	D1, (B2*)
L0101	јні	іні	H1,**HH1	ไปไ
L0102	[C]	[0],01	[C1,D1	jc1,D1
L0104	[C,C1,D1	[C,C1,D1	[C,C1,D1	[0,01,01
Microcode	j	i	j	İ
C1780.BIN	Rev 3.0	Rev 4.0	Rev 5.0	Rev 7.0
AS-T213*-DE	İF	İG	H	J
-	į		**Rev 6.0	i
BACKPLANE	j	i	j	j
70-17654	İA	İA	A	İA

### 5.0 11/782 SYSTEM REVISION CHART

11782	01
11780 SYSTEM	07
MA780	01

(See 11/780 options for revision information)

## 6.0 VAX-11/785 & VAX-11/780-5 SYSTEMS REVISION CHART

The following is a 'quick' check list for the 11785 unit rev. It DOES NOT replace the RM document but is to be used for reference only.

ALL REVS SHOWN ARE MINIMUM COMPATIBLE REVISIONS FOR EACH PART.

11785	00	01	02
	*00	В	С
KC785	00	Α	A
MS780-C	1 B	1B	1B
MS780-E	00	00	00
MS780-H	00	00	00
DW780	01	01	01
FP785	00	D	D
RH780	04,AA1	04,AA1	04,881
DR780	01	01	01
C1780	01	01	F1
MA780	01	01	01

^{*} Option Revision 00 is equivalent to A

FP785 OPTION REVISION CHART

FP785	00 B1 C	1 D1
M7540	B1   B1   C	1   C1,D1,D2
M7541	B1,C1 B1,C1  D	1   E1
M7542	B1   B1   C	1   C1
M7543	A1   A1   A	1   A1
M7544	B1   C1  D1              DD	,D2, D1,D2 1  DD1

## KA785 OPTION LEVEL REVISION CHART

_				
KA785	00	В	C 	
M7459	В	В	В	
M7460	В	С	С	
M7461	В	В	В	
M7462	В	В	В	
M7463	В	В	В	
M7464	В	В	В	
M7465	С	С	С	
M7466	В	В	В	
M7467	Α	Α	Α	
M7468	В	В	В	
M7469	С	С	С	
M7470	С	С	C	
M7471	Α	Α	Α	
M7472	В	В	В	
M7473	В	В	В	
M7474	В	В	В	
M7475	В	В	В	
M7476	В	 В	В	
M7477	D	D	D	
70-20579-01		C		
Machine uCODE SSU***.WCS AS-T793*-ME	01A B	01A/01B B/D	01C F	

#### 7.0 VAX 821 SYSTEM REVISION MATRIX

Option name:	/AV Book Cycres		
\   	/AX 8200 SYSTEM	A	
Part No.	Description		
VMS	OPERATING SYSTEM	V4.4	
ULTRIX-32*	OPERATING SYSTEM	V1.2	· • •
OPTIONS	VAX OPTIONS	REF	
EVNDX	DIAGNOSTICS (see Note 1)	24.0	
821XX	SYSTEM KERNAL	A	
EVNDX i number	number in the Revision column for is the minimum Diagnostic Release that is compatible with the Revision.		. <b></b>

Option name:			
	21 SYSTEM KERNEL	Α	
Part No.	Description		
T1001	VAX KA820 CPU Module	AB1	
т1008	2MB Memory Module	Al	
H7250	Power AC input Module	Al	
H7251	Power Regulator - +5B, +24, etc	Al	
H7253	Power Regulator - +5v etc.	Cl	
54-16133-01	Power Distribution Board	Bl	
H9400-AA	BI cage assembly	Cl	
54-16572-01	Polaris Control Module (PCM)	Al	
54-16574-01	Scorpio Control Panel (SCP)	Al	
877-D	120v Power controller	A 1	
877-в	240v Power controller	El	

^{*} Not Supported in VAXcluster Systems.

### 8.0 VAX 831 SYSTEM REVISION MATRIX

	/AX 8300 SYSTEM	Α
Part No.	Description	
VMS	OPERATING SYSTEM	V4.4
ULTRIX-32	OPERATING SYSTEM	V1.2
OPTIONS	VAX OPTIONS	REF
EVNDX	DIAGNOSTICS (see Note 1)	24.0
831XX	SYSTEM KERNAL	A
EVNDX inumber	e number in the Revision column for s the minimum Diagnostic Release that is compatible with the Revision.	

-	Option name:	O21 CVCTER MEDNEL		
		331 SYSTEM KERNEL -	A	
	Part No.	Description		
	The 831 is ide	entical to the 821 except for an additional MODULE	     A	   
	VMS	OPERATING SYSTEM	V4.4	

# 9.0 VAX 8600/VAX 8650 SYSTEM/OPTIONS REVISION CHARTS

The following is a 'quick' check list for the KA86 unit rev. It DOES NOT replace the RM document but is to be used for reference only.

KA86 UNIT REV = H3 = O7HEX (SID REGISTER)

UNIT	REV			
8600	Н3	H4	J1	кі
MODULES  LO200-BA (MEM) LO201-OO (CSL) LO202-OO (SBS) LO203-OO (SBA) LO204-OO (MCD) LO205-OO (MAP) LO206-OO (IDP) LO207-OO (ICA) LO208-OO (IBD) LO209-OO (EDP) LO210-OO (EBC) LO211-OO (EBD)	C1	C1	C1	C1
	E1,E2	E1,E2,E3,F1	E1,E2,E3,F1	E1,E2,E3,F1
	B1-B3	B1,B2,C1-C3	B1,B2,C1-C3	B1,B2,B3
	B1,B2,C1	D5,D6,F1	D5,D6,F1	B1,B2,C1-C3
	D5,D6	D3-D6,E1	D3-D6,E1	D5,D6,F1
	D3-D6	F3,F4,F5,H1	F3,F4,F5,H1	D3-D6,E1
	F3-F5	H3,H4,H5	J1	F3,F4,F5,H1
	H3-H5	F5,F6,F7,F8	F5,F6,F7,F8	J1
	F5,F6	C2,C3,C4,C5	C2,C3,C4,C5	F5,F6,F7,F8
	C2,C4	C3-C7	C3-C7	C2,C3,C4,C5
	C3-C7	D3,D4,D5	D3,D4,D5,E1	C3-C7
L0212-00 (FBA) L0213-00 (FBM) L0214-00 (ICB) L0215-00 (CSA) L0216-00 (CSB) L0217-00 (CLK) L0218-00 (FJM) L0219-00 (EBE) L0220-00 (MCC) L0222-00 (MTM) L0223-00 (FTM) L0224-00 (STM)	B3,E7,F1 C5,C7 F4-F8 B2-B4 B2-B5 C5,C6,E1 A2 B2-B4 K1-K4 B1,B2 A2	•	B3,E7,F1,H1&2   C5,C7,C8   H1   B2-B4   B2-B5   C5,C6,E1,E2   A2   B2-B4   K1-K4   B1,B2   A2   A2	
L0100-C1780 (IL1)	D1	D1	D1	D1
L0101-C1780 (IPB)	H1	H1	H1	J1,K1
L0102-C1780 (IDP)	C1,D1	C1,D1	C1,D1	C1,D1
L0104-C1780 (IS1)	C,C1,D1	C,C1,D1	C,C1,D1	C,C1,D1
M8270-DW780 (USI) M8271-DW780 (UCB) M8272-DW780 (UMD) M8273-DW780 (UAI) M9302-DW780 (UNIB	C,D F1,F2 D C	C,D   F1,F2   D   C	C,D   F1,F2   D   C	C,D   F1,F2   D   D1
TERM) M9040-SBI TERM M9044-DW780 (UBT)	A	A	A	A
	A2	B1	B1	B1
	B,C	B,C	B,C	B,C
M7485-YA UDA50	H1	H1	H1	H1
M7486-UDA50	H2	H2	H2	H2
M8739-TU81	B5	B5	B5	B5

### VAX 8600/VAX 8650 SYSTEM/OPTIONS REVISION CHARTS (Cont.)

UNIT		REV	1	REV	REV	REV
	8600	Н3	1	Н4	J1	K1
BACKPLA	NES 70-19193-01 (MEM)	A1,A2		A1,A2	A1,A2	A1,A2
	70-19194-00 (1/0) 70-19195-01 (ABUS)	C1,C2 A1,A2	•	A1,A2	C1,C2	A1,A2
	70-19196-01 (CPU   70-19197-01 (1/0)   70-19198-01 (CPU)	A1   A1   A1,A2		A1 A1 A1,A2	A1   A1   A1,A2	A1   A1   A1,A2
DIAGNOS	TIC MEDIA* BB-T990?-DE CSL T/	     DE E	: <u>-</u>	F	     F	 
	BC-T987?-ME RLO2 E BC-T988?-YE UPD TA	300T E		F A	F	F A
	BC-T989?-DE RL02 [ BB-FF15?-DE MACRO BB-FF58?-ME CSL PA	TAP D		F D B	F   D   B	F   D   B

SOFTWARE*

VMS OPERATING SYSTEM V4.1, 4.1A, 4.2, 4.3

*NOTE: READ FILE RL2REV.MEM FOR THE REVISIONS OF ALL FILES THAT

ARE ON THE RLO2.

NOTE: THIS IS NOT A TOTAL RM CHART OF THE 8650, SEE THE OFFICIAL RM DOCUMENT FOR COMPLETE DETAILS.

UNIT	REV		
8650	В1	B2	C1
MODULES  L0201-00 (CSL) L0204-00 (MCD) L0205-00 (MAP) L0206-00 (IDP) L0207-00 (ICA) L0208-00 (IBD) L0209-00 (EDP) L0210-00 (EBC) L0211-00 (EBD) L0214-00 (ICB) L0215-00 (CSA) L0216-00 (CSB) L0218-00 (FJM) L0219-00 (EBE) L0230-00 (MCC) L0231-00 (CLK)	F1 F1 E1 H1 H3-H5 F5-F8 C2-C5 C3-C7 E1 F4-F8 B2-B4 B2-B5 A2 B2-B4	F1 F1 E1 H1 H3-H5 F5-F8 C2-C5 C3-C7 E1 F4-F8 B2-B4 B2-B5 A2 B2-B4	F1 F1 F1 F1 F1 F1 F1 F1 F3 F5-F8 C2-C5 C3-C7 E1 H1 B2-B4 B2-B5 A2 B2-B4 A1,A2 B1
L0222-00 (MTM)	B1,B2	B1,B2	B1,B2
L0223-00 (FTM)	A2	A2	A2
L0202-00 (SBS)	B1-B3	B1-B3	B1-B3
L0203-00 (SBA)	B1,B2,C1	B1,B2,C1-C3	B1,B2,C1-C3
L0224-00 (STM)	B4-B6	B4-B6	B4-B6
L0212-00 (FBA)	H1	H1,H2	H1,H2
L0213-00 (FBM)	C5,C6	C5,C7,C8	C5,C7,C8
L0100-C1780(ILI)	D1	D1	D1
L0101-C1780(IPB)	H1	H1	H1
L0102-C1780(IDP)	C1,D1	C1,D1	C1,D1
L0104-C1780(ISI)	C,C1,D1	C,C1,D1	C,C1,D1
M8270-DW780 (USI)   M8271-DW780 (UCB)   M8272-DW780 (UMD)   M8273-DW780 (UAI)   M9302-DW780 (UNIB TERM)   M9040-SBI TERM M9044-DW780 (UBT)	C,D	C,D	C,D
	F1,F2	F1,F2	F1,F2
	D	D	D
	C	C	C
	A	A	A
	A2	B1	B1
	B,C	B,C	B,C

UNIT	REV		
8650	B1	B2	C1
DIAGNOSTIC MEDIA  BB-FF15*-DE  BC-FG45*-ME  BC-FG47*-DE  BB-FG48*-DE	D	D B, REV 1.2 B, REV 1.2 B, REV 1.2	•
SOFTWARE  VMS OPERATING  SYSTEM	   x4.3, V4.3	X4.3,V4.3	X4.3,V4.3

NOTE: X4.3 IS A FIELD TEST VERSION

Option name:	/AX 8800 SYSTEM	SYST	EM REVISION
`	VAN GOUD STSTEM	A1	B1
Part No.	Description		REVISIONS
VMS	Operating System	V4.4	V4.4
ZM920-C3	VAX 8800 Complete Diagnostic Set	22A	22C
EVNDX	VAX Diagnostic Index (Reference)	23	23
882BA-XE/XF	CPU System Kernel	Al	В1
ms88	Memory Array	A1	Al
	VAXB! Native Adapters - Reference Only		
CIBCI-M	VAXBI Native C1750 Adapter	5	See note
DWBUA-CA	VAXBI Native UNIBUS Adapter		ee note
KDB50-B	VAXB  Native SI Adapter		See note
KLESI-BB	VAXB  Native LES  Adapter		see note
DMB32-M	VAXB  Native Comm. Adapter		See note
DEBNT-M	VAXB  Native NI Adapter		See note
DRB32-M	VAXB  Native DRB Bus Converter		See note

Notes: These adapters are included as a reference to which VAXBI adapters may be configured into a system. This does not imply any configuration rules or restrictions.

Refer to option RM documents for current option and unit revisions.

#### VAX 882BA UNIT REVISION CHART (Con't)

1	Option name:  882BA  - Cpu System Kernel		ASE REVIS	510N 
Part No.	Part No.   Description		REVISIONS	5
PC38N-AA	VAX 8800 Console Subsystem	A1	Al	
87XBA	Basic Hardware Group	A1	B1	
н9652	Front End Cab	A 1	Al	
H7059-A	Power Complex	A1	Al	<b></b> - 

Option name:	PC 38N-AA	RELE	ASE REVIS	SION
	Console Subsystem	A 1	A1	1
Part No.	Description	1	REVISION	S 
PC380-AA	KDJ11-CA, RX50-A, RCX50, H7862-C	A1	A1	
RCD52-A	RCD52,RCD52-A 31MB DISK	A 1	A 1	
MSC11-B	512KB RAM Daughter Board	A 1	A1	
54-15539-01		B5	B5	
BT-ZMAAD-C3	VAX 8800 Customer Console Kit (NOTE 1)	22A	22C	
BL-FH41*-ME	      RX100 VAX 8800 Console (NOTE 2)		     B	
	CONSOLE2.TSK Main Microcode	1	2	
BL-FH43*-ME	RX102 VAX 8800 VMS Boot File (NOTE 2)	Α	В	
	C1780.BIN	6	7	

- | 1. Verify VMS revision for compatibility
- 2. This item reference only. * = Rev

Option name:	770A VC/VD	RELEA	SE REVIS	SION
•	37XBA-XC/XD Basic Hardware Group	Αl	B1	
Part No.	Description	F	REVISIONS	5
KA88-AA	CPU Module Set	Αl	В1	
KA88-AB	Attached CPU Module Set	Αl	Bl	
DB88-AA	VAXBI Channel #0	Αl	A 1	
F1013-00	NMI Load Board	A 1	A 1	
70-22109-03/04	CPU Assembly	Αl	A 1	
70-21696-02	CPU Backplane Assembly	Αl	Al	
70-21701-01	Memory Backplane Assembly	A 1	A 1	
17-00555-01	Cable Assembly, CPU to Memory	Cl	C1	
17-01172-01	Cable Assembly, Console to CLK	B1	B1	

	- KA88-AA CPU Module Set		SE REVIS	SION
Part No.	Description	F	REVISIONS	5
F1001-00	Memory Controller (MCL)	A1	A1	
F1002-00	Shifter Module (SHR)	A 1	A1	
F1003-00	ALU Slice l (SLC1)	A 1	A1	
F1004-00	ALU Slice O (SLCO)	A1	A 1	
F1005-00	Address Data Path (ADP)	A1	B1	
F1006-00	Cache Control/sequencer (CCS)	A1	A 1	
F1007-00	Instruction Decode (DEC)	A1	A 1	
F1008-00	Sequencer (SEQ)	A 1	A 1	
F1009-00	Writeable Control Store (WCS)	A 1	A1	
F1010-00	Clock/console Interface (CLK)	A1	A 1	

	KA88-AB Attached CPU Module Set	RELE/	ASE REVIS	SION 
Part No.	Description	   F	REVISIONS	5
F1002-00	Shifter Module (SHR)	A1	Al	 
F1003-00	ALU Slice l (SLC1)	A1	A1	
F1004-00	ALU Slice O (SLCO)	   A1	A1	
F1005-00	Address Data Path (ADP)	A 1	B1	 
F1006-00	Cache Control/sequencer (CCS)	A1	A1	
F1007-00	Instruction Decode (DEC)	A1	A1	 
F1008-00	Sequencer (SEQ)	A1	A1	
F1009-00	Writeable Control Store (WCS)	A1	A1	
H7180-A	Floating 5 OR 5.2V 200A Switching Reg	C1	C1	
H7187-A	+/- 2.5 100A Switching Reg	D1	Dl	

#### 11.0 CIBCI REVISION CHART

Option name:	   A	   B	
Part No.	Description		
T1017	ADAPTER CONTROL MODULE	B1	C1,D1
T1018	ADAPTER DATA MODULE	B1	C1
L0100	LINK INTERFACE MODULE	D1	D1
L0101	PACKET BUFFER MODULE	н1	J1,K1
L0400	DATA PATH MODULE	B1,C1	C1
H7202-D	POWER SUPPLY	D	l D
54-15555	BACKPLANE ASSEMBLY	C1	C1

12.0 HSC50 OPTION LEVEL REVISION CHART

HSC50-AA 60hz, no disks   D1	·					•
Does not include Disk   Data Channels	Does not i	nclude		D1   	E1	
SYS TAPE	Does not i	·				
NOLY	MODULE	SLOT	ETCH	MR/PR	MR/PR	
BET788*-**	ONLY			*=	QX926-	#1
ONLY				•	V 300 	
RELEASE	ONLY			*=		
ALL CURRENT TAPES AND RELEASE NOTES   (00-ZD300-CG)	RELEASE NOTES			<b>*=</b>		
B D1 D1   D1   L0105   1   D   D1,D2   E1,E3   E1   E2,E4     E2,E4     C   C1   C1   C1   CAC   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH   CAH	ALL CURRENT		ND RELEAS		İ	
E   E2,E4	L0100	14	_	•		
E   E2,E4 	L0105	1	D	:	E1,E3	
-AC			E		E2,E4	
L0107-YA  12   C  C1-C3  C1-C3	-AC	2	С	C1 	C1 	
	L0107-YA	12	C	C1-C3	C1-C3	

HSC50 OPTION LEVEL REVISION CHART (Con't)

MODULE	SLOT	ETCH	MR/PR	MR/PR
LO108-YA (HSC5X-BA)		С	C6,C7  C8	C6,C7    C8
		D	C5-C10	C5-C10
		Ε	C1-C3	C1-C3
		F		C23
LO108-YB (HSC5X-CA)		С	N/A	N/A
		D	C10	C10
		Ε	c3,c4	c3-c4
		F		C24
L0109	13	E	E1,E2	E1,E2

HSC50 OPTION LEVEL REVISION CHART (Cont.)

HSC50-AA Does not Data Chani	include		D1	E1
HSC50-AB Does not Data Chan	include		D1 	E1
MODULE	SLOT	ETCH	MR/PR	MR/PR
TU58-XA			OPTION  REVF,K	OPTION   REVF,K
TU58-XB   (54-13489)		E   F	F3,F4  K,L,M	F3,F4    K,L,M
BEZEL   ASSY.   (70-20186)   TU58 UNITS	SIDE FRON		A1   	A1   
OPERATOR CONTROL PANEL (54-15286			   C 	   C 
BACKPN (54-1448)		E	A1,A2 	A1,A2 
MAN PS (7020033) 60hz= -01 50hz= -02			A1,B1,  C1,C2 	A1,B1,  C1,C2 
60HZ= -03 50HZ= -04	•			C1   C1
0PT PS (70-20184) 60hz= -01 50hz= -02			A1,B1,  B2 	Al,Bl,  B2 
PWR CTRLR (70-19122) 60hz= -00 (70-20613) 50hz			A1,A2  A3,B1    A1,A2  B1,C1	A1,A2  A3,B1    A1,A2  B1,C1

Key: MR= Module Revision

PR= Part Revision

Note: The 50hz version of the HSC50 is available starting with

revision B1.

Note #1: UTILITY TAPE INCLUDED IN SYSTEM TAPE KIT. KIT AVAILABLE

THROUGH SDC.

Note #2: DIAGNOSTICS NO LONGER SHIP TO CUSTOMER WITH HARDWARE.

DIAGNOSTICS AVAILABLE THROUGH VAXDIAGNOSTIC RELEASE #24.

Note #3 DOCUMENTATION KIT ONLY (RELEASE NOTES AND USERS GUIDE).

IMPORTANT -- THE PREVIOUS CHARTS FOR THE HSC50 DO NOT AGREE WITH THE RM DOCUMENT. THE RM DOCUMENT IS CURRENTLY IN THE PROCESS OF BEING UPDATED.

HSC50

INST MAN. EK-HSC50-IN-001 USER GUIDE EK-HSC50-UG-002
SERVICE MAN EK-HSC50

EK-HSC50-SV-002

TECH. SUMMARY EJ-25786-45 (INTERNAL USE ONLY)

HSC50 (Rev D1)

FCO HSC50-R-008 AVAILABLE APPROX. 10/85

> EQ-01368-01 BE-T492H-ME - HSC50 System Tape (V250)

> > BE-T788H-ME - Utility Tape (V250)

AAX703D-TK Release Notes

QUICK CHECK: HSC50 firmware is at Rev. V250

HSC50 (Rev E1)

AVAILABLE APPROX. 7/86

QX926-HG - HSC50 System and Utility tapes Release notes and USERS Manual.

NOTE: This is NOT an FCO. It is a software kit orderable

through SDC.

QUICK CHECK: HSC50 firmware is at Rev. 3.00

IT IS MANDATORY TO INSTALL FCO HSC50-R-0005 BEFORE ***** **** WARNING

UPGRADING TO V3.00. FAILURE TO DO SO WILL RESULT IN

FAILURE TO FIND FILE (EXEC.INI) AT BOOT-UP.

# FCO HSC50-R-0005 INFORMATION

EQ-01304-01 (FCO PARTS AND DOCUMENTATION) FA-04581-01 (FCO DOCUMENTATION ONLY)

QTY PART NUMBER DESCRIPTION

1 LO105 P.IOC MODULE (REV E1)

QUICK CHECK: LO105 MODULE REVISION WILL BE:

E1 FOR ETCH REVISION D

AND

E2 FOR ETCH REVISION E

13.0 HSC70 OPTION LEVEL REVISION CHART

HSC70-AA 60hz, no disks	15.0 115070 0					<b>L</b>
Does not include Disk   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data Channels   Data	Does not	include		A 1	B1 	
DIAG.	Does not	include		A 1	B1 	
FLOPPY	MODULE	SLOT	ETCH	MR/PR	MR/PR	
NOTES	FLOPPY			*=	*=	
KIT	•				•	
B	•				•	
D   A2   A2   A2   L0107-YA  12   C   C2,C3 C2,C3   L0108-YA 3,4,5,   C   C8   C8   C8   C8   C9,10   C10   C23   C23   C23   C23   C23   C23   C23   C23   C23   C23   C23   C23   C23   C23   C23   C23   C23   C23   C23   C23   C23   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24   C24	L0100	14		•		
L0107-YA  12   C   C2,C3 C2,C3  L0108-YA 3,4,5,   C   C8   C8   C8   C8   C8   C8   C8	L0111	1	С	A1	Al	
L0107-YA   12   C   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C3,C4   C3,C4   C3,C4   C3,C4   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C3,C4   C3,C4   C3,C4   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C3,C4   C3,C4   C3,C4   C3,C4   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2,C3   C2			D	A 2	A2	
L0108-YA 3,4,5, C   C8   C8   (HSC5X-BA)   6,7,8,   9,10   D   C8-C10   C8-C10   E   C1-C3   C1-C3   C23   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-C3   C1-	LO117-AA	2	Α	A2	A2	
(HSC5X-BA)   6,7,8,	L0107-YA	12	C	C2,C3	C2,C3	
9,10   D   C8-C10   C8-C10     E   C1-C3   C1-C3     F                   L0108-YB   3,4,5,               (HSC5X-CA)   6,7,8,				C8	C8	
F				C8-C10	c8-c10	
L0108-YB 3,4,5,  (HSC5X-CA) 6,7,8,   9,10			E	C1-C3	C1-C3	
(HSC5X-CA)   6,7,8,			F		C23	
F   C24   C24		6,7,8,	_	C10	    C10	
			E	C3,C4	  C3-C4	
L0109   13   E   E1,E2  E1,E2			F	C24	  C24	
T	L0109	13	E	E1,E2	E1,E2	

### HSC70 OPTION LEVEL REVISION CHART (Con't.)

			•					
HSC70-AA Does not i Data Chanr	include D		A1 	B1				
Does not i	HSC70-AB 50hz, no disks Does not include Disk Data Channels							
MODULE	SLOT	ETCH	MR/PR	MR/PR				
OPERATOR CONTROL PANEL 54-15286-				C				
BACKPN   5417764-01		l C	A1	C1,D1				
MAIN PS (7020033) 60hz= -03 50hz= -04			C1	C1				
OPT PS (70-20184) 60hz= -03 50hz= -04			B2   	B2				
PWR CTRLR 30-24374 60hz= -01 50hz= -02			B1       B1	B1				
RX33 Drv    30-24962-01	ļ.		A1	A1				

Key: MR= Module Revision PR= Part Revision

Note #1: SYSTEM, UTILITY TAPE, RELEASE NOTES AND USERS MANUAL INCLUDED IN SOFTWARE KIT.

KIT AVAILABLE THROUGH SDC.

Note #2: DIAGNOSTICS NO LONGER SHIP TO CUSTOMER WITH HARDWARE.
DIAGNOSTICS AVAILABLE THROUGH VAXDIAGNOSTIC RELEASE #24.

Note #3 DOCUMENTATION KIT ONLY (RELEASE NOTES, USERS GUIDE).

IMPORTANT -- THE PREVIOUS CHARTS FOR THE HSC70 DO NOT AGREE WITH THE RM DOCUMENT.

THE RM DOCUMENT IS CURRENTLY IN THE PROCESS OF BEING UPDATED.

#### HSC70 DOCUMENTATION

HSC70 Installation Manual HSC User Guide HSC70 Service Manual HSC70 PRINT SET EK-HSC70-IN-001 AA-GMEAA-TK EK-HSC70-SV-001 MP-01426

### 14.0 UDA50 OPTION REVISION CHART

UDA50A Microcode Date Code	Ver 3 (Y09)	Ver 4 (U21)	     Ver 5
M7485-YA	D	E,F	н1
M7486		D	H2

- * UDA Rev 5 refers to microcode version 5.
- * Quick check for M7485-YA Rev H1: Chip E1 is 23-157F4-00

### 15.0 RAGO REVISION CHART

NOTE: All you need is Rev. Kl of the drive logic module, which is Rev. 3 of the microcode or above, everything else is backward or forward compatable. Kl fixed a microcode bug, but was not a functional change.

RA60	Al	A2	A3	A4	A5	A6	A7
MODULE	======     ======						
POSTAMP/DATA SEPERATOR 54-15264-01	D1	E 1	F1	F1.	F1	F1	F1
DRIVE LOGIC MODULE 54-15266-01 (see note 6)	El	FI	HI	J1 note 5	  K1/H1 	K1/H1	L1,M1
RD/WT PREAMP MODULE 54-15268-01	В	С	D1	D1	DI	E1	E2
SD/10 MODULE 54-15270-01	F1	F 2	   H1 	J1 	J2,L1,   M1  note 5	   N1 	N1 
FRONT PANEL MODULE 54-15272-01	B1	C1	DI	El	E 2	E 2	E2,H 
CAP/RECTIFIER MODULE 54-15274-01	A1	A1	Al	A1	A1	Al	A1,B
MOTHER BOARD 54-15278-01	D	D	E1	E1	E1	El	E1
REGULATOR MODULE 54-15276-01	C	C	D	D1	D1	DI	D1/D3 
HEATSINK MODULE 54-15280-01	El	E2	F1 	H1	   H1 	H1	H1 
MOTOR CONTROL MODULE 54-15282-01	C	D	D1	D1	D1 	DI	C
SPINDLE ASSY 70-17740-01	B1  note 7	B1 note 7	Cl	Cl	C1	C1	C1
POSITIONER ASSY 70-18452-01	A	   A 	B			C	C,D
TRANSFORMER ASSY 70-18477-01	B	C	D1	DI	D1	DI	D1

#### NOTES FOR THE RAGO:

- 1. THIS CHART REFLECTS PART REVISION LEVELS.
- 2. ALL MODULE REVISIONS ARE PRESENTLY DOWNWARD COMPATABLE. (reference note 6.)
- 3. HARDWARE REV LEVEL 1 = REV A DRIVE.
- 4. HARDWARE REV LEVEL IS SWITCH SETTABLE BY F.S. ENGINEER.
- 5. Postamp/data separator Rev.'Fl' has new flip chip handles colored blue.

  Drive Logic Rev.'Jl' has new flip chip handles colored gray .

  SD/10 Rev.'Kl' has new flip chip handles colored red.
- 6. An RA60 that is to be run on an HSC cluster, dual ported, on VMS V4.X, must have master microcode version 3.0 OR LATER. This can be checked by looking at the stickers on the master EPROMS of the drive logic. FCO RA60-R0003 documented in Speed Bulletin 358 will upgrade a drive to microcode rev 4 effective 1-Jun-86. Previsious EQ-kits for this FCO would upgrade drive to microcode revision 3.0.

ROM	Version 3	Version 4
E27	23-04989-00	23-100B9-00
E29	23-05089-00	23-101B9-00
E32	23-096B9-00	23-102B9-00
E 37	23-048B9-00	23-09989-00

7. Rev. 'Bl' spindles are not usable in drives that have a spindle lock.

# 16.0 RA80 DRIVE OPTION REVISION CHART

	====	====	=====	====	====	====				
RA80-A	A6	A7	A8	A9	A10	A11	A12	A13	A14	D1
70-18330-0x (RA80)	F1	F2	F3	н	J	K	L	M	N	P
MSCP HARDWARE REVISION	0	0	0	0	0	0	0	0	1 0	1
MSCP SOFTWARE REVISION	10	10	10	10	10	10	10	10	10	11
POWER SUPPLY H7660 - A	A2	B1	B1	C				,		
HDA ASSEMBLY 70-16795-00	C	C	C	C	C	С	C	C		
MICROPROCESSOR MODULE 54-15284-01	B2	B3 	C	C	C	С	С	C	C/C1	ום
PERSONALITY MODULE 54-14923-00	D	D	D	D	D	D	D	D	D 	D
SERVO MODULE 54-14014-00	M	M 	M 	M	M	М	M	M	M	M
READ/WRITE MODULE 54-13596-00	E	F	F	F	F	F	F	F/H		

### CLUSTER SUPPORTED RA80

RA80-A	01	
70-18330-0x (RA80)	P	
MSCP HARDWARE REVISION	1	
MSCP SOFTWARE REVISION	11	
POWER SUPPLY H7660 - A	C	
HDA ASSEMBLY 70-16795-00	C	
MICROPROCESSOR MODULE 54-15284-01	D 1 	
PERSONALITY MODULE 54-14923-00	D	
SERVO MODULE 54-14014-00	M  -	   
READ/WRITE MODULE 54-13596-00	F/H 	

The RA80 must be at MSCP Hardware Revision level 1 to be totally compatible with the HSC50 in a dual-port configuration.

The RA80 is currently at hardware (controller readable) (front panel switch setting) level 1.

The module revision levels are CS revisions.

The top level (RA80-A) changes between Al4 and Dl were sheet metal and documentation changes that have no effect to Field Service or the cluster environment.

# 17.0 RA81 OPTION REVISION CHART

### HARDWARE REVISION O

RA81 70-19062-01	A				
POWER SUPPLY H7660 A/B	A2				   
HDA ASSY 70 19044-01	C1				
MICROPROCESSOR 54-15247-00	ETCH B   ABCD				
SERVO 54-15251-00	ETCH B   ABC				
PERSONALITY 54-15249-00	ETCH B   A,B,B1    C				
R/W MODULE ETCH B 54-15253-00	A/B   				

### HARDWARE REVISION 1

	=======	======	======		======	
RA81 70-19062-01	B					<u> </u>
POWER SUPPLY H7660 A/B	A2					
HDA ASSY 70 19044-01	C1					
MICROPROCESSOR 54-15247-00	ETCH B   CS E/F					
SERV0 54-15251-00	ETCH B   C ABC					
PERSONALITY 54-15249-00	ETCH B   C B2/D					
R/W MODULE ETCH B 54-15253-00	A/B			   		

# RA81 OPTION REVISION CHART (Cont.)

### HARDWARE REVISION 2

RA81 70-19062-01	l c	C1		l	
POWER SUPPLY H7660 A/B	A2	A2			
HDA ASSY 70-19044-01	C1	C1,D1			
MICROPROCESSOR 54-15247-00		B ETCH B    CS E2			
SERV0 54-15251-00	ETCH E	B ETCH B   CS F			
PERSONALITY 54-15249-00		B ETCH B    CS E			
R/W MODULE ETCH B 54-15253-00	A,B	A,B,C			

### HARDWARE REVISION 3

RA81 70-19062-01	D	======			 		====
POWER SUPPLY H7660 A/B	D2		   		     		===
HDA ASSY 70-19044-01	C1,D1						
MICROPROCESSOR 54-15247-00	ETCH B    E3						
SERVO MODULE 54-15251-00	ETCH B   CS E/F						
PERSONALITY 54-15249-00	ETCH B   CS F					~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~	
R/W MODULE ETCH B 54-15253-00	A,B,C					~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~	

# RA81 OPTION REVISION CHART (Cont.) HARDWARE REVISION 4

	======	======			*****	=======================================
RA81 70-19062-01	El	E2	E3	F	н	
POWER SUPPLY H7660-A/B	A2 	A2,D1	A2,D1	A2,D1	A2,D1 	
HDA ASSY 70-19044-01	C	C	C,C1	C1	C1	
HDA 70-18491-01				 	   D1	
MICROPROCESSOR MODULE 54-15247-00			CS K	CS K	CS K	
SERVO MODULE 54-15251-00	ETCH B  CS F 	CS F ET C1	ETCH B  CS H  ET C1  CS J1	CS H1	ET CI	
PERSONALITY MODULE 54-15249-00	ETCH B  CS F		CS F ET C1	CS F ET C1	ETCH B CS F ET C1 CS H1	     
R/W MODULE ETCH B 54-15253-00	D	D D	D	D	D	

### HARDWARE REVISION 6

HARDWARE	REVISION	5
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RA81 70-19062-01	11	11		
POWER SUPPLY H7660 A/B	A2,D1     D2	E1     E2		
HDA 70-18491-01	E1	E1		
MICROPROCESSOR 54-15247-00	ETCH B   CS K    ET C1    C L1/2	ETCH B   CS K    ET C1/2  C L2,3		
SERVO MODULE 54-15251-00	ETCH B   CS H1    ET C1    CS L1	ETCH B   CS H1    ET C1    CS L1		
PERSONALITY 54-15249-00	ETCH B   CS F    ET C1    CS H1	ETCH B   CS F    ET C1    CS H1		
R/W MODULE ETCH B 54-15253-00	F1	F1   		

# CLUSTER SUPPORTED RA81 HARWARE REVISION 7

### HARDWARE REVISION 8

RA81 70-19062-01	PI   SEE NOTE	S1	
POWER SUPPLY	E2,F1	F	
HDA 70-18491-01	H1,J1  		
MICROPROCESSOR 54-15247-00	ETCH B   CS K    ET C1    CS M	ETCH B   CS K    ETCH C   CS M	
SERVO MODULE 54-15251-00	ETCH B   CS H    ET C1    CS L	ETCH B   CS H    ETCH C   CS L	
PERSONALITY 54-15249-00	ETCH B   CS F    ET C1    CS H	ETCH B   CS F    ETCH C   CS H	
R/W MODULE ETCH B 54-15253-00	F1   	F1   	

^{*} NOTE * 70-19062 REVISION P1 INCLUDES THE HDA GROUND STRAP RA81 FCO 14.

### 18.0 TA78 OPTION REVISION CHART

TA78-BE/BJ		A 1		B1		C1
TU78 TAPE DRIVE (SLAVE)		DI	1	D 1	1	D1
TS 78 FORMATTER		A 1		Bl		C1
TS 78 POWER SUPPLY		Al	 	B1		B1

# 19.0 TA81 OPTION REVISION CHART

TA81-CA/CB	Al	В١
PWA READ AMPLIFIER	A0 225Y	A0 225Y
PWA SERVO CONTROL	B1   236Y	B2 941Y
PWA WRITE DRIVER	AO   216Y	B1 217Y
PWA STI	AO   144Y	AO 144Y
PWA FORMATTER WRITE	AO   981Y	A0 981Y
PWA FORMATTER READ	B1   131Y	B1   131Y
PWA POWER AMPLIFIER	A2   194Y	A2   194Y
POWER SUPPLY 50/60 HZ   50 HZ   60 HZ	A2   963Y   964Y	A2   963Y   964Y
MAGNETIC HEAD	A0   380Y	AO   380Y
REEL MOTOR	AO   162Y	A0 162Y
COMPRESSOR UNIVERSAL	Al   537Y	A1   537Y
120V 24AMP POWER CTRL	AO	AO
220V-240V 12AMP POWER CTRL	AO	l AO

### 20.0 TECHNICAL DOCUMENTATION

CURRENTLY AVAILABLE FROM PUBLISHING & CIRCULATION IN NORTHBORO, MASS. IS A DOCUMENT CALLED A " PRODUCTS DIRECTORY ".

THE PART # IS ED-24847-92.

THIS DOCUMENT HAS THREE PARTS TO IT.

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1- FORMS CATALOG (A LIST OF ALL ORDERABLE FORMS)
2- TECHNICAL DOC. CATALOG (A LIST OF ALL ORDERABLE DOCUMENTATION
FOR DEC PRODUCTS)
3- PROMOTIONAL CATALOG (SPD'S - BULLETINS - SUMMARIES)
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THIS DOCUMENT CONTAINS ALL PART NUMBERS FOR DOCUMENTATION PERTAINING TO DEVICES THAT DIGITAL MAKES. IF YOU NEED TO ORDER DOCUMENTATION FOR YOUR HARDWARE REFER TO THIS DOCUMENT.

BELOW IS A LIST OF DOCUMENTS WHICH PERTAIN TO DEVICES THAT USE THE CI BUS.

#### DOCUMENTATION TREE FOR THE C1780/HSC50/C1750.

TECH. DESC. USER GUIDE MAINT. ADV.	C   780 EK-C   780-TD-001 EK-C   780-UG-001 EK-C   VAX-RM-001	
TECH. DESC. USER GUIDE MAINT. ADV.	C1750 EK-C1750-TD-001 EK-C1750-UG-PRE EK-C1VAX-RM-001	
USER GUIDE	SCOO8 EK-SCOO8-UG-OO1	
INST MAN. MAINT. GUIDE USER GUIDE SERVICE MAN TECH. SUMMARY	EK-HSC50-UG-002 EK-HSC50-SV-002	(INTERNAL USE ONLY)

### 21.0 REVISION DOCUMENTATION

For additional information on revisions noted above, the following documents should be referenced:

	Unit	Document	Updated	Available vi	ia (	Date
	11/725	11/725 Revision Control Document	2/12/86	VAX Library Speed Bitn.		
	11/730	11/730 Revision Control Document	11/18/85			
	11/750	11750/11751 Revision Control Document	6/28/85	VAX Library	Fiche	7/85
		Over the ENET (copy).	•••••	.VOLKS::CLUST 750REV		
	11/780 11/782	11780/11782 Revision Control Document	2/86	VAX Library	Fiche 2	2/86
	11/785		vailable /85	VAX Library VOLKS::CLUST 785REV.DOC		TBD
For all	documents	over the ENET (copy).	••••••	VOLKS::CLUST 725REV. 730REV. 750REV. 780REV. 785REV. CLUSTER	.DOC .DOC .DOC .DOC	
	KA86-AA	VAX8600 Revision Matr	ix	VAX Library	Fiche	TBD
	нѕс50	Revision Matrix	12/84	RIGHT STUFF	Publicat	ion
	UDA50	11	11	'	ı	
	RA60	П	11	ı	1	
	RA80	II.	11	ı	ı	
	RA81	н	11	1	<b>!</b>	

#### 22.0 NOTES

### CI7XO/CIBCI V7.0 UCODE APPLICABILITY

The V7.0 Cl_ucode is being made available for Cl7XO and VAX8800 on a restricted basis as a preliminary FCO, for customers heavily impacted by known errors fixed by and urgently needing V7.0 Cl_ucode. The known problems are:

- 1. VAX8800 CIBCI may exhibit frequent CI "ARBITRATION Timeouts & MISC_ERR_50000" due to the VAX8800's high performance. The CIBCI on VAX8800 will ship with V7.0 CI_ucode PROMs as of APR-1986.
- 2. Any VAX system with CI "BUFFER LENGTH VIOLATION" packet error, which are crashing with CI DATA_FREE_Q corruption or heavily impacted by this error. Attached CI nodes should also be upgraded.
- 3. Large VAX8600 or VAX8650 VAXclusters having unacceptable CI "MISC_ERR_50000 or ARB_TIMEOUT" error rates. All attached CI_nodes should also be upgraded to V7.0.

#### C1780-F1-1-005 FCO KIT CONTENTS:

	Quantity	Description
EQ-01422-01 EQ-01422-02	(6 EA) (1 EA)	Microcode ROMS BB-FG70B-DE C1780.BIN plus Diagnostics
EQ-01422-03	(1 EA)	. BB-F104A-ME C1780.BIN only
FA-04707-01	(1 EA)	FCO Document (IN EACH KIT)
C1750-E1-1-004 FC0 KI	CONTENTS:	
	Quantity	Description
EQ-01421-01 EQ-01421-02	(6 EA) (1 EA)	Microcode ROMS BB-FG70B-DE C1780.BIN plus Diagnostics
EQ-01421-03	(1 EA)	BB-F104A-ME C1780.BIN only

REQUIREMENTS: C1750's should have had FCO #C1750-R-00Cl and C1780's should have had FCO C1780-R-00Dl installed.

**END**