

PROCESSOR TYPE PDP-11 Family

**7008993-E0001 CODE: F**

DEC-73 - PROBLEM 1: Changes within the LK40, M7011 module, to sync clocks, make it necessary to move pins and add a jumper.

CORRECTION 1: Move pins and add jumper. A "QUICK CHECK" version of the rework instructions is as follows: Remove wire from P4-K and insert in P4-E, SERIAL IN, TTL. Remove wire from P4-S and insert in P4-CC, CLK IN, TTL. A one foot length of black, #22 AWG, stranded wire, with a Berg pin #48015, #12-10089-4, at each end is added from P4-HH to P4-VV.

PROBLEM 2: When cable is being made, it is difficult to keep wires short at the P1 end so that, when completed, the strain relief will clamp to the cable covering.

CORRECTION 2: Change Note 5 explaining methods of building cable.

CORRECTION 3: Update model and update tester.

NOTE: This FCO, installed in conjunction with an M7011 module at etch revision "E" or later, will result in slaving the PDP-11/05 SCL line to the LK40; no SCL CLK adjustment is required.

In-plant effectivity -Phase-in

Field effectivity -Rework all #70-08993's in all GT40's when slaving the PDP-11/05 SCL line to the LK40 is required. The M7011 must be at etch revision "E" or later.

( Time To Install And Test 1.0 Hour. ) ( Documentation \$ 5.00 , Parts \$ 1.00 )

The DEC on-site labor charge will be the time required to install and test the FCO at the then current hourly rate. ( Kit Contents -PF1151 - FCO/Prints And Parts )