i.			6	PDP-1	l Family	Projection		4/3/69
MODEL	CP	LOGICAL POWER	ARITH POWER	SPEED	PRICE	CONFIGURATION	SOFTWARE PAPER TAPE	DISK
PDP-11/10		.7	• 7	2-3u	4K	Technilogically cost reduced 11/20 with MOS		
PDP-11/20	KA11	1	1	2.2u	5.2K	CP,1KBROM, 128 by R/W, Turnkey Console		
PDP-11/30	KA11		1	2.2u	9.3K	CP, 8KB Core, Console, TTY	Assembler, Editor, Math Utility FOCAL, BASIC, (ASA Basic Fortran)	8-like monitor (syst.builder w/ODT,DOT,PIP) <sup>2</sup>
PDP-11/40	KBll	21	10-20	1.2u	13K		WKB -Possible & For- tran IV Improved Assembler	Fortran IV
PDP-11/45	KB11	21	10-20	1.2u	15K + Disk	11/45 with memory protect/relocate max core 262KB, Max phys memory (using disk) 2	y	Super Monitor  65KB virtual  mem/user for  cither small or  large Disk
PDP-11/50	KCll	21	,50-100	1.2u	25K	adds hardware floating point 33 bit processor, 16 bit memory (16KB	6	
PDP-11/55	KCll	21	50-100	1.2u	27K + Disk	with memory pro- tect/relocate	COMPANY	CONFIDENTIAL
PDP-11/65	KDll	4	100-200	1.2u/ 32 bit	45K +	32 bit separate memory bus 32 bit		

## NOTES:

1. If microprogrammed, then logical power could be tailored to user and go to 20-50, 40-100 for 11/65.

processor.

Disk

- 2. Business länguage system under consideration.
- 3. Possible by-product of FOCAL.
- 4. Super monitor for 11-45, 55, 65 is priority multi-user real-time system.