

DRCI DEC/X11 SYSTEM EXERCISER MODULE MACY11 30A(1052) 12-OCT-78 16:31 PAGE 2
XDRCIO.P11 12-OCT-78 11:55

SFQ 0001

.REM _

IDENTIFICATION

PRODUCT CODE: AC-E733I-MC
PRODUCT NAME: CXDRCIO DR11-C MODULE
PRODUCT DATE: SEPTEMBER 1978
MAINTAINER: DEC/X11 SUPPORT GROUP

THE INFORMATION IN THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE AND SHOULD NOT BE CONSTRUED AS A COMMITMENT BY DIGITAL EQUIPMENT CORPORATION. DIGITAL EQUIPMENT CORPORATION ASSUMES NO RESPONSIBILITY FOR ANY ERRORS THAT MAY APPEAR IN THIS MANUAL.

THE SOFTWARE DESCRIBED IN THIS DOCUMENT IS FURNISHED TO THE PURCHASER UNDER A LICENSE FOR USE ON A SINGLE COMPUTER SYSTEM AND CAN BE COPIED (WITH INCLUSION OF DIGITAL'S COPYRIGHT NOTICE) ONLY FOR USE IN SUCH SYSTEM, EXCEPT AS MAY OTHERWISE BE PROVIDED IN WRITING BY DIGITAL.

DIGITAL EQUIPMENT CORPORATION ASSUMES NO RESPONSIBILITY FOR THE USE OR RELIABILITY OF ITS SOFTWARE ON EQUIPMENT THAT IS NOT SUPPLIED BY DIGITAL.

COPYRIGHT (C) 1973,1978 DIGITAL EQUIPMENT CORPORATION

1. ABSTRACT

DRC IS A IOMOD THAT EXERCISES UP TO SIXTEEN DR11-C'S. THE MODULE USES THE MAINTENANCE MODE TO CHECK DATA TRANSFERS TO AND FROM THE DR11-C. IT TRANSMITS AND RECEIVES 64 WORST-CASE 16 BIT WORDS AND ALSO TESTS THE ABILITY OF THE DR11C TO GENERATE BOTH A-REQUEST AND B-REQUEST INTERRUPTS. IT WILL DROP ITSELF IF RUN IN AN XXDP CHAIN WHEN THE MANUEL INTERVENTION BIT IS NOT SET.(BIT 0 IN LOC.52)

2. REQUIREMENTS

HARDWARE: ONE DR11-C WITH A MAINTENANCE CABLE

STORAGE:: DRC REQUIRES:

- 1. DECIMAL WORDS: 308
- 2. OCTAL WORDS: 0464
- 3. OCTAL BYTES: 1150

3. PASS DEFINITION

ONE PASS OF THE DRC MODULE CONSISTS OF TRANSMITTING AND RECEIVING 64 WORDS AND GENERATING ONE A-REQUEST AND ONE B-REQUEST INTERRUPT.

4. EXECUTION TIME

ONE PASS OF DRC RUNNING ALONE ON A PDP11/03 PROCESSOR TAKES APPROXIMATELY THIRTY SECONDS

5. CONFIGURATION REQUIREMENTS

DEFAULT PARAMETERS:

DEVADR: 167770, VECTOR: 1, BR1: 5, DEVcnt: 1

REQUIRED PARAMETERS:

AT CONFIGURATION TIME USER MUST SUPPLY THE LOWEST VECTOR OF THE DR11-C'S.

6. DEVICE/OPTION SET-UP

CONNECT THE MAINTENANCE CABLE TO TIE OUTPUT BACK TO INPUT

DRCI DEC/X11 SYSTEM EXERCISER MODULE MACY11 30A(1052) 12-OCT-78 16:31 PAGE 4
XDRCI0.P11 12-OCT-78 11:55

SEQ 0003

7. MODULE OPERATION

TEST SEQUENCE:

- A. SET UP VECTORS AND ADDRESS POINTER
- B. OUTPUT TEST DATA TO OUTPUT BUFFER
- C. COMPARE OUTPUT BUFFER WITH TEST DATA-REPORT ANY DATA ERROR
- D. COMPARE INPUT BUFFER WITH TEST DATA-REPORT ANY DATA ERROR
- E. IF NOT 64 TRANSFERS, BUBBLE TEST DATA2 AND REPEAT B-D
- F. IF 64 TRANSFERS GENERATED AND TEST A/B INTERRUPTS
- G. IF NO INTERRUPT-DO NOT REPORT END PASS
- H. IF INTERRUPT-REPORT END PASS RESTART AT A

IF DEVICE FAILS TO GENERATE INTERRUPT A MESSAGE WILL BE PRINTED.

8. OPERATION OPTIONS

NONE

9. NON-STANDARD PRINTOUTS

"DEVICE FAILED TO INTERRUPT" MESSAGE IF INTERFACE FAILS TO INTERRUPT ON EITHER REQUEST BIT.

DRCI DEC/X11 SYSTEM EXERCISER MODULE MACY11 30A(1052) 12-OCT-78 16:31 PAGE 5
XDRCI0.P11 12-OCT-78 11:55

SEQ 0004

```

FDR11-C DEC/X11 EXERCISER MODULE
        IONODM <DRCI>167770,1,5    5000,57
        MODULE 140000,DRCI,167770,1,5    5000,57
        ;       TITLE DRCI DEC/X11 SYSTEM EXERCISER MODULE
        ;       DDXCOM VERSION 6      23-MAY-78
        ;       LIST BIN
***** BEGIN *****

000000, 051104 044503 040 MODNAME: ASCII /DRCI / ;MODULE NAME
000005, 00000 XFLAG: .BYTE OPEN          ;USED TO KEEP TRACK OF WRUFF USAGE
000006, 00000 ADDR: 167770+0           ;1ST DEVICE ADDR.
000010, 00001 VECTO: 1+0               ;1ST DEVICE VFCTCP.
000012, 240 BRI: .BYTE PRTY5+0         ;1ST HR LEVEL.
000013, 000 BRS: .BYTE PRTY+0          ;2ND HR LEVEL.
000014, 00000 DIV1: 0                 ;DEVICE INDICATOR 1.
000015, 00000 SR1: OPEN              ;SWITCH REGISTER 1.
000020, 00000 SR2: OPEN              ;SWITCH REGISTER 2.
000022, 00000 SR3: OPEN              ;SWITCH REGISTER 3.
000024, 00000 SR4: OPEN              ;SWITCH REGISTER 4.

***** STATUS *****

000026, 140000 STAT: 140000          ;STATUS WORD
000039, 00000 IM: START             ;MODULE START ADDR.
000040, 00000 SP: DDPTR            ;MODULE STACK POINTER.
000034, 00000 SPOINT: 0             ;PASS COUNTER.
000036, 00500 ICOUNT: 5000          ;# OF ITERATIONS PER PASS=5000
000040, 00000 ICOUNT: 0             ;LOC TO COUNT ITERATIONS
000042, 00000 SOFCNT: 0             ;LOC TO SAVE TOTAL SOFT ERRORS
000044, 00000 HRDCNT: 0             ;LOC TO SAVE TOTAL HARD ERRORS
000046, 00000 SDPAS: 0              ;LOC TO SAVE SOFT ERRORS PER PASS
000048, 00000 SHDPS: 0              ;LOC TO SAVE HARD ERRORS PER PASS
000052, 00000 SYCSNT: 0             ;# OF SYS ERRORS ACCUMULATED
000054, 00000 RANNUM: 0             ;HOLDS RANDOM # WHEN RAND MACRE IS CALLED
000056, 00000 CONFIG: 0              ;RESERVED FOR MONITOR USE
000060, 00000 RES1: 0               ;RESERVED FOR MONITOR USE
000062, 00000 RES2: 0               ;RESERVED FOR MONITOR USE
000064, 00000 SVR0: OPEN            ;LOC TO SAVE R0.
000066, 00000 SVR1: OPEN            ;LOC TO SAVE R1.
000068, 00000 SVR2: OPEN            ;LOC TO SAVE R2.
000070, 00000 SVR3: OPEN            ;LOC TO SAVE R3.
000072, 00000 SVR4: OPEN            ;LOC TO SAVE R4.
000074, 00000 SVR5: OPEN            ;LOC TO SAVE R5.
000076, 00000 SVR6: OPEN            ;LOC TO SAVE R6.
000100, 00000 CSRA: OPEN            ;ADDR OF CURRENT CSR.
000102, 00000 SB: 0                 ;ADDR OF GOOD DATA, OR
000104, 00000 ACSR: OPEN            ;CONTENTS OF CSR.
000104, 00000 WASADR: 0              ;ADDR OF BAD DATA, OR
000106, 00000 ASTAT: OPEN            ;STATUS REG CONTENTS.
000106, 00000 ERRTYP: 0              ;TYPE OF ERROR
000106, 00000 ASB: OPEN              ;EXPECTED DATA.
000110, 00000 AWAS: OPEN             ;ACTUAL DATA.
000114, 00000 RSTP: RESTART          ;RESTART ADDRESS AFTER END OF PASS
000116, 00000 WTO: OPEN              ;WORDS TO MEMORY PER ITERATION
000116, 00000 WDFR: OPEN              ;WORDS FROM MEMORY PER ITERATION
000120, 00000 INTB: OPEN              ;# OF INTERRUPTS PER ITERATION
000122, 00057 IDNUM: 57              ;MODULE IDENTIFICATION NUMBER=57

```

DRCI DEC/X11 SYSTEM EXERCISER MODULE MACY11 30A(1052) 12-OCT-78 16:31 PAGE 6
XDRCI0.P11 12-OCT-78 11:55

GPO 2025

```

000040          .REPT    SPSIZ      ;MODULE STACK STARTS HERE.
                  .NLIST
                  .WORD    0
                  .LIST
                  .ENDR

000224*        MODSP:
                  ;***** LOCAL VARIABLE STORAGE *****
192  000224*  000000  FLAG: OPEN
193  000226*  000000  SELECT: OPEN
194  000230*  177777  ALLON: -1
195  000230*  000000  ALLOFF: 0
196  000232*  177776  BUBBLE: 177776
197  000236*  177777  CBIT: -1
198
199
200
201
202          ;THIS MODULE TESTS THE DR11-C GENERAL DEVICE INTERFACE
203          ;MAINTENANCE CABLE MUST BE INSTALLED FOR THIS TEST
204
205
206
207
208
209
210          ;INITIALIZATION FOR GENERAL DEVICE INTERFACE
211
212
213  000240*  012767  000100  177646  START: MOV     #64,,WDTO      ;64 WORDS TO MEM/ITERATION
214  000242*  012767  000100  177639  MOV     #64,,WDPF      ;64 WORDS FROM MEM/ITERATION
215  000244*  012767  000002  177636  MOV     #64,,WDPF      ;64 WORDS FROM MEM/ITERATION
216  000262*  005737  000042  TST     #42          ;INTERUPTS/ITERATION
217  000266*  001411  BEQ     RESTRT      ;IS THIS XDP CHAIN
218  000270*  032737  000001  000052  BEQ     RESTRT      ;BR IF NO
219  000276*  001005  BNE     RESTRT      ;BR IF YES
220  000280*  000403  000000*  000750*  MSGNS,BEGIN,CHAIN   ;MAN. INTER. ALLOWED?
221  000284*  000403  000000*  000750*  MSGNS,BEGIN,CHAIN   ;ASCII MESSAGE CALL WITH COMMON HEADER
222  000312*  016767  177476  177706  RESTRT: MOV     #DIV1,SELECT  ;COPY THE DEVICE SELECTION PARAMETER
223  000320*  001002  BNE     REST      ;ARE ANY SELECTED? IF YES GO RUN THEM
224
225  000322*  104410  000000*  DROP: ENDS,BEGIN      ;IF NO, DROP THE MODULE.
226
227  000325*  016700  177674  REST: MOV     SELECT,RO      ;COPY THE DEVICE SELECTION PARAMETER
228  000331*  016700  177673  BEQ     DROP      ;ARE ANY SELECTED? IF NO, DROP THE MODULE
229  000334*  016700  177701  177540  MOV     #DIV1,SPADR  ;LOAD PUSS ADDRESS OF P1 AS SHOULD P5 ADDRESS
230  000342*  016705  177440  MOV     #ADD1,R5      ;GET DEVICE ADDRESS
231  000346*  016702  177436  MOV     VECTOR,R2      ;LOAD DEVICE VECTOR
232  000352*  006200  1$:     ASR     R0          ;ISOLATE A SELECTION FLAG
233  000354*  103406  BCS     3$          ;IF SELECTED, GO SET UP VECTORS
234  000356*  001416  BEQ     SETUP1      ;IF NO MORE SELECTED, GO DO DATA SETUP
235  000360*  062702  000010  ADD     #10,R2      ;POINT TO NEXT VECTOR
236  000364*  162705  000010  2$:     SUR     #10,R5      ;POINT TO NEXT ADDRESS
237  000372*  012723  000760*  3$:     BR     1$          ;GO PROCESS NEXT DEVICE
238  000377*  012723  000760*  MOV     #RACTA,(R2)+  ;SET UP REPORT TO SERVICE ROUTINE
239  000378*  036755  177410  MOV     #R1,(R2)+      ;SET UP A PRIORITY TO SERVICE ROUTINE
240  000402*  012723  000774*  MOV     #R1,(R2)+      ;SET UP A VECTOR TO SERVICE ROUTINE

```

DRCI DEC/X11 SYSTEM EXERCISER MODULE MACY11 30A(1052) 12-OCT-78 16:31 PAGE 7
XDRCI0.P11 12-OCT-78 11:55

SEG 0006

```

241 000406 016722 177400      MOV    R1,(R2)+      ;SET R PRIORITY
242 000412 000764                BR    2$             ;GO ADJUST ADDRESS POINTER
243 000414 016700 177606      SETUP1: MOV    SELECT,R0   ;COPY SELECTION PARAMETER
244 000420 016705 177362      MOV    ADDR,R5     ;RESET POINTER TO FIRST ADDRESS
245 000424 005725                TST    (R5)+        ;POINT TO DATA BUFFER WORD
246 000426 005684 177777 177602      NEXT: MOV    R1,CRIT   ;FIRST ROTATE WANTS 1'S INTO LSR
247 000426 016701 177566      CLR    R4             ;FLAG REGISTER (NOTES SWITC FROM ALLON PATTERN TO ALL
248 000426 016703 177566      MOV    ALLOL,R1   ;SET UP INITIAL DATA PATTERN
249 000442 006200                MOV    BUBBLE,R3  ;SET UP ALTERNATE DATA PATTERN
250 000446 006200                ASR    R0             ;ISOLATE A SELECTION FLAG
251 000450 103404                BCS    DRACT1   ;IF SELECTED GO CHECK DATA
252 000452 001532                BEQ    ENPS     ;IF NO MORE SELECTED CALL FOR END OF PASS
253 000452 0162705 000010      SETUP2: SUB    #10,R5   ;POINT TO THE NEXT DEVICE'S BUFFER WORD
254 000460 000762                CLR    R1             ;GO PROCESS NEXT DEVICE
255 ;*****C
256 ;CHECK DR11-C DATA TRANSFER CAPABILITY
257 ;SEND VERIFY AND CHECK (THROUGH MAINTAINANCE CABLE) ALTERNATING WORDS
258 ;OF 177777 AND 0-BUBBLING-THROUGH-1'S, THEN ALTERNATING 0 AND 0-
259 ;BUBBLING-THROUGH-1'S. TOTAL OF 64 PIT-PATTERNS SENT AND TESTED IN
260 ;EACH PASS.
261 000462 010115      DRACT1: CMP    R1,(R5)  ;MOVE DATA TO OUTPUT BUFFER
262 000464 001413      BEQ    2$             ;CHECK DATA
263 000466 001411      MOV    R5,WASADR ;BRANCH IF DATA GOOD
264 000470 016567 177410      MOV    R1,ASB   ;BAD DATA ADDRESS
265 000474 010167 177406      MOV    R1,ASB   ;MOVE "SHOULD BE"
266 000500 011567 177404      MOV    R5,AWAS   ;MOVE "WAS"
267 ;*****D
268 000504 104404 000000      DATERS-BEGIN: ;DATA ERROR!!!
269 ;*****E
270 000510 000417      1$:    CMP    R1,2(R5) ;NEXT DATA
271 000512 020165 000002      BEQ    2$             ;CHECK RECEIVED DATA
272 000516 001414      MOV    R5,WASADR ;BRANCH IF DATA GOOD
273 000520 010567 177360      ADD    R2,WASADR ;BAD DATA ADDRESS
274 000524 0162762 000092 177352      MOV    R1,ASB   ;MAKE IT THE CURRENT ADDRESS
275 000524 010167 177350      MOV    R1,ASB   ;MOVE "SHOULD BE"
276 000536 016567 000002 177344      MOV    R5,AWAS   ;MOVE "WAS"
277 ;*****F
278 000544 104404 000000      DATERS-BEGIN: ;DATA ERROR!!!
279 ;*****G
280 ;THIS SECTION FINDS AND LOADS INTO R1 THE NEXT PATTERN IN THE WORST-CASE
281 ;TEST SEQUENCE, AND DECIDES WHEN TO END THE TEST.
282 ;*****H
283 000550 020167 177454      2$:    CMP    R1,ALLOL ;SWITCH TO BUBBLE PATTERN
284 000554 001413 177450      BEQ    4$             ;IF STRAIGHT 1/0 PATTERN
285 000556 020167 177450      CMP    R1,ALLOFF ;IS NON
286 000562 001410                BEQ    4$             ;IN USE
287 000562 005704                TST    R4             ;SWITCH TO STRAIGHT 1/0: DECIDE WHICH
288 000564 005704                CMP    R1,ALLOFF ;FLAG RESET, SO IT'S ALL-ON PATTERN
289 000564 005704                BEQ    4$             ;PATTERN SET, IF ALL-OFF PATTERN
290 000564 005704                MOV    R5,ALLOFF,R1 ;REPEAT TEST WITH NEW WORD
291 000574 000732 177436      BR    DRACT1,R1 ;ALL-ON PATTERN
292 000576 016701 177426      3$:    MOV    ALLOL,R1   ;REPEAT TEST SEQUENCE
293 000602 000727                BR    DRACT1       ;PUT IN DATA BUFFER
294 000604 010301                MOV    R3,R1       ;LOAD C WITH MSB OF CRIT (LAST MSB OF R3)
295 000606 001617 177424      ROL    CB1T       ;SHIFT 0 THROUGH WORD, TO LEFT
296 000612 006103                ROL    R3             ;SFQ 0007

```

DRCI DEC/X11 SYSTEM EXERCISER MODULE MACY11 30A(1052) 12-OCT-78 16:31 PAGE 8
XDRCI0.P11 12-OCT-78 11:55

```

297 000614 103403      BCS    5$             ;DO WE SET OR RESET NEXT CRIT?
298 000616 005067 177414      CLR    CBIT           ;CONINUE IF NOT
299 000622 000403 177336      BR    GS             ;WAS 1, SO CRIT RESET
300 000624 012767 177777 177404 5$:    MOV    #1,CBIT   ;WAS 1, SO CRIT SET
301 000632 026701 177376 5$:    CMP    R1,CBIT   ;IS 0 BACK TO LSP WHERE IT STARTED?
302 000636 001311                BNE    DRACT1   ;IF NOT, CONTINUE
303 000636 001311                TST    R4             ;NO HAS BUBBLED ALL AROUND, ARE WE FINISHED?
304 000639 104404                BIT    R4             ;YES (FLAG SET); TEST INTERRUPTS
305 000644 005304                DEC    R4             ;NO SET FLAG
306 000646 016701 177360      MOV    ALLOFF,R1   ;REPEAT WHOLE THING WITH ALL-OFF PATTERN INSTEA
307 000652 000703                BR    DRACT1       ;RETURN TO TEST LOOP
308
309
310
311
312
313
314
315 000654 005015      INTEST: CLR    (R5)           ;CLEAR OUTPUT & INPUT(VIA CAPLE) BUFFERS
316 000656 012704 000002      MOV    #2,R4           ;BREAK LOOP COUNTER
317 000656 012704 000002      CLR    FLAG           ;FLAG: BOTH INTERRUPTS IN
318 000656 012704 000002      CLR    (R5)           ;CLEAR CONTROL REGISTER
319 000670 012745 000003      MOV    #2,R5           ;SET MAINTAINANCE INTERRUPTS
320 000674 010567 177174      MOV    #4,SVRS      ;SAVE PC BEFORE INTERRUPT COMES
321 000700 052715 000040      SIS    #40,(R5)       ;ENABLE A INTERRUPT
322 000704 104407 000000      TIMER: BREAK,BEGIN ;TEMPORARY RETURN TO MONITOR
323 000710 104407 000000      ;THEN CONTINUE AT NEXT INSTRUCTION
324 000710 104407 000000      TST    FLAG           ;IF FLAG IS CLEAR, NO INTERRUPT YET
325 000720 010105                BNE    RESET         ;REDUCE COUNT, IF NOT EXCEEDED, BREAK AGAIN
326 000722 005304                DEC    R4             ;WAIT A LITTLE LONGER
327 000724 010136                RNE    TIMER         ;RESTORE R4 VALUE
328 000726 104403 000000 000754*  RESET: MSGNS,REGIN,HUNG ;ASCII MESSAGE CALL WITH COMMON HEADERS
329 000734 005725                TST    (R5)+        ;RESTORE R5 VALUE
330 000734 005725                BR    SETUP2       ;GO PROCESS NEXT DEVICE
331 000736 008646                ENPS: ENDITS,BEGIN ;SIGNAL END OF ITERATION
332 000740 104413 000000      ENPS: ENDITS,BEGIN ;MONITOR SHALL TEST END OF PASS
333 000744 000167 177342      JMP    RESTRT       ;JMP RESTRT
334
335
336
337
338 000750 001012*      CHAIN: CHAINM
339 000752 177177*      HUNG: PA1777
340 000756 177177*      ;INPUT/OUTPUT SERVICE ROUTINES
341
342
343
344
345
346
347
348 000760 042777 000100 177106  DRACTA: BIC    #100,@SVRS ;DISABLE A INTERRUPT IMMEDIATELY, THROUGH SAVED RE
349 000766 005267 177232      INC    FLAG           ;RETURN TO BREAK LOOP
350 000772 000002 177072  DRACTB: BIC    #40,@SVRS ;DISABLE B INTERRUPT IMMEDIATELY, THROUGH SAVED RS
351 000774 042777 000040 177072      BIS    #100,@SVRS ;ENABLE A INTERRUPT NOW

```

DRCI DEC/X11 SYSTEM EXERCISER MODULE MACY11 30A(1052) 12-OCT-78 16:31 PAGE 9
XDRCIO.P11 12-OCT-78 11:55

SG 0000

```

353 001010- 000002          RTI           ;RETURN
354
355
356 001017- 051104 020103 040503 CHAINM: .ASCIZ  "DRC CANNOT BE RUN IN THIS CHAIN, MANUAL INTERVENTION NOT ALLOWED."
357 001020- 047106 025117 041000
358 001022- 040105 025522 020116
359 001034- 047111 040400 044510
360 001042- 020113 044103 044501
361 001050- 026116 046440 047101
362 001056- 040525 020114 047111
363 001064- 042524 053122 047105
364 001072- 044524 047117 047040
365 001100- 052117 040440 046114
366 001106- 053917 042609 000956
367 001122- 043040 048500 045514
368 001130- 020104 047524 044400
369 001136- 052116 051105 052522
370 001144- 052120 0000
371
372 001150- .EVEN
373 000001 .END

```

DRCI DEC/X11 SYSTEM EXERCISER MODULE MACY11 30A(1052) 12-OCT-78 16:31 PAGE 11
XDRCI0.P11 12-OCT-78 11:55 CROSS REFERENCE TABLE -- USER SYMBOLS

850 0009

DRCI DEC/X11 SYSTEM EXERCISER MODULE MACY11 30A(1052) 12-OCT-78 16:31 PAGE 12
XDRC10.P11 12-OCT-78 11:55 CROSS REFERENCE TABLE -- USEP SYMBOLS

STG 0010

DRCI DEC/X11 SYSTEM EXERCISER MODULE MACY11 30A(1052) 12-OCT-78 16:31 PAGE 13
XDRC10.P11 12-OCT-78 11:55 CROSS REFERENCE TABLE -- USER SYMBOLS

ANS 0011

STAT	000026R	150#				
SVR0	000052R	165#				
SVR1	000064R	166#				
SVR2	000065R	167#				
SVR3	000070R	168#				
SVR4	000072R	169#				
SVR5	000074R	170#	320*	348*	351*	352*
SVR6	000076R	171#				
SYSCNT	000052R	160#				
	000054R	162#				
TRODFD	= 00007	191#	328			
VECTOR	000010	191#				
WASADR	000104R	175#	231*			
WFDR	000116R	182#	214*	273*	274*	
WDTO	000114R	181#	213*			
XFLAG	000005R	139#				
.	= 001150R	372#				

. ABS. 000000 000
001150 001

ERRORS DETECTED: 0

DEFAULT GLOBALS GENERATED: 0
XDRC10,XDRC10/SOL/CRF:SYM=DDXCOM,XDRC10
RUN-TIME: 1 1 .2 SECONDS
RUN-TIME RATIO: 11/2=4.1
CORE USED: ?K (13 PAGES)