

IDENTIFICATION

PRODUCT CODE: AC-E893B-MC  
PRODUCT NAME: CXDPBBO DUP-11 MODULE  
PRODUCT DATE: SEPTEMBER 1978  
MAINTAINER: DEC/X11 SUPPORT GROUP

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1. ABSTRACT

DPB IS AN IOMOD THAT EXERCISES UP TO EIGHT CONSECUTIVELY  
ADDRESSED SYNCHRONOUS LINE INTERFACES (DUP11) BY TRANSMITTING A STANDARD  
BINARY COUNT PATTERN USING THE SYSTEM TEST MODE FEATURE. THE  
RECEIVED DATA IS COMPARED WITH THE TRANSMITTED DATA AND ANY ERRORS  
ARE REPORTED VIA THE CONSOLE TTY. ALL AVAILABLE INTERFACES  
(UP TO 8) ARE ACTIVATED AND RUNNING SIMULTANEOUSLY.

2. REQUIREMENTS

HARDWARE: DUP11 SYNCHRONOUS INTERFACE  
STORAGE:: DPB REQUIRES:  
1. DECIMAL WORDS: 585  
2. OCTAL WORDS: 1111  
3. OCTAL BYTES: 2222

3. PASS DEFINITION

ONE PASS OF THE DPB MODULE CONSISTS OF TRANSMITTING AND RECEIVING  
48,080 8-BIT CHARACTERS (TOTAL) PER ACTIVE DEVICE.

4. EXECUTION TIME

DPB RUNNING ALONE ON A PDP11/40 PROCESSOR TAKES APPROXIMATELY  
1 MINUTE TO COMPLETE ONE PASS. THIS TIME INCREASES SLIGHTLY  
AS MORE DEVICES ARE ADDED.

5. CONFIGURATION PARAMETERS

DEFAULT PARAMETERS:

DEVADR: 000001, VECTOR:001, BR1:5, BR2:5, DEVCNT:1  
THE DEVICE IS TESTED IN SDLC MODE AS A SECONDARY STATION,  
WITH A HARDWARE BCC CHECK USING THE CRC/CCITT POLYNOMIAL.

REQUIRED PARAMETERS: THE CSR AND VECTOR MUST BE SET UP BY THE USER.

6. DEVICE/OPTION SETUP

NONE: NO SPECIAL SETUP IS REQUIRED IN SYSTEM TEST MODE

7. MODULE OPERATION

TEST SEQUENCE:

- A. TEST UP TO 8 POSSIBLE DEVICES FOR SELECTION
- B. STORE THE NO. OF DEVICES TO BE TESTED AND SET UP THE VECTORS AND PRIORITIES FOR THESE DEVICES
- C. LOAD ALL REGISTERS--SECONDARY STATION, RECEIVER AND TRANSMITTER ON, SYSTEM TEST MODE AND INTERRUPT ENABLES FOR ALL ACTIVE DEVICES. ENABLE SELECTED DEVICES.
- D. TRANSMITTER INTERRUPT SERVICE:
  - 1.) TEST FOR FALSE INTERRUPT (READY (0)); REPORT ERRORS
  - 2.) OUTPUT NEXT CHARACTER TO THE DEVICE
  - 3.) RETURN TO MONITOR TO WAIT FOR RECEIVER INTERRUPT.
- E. RECEIVER INTERRUPT SERVICE:
  - 1.) TEST FOR FALSE INTERRUPT (DONE (0)); REPORT ERRORS
  - 2.) CHECK FOR DATA ERROR; REPORT ERRORS
  - 3.) RETURN TO MONITOR TO WAIT FOR TRANSMITTER INTERRUPT
- F. REPEAT D AND E UNTIL ALL DEVICES HAVE BEEN PROCESSED, THEN CHECK FOR BCC ERRORS
- G. TURN OFF ALL ACTIVE DEVICES AND DECREMENT ITERATION COUNT. IF NOT 0, RESTART AT B.
- H. SIGNAL END PASS.

8. OPERATION OPTIONS

- A. LOCATION DVID1 (DPB 14) MAY BE CHANGED TO SELECT ANY COMBINATION OF DEVICES BIT0=DEVO, BIT1=DEV1 .....BIT7=DEV7.  
NOTE: IF DVID1 IS INITIALLY 0 DPB WILL BE DROPPED FROM TEST.

9. NON STANDARD PRINTOUTS

NONE: ALL PRINTOUTS HAVE STANDARD FORMATS AS DESCRIBED IN THE DEC/X11 DOCUMENT.



203  
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205 100000  
206 040000  
207 020000  
208 010000  
209 010000  
210 002000  
211 001000  
212 000400  
213 000200  
214 000100  
215 000040  
216 000020  
217 000010  
218 000004  
219 000002  
220 000001  
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226 100000  
227 040000  
228 020000  
229 010000  
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231 002000  
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243 100000  
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246 010000  
247 002000  
248 001000  
249 000400  
250 100000  
251 001000  
252 010000  
253  
254 100000  
255 040000  
256 020000  
257 010000  
258 004000

;BIT DEFINITIONS  
BIT15=100000  
BIT14=40000  
BIT13=20000  
BIT12=10000  
BIT11=4000  
BIT10=2000  
BIT9=1000  
BIT8=400  
BIT7=200  
BIT6=100  
BIT5=40  
BIT4=20  
BIT3=10  
BIT2=4  
BIT1=2  
BIT0=1

CONTROL REGISTER DEFINITIONS

;RXCSR BIT DEFINITIONS  
DSCA=BIT15 ;DATA SET CHANGE A  
RINC=BIT14 ;RING  
CS=BIT13 ;CLR TO SEND  
CARDET=BIT12 ;CARRIER DETECT  
RECACT=BIT11 ;REC ACTIVE  
SRD=BIT10 ;SEC REC DATA  
DS=BIT9 ;DATA SET RDY  
SPSVN=BIT8 ;STRIP SYNC  
RXDDNE=BIT7 ;REC DONE  
RINTEN=BIT6 ;REC INTR ENABLE  
DSMFC=BIT5 ;DSM INTR ENABLE  
RCVEN=BIT4 ;REC ENABLE  
STD=BIT3 ;SEC XMIT DATA  
RTS=BIT2 ;REC TO SEND  
DTS=BIT1 ;DATA TERM RDY  
DSCB=BIT0 ;DATA SET CHANGE B

;RXDBUF BIT DEFINITIONS  
RXDERR=BIT15 ;REC DATA ERROR  
OVRERR=BIT14 ;OVERRUN ERROR  
CRCERR=BIT12 ;CRC ERROR  
RABORT=BIT10 ;REC ABORT  
REOM=BIT9 ;REC END OF MESSAGE  
RSUM=BIT8 ;REC START OF MESSAGE

;PARCSR BIT DEFINITIONS  
DECMOD=BIT15 ;DEC MODE (DDCMP)  
CRCEH=BIT9 ;CRC ENABLE  
PRISEC=BIT12 ;PRI SEC SELECT

;TXCSR BIT DEFINITIONS  
TXDLAT=BIT15 ;TX DATA LATE  
MTDATA=BIT14 ;MAINT DATA OUT  
CLK=BIT13 ;CLK  
WMODEB=BIT12 ;MAINT MODE B  
WMODEA=BIT11 ;MAINT MODE A

259 002000  
260 001000  
261 000400  
262 000200  
263 000100  
264 000020  
265 000010  
266  
267 000000  
268 014000  
269 010000  
270 004000  
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273 100000  
274 040000  
275 020000  
276 010000  
277 004000  
278 002000  
279 001000  
280 000400  
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283 000000  
284 000002  
285 000004  
286 000004  
287 000006  
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289 000224 000000  
290 000226 000000  
291 000230 000000  
292 000234 000000  
293 000234 000000  
294 000236 000000  
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BITW=BIT10 ;BIT WINDOW INPUT  
TXACT=BIT9 ;TX ACTIVE  
HRSST=BIT8 ;HARD RESET  
TXDDNE=BIT7 ;XMIT DONE  
TXINTE=BIT6 ;XMIT DONE INTR ENABLE  
SEMD=BIT4 ;SEMD  
HDXEN=BIT3 ;DI/FOX

;TXCSR WRD DEFINITIONS  
USER=0 ;USER MODE  
MMODE=14000 ;MAINT INT MODE  
WMT=1000 ;WMT MODE  
SYSTST=4000 ;SYSTEM TEST MODE

;TXDBUF BIT DEFINITIONS  
RCRC7=BIT15 ;CRC BIT WINDOW  
RCRCIN=BIT14 ;DITTO  
TCRC7=BIT13 ;DITTO  
TCRCIN=BIT12 ;DITTO  
THER=BIT11 ;MAINTENANCE TIMER  
TABORT=BIT10 ;TRANSMIT ABORT  
TEOM=BIT9 ;TRANSMIT END OF MESSAGE  
TSUM=BIT8 ;TRANSMIT START OF MESSAGE

;MISC. PROGRAM DEFINITIONS AND EQUATES  
RXCSR=R0 ;RECEIVER CONTROL REGISTER  
RXDBUF=2 ;RECEIVER DATA BUFFER REGISTER  
PARCSR=2 ;PARAMETER STATUS REGISTER  
TXCSR=4 ;TRANSMITTER CONTROL REGISTER  
TXDBUF=6 ;TRANSMITTER DATA BUFFER REGISTER

N.DUPS: OPEN ;SOFTWARE MEMORY  
TOTAL: OPEN ;STORAGE FOR DATA CHECK  
COUNT: OPEN ;ITERATION COUNT  
RXBF: OPEN ;REC BUFFER ADORS STORAGE  
SAVB: OPEN ;REC ISR TEMPORARY STORAGE  
SELECT: OPEN ;SOFTWARE POINTER TO ACTIVE DEVICES

.MACRO SPUSH SA,SB,SC,SD,SE,SF  
MOV SA,-(SP) ;SAVE \*SA ON THE STACK  
.IIF NB <SB> MOV SB,-(SP) ;SAVE \*SB ON THE STACK  
.IIF NB <SC> MOV SC,-(SP) ;SAVE \*SC ON THE STACK  
.IIF NB <SD> MOV SD,-(SP) ;SAVE \*SD ON THE STACK  
.IIF NB <SE> MOV SE,-(SP) ;SAVE \*SE ON THE STACK  
.IIF NB <SF> MOV SF,-(SP) ;SAVE \*SF ON THE STACK  
.ENDM SPUSH

.MACRO SPOP SA,SB,SC,SD,SE,SF  
.IIF NB <SA> MOV (SP)+,SA ;POP STACK TO \*SA  
.IIF NB <SB> MOV (SP)+,SB ;POP STACK TO \*SB  
.IIF NB <SC> MOV (SP)+,SC ;POP STACK TO \*SC  
.IIF NB <SD> MOV (SP)+,SD ;POP STACK TO \*SD  
.IIF NB <SE> MOV (SP)+,SE ;POP STACK TO \*SE  
.IIF NB <SF> MOV (SP)+,SF ;POP STACK TO \*SF  
.ENDM SPOP

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316 000240* 016767 177550 177770 START: ;BEGIN TESTING FOR THE DUP-11
317 000240* 010100 MOV DVID1,SELECT ;GET ACTIVE DEVICES
318 000250* BME RESTR1 ;BR IF ANY ARE SELECTED
319 000250* 104410 000000* DROP: ENDS,BEGIN ;;INCORRECT NUMBER OF DUP11'S
320
321
322 000254* 032767 177400 177754 RESTR1: ;SETUP VECTORS FOR ACTIVE DEVICES
323 000262* 001372 BIT #C377,SELECT ;MAKE SURE NO MORE THAN 8 SELECTED.
324 000264* 016701 BME DROP ;INVALID-NO MORE THAN 8 DEVICES!!
325 000270* 001767 MOV SELECT,R1 ;GET IMAGE OF RUNNING DEVICES
326 000272* 005067 CLR N,DUPS ;CLEAR THE # OF DUP'S (SOFTWARE)
327 000276* 016702 MOV VECTOR,R2 ;GET INITIAL VECTOR
328 000302* 016700 MOV ADDR,R0 ;GET INITIAL ADDRESS
329 000306* 012703 MOV #LXKFB,R3 ;SET ISR POINTER
330 000312* 012767 002114* 177712 MOV #RXBF0,RXBF ;SET UP BUFFER POINTERS
331 000320* 006201 1$: ASR R1 ;ACTIVE?
332 000322* 103410 BCS 38 ;BR IF DONE
333 000324* 001437 BEQ SETUP1 ;BR IF DONE
334 000326* 062703 000024 ADD #2,R3 ;PCP ISR POINTER
335 000332* 062702 000010 2$: ADD #10,R2 ;POP VECTOR
336 000336* 062700 000010 BR #10,R0 ;POP CSR
337 000342* 005267 INC R1 ;CONTINUE
338 000344* 005267 177654 3$: INC N,DUPS ;UPDATE THE # TO RUN
339 000350* 010312 MOV R3,(R2) ;LOAD ISR POINTER (RECEIVER)
340 000352* 016762 177434 000002 MOV BR1,4(R2) ;LOAD PRIORITY
341 000354* 016762 000004 MOV BR2,6(R2) ;LOAD PRIORITY
342 000364* 005063 000010 CLR 10(R3) ;CLR REC BYTE COUNT
343 000370* 062703 000012 ADD #12,R3 ;UPDATE POINTER
344 000374* 010362 000004 MOV R3,4(R2) ;LOAD TRANSMITTER ISR
345 000400* 016762 000004 000006 MOV BR2,6(R2) ;LOAD PRIORITY
346 000406* 010063 000004 MOV R0,4(R3) ;LOAD CSR POINTER
347 000412* 005063 000010 CLR 10(R3) ;CLEAR TX BYTE COUNT
348 000416* 062703 000012 ADD #12,R3 ;POP ISR POINTER
349 000422* 000743 BR 2$ ;CONTINUE
350
351 ;SET UP BUFFERS
352 000424* 016767 177574 177466 SETUP1: MOV N,DUPS,INTR ;SET # OF INTERRUPTS
353 000426* 016767 177566 MOV N,DUPS,WDTO ;SET # OF WORDS TO MEM
354 000440* 016767 177560 177450 MOV N,DUPS,WDFR ;SET # OF WORDS FROM MEM
355 000446* 006367 177446 ASL INTR ;DOUBLE INTERRUPTS
356 000452* 012700 000010 MOV #8,R0 ;LOAD BUFFER SIZE
357 000454* 012703 002074* MOV #1560,R3 ;LOAD BUFFER START
358 000462* 012723 000135 1$: MOV #135,(R3)+ ;UPDT
359 000466* 005300 DEC R0 ;FOR FINISH
360 000470* 001374 BME 1$ ;BR IF MORE TO GO
361 000472* 012700 000010 MOV #8,R0 ;LOAD BUFFER STZ
362 000476* 012703 002114* MOV #RXBF0,R3 ;LOAD START ADDRESS
363 000502* 012723 000136 2$: MOV #136,(R3)+ ;CLEAR
364 000506* 005300 DEC R0 ;LOWER THE # TO DO
365 000510* 001374 BME 2$ ;BR IF MORE TO GO
366
367 ;PRELIMINARY DEVICE SETUP
368 000512* 016700 177270 SETUP2: MOV ADDR,R0 ;LOAD FIRST CSR
  
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369 000516* 016701 177514 MOV SELECT,R1 ;SET UP TO GET ACTIVE DEVICES
370 000522* 006201 R1 ;GET AN ACTIVE
371 000524* 103404 BCS 2$ ;DONE
372 000526* 001410 BEQ ACTIV ;BR IF DONE
373 000530* 062700 000010 ADD #10,R0 ;UPDATE FOR THE NEXT ONE
374 000534* 000772 BR 1$ ;CONTINUE
375 000536* 004767 000766 2$: JSR PC,PRELIM ;GO DO THE DEVICE SETUP
376 000542* 062700 000010 ADD #10,R0 ;UPDATE FOR THE NEXT ONE
377 000546* 000765 BR 1$ ;CONTINUE
378 000550* 016701 177462 177444 ACTIV: MOV SELECT,R1 ;GET THE ACTIVE DUP'S
379 000554* 016767 177444 MOV N,DUPS,TOTAL ;SET UP FOR DATA CHECK
380 000562* 016700 177220 MOV ADDR,R0 ;GET FIRST CSR
381 000566* 006201 R1 ;GET AN ACTIVE ONE
382 000570* 103404 BCS 3$ ;BR IF ACTIVE
383 000572* 001406 BEQ WAIT ;BR IF DONE
384 000574* 062700 000010 ADD #10,R0 ;UPDATE CSR
385 000600* 000772 BR 1$ ;CONTINUE
386 000602* 004767 000764 3$: JSR PC,READY ;TURN ON DEVICE
387 000606* 000772 BR 2$ ;CONTINUE
388
389 ;DELAY AND SCAN FOR FINISH ROUTINE
390 000610* 005005 WAIT: CLR R5 ;SET FOR A LONG DELAY
391 000612* 012704 000002 MOV #2,R4 ;APPROX 30 SECONDS
392
393 SCAN:
394 000616* 1$:
395 000616* 104407 000000* BREAK$,BEGIN ;TEMPORARY RETURN TO MONITOR....
396 000622* 104407 000000* BREAK$,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
397 000626* 005767 177374 TST TOTAL ;GET THE # OF ACTIVE DUPS
398 000632* 001004 BME 3$ ;BR IF MORE TO GO
399
400 -----
401 ;-----
402 000634* 104413 000000* ENDT$,BEGIN ;SIGNAL END OF ITERATION.
403 000640* 000167 177410 2$: JMP RESTR1 ;MONITOR SHALL TEST END OF PASS
404 000644* 005305 3$: DEC R5 ;RESTART PROGRAM
405 000646* 001363 BNE 1$ ;STALL FOR ALL DUP'S TO FINISH
406 000650* 105304 DECB R4 ;BR IF DELAY NOT ZERO
407 000652* 001361 BNE 1$ ;OR ANOTHER DELAY
408 000654* 104403 000000* 000666* MSGNS,BEGIN,HUNG ;BR IF MORE TO GO
409 000666* 002154* ENDS,BEGIN ;ASCII MESSAGE CALL WITH COMMON HEADER
410 000670* 177777 HUNG: HUNG ;DROPPED THE MODULE
  
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411 ;TRANSMITTER INTERRUPT SERVICE ROUTINE
412 TXISR:
413 MOV RO,-(SP) ;SAVE RO ON THE STACK
414 MOV R1,-(SP) ;SAVE R1 ON THE STACK
415 MOV (R5)+,RO ;GET DEVICE CSR AND POP OFFSET TO R5
416 MOV (R5)+,R1 ;GET DATA ADDRESS AND LEAVE R5=COUNT
417 BIT #TXDLAY,TXCSR(RO) ;OVERFLOW?
418 BEQ 55 ;BR IF NO
419 CLR RO,CSRA ;LOAD FOR PRINTOUT
420 MOV ACSR ;CLEAR
421 MOV #MRESET,TXCSR(RO) ;TURN OFF DEVICE
422 DEC TOTAL ;LOWER THE # TO DO
423 MOV (SP)+,R1 ;POP STACK TO R1
424 MOV (SP)+,RO ;POP STACK TO RO
425 MOV (SP)+,R5 ;POP STACK TO R5
426 ;-----
427 FIRQ$,BEGIN,1$ ; QUEUE UP TO CONTINUE AT 1$ AND RTI
428 ;-----
429 1$: MOV #41,ERRTYP ;XMITTER DATA LATE
430 ;*****
431 SIFERS,BEGIN,NULL ;OVERFLOW ERROR IN TRANSMITTER
432 ;*****
433 JMP SCAN ;CONTINUE SCANNING FOR END
434 MOV (R1),TXDBUF(RO) ;PUSH OUT DATA
435 INCB (R1) ;UPDATE DATA
436 INC (R5) ;UPDATE THE COUNT
437 CMP #1202.,(R5) ;CHECK FOR FINISH
438 BNE 3$ ;BR IF MORE TO GO
439 MOV #TEOM,TXDBUF(RO) ;END MESSAGE
440 BIC #TXINTE,TXCSR(RO) ;TURN OFF INTERRUPTS
441 ;-----
442 2$: MOV (SP)+,R1 ;POP STACK TO R1
443 MOV (SP)+,RO ;POP STACK TO RO
444 MOV (SP)+,R5 ;POP STACK TO R5
445 RTI ;RETURN
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446 ;RECEIVER INTERRUPT SERVICE ROUTINE
447 RXISR:
448 MOV RO,-(SP) ;SAVE RO ON THE STACK
449 MOV R1,-(SP) ;SAVE R1 ON THE STACK
450 MOV (R5)+,RO ;GET BUFFER ADDR AND LEAVE R5=BYTE COUNT
451 MOV (R5)+,R1 ;CHECK FOR DONE
452 TSTB (RO) ;BR IF SET
453 BMI 2$ ;SETUP FOR TYPEOUT
454 MOV RO,CSRA ;DITTO
455 MOV (RO),ACSR ;DITTO
456 MOV #MRESET,TXCSR(RO) ;TURN OFF DEVICE
457 DEC TOTAL ;LOWER THE # TO D
458 MOV (SP)+,R1 ;POP STACK TO R1
459 MOV (SP)+,RO ;POP STACK TO RO
460 MOV (SP)+,R5 ;POP STACK TO R5
461 ;-----
462 FIRQ$,BEGIN,1$ ; QUEUE UP TO CONTINUE AT 1$ AND RTI
463 ;-----
464 1$: MOV #11,ERRTYP ;ILLEGAL INTERRUPT
465 ;*****
466 HRDERS,BEGIN,NULL ;FALSE INTERRUPT
467 ;*****
468 JMP SCAN ;CONTINUE SCANNING FOR END
469 MOV RXDBUF(RO),SAVBF ;GET THE BUFFER
470 TST SAVBF ;CHECK FOR DATA ERROR
471 BMI 3$ ;BR IF ERROR FOUND
472 BIT #RECACT,(RO) ;CHECK FOR ACTIVE
473 BEQ 5$ ;BR IF NO ACTIVE
474 INC (R5) ;UPDATE THE # TO DO
475 CMP #1200.,(R5) ;CHECK FOR DATA END
476 BMI 11$ ;BR IF DATA DONE
477 CMPB (R1),SAVBF ;CHECK DATA
478 BNE 7$ ;BR IF NO A MATCH
479 INCB (R1) ;UPDATE DATA
480 BR 14$ ;LEAVE
481 MOV RO,CSRA ;SETUP FOR TYPEOUT
482 CLR ACSR ;DITTO
483 MOV #MRESET,TXCSR(RO) ;TURN OFF DEVICE
484 DEC TOTAL ;DECREMENT THE # TO DO
485 MOV (SP)+,R1 ;POP STACK TO R1
486 MOV (SP)+,RO ;POP STACK TO RO
487 MOV (SP)+,R5 ;POP STACK TO R5
488 ;-----
489 FIRQ$,BEGIN,4$ ; QUEUE UP TO CONTINUE AT 4$ AND RTI
490 ;-----
491 4$: MOV #17,ERRTYP ;OVERRUN OR CRC ERROR
492 ;*****
493 HRDERS,BEGIN,NULL ;HARDWARE DATA ERROR
494 ;*****
495 JMP SCAN ;CONTINUE SCANNING FOR END
496 MOV RO,CSRA ;SETUP FOR TYPEOUT
497 MOV (RO),ACSR ;DITTO
498 MOV #MRESET,TXCSR(RO) ;TURN OFF DEVICE
499 DEC TOTAL ;LOWER THE # TO DO
500 MOV (SP)+,R1 ;POP STACK TO R1
501 MOV (SP)+,RO ;POP STACK TO RO
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502 001274 012605 MOV (SP)+,R5 ;POP STACK TO R5
503 -----
504 001276 000004 000000 001304 PIRQS,BEGIN,6$ ; QUEUE UP TO CONTINUE AT 6$ AND RTI
505
506 001304 012767 000042 176574 6$: MOV #42,ERRTYP ;ACTIVE CLEARED AFTER DONE
507 *****
508 001312 104405 000000 000000 HRDRS,BEGIN,NULL ;ACTIVE CLEARED AFTER A DONE
509 *****
510 001320 000167 177272 7$: JMP SCAN ;CONTINUE SCANNING FOR END
511 001324 010167 176552 MOV R1,SBADR ;LOAD GOOD DATA ADRS
512 001330 010067 176544 MOV R0,CSRA ;LOAD CSR
513 001334 012767 000234 176542 MOV #SAVBF,WASADR ;LOAD BAD DATA ADRS
514 001342 118167 176540 MOVB (R1),ASB ;LOAD GOOD DATA
515 001346 118167 176662 176534 MOVB SAVBF,WAS ;LOAD BAD DATA
516 001354 012760 000400 000004 MOV #MRESET,TXCSR(R0) ;TURN OFF DEVICE
517 001362 005367 176640 DEC TOTAL ;LOWER THE # TO DO
518 001366 012601 MOV (SP)+,R1 ;POP STACK TO R1
519 001370 012600 MOV (SP)+,R0 ;POP STACK TO R0
520 001372 012605 MOV (SP)+,R5 ;POP STACK TO R5
521 -----
522 001374 000004 000000 001402 PIRQS,BEGIN,10$ ; QUEUE UP TO CONTINUE AT 10$ AND RTI
523
524 001402 10$:
525 *****
526 001402 104404 000000 DATERS,BEGIN ;DATA ERROR!!!
527 *****
528 001406 000167 177204 JMP SCAN ;CONTINUE SCANNING FOR END
529 001412 022715 002262 11$: CMP #1202,(R5) ;CHECK FOR CRC DONE
530 001416 001940 BNE #48 ;BR IF NO
531 001420 032767 010000 176606 BIT #CRCERR,SAVBF ;CHECK FOR CRC ERROR
532 001426 001427 BEQ 13$ ;BR IF NO
533 001430 010067 176444 MOV R0,CSRA ;SET UP FOR TYPEOUT
534 001434 005967 176442 CLR ACSR
535 001440 012760 000400 MOV #MRESET,TXCSR(R0) ;TURN OFF DEVICE
536 001446 005367 176554 DEC TOTAL ;LOWER THE # TO DO
537 001452 012601 MOV (SP)+,R1 ;POP STACK TO R1
538 001454 012600 MOV (SP)+,R0 ;POP STACK TO R0
539 001456 012605 MOV (SP)+,R5 ;POP STACK TO R5
540 -----
541 001460 000004 000000 001466 PIRQS,BEGIN,12$ ; QUEUE UP TO CONTINUE AT 12$ AND RTI
542
543 001466 012767 000043 176412 12$: MOV #43,ERRTYP ;CRC ERROR
544 *****
545 001474 104405 000000 000000 HRDRS,BEGIN,NULL ;HARDWARE DETECTED CRC ERROR
546 *****
547 001502 000167 177110 JMP SCAN ;CONTINUE SCANNING FOR END
  
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548 001506 012760 000400 000004 13$: MOV #MRESET,TXCSR(R0) ;TURN OFF DEVICE
549 001514 005367 176506 DEC TOTAL ;LOWER THE # TO DO
550
551 001520 012601 14$: MOV (SP)+,R1 ;POP STACK TO R1
552 001524 012600 MOV (SP)+,R0 ;POP STACK TO R0
553 001526 012605 MOV (SP)+,R5 ;POP STACK TO R5
554 RTI ;RETURN
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579 ;SERVICE CODE FOR LINKING A PARTICULAR DEVICE
580 ;TO A COMMON TRANSMIT OR RECEIVE INTERRUPT SERVICE ROUTINE.
581
582 001614*
583 LNKTAB:
584 .MACRO $JS,$Q
585 JSR R5,RXISR ;ANSWER FOR DEVICE *SQ REC ISR
586 0 ;CSR FOR DEVICE *SQ REC ISR
587 RXBF*$Q ;BUFFER LOC FOR DEVICE *SQ
588 BCNT*$Q ;BYTE COUNT ADDRESS FOR RXBF*$Q
589
590 JSR R5,TXISR ;ANSWER FOR DEVICE *SQ TX ISR
591 0 ;CSR FOR DEVICE *SQ TX ISR
592 TXBF*$Q ;DATA ADDRESS FOR DEVICE *SQ
593 OFSET*$Q ;BYTE COUNT ADDRESS FOR DEVICE *SQ
594
595 .ENDM $JS
596
597 001614* 004567 177212 JSR R5,RXISR ;ANSWER FOR DEVICE 0 REC ISR
598 0 ;CSR FOR DEVICE 0 REC ISR
599 RXBF0 ;BUFFER LOC FOR DEVICE 0
600 BCNT0 ;BYTE COUNT ADDRESS FOR RXBF0
601
602 001626* 004567 177040 JSR R5,TXISR ;ANSWER FOR DEVICE 0 TX ISR
603 0 ;CSR FOR DEVICE 0 TX ISR
604 TXBF0 ;DATA ADDRESS FOR DEVICE 0
605 OFSET0 ;BYTE COUNT ADDRESS FOR DEVICE 0
606
607 001640* 004567 177166 JSR R5,RXISR ;ANSWER FOR DEVICE 1 REC ISR
608 0 ;CSR FOR DEVICE 1 REC ISR
609 RXBF1 ;BUFFER LOC FOR DEVICE 1
610 BCNT1 ;BYTE COUNT ADDRESS FOR RXBF1
611
612 001652* 004567 177014 JSR R5,TXISR ;ANSWER FOR DEVICE 1 TX ISR
613 0 ;CSR FOR DEVICE 1 TX ISR
614 TXBF1 ;DATA ADDRESS FOR DEVICE 1
615 OFSET1 ;BYTE COUNT ADDRESS FOR DEVICE 1
616
617 001664* 004567 177142 JSR R5,RXISR ;ANSWER FOR DEVICE 2 REC ISR
618 0 ;CSR FOR DEVICE 2 REC ISR
619 RXBF2 ;BUFFER LOC FOR DEVICE 2
620 BCNT2 ;BYTE COUNT ADDRESS FOR RXBF2
621
622 001676* 004567 176770 JSR R5,TXISR ;ANSWER FOR DEVICE 2 TX ISR
623 0 ;CSR FOR DEVICE 2 TX ISR
624 TXBF2 ;DATA ADDRESS FOR DEVICE 2
625 OFSET2 ;BYTE COUNT ADDRESS FOR DEVICE 2
626
627 001710* 004567 177116 JSR R5,RXISR ;ANSWER FOR DEVICE 3 REC ISR
628 0 ;CSR FOR DEVICE 3 REC ISR
629 RXBF3 ;BUFFER LOC FOR DEVICE 3
630 BCNT3 ;BYTE COUNT ADDRESS FOR RXBF3
631
632 001722* 004567 176744 JSR R5,TXISR ;ANSWER FOR DEVICE 3 TX ISR
633 0 ;CSR FOR DEVICE 3 TX ISR
634 TXBF3 ;DATA ADDRESS FOR DEVICE 3
635 OFSET3 ;BYTE COUNT ADDRESS FOR DEVICE 3

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635
636 001734* 004567 177072 JSR R5,RXISR ;ANSWER FOR DEVICE 4 REC ISR
637 0 ;CSR FOR DEVICE 4 REC ISR
638 RXBF4 ;BUFFER LOC FOR DEVICE 4
639 BCNT4 ;BYTE COUNT ADDRESS FOR RXBF4
640
641 001746* 004567 176720 JSR R5,TXISR ;ANSWER FOR DEVICE 4 TX ISR
642 0 ;CSR FOR DEVICE 4 TX ISR
643 TXBF4 ;DATA ADDRESS FOR DEVICE 4
644 OFSET4 ;BYTE COUNT ADDRESS FOR DEVICE 4
645
646 001760* 004567 177046 JSR R5,RXISR ;ANSWER FOR DEVICE 5 REC ISR
647 0 ;CSR FOR DEVICE 5 REC ISR
648 RXBF5 ;BUFFER LOC FOR DEVICE 5
649 BCNT5 ;BYTE COUNT ADDRESS FOR RXBF5
650
651 001772* 004567 176674 JSR R5,TXISR ;ANSWER FOR DEVICE 5 TX ISR
652 0 ;CSR FOR DEVICE 5 TX ISR
653 TXBF5 ;DATA ADDRESS FOR DEVICE 5
654 OFSET5 ;BYTE COUNT ADDRESS FOR DEVICE 5
655
656 002004* 004567 177022 JSR R5,RXISR ;ANSWER FOR DEVICE 6 REC ISR
657 0 ;CSR FOR DEVICE 6 REC ISR
658 RXBF6 ;BUFFER LOC FOR DEVICE 6
659 BCNT6 ;BYTE COUNT ADDRESS FOR RXBF6
660
661 002016* 004567 176650 JSR R5,TXISR ;ANSWER FOR DEVICE 6 TX ISR
662 0 ;CSR FOR DEVICE 6 TX ISR
663 TXBF6 ;DATA ADDRESS FOR DEVICE 6
664 OFSET6 ;BYTE COUNT ADDRESS FOR DEVICE 6
665
666 002030* 004567 176776 JSR R5,RXISR ;ANSWER FOR DEVICE 7 REC ISR
667 0 ;CSR FOR DEVICE 7 REC ISR
668 RXBF7 ;BUFFER LOC FOR DEVICE 7
669 BCNT7 ;BYTE COUNT ADDRESS FOR RXBF7
670
671 002042* 004567 176624 JSR R5,TXISR ;ANSWER FOR DEVICE 7 TX ISR
672 0 ;CSR FOR DEVICE 7 TX ISR
673 TXBF7 ;DATA ADDRESS FOR DEVICE 7
674 OFSET7 ;BYTE COUNT ADDRESS FOR DEVICE 7
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676 ;BUFFER AREAS
677
678 .MACRO $OF,$Q
679 OFSET$Q: .WORD 0 ;TRANSMITTER BYTE COUNT POINTER FOR DEVICE *$Q
680 .ENDM
681 002054 000000 OFSET0: .WORD 0 ;TRANSMITTER BYTE COUNT POINTER FOR DEVICE 0
682 002056 000000 OFSET1: .WORD 0 ;TRANSMITTER BYTE COUNT POINTER FOR DEVICE 1
683 002060 000000 OFSET2: .WORD 0 ;TRANSMITTER BYTE COUNT POINTER FOR DEVICE 2
684 002062 000000 OFSET3: .WORD 0 ;TRANSMITTER BYTE COUNT POINTER FOR DEVICE 3
685 002064 000000 OFSET4: .WORD 0 ;TRANSMITTER BYTE COUNT POINTER FOR DEVICE 4
686 002066 000000 OFSET5: .WORD 0 ;TRANSMITTER BYTE COUNT POINTER FOR DEVICE 5
687 002070 000000 OFSET6: .WORD 0 ;TRANSMITTER BYTE COUNT POINTER FOR DEVICE 6
688 002072 000000 OFSET7: .WORD 0 ;TRANSMITTER BYTE COUNT POINTER FOR DEVICE 7
689
690 .MACRO $TB,$Q
691 TXBF$Q: .WORD 0 ;TRANSMITTER BUFFER FOR DEVICE *$Q
692 .ENDM
693
694 002074 000000 TXBF0: .WORD 0 ;TRANSMITTER BUFFER FOR DEVICE 0
695 002076 000000 TXBF1: .WORD 0 ;TRANSMITTER BUFFER FOR DEVICE 1
696 002100 000000 TXBF2: .WORD 0 ;TRANSMITTER BUFFER FOR DEVICE 2
697 002102 000000 TXBF3: .WORD 0 ;TRANSMITTER BUFFER FOR DEVICE 3
698 002104 000000 TXBF4: .WORD 0 ;TRANSMITTER BUFFER FOR DEVICE 4
699 002106 000000 TXBF5: .WORD 0 ;TRANSMITTER BUFFER FOR DEVICE 5
700 002110 000000 TXBF6: .WORD 0 ;TRANSMITTER BUFFER FOR DEVICE 6
701 002112 000000 TXBF7: .WORD 0 ;TRANSMITTER BUFFER FOR DEVICE 7
702
703 .MACRO $RB,$Q
704 RXBF$Q: .WORD 0 ;RECEIVER BUFFER FOR DEVICE #*$Q
705 .ENDM
706
707 002114 000000 RXBF0: .WORD 0 ;RECEIVER BUFFER FOR DEVICE #0
708 002116 000000 RXBF1: .WORD 0 ;RECEIVER BUFFER FOR DEVICE #1
709 002120 000000 RXBF2: .WORD 0 ;RECEIVER BUFFER FOR DEVICE #2
710 002122 000000 RXBF3: .WORD 0 ;RECEIVER BUFFER FOR DEVICE #3
711 002124 000000 RXBF4: .WORD 0 ;RECEIVER BUFFER FOR DEVICE #4
712 002126 000000 RXBF5: .WORD 0 ;RECEIVER BUFFER FOR DEVICE #5
713 002130 000000 RXBF6: .WORD 0 ;RECEIVER BUFFER FOR DEVICE #6
714 002132 000000 RXBF7: .WORD 0 ;RECEIVER BUFFER FOR DEVICE #7
715
716 .MACRO $BC,$Q
717 BCNT$Q: .WORD 0 ;RECEIVER BYTE COUNT POINTER FOR DEVICE *$Q
718 .ENDM
719
720 002134 000000 BCNT0: .WORD 0 ;RECEIVER BYTE COUNT POINTER FOR DEVICE 0
721 002136 000000 BCNT1: .WORD 0 ;RECEIVER BYTE COUNT POINTER FOR DEVICE 1
722 002140 000000 BCNT2: .WORD 0 ;RECEIVER BYTE COUNT POINTER FOR DEVICE 2
723 002142 000000 BCNT3: .WORD 0 ;RECEIVER BYTE COUNT POINTER FOR DEVICE 3
724 002144 000000 BCNT4: .WORD 0 ;RECEIVER BYTE COUNT POINTER FOR DEVICE 4
725 002146 000000 BCNT5: .WORD 0 ;RECEIVER BYTE COUNT POINTER FOR DEVICE 5
726 002150 000000 BCNT6: .WORD 0 ;RECEIVER BYTE COUNT POINTER FOR DEVICE 6
727 002152 000000 BCNT7: .WORD 0 ;RECEIVER BYTE COUNT POINTER FOR DEVICE 7
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730
731 ;ASCII MESSAGES
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```
732 002154 042045 050125 030461 MHUNG: .ASCIZ "&DUP11 MODULE IS HUNG - SEE LISTING!"
733 002222 .EVEN
000001 .END
```





DPBB DEC/X11 SYSTEM EXERCISER MODULE MACV11 30A(1052) 12-OCT-78 16:30 PAGE 24  
XDPBB0.P11 12-OCT-78 11:53 CROSS REFERENCE TABLE -- USER SYMBOLS

SEQ 0022

XDPBB0, XDPBB0/SOL /CRF:SYM=DDXCOM, XDPBB0  
RUN-TIME: 2 2 4 SECONDS  
RUN-TIME RATIO: 12/5=2.5  
CORE USED: 7K (13 PAGES)