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IDENTIFICATION

PRODUCT CODE: AC-E947B-MC  
PRODUCT NAME: CXDVABO DV11 MODULE  
PRODUCT DATE: SEPTEMBER 1978  
MAINTAINER: DEC/X11 SUPPORT GROUP

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1. ABSTRACT

DVA IS AN IOMOD THAT EXERCISES UP TO AND INCLUDING FOUR (CONSECUTIVELY ADDRESSED) DV11 SYNCRONOUS INTERFACES. IT USES MAINTAINCE MODE TO XMIT AND RECEIVE A SET DATA PATTERN RESEMBLING A BINARY COUNT FROM 00-77. THE RECEIVER AND TRANSMITTER ISR ARE ALWAYS AT PRIORITY FIVE. (BR1,BR2) DATA CHECKING IS PERFORMED AT LEVEL 0 AND DONE OUTSIDE THE ISRS.

2. REQUIREMENTS

HARDWARE: AT LEAST 1 DV11-AA AND ONE DV11-BA

STORAGE:: DVA REQUIRES:

1. DECIMAL WORDS: 3939
2. OCTAL WORDS: 07543
3. OCTAL BYTES: 17306

3. PASS DEFINITION

ONE PASS OF THE DVA MODULE CONSISTS OF HANDLING 100(8) CHARS TIMES THE NUMBER OF SELECTED LINES TIMES THE NUMBER OF SELECTED DEVICES TIMES 600(8).

4. EXECUTION TIME

THIS VARIES DUE TO THE CONFIGURATION BUT SHOULD ALWAYS BE UNDER 01 MIN.

5. CONFIGURATION PARAMETERS.

DEFAULT PARAMETERS:

ADDR: 175000 VECTOR: 310, BR1: 5, BR2: 5, DVID1: 1, SR1:0 FIRST EIGHT LINES FOR EACH DV11.

SYNC "A" SET TO 226

USER \*MUST\* SET SOFTWARE "SYNC" TO "377"

FOR TEST OF AN ASYNC LINE CARD. SEE SECTION 8.

USER MAY ALTER PARAMETERS. NO MORE THAN 4 DV11'S MAY BE RUN.

6. DEVICE OPTION SETUP

1. PARITY OFF
2. AT LEAST 6 BITS PER CHAR
3. SYNC "A" OVER(GREATER THAN) 20(8)
4. FULL DUPLEX

DEFAULT SHIP OF 8 BITS, PARITY OFF, FULL DUPLEX, ETC OK

## TRANSMITTER PATTERN

## RECEIVER PATTERN

## **DEFINITIONS:**

\*\*\* PAD(JUNK) CHAR XMITTED TO STAGGER INTERRUPTS. (NOT RECEIVED).  
 "S" SYNC CHAR XMITTED TO GET RECEIVER ACTIVE. (NOT RECEIVED).  
 "D" TX: DATA TRANSMITTED FROM MEMORY. RX: DATA RECEIVED AND STORED IN MEMORY.  
 "#" DLE CHAR IDLED FROM DV11 TRANSMITTER AND STORED INTO MEMORY BY RX.  
 "\\" INDICATES BUS INTERRUPT.  
 "B" BCC CHAR (RECEIVED BUT NOT STORED).  
 "E" ETX (END OF TEXT) LAST CHAR RECEIVER EXPECTS (NOT STORED).  
 " " SPACES USED FOR CLARITY. NO FUNCTION

7. MODULE OPERATION

1. LOAD SOFTWARE POINTERS IN LNKTABLE.
2. LOAD VECTORS AND PRIORITIES IN TABLE
3. DETERMINE WHICH SET OF LINES TO TEST  
(PING. PONG =0 LINES 00-07; PING. PONG <>0 LINES 08-15)
4. CLEAR ALL SECONDARY REGISTERS AND MASTER CLEAR DEVICE.
5. LOAD ALL SECONDARY REGISTERS FOR SELECTED LINES
6. UPDATE LINE COUNTER IF NOT DONE GOTO 5.
7. LOAD INDIVIDUAL SECONDARY REGISTERS THAT VARY FROM  
LINE TO LINE OR DEVICE TO DEVICE.
8. ENABLE SELECTED DEVICES.
9. SCAN FOR ALL LINES OF ALL DEVICES TO FINISH
10. IF NOT DONE GOTO 9.  
IF HUNG REPORT SO AND DROP MODULE.
11. CHECK DATA FOR ALL LINES SELECTED FOR ALL DEVICES SELECTED.
12. SWITCH TO NEXT GROUP OF LINES (COM PING.PONG )
13. DECREMENT ITERATION COUNT
14. IF NOT =0 GOTO 1
15. SIGNAL ENDPAS.
16. TXISR: TRANSMITTER INTERRUPT SERVICE ROUTINE.
17. GET INTERRUPTING DVSCR.
18. WAS DVSCR 15=1? (IF NOT; REPORT ERROR)
19. READ DVNSR
20. CHECK FOR EITHER PRI BC OR ALT BC =0
21. RELOAD BA AND BC.
- RTI
- RXISR: RECEIVER INTERRUPT SERVICE ROUTINE.
23. GET INTERRUPTING DVSCR
24. WAS DVSCR07=1? (IF NOT; REPORT ERROR)
25. ARE ANY ERROR CODES SET?
26. IF NO ERROR CODES; WAS THIS SPECIAL CHAR 77?
27. IF YES (CHAR =77) THEN SHUT TX OFF AND RESYNC RX. (SIGNAL END OF LINE ACTIVITY)
28. IF ERROR CODE PRESENT THEN WAS IT BCC CODE?
29. IF NO THEN REPORT ERROR. IF YES WAS BCC =0? (IF NOT; REPORT ERROR)
30. RTI

8. OPERATOR OPTIONS

LOCATIONS IN DVA FROM 164(8) TO 232(8) ARE BASICALLY SELF EXPLANATORY AND MAY ALTERED VIA THE MOD CMD TO ALTER TO THE SPECIFIC DV11 CONFIGURATION. NOTE: IF YOU HAVE MORE THAN 8 LINES INSTALLED; IT IS YOUR RESPONSIBILITY TO ALTER THE "LINES(1,2,3,4) LOCATIONS TO HAVE THESE LINES TESTED.

ASYNCRONOUS LINE CARD TESTING!! FOR TESTING OF ASYNC LINE CARDS; BOTH SYNC CHARS FOR THAT LINE CARD \*MUST\* BE SET TO "377" (WORD=177777) IN THIS PROGRAM. LOOK AT LOCATIONS 164(8) THRU 232(8) FOR CORRECT LOCATION OF SYNC CHARS FOR LINE CARD.

9. NON-STANDARD PRINTOUTS

IF THE MODULE "HANGS" IN WHICH NOT ALL LINES FOR EACH DV11 SELECTED FAIL TO FINISH; A "HUNG" MESSAGE IS PRINTED OUT. TO FIND OUT WHAT LINE(S) ON WHICH DEVICE(S) HAVE FAILED; COMPARE THE LINES SELECTED BETWEEN 164(8)-222(8) TO THE IMAGE BEING CREATED AT THE "LNKTAB" ARGUMENT FOLLOWING THE DVSCR IMAGE OF THE TRANSMITTER "JSR" ROUTINE.

THAT IS TO SAY:

LINES1(164) SHOULD EQUAL {COMPARE TO} LOC: XXX1 016742  
LINES2(176) SHOULD EQUAL {COMPARE TO} LOC: XXX2 016762  
LINES3(210) SHOULD EQUAL {COMPARE TO} LOC: XXX3 017002  
LINES4(222) SHOULD EQUAL {COMPARE TO} LOC: XXX4 017022

FOR A COMPLETE RUN OF DVA. IF ANY OF THE FOLLOWING LOCATIONS FAIL TO COMPARE A "HUNG" MESSAGE WILL BE PRINTED AND THE MODULE WILL BE DROPPED.

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217 000000* IOMOD <DVAB> 175000,310,5,5,0,600,74
218 000000* MODULE 140000,DVAB,175000,310,5,5,0,600,74
219 , TITLE DVAB DEC/X11 SYSTEM EXERCISER MODULE
220 , DDXCOM VERSION 6 23-MAY-78
221 , LIST BIN
222 ****
223 000000* BEGIN:
224 000005* 053104 041101 040
225 , ASCII /DVAB / ;MODULE NAME
226 , USED TO KEEP TRACK OF WBUFF USAGE
227 , ADDR: 175000+0 ;1ST DEVICE ADDR.
228 , VECTOR: 310+0 ;1ST DEVICE VECTOR.
229 , BR1: .BYTE PRTV5+0 ;1ST BR LEVEL.
230 , BR2: .BYTE PRTV5+0 ;2ND BR LEVEL.
231 , DIVID1: 0 ;DECODE REGISTER 1.
232 , SR1: OPEN ;SWITCH REGISTER 1
233 , SR2: OPEN ;SWITCH REGISTER 3
234 , SR3: OPEN ;SWITCH REGISTER 3
235 , SR4: OPEN ;SWITCH REGISTER 4
236 000026* 140000 STAT: 140000 ;STATUS WORD
237 000030* 000314* INIT: START ;MODULE START ADDR.
238 , MODSP: ;MODULE WORK POINTER.
239 , 000034* 000000 PASCNT: 0 ;PASS COUNTER.
240 , 000036* 000600 ICOUNT: 600 ;# OF ITERATIONS PER PASS=600
241 , 000040* 000000 ICOUNT: 0 ;LOC TO COUNT ITERATIONS
242 , 000042* 000000 SOFCNT: 0 ;LOC TO SAVE TOTAL SOFT ERRORS
243 , 000044* 000000 HRDCNT: 0 ;LOC TO SAVE TOTAL HARD ERRORS
244 , 000046* 000000 SOFPAS: 0 ;LOC TO SAVE SOFT ERRORS PER PASS
245 , 000050* 000000 HRPAS: 0 ;LOC TO SAVE HARD ERRORS PER PASS
246 , 000052* 000000 SVSCNT: 0 ;# OF SYS ERRORS ACCUMULATED
247 , 000054* 000000 RANNUM: 0 ;HOLDS RANDOM # WHEN RAND MACRO IS CALLED
248 , 000056* 000000 CONFIG: ;RESERVED FOR MONITOR USE
249 , 000058* 000000 RES1: 0 ;RESERVED FOR MONITOR USE
250 , 000060* 000000 RES2: 0 ;RESERVED FOR MONITOR USE
251 , 000062* 000000 SVR1: OPEN ;LOC TO SAVE R0
252 , 000064* 000000 SVR2: OPEN ;LOC TO SAVE R1
253 , 000066* 000000 SVR3: OPEN ;LOC TO SAVE R2
254 , 000070* 000000 SVR4: OPEN ;LOC TO SAVE R3
255 , 000072* 000000 SVRS: OPEN ;LOC TO SAVE R5
256 , 000074* 000000 SVRO: OPEN ;LOC TO SAVE R6
257 , 000076* 000000 SVRA: OPEN ;ADDR OF CURRENT CSR.
258 , 000078* 000000 CSRD: ;ADDR OF GOOD DATA, OR
259 , 000102* 000000 ACSR: OPEN ;CONTENTS OF CSR.
260 , 000104* 000000 WASADR: OPEN ;ADDR OF BAD DATA, OR
261 , 000104* 000000 ASTAT: OPEN ;STATUS REG CONTENTS.
262 , 000106* 000000 ERRTYP: ;TYPE OF ERROR
263 , 000106* 000000 ASBT: OPEN ;EXPECTED DATA
264 , 000108* 000000 ASAD: OPEN ;ACTUAL DATA
265 , 000112* 000347* RSTT: RESTART ;RESTART ADDRESS AFTER END OF PASS
266 , 000114* 000000 WDTO: OPEN ;WORDS TO MEMORY PER ITERATION
267 , 000116* 000000 WDFR: OPEN ;WORDS FROM MEMORY PER ITERATION
268 , 000120* 000000 INTR: OPEN ;# OF INTERRUPTS PER ITERATION
269 , 000122* 000074 IDNUM: 74 ;MODULE IDENTIFICATION NUMBER=74
270 , 000224* MODSP: ;MODULE IDENTIFICATION NUMBER=74
271 ****
272

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273 ,*SETUP VARIABLES NEEDED FOR DV11 CONFIGURATION
274
275 000224* 000377 LINES1: ^B<0000000011111111> ;DEFAULT ALL 8 LINES
276 000236* 226 SYNC11: .BYTE 226,226 ;DEFAULT SYNC A {1}
277 000230* 226 SYNC12: .BYTE 226,226 ;DEFAULT SYNC A {2}
278 000232* 226 SYNC13: .BYTE 226,226 ;DEFAULT SYNC A {3}
279 000234* 226 SYNC14: .BYTE 226,226 ;DEFAULT SYNC A {4}
280
281 000236* 000377 LINES2: ^B<0000000011111111> ;DEFAULT ALL 8 LINES
282 000240* 226 SYNC21: .BYTE 226,226 ;DEFAULT SYNC A {1}
283 000242* 226 SYNC22: .BYTE 226,226 ;DEFAULT SYNC A {2}
284 000244* 226 SYNC23: .BYTE 226,226 ;DEFAULT SYNC A {3}
285 000246* 226 SYNC24: .BYTE 226,226 ;DEFAULT SYNC A {4}
286
287 000250* 000377 LINES3: ^B<0000000011111111> ;DEFAULT ALL 8 LINES
288 000252* 226 SYNC31: .BYTE 226,226 ;DEFAULT SYNC A {1}
289 000254* 226 SYNC32: .BYTE 226,226 ;DEFAULT SYNC A {2}
290 000256* 226 SYNC33: .BYTE 226,226 ;DEFAULT SYNC A {3}
291 000260* 226 SYNC34: .BYTE 226,226 ;DEFAULT SYNC A {4}
292
293 000262* 000377 LINES4: ^B<0000000011111111> ;DEFAULT ALL 8 LINES
294 000264* 226 SYNC41: .BYTE 226,226 ;DEFAULT SYNC A {1}
295 000266* 226 SYNC42: .BYTE 226,226 ;DEFAULT SYNC A {2}
296 000268* 226 SYNC43: .BYTE 226,226 ;DEFAULT SYNC A {3}
297 000272* 226 SYNC44: .BYTE 226,226 ;DEFAULT SYNC A {4}
298
299 000000 DVSCR=R0 ;SYSTEM CONTROL REGISTERS
300 , 000002 DWRIC=2 ;RECEIVER INTERRUPT CHAR REGISTER
301 , 000004 DWRIC=4 ;TRANSMITTER INTERRUPT REGISTER
302 , 000006 DSSR=6 ;SECONDARY REGISTER SELECT REGISTER
303 , 000007 DSSRSH=7 ;SECONDARY REGISTER SELECT REGISTER (HIGH BYTE)
304 , 000010 DVSRSA=10 ;SECONDARY REGISTER ACCESS REGISTER
305 , 000012 DVSRF=12 ;SPECIAL FUNCTIONS REGISTER
306 , 000014 DVNSR=14 ;INPR STATUS REGISTER
307 , 000016 DVRES=16 ;RESERVED REGISTER
308
309 , 000000 MODE0=0*40
310 , 000040 MODE1=1*40
311 , 000100 MODE2=2*40
312 , 000140 MODE3=3*40
313 , 000200 MODE4=4*40
314 , 000240 MODE5=5*40
315 , 000300 MODE6=6*40
316 , 000340 MODE7=7*40
317
318 000274* 000001 PING-PONG: .BLKW 1
319 000276* 000001 VA: .BLKW 1
320 000300* 000001 PA: .BLKW 1
321 000302* 000001 EA: .BLKW 1
322 , 000306* 000001 SELECT: .BLKW 1
323 , 000310* 000001 XGND: .BLKW 1
324 , 000312* 000001 HOLD: .BLKW 1
325 , STORE: .BLKW 1

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326
327
328
329 000314 012767 002000 177572 ; BEGIN THE TESTS FOR THE DV11
330 000322 012767 002000 177566 START: MOV #2000,WDTO ; 2000 WORDS TO MEM/ITERATION
331 000330 012767 000100 177562 MOV #2000,WDR ; 2000 WORDS FROM MEM/ITERATION
332 000345 012767 177322 CLR PONG ; PONG
333 000346 012767 177442 RESTRT: CLR ABORT ; CLEAR ABORT LINE DO
334 000346 001002 MOV DVID1,SELECT ; SET ACTIVE SERVICE FLAG
335 000354 001002 BNE CONT ; SET ACTIVE DEVICES
336 000356 104410 000000* BNE,BEGIN ; JBR IF ANY *ARE* SELECTED
337
338
339 000362 032767 177760 177714 ENDS,BEGIN ; NO DV11'S SELECTED!
340 000370 001372 177706 CONT: BIT #CC17,SELECT ; MAKE SURE NO MORE THAN 4 DEVICES SELECTED
341 000372 001372 177706 BNE DROP ; ABSOLUTELY INVALID, NO MORE THAN 4 DEVICES!!
342 000376 012767 SETUP1: MOV SELECT,R1 ; SET IMAGE OF RUNNING DEVICES
343 000400 012702 BEQ DROP ; BR IF ALL DV11'S DROPPED
344 000404 012767 177404 MOV VECTOR,R2 ; SET INITIAL VECTOR
345 000412 012767 177405 MOV ADDR,RO ; SET INITIAL CSW
346 000412 012767 177405 MOV PINGTAB,R3 ; SET BUFFER FOR ISR
347 000422 012767 177510 000016 MOV LINES5/30,(R3) ; LOAD DEFAULT ACTIVE LINES
348 000430 012763 177514 000056 MOV LINES5/46,(R3) ; LOAD DEFAULT ACTIVE LINES
349 000436 012763 177520 000076 MOV LINES4/62,(R3) ; LOAD DEFAULT ACTIVE LINES
350
351 000444 002011 1S: ASR R1 ; ACTIVE?
352 000446 103410 BCS 3S ; BR IF YES
353 000449 022470 000020 2S: ADD R0,R2 ; POP VECTOR POINTER (20)
354 000456 062703 000020 ADD R0,R3 ; POP SOFTWARE ISR POINTER (20)
355 000462 062700 000040 ADD R0,R0 ; POP CSR POINTER (40)
356
357 000470 010312 3S: MOV R3,(R2) ; LOAD ISR POINTER (RECV)
358 000472 012762 177314 000002 MOV R1,(R2) ; LOAD PRIORITY
359 000472 012762 177314 000002 MOV R0,(R3) ; LOAD DVSCR POINTER
360 000514 012762 000004 ADD R0,(R3) ; LOAD ISR POINTER (TRAN)
361 000516 116762 177371 000006 MOV R2,(R2) ; LOAD PRIORITY
362 000524 010663 000014 MOV R0,14(R3) ; LOAD DVSCR POINTER
363 000530 005767 177540 TST PING.PONG ; WHICH SET OF LINES??
364 000534 005767 BNE 4S ; BR IF SECOND SET OF LINES
365 000534 005767 000016 CLRB 4S ; ZERO LOW LINES SELECTED
366 000534 005767 000016 BR 4S ; CONTINUE
367 000544 105063 000017 4S: CLRB 5S ; ZERO HIGH LINES SELECTED
368 000550 000740 000017 BR 5S ; CONTINUE
369 000552 012700 177230 SETUP2: MOV ADDR,RO ; SET INITIAL DVSCR
370 000552 012701 000224 MOV LINES1,R1 ; SET LINES POINTER
371 000556 012701 000224 TST PING.PONG ; WHICH SET OF LINES??
372 000562 005767 177506 BNE 5S ; BR IF FIRST SET OF LINES SELECTED
373 000562 005767 INC R1 ; MAKE EQUAL TO HIGH BYTE
374 000572 012767 017106* 177510 1S: MOV #BUFFER_TABLE,HOLD ; BUFFER TABLE HOLD
375 000600 010105 000001 MOV R1,R5 ; PREPARE TO GET SYNC CHAR PONTER
376 000602 042705 000001 BIC #1TO,R5 ; INSURE EVEN ADDRESS
377 000606 005725 TST (R5)+ ; POP TO SYNC CHARS
378 000610 005767 177460 TST PING.PONG ; WHAT SET OF LINES??
379 000614 005767 BNE 7S ; BR IF LOW SET*
380 000616 022525 CMP (R5)+(R5)+ ; SET OFFSET TO LAST TWO SYNC LINES.
381

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382 000620 006267 177460 2S: ASR SELECT ; ACTIVE?
383 000624 102411 BCS 3S ; YES
384 000626 001002 BNE 3S ; BR IF NOT DONE
385 000630 000167 000556 JMP 3S ; JUMP IT DONE
386 000634 052700 000040 3S: ADD #40,R0 ; POP DVSCR POINTER
387 000640 052701 000012 ADD #12,R1 ; POP LINES POINTER
388 000642 052702 000001 ADD #4,R5 ; POP LINE NO. GET SYNC CHAR POINTER
389 000652 005767 000001 TST (R5); INSURE EVEN ADDRESS
390 000654 052767 000040 177426 ADD #32+,HOLD ; POP BUFFER POINTER
391 000662 000756 BR 5S ; CONTINUE
392
393 ; ROUTINE USED TO CLEAR ALL DV11
394 ; SECONDARY REGISTERS FOR ALL LINES
395 ; THIS MAKES SURE NO JUNK IS LEFT FOR UCPU TO FIND.
396 000664 012710 004000 4S: MOV #0,DVSCR ; INITALIZE DV11
397 000670 005060 000010 CLR DVSRSH(R0) ; ZERO RAMS
398 000674 052760 170361 000006 ADD #CC<BIT11+BIT10+BIT9+BIT8+BIT3+BIT2+BIT1+BIT0>+BIT0,DVSRSH(R0)
399 000702 001372 BNE 5S ; UPDATE TO NEXT LINE AND SEC RFG BR IF NOT DONE
400 000704 111103 MOVB (R1),R3 ; GET ACTIVE LINES
401 000706 005767 CLR R2 ; ZERO LINE NO. IMAGE
402 000714 005767 177360 TST PING.PONG ; SET GROUP OR SECOND?
403 000716 052702 000010 BIS #BIT3,R2 ; ADJUST LINES
404 000722 012767 000004 177356 6S: MOV #4,XONT ; SET TO CHANGE SYNC CHARS EVERY 4 LINES
405 000730 012767 000001 000544 6S: MOV #1,17$ ; SET FOR PAD(JUNK) CHAR COUNT.
406 000736 000241 CLC R3 ; CLEAR CARRY
407 000740 106003 RDRB R3 ; LINE ACTIVE?
408 000744 052760 INC R2 ; UPDATE R2
409 000744 052760 8S: DEC XCNT ; 4 LINES DONE?
410 000746 005367 177334 BNE 9S ; BR IF NO
411 000746 005367 TST (R5)+ ; POP POINTER TO NEXT SYNCs
412 000752 010104 11S: MOV #4,XCNT ; RESET COUNTER
413 000754 005725 11S: TST R3 ; ALL LINES DONE?
414 000756 012767 000004 177322 MOVB (R1),R3 ; ALL DONE?
415 000762 105703 TSTB R3 ; BR IF NO
416 000762 005367 11S: INC R2 ; CONT
417 000770 012704 012752* 10S: MOV #FUNC,X,R4 ; GET SECONDARY REGISTER DATA
418 000776 110260 000006 MOV R2,DVSRSH(R0) ; LOAD LINE NUMBER
419 000102 105060 000007 CLR BIS,DVSRSH(R0) ; CLEAR DVSRSH IMAGE
420 000106 052760 134000 000004 11S: MODE,DVLCR(R0) ; MODE DVLCR(R0)
421 000106 052760 134000 TST DVLCR(R0) ; WAIT FOR STROBE TO FINISH
422 000104 052760 000004 12S: BIS #BIT15+RENABL+M,DVSRSH(R0) ; WAIT FOR STROBE TO FINISH
423 000104 052760 000004 12S: NOV (R5)+(R5)+,DVSRSH(R0) ; LOAD SECONDARY REGISTERS
424 000104 052760 000010 INC BIS,DVSRSH(R0) ; UPDATE SECONDARY REG POINTER
425 000106 105760 000007 TSTB DVSRSH(R0) ; ALL DONE?
426 000106 105760 000007 BNE 12S ; BR IF NO
427 001036 001371 MOV R2,DVSRSH(R0) ; LOAD LINE
428 001040 010260 000006 MOVB R2,DVSRSH(R0) ; SELECT LTERMATE BA FOR EA BITS.
429 001044 012760 000006 000007 MOVB PC,SET.EA ; GO GET EA BITS.
430 001056 004267 000452 JSK (R5),STORE ; PREPARE TO
431 001056 004267 000452 CLR (R5),STORE ; CLEAR TABLE+SYNC+MODE
432 001062 005061 177254 MOVB (R5),STORE ; STORE
433 001066 111561 177250 ADD TX CONTROL TABLE,STORE ; SO THAT CNTRL BYE IS =0
434 001072 062767 006156* 177212 CLR BIS,DVSRSH(R0) ; IS THIS AN ASYNC LINE CARD?
435 001100 105071 177206 CMPB #377,(R5) ; IS THIS AN ASYNC LINE CARD?
436 001100 022525 000377 BNE 18S ; BR IF NOT ASYNC
437 001110 031042

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438 001112* 005067 177174      CLR   STORE    ;ZERO
439 001115* 111567 177170      MOVB  (R5),STORE ;#GET "SYNC" CHAR (S/B 377 FOR ASYNC)
440 001122* 002767 002156* 177162      ADD   @RX CONTROL TABLE,STORE
441 001135* 013760 000000* 177134      MOVB  #BB174,STORE ;#SET DSCRD AND MODE ZERO.
442 001144* 120000 000004      MOVB  #BIT14+BIT13,DVLCR(RO) ;#SET RD ENABLE.
443 001150* 100775 000004      TST   DVLCR(RO) ;#WAIT FOR STROBE TO FINISH
444 001152* 012760 115000 000004      BMI   -4          ;#WAIT FOR STROBE DONE.
445 001160* 005760 000004      MOV   #BIT15+BIT12+BIT11+BIT9,DVLCR(RO) ;#STROBE IN 8 BITS/PER/CHAR
446 001162* 100775 000004      TST   DVLCR(RO) ;#STROBE IN 8 BITS/PER/CHAR
447 001162* 100775 000004      BMI   -4          ;#WAIT FOR STROBE TO FINISH
448 001162* 005760 177200 000004      MOV   #BIT15+BIT14+BIT13+BIT12+BIT11,DVLCR(RO) ;#STROBE IN 800 BAUD RATE.
449 001200* 100775 000004      BMI   -4          ;#WAIT FOR STROBE DONE.
450 001202* 012760 107000 000004      MOV   #BIT15+BIT11+BIT10+BIT9,DVLCR(RO) ;#STROBE IN MAIN INTERNAL MODE
451 001210* 005760 000004      TST   DVLCR(RO) ;#STROBE IN MAIN INTERNAL MODE
452 001214* 100775 000004      BMI   -4          ;#WAIT FOR STROBE DONE
453 001214* 100775 000004      INCB  DVSRSH(R0) ;SELECT BYTE CNT (ALTERNATE)
454 001215* 105260 000007      MOV   #2,6,DVSRRA(RO) ;READ FOR 2 SYNC CHARS
455 001220* 010289 177076 000010      MOVB  #2,7,DVSRRA(RO) ;GET LINE 2 SYNC
456 001220* 010289 177076 000010      MOVB  #2,8,STORE ;GET LINE 2 (MAKE EVEN)
457 001234* 006357 177052      ASL   STORE    ;MULT BY 2 (MAKE EVEN)
458 001240* 066767 177044 177044      ADD   HOLD,STORE ;GET POINTER FOR RX BUFFER
459 001245* 112760 000004      MOVB  #4,DVSRSH(R0) ;SEL RX BA
460 001254* 011767 177032 177014      MOV   #STORE,VA ;PREPARE TO GET EA BITS FOR RX BA.
461 001262* 004767 000216      JSR   PC,SET,EA ;GO GET THEM
462 001292* 004767 000010 176774      MOVB  #4,6,DVSRH(R0) ;READ TX TABLE BASE ADD
463 001302* 016067 000010 176774      MOVB  #4,7,DVSRH(R0),VA ;READ IT
464 001302* 004767 000176      JSR   PC,SET,EA ;SET EA BITS
465 001306* 105260 000007      INCB  DVSRSH(R0) ;SEL RX TABLE BASE ADDR
466 001312* 016067 000010 176756      MOV   DVSRRA(RO),VA ;READ IT
467 001320* 004767 000160      JSR   PC,SET,EA ;SET EA BITS
468 001324* 105060 000007      CLR   DVSRSH(R0) ;SEL TX BA PRI.
469 001325* 004767 000142      MOVB  #4,8,DVSRH(R0),VA ;READ EA BITS
470 001342* 016767 116734 011334      JSR   PC,SET,EA ;SAVE PRINCIPLE EA BITS FOR ISR
471 001350* 016767 116724 011330      MOVB  EA,PRI,PA ;SAVE PRINCIPLE PA FOR ISR
472 001356* 062760 000020 000010      MOV   PA,PRI,PA
473 001364* 105260 000007      ADD   #20,DVSRH(R0) ;POINT TXBA TO PAD(JUNK) CHARS.
474 001364* 016760 000006 000010      INCB  DVSRSH(R0) ;SET TX BA BYTE CNT.
475 001370* 016760 000006 000010      MOV   #178,DVSRH(R0) ;SET NUMBER OF PAD(JUNK) CHARS TO BE SENT.
476 001402* 0082469 000004      NEG   DVSRRA(RO) ;TAKE COUNT 2'S COMP.
477 001406* 0082469 000004      INC   ICS          ;TAKE COUNT 2'S COMP.
478 001406* 000167 177332      JMP   BS          ;CONT
479 001412* 016700 176370      MOV   ADDR,RO ;GET DVSCR POINTER
480 001416* 016767 176372 176660      MOVB  DVID1,SELECT ;GET ACTIVE DEVICES
481 001424* 016701 176654      MOV   SELECT4,R1 ;GET ACTIVE DV11'S
482 001430* 016701 176654      ASR   R1          ;ACTIVE?
483 001432* 1007404      BSR   16S          ;BR IF YES
484 001434* 001436 0082700 000040      BSR   SCANNER ;BR IF DONE
485 001442* 007772      ADD   #40,RO ;POP DVSCR POINTER
486 001442* 007772      BR   145          ;CONT
487 001444* 012767 013146* 176624* 165:  MOV   #TXBAS,VA ;GET EA BITS FOR ALTERNATE BA
488 001454* 104415 000000* 000276*  GETPAS,BEGIN,VA ;GET PHYSICAL ADDRESS FROM 16-BIT VA
489 001460* 116767 176616 011217      MOVB  EA,ALT,EA ;SAVE ALTERNATE EA BITS
490 001465* 116767 176606 011214      MOV   PA,ALT,PA ;SAVE ALTERNATE PA FOR ISR
491 001474* 052710 030101      BIS   #NFR,IE+STORE,IR ;TAKE IR TO DVSCR
492 001474* 052710 030101      RTS   PC          ;SET MICRO-PROCESSOR GO!!
493 001500* 000756      BR   15$          ;CONT

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494 001502* 000000      17$: 0
495 001504*           SET.EA:
496 001504*           SET.EA: GETPAS,BEGIN,VA ;GET PHYSICAL ADDRESS FROM 16-BIT VA
497 001504* 104415 000000* 000276* BIC   #BIT5+BIT4,@DVSCR
498 001512* 042710 000060      ;CLEAR EA BITS
499 001516* 056710 176560      BIS   EA,DVSCR ;SET EA BITS
500 001522* 016760 176552 000010      MOV   PA,DVSRRA(RO) ;LOAD PA
501 001530* 000207      RTS   PC          ;LOAD PA
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DVBAB DEC/X11 SYSTEM EXERCISER MODULE      MACY11 30A(1052) 12-OCT-78 16:34 PAGE 15
XDVBA0..P11 12-OCT-78 11:57

607 002070 104403 000000 002106 MSGNS,BEGIN,HUNG ;ASCII MESSAGE CALL WITH COMMON HEADER
608 002076 104410 000000 ENDS,BEGIN
609 002102 000000 ABORT: WORD 0 ;ABORT FLAG
610 002104 000000 BUFPNT: 0
611 002106 000000 HUNG: XHUNG :POINTER TO ASCIZ MSG
612 002110 177777 -1 :TERMINATOR
613 002112 042045 030526 020061 XHUNG: .ASCIZ "%DV11 MODULE IS HUNG. SEE LISTING%"*
614 002120 047515 052504 042514
615 002126 044440 020123 042510
616 002134 048508 020056 042523
617 002142 042008 040454 052133
618 002150 047111 022807 000
619           002155 -EVEN

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SEQ 0014

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DVB DEC/X11 SYSTEM EXERCISER MODULE      MACY11 30A(1052) 12-OCT-78 16:34 PAGE 16
XDVABO-P11   12-OCT-78 11:57

620 002156*          RX_CONTROL_TABLE:
621 ;UNUSED AREA OF CONTROL TABLE IS FILLED WITH "GEN INT"(MODE0)
622 ;TO CATCH RECEIVER GOING TO WRONG CNTRL BYTE.
623
624
625 002156*          .=RX_CONTROL_TABLE+0           ;MODE 0
626 002156*          .=-+0
627
628 002156*          .BYTE  BIT3+MODE0          ;00 INC/BCC+MODE0
629 002156*          .BYTE  BIT3+MODE0          ;01 INC/BC+MODE0
630 002156*          .BYTE  BIT3+MODE0          ;02 INC/BCC+MODE0
631 002156*          .BYTE  BIT3+MODE0          ;03 INC/BC+MODE0
632 002156*          .BYTE  BIT3+MODE0          ;04 INC/BCC+MODE0
633 002156*          .BYTE  BIT3+MODE0          ;05 INC/BC+MODE0
634 002156*          .BYTE  BIT3+MODE0          ;06 INC/BCC+MODE0
635 002156*          .BYTE  BIT7+BIT3+MODE1    ;07 DSCARD+INC/BCC+MODE1
636 002156*          .BYTE  BIT7+MODE0          ;10 DSCARD+MODE0
637 002156*          .BYTE  BIT7+MODE0          ;11 DSCARD+MODE0
638 002156*          .BYTE  BIT7+MODE0          ;12 DSCARD+MODE0
639 002156*          .BYTE  BIT7+MODE0          ;13 DSCARD+MODE0
640 002156*          .BYTE  BIT7+MODE0          ;14 DSCARD+MODE0
641 002156*          .BYTE  BIT7+MODE0          ;15 DSCARD+MODE0
642 002156*          .BYTE  BIT7+MODE0          ;16 DSCARD+MODE0
643 002156*          .BYTE  BIT7+MODE0          ;17 DSCARD+MODE0
644 002156*          .BYTE  BIT7+MODE0          ;18 DSCARD+MODE0
645 002156*          .BYTE  BIT7+MODE0          ;19 DSCARD+MODE0
646 002156*          .BYTE  BIT7+MODE0          ;20 DSCARD+MODE0
647 002156*          .BYTE  BIT7+MODE0          ;21 DSCARD+MODE0
648 002156*          .BYTE  BIT7+MODE0          ;22 DSCARD+MODE0
649 002156*          .BYTE  BIT7+MODE0          ;23 DSCARD+MODE0
650 002156*          .BYTE  BIT7+MODE0          ;24 DSCARD+MODE0
651

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SEQ 0015

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652      002556*          :=RX_CONTROL.TABLE+400      ;MODE1
653      002566*          :=.+10
654      002567*          .BYTE BIT3+MODE1      ;10 INC/BCC+MODE1
655      002570*          .BYTE BIT3+MODE1      ;11 INC/BCC+MODE1
656      002571*          .BYTE BIT3+MODE1      ;12 INC/BCC+MODE1
657      002572*          .BYTE BIT3+MODE1      ;13 INC/BCC+MODE1
658      002573*          .BYTE BIT3+MODE1      ;14 INC/BCC+MODE1
659      002574*          .BYTE BIT3+MODE1      ;15 INC/BCC+MODE1
660      002575*          .BYTE BIT3+MODE1      ;16 INC/BCC+MODE1
661      002577*          .BYTE BIT3+MODE1      ;17 DSCARD+INC/BCC+MODE2
662      002575*          .BYTE BIT3+BIT3+MODE2
663
664      003156*          :=RX_CONTROL.TABLE+1000     ;MODE2
665      003179*          :=.+20
666      003200*          .BYTE BIT3+BIT1+MODE2      ;20 INC/BCC+STORE+MODE2
667      003201*          .BYTE BIT3+BIT1+MODE2      ;21 DSCARD+INC/BCC+MODE2
668      003202*          .BYTE BIT3+BIT1+MODE2      ;22 INC/BCC+STORE+MODE2
669      003203*          .BYTE BIT3+BIT1+MODE2      ;23 DSCARD+INC/BCC+MODE2
670      003204*          .BYTE BIT3+BIT1+MODE2      ;24 INC/BCC+STORE+MODE2
671      003205*          .BYTE BIT3+BIT1+MODE2      ;25 DSCARD+INC/BCC+MODE2
672
673      003205*          .BYTE BIT4+BIT3+MODE3      ;26 DSCARD+INC/BCC+MODE2
674
675      003556*          :=RX_CONTROL.TABLE+1400     ;MODE3
676      003606*          :=.+30
677      003607*          .BYTE BIT1+MODE3      ;30 RESERVED+MODE3
678      003610*          .BYTE BIT1+MODE3      ;31 RESERVED+MODE3
679      003611*          .BYTE BIT1+MODE3      ;32 RESERVED+MODE3
680      003612*          .BYTE BIT1+MODE3      ;33 RESERVED+MODE3
681      003613*          .BYTE BIT1+MODE3      ;34 RESERVED+MODE3
682      003614*          .BYTE BIT1+MODE3      ;35 RESERVED+MODE3
683      003615*          .BYTE BIT1+MODE4      ;36 RESERVED+MODE4
684

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685      004156*          :=RX_CONTROL.TABLE+2000     ;MODE4
686      004159*          :=.+40
687      004216*          .BYTE BIT3+BIT1+MODE4      ;40 INC/BCC+RESERVED+MODE4
688      004219*          .BYTE BIT3+BIT1+MODE4      ;41 INC/BCC+RESERVED+MODE4
689      004220*          .BYTE BIT3+BIT1+MODE4      ;42 INC/BCC+RESERVED+MODE4
690      004221*          .BYTE BIT3+BIT1+MODE4      ;43 INC/BCC+RESERVED+MODE4
691      004222*          .BYTE BIT3+BIT1+MODE4      ;44 INC/BCC+RESERVED+MODE4
692      004223*          .BYTE BIT3+BIT1+MODE4      ;45 INC/BCC+RESERVED+MODE4
693      004224*          .BYTE BIT3+BIT1+MODE4      ;46 INC/BCC+RESERVED+MODE4
694      004225*          .BYTE BIT1+MODE5      ;47 RESERVED+MODE5
695
696      004556*          :=RX_CONTROL.TABLE+2400     ;MODE5
697      004626*          :=.+50
698      004629*          .BYTE BIT3+BIT1+MODE5      ;50 INC/BCC+RESERVED+MODE5
699      004630*          .BYTE BIT3+BIT1+MODE5      ;51 INC/BCC+RESERVED+MODE5
700      004631*          .BYTE BIT3+BIT1+MODE5      ;52 INC/BCC+RESERVED+MODE5
701      004632*          .BYTE BIT3+BIT1+MODE5      ;53 INC/BCC+RESERVED+MODE5
702      004633*          .BYTE BIT3+BIT1+MODE5      ;54 INC/BCC+RESERVED+MODE5
703      004634*          .BYTE BIT3+BIT1+MODE5      ;55 INC/BCC+RESERVED+MODE5
704      004635*          .BYTE BIT1+MODE6      ;56 INC/BCC+RESERVED+MODE5
705
706      005156*          :=RX_CONTROL.TABLE+3000     ;MODE6
707      005159*          :=.+60
708      005236*          .BYTE BIT3+BIT1+MODE6      ;57 RESERVED+MODE6
709      005237*          .BYTE BIT3+BIT1+MODE6      ;58 INC/BCC+STORE+MODE6
710      005240*          .BYTE BIT3+BIT1+MODE6      ;59 DSCARD+INC/BCC+MODE6
711      005241*          .BYTE BIT3+BIT1+MODE6      ;60 INC/BCC+STORE+MODE6
712      005242*          .BYTE BIT3+BIT1+MODE6      ;61 DSCARD+INC/BCC+MODE6
713      005243*          .BYTE BIT3+BIT1+MODE6      ;62 INC/BCC+STORE+MODE6
714      005244*          .BYTE BIT3+BIT1+MODE6      ;63 DSCARD+INC/BCC+MODE6
715      005245*          .BYTE BIT4+BIT3+MODE7      ;64 INC/BCC+STORE+MODE6
716
717
718      005556*          :=RX_CONTROL.TABLE+3400     ;MODE7
719      005549*          :=.+70
720      005646*          .BYTE BIT3+BIT1+MODE7      ;65 INC/BCC+RESERVED+MODE7
721      005647*          .BYTE BIT3+BIT1+MODE7      ;66 INC/BCC+RESERVED+MODE7
722      005650*          .BYTE BIT3+BIT1+MODE7      ;67 INC/BCC+RESERVED+MODE7
723      005651*          .BYTE BIT3+BIT1+MODE7      ;68 INC/BCC+RESERVED+MODE7
724      005652*          .BYTE BIT3+BIT1+MODE7      ;69 INC/BCC+RESERVED+MODE7
725      005653*          .BYTE BIT3+BIT1+MODE7      ;70 INC/BCC+RESERVED+MODE7
726      005654*          .BYTE BIT3+BIT2+MODE7      ;71 INC/BCC+EXP+BCC+MODE7
727      005655*          .BYTE BIT0+MODE0      ;72 GEN/INI+MODE0
728

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DVAB DEC/X11 SYSTEM EXERCISER MODULE  
XDVABo.P11 12-OCT-78 11:57

MACY11 30A(1052) 12-OCT-78 16:34 PAGE 19

SEQ 0018

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729 006156 006156 =RX_CONTROL_TABLE+4000
730 006156 006156 TX_CONTROL_TABLE:
731 006156 006156 =TX_CONTROL_TABLE+0
732 006156 006156 ;MODE 0
733 006156 006156 =+0
734 006156 010 .BYTE BIT3+MODE0 ,00 INC/BCC+MODE0
735 006156 010 .BYTE BIT3+MODE0 ,01 INC/BCC+MODE0
736 006156 010 .BYTE BIT3+MODE0 ,02 INC/BCC+MODE0
737 006156 010 .BYTE BIT3+MODE0 ,03 INC/BCC+MODE0
738 006156 010 .BYTE BIT3+MODE0 ,04 INC/BCC+MODE0
739 006156 010 .BYTE BIT3+MODE0 ,05 INC/RCC+MODE0
740 006156 010 .BYTE BIT3+MODE0 ,06 INC/RCC+MODE0
741 006156 050 .BYTE BIT3+MODE0 ,07 INC/BCC+MODE1
742 006156 001 .BYTE BIT0+MODE0 ,08 RESERVED+MODE0
743 006156 001 .BYTE BIT0+MODE0 ,09 RESERVED+MODE0
744 006156 001 .BYTE BIT0+MODE0 ,10 RESERVED+MODE0
745 006156 001 .BYTE BIT0+MODE0 ,11 RESERVED+MODE0
746 006156 001 .BYTE BIT0+MODE0 ,12 RESERVED+MODE0
747 006173 001 .BYTE BIT0+MODE0 ,13 RESERVED+MODE0
748 006174 001 .BYTE BIT0+MODE0 ,14 RESERVED+MODE0
749 006175 001 .BYTE BIT0+MODE0 ,15 RESERVED+MODE0
750 006176 001 .BYTE BIT0+MODE0 ,16 RESERVED+MODE0
751 006177 001 .BYTE BIT0+MODE0 ,17 RESERVED+MODE0
752 006200 001 .BYTE BIT0+MODE0 ,18 RESERVED+MODE0
753 006201 001 .BYTE BIT0+MODE0 ,19 RESERVED+MODE0
754 006202 001 .BYTE BIT0+MODE0 ,20 RESERVED+MODE0
755 006203 001 .BYTE BIT0+MODE0 ,21 RESERVED+MODE0
756 006204 001 .BYTE BIT0+MODE0 ,22 RESERVED+MODE0
757 006205 001 .BYTE BIT0+MODE0 ,23 RESERVED+MODE0
758 006206 000074 RXB10: BLKB ,24 THIS IS RECEIVER BUFFER FOR DEVICE #1 LINE #0
759 006302 000074 RXB11: BLKB ,25 THIS IS RECEIVER BUFFER FOR DEVICE #1 LINE #1
760 006372 000074 RXB12: BLKB ,26 THIS IS RECEIVER BUFFER FOR DEVICE #1 LINE #2
761 006472 000074 RXB13: BLKB ,27 THIS IS RECEIVER BUFFER FOR DEVICE #1 LINE #3
762 .MEXIT

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DVAB DEC/X11 SYSTEM EXERCISER MODULE  
XDVAB0.P11 12-OCT-78 11:57

MACY11 30A(1052) 12-OCT-78 16:34 PAGE 20

SEQ 0019

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763      006556.          .=TX_CONTROL.TABLE+400      ;MODE1
764      006566.          .=.+10
765          .BYTE  BIT3+MODE1
766          .BYTE  BIT3+MODE1
767          .BYTE  BIT3+MODE1
768          .BYTE  BIT3+MODE1
769          .BYTE  BIT3+MODE1
770          .BYTE  BIT3+MODE1
771          .BYTE  BIT3+MODE1
772          .BYTE  BIT3+MODE2
773 006575.          RXB14: .BLKB 74      ;THIS IS RECEIVER BUFFER FOR DEVICE #1 LINE #4
774 006576.          RXB15: .BLKB 74      ;THIS IS RECEIVER BUFFER FOR DEVICE #1 LINE #5
775 006577.          RXB16: .BLKB 74      ;THIS IS RECEIVER BUFFER FOR DEVICE #1 LINE #6
776 006578.          RXB17: .BLKB 74      ;THIS IS RECEIVER BUFFER FOR DEVICE #1 LINE #7
777          .MEXIT
778
779      007156.          .=TX_CONTROL.TABLE+1000      ;MODE2
780      007176.          .=.+20
781          .BYTE  BIT3+BIT0+MODE2
782          .BYTE  BIT3+BIT0+MODE2
783          .BYTE  BIT3+BIT0+MODE2
784          .BYTE  BIT3+BIT0+MODE2
785          .BYTE  BIT3+BIT0+MODE2
786          .BYTE  BIT3+BIT0+MODE2
787          .BYTE  BIT3+BIT0+MODE2
788          .BYTE  BIT3+BIT0+MODE3
789 007205.          RXB110: .BLKB 74      ;THIS IS RECEIVER BUFFER FOR DEVICE #1 LINE #10
790 007206.          RXB111: .BLKB 74      ;THIS IS RECEIVER BUFFER FOR DEVICE #1 LINE #11
791 007207.          RXB112: .BLKB 74      ;THIS IS RECEIVER BUFFER FOR DEVICE #1 LINE #12
792 007208.          RXB113: .BLKB 74      ;THIS IS RECEIVER BUFFER FOR DEVICE #1 LINE #13
793          .MEXIT

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DVAB DEC/X11 SYSTEM EXERCISER MODULE  
XDVAB0.P11 12-OCT-78 11:57

MACY11 30A(1052) 12-OCT-78 16:34 PAGE 21

SFQ 0020

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J94      007556-          ;=TX_CONTROL.TABLE+1400      ;MODE3
J95      007606-          ;.=.+30
J96      007606-          .BYTE BIT1+MODE3           ;30  SND/DLE+MODE3
J97      007607-          .BYTE BIT1+MODE3           ;31  SND/DLE+MODE3
J98      007610-          .BYTE BIT1+MODE3           ;32  SND/DLE+MODE3
J99      007611-          .BYTE BIT1+MODE3           ;33  SND/DLE+MODE3
J00      007612-          .BYTE BIT1+MODE3           ;34  SND/DLE+MODE3
J01      007613-          .BYTE BIT1+MODE3           ;35  SND/DLE+MODE3
J02      007613-          .BYTE BIT1+MODE3           ;36  RESERVED+MODE3
J03      007613-          .BYTE BIT0+MODE3           ;37  SND/DLE+MODE4
J04      007616-          .BYTE BIT1+MODE4           ;38  SND/DLE+MODE4
J05      007712-          RXB114:    .BLKB 74   ;THIS IS RECEIVER BUFFER FOR DEVICE #1 LINE #14
J06      000074-          RXB115:    .BLKB 74   ;THIS IS RECEIVER BUFFER FOR DEVICE #1 LINE #15
J07      000074-          RXB116:    .BLKB 74   ;THIS IS RECEIVER BUFFER FOR DEVICE #1 LINE #16
J08      010102-          RXB117:    .BLKB 74   ;THIS IS RECEIVER BUFFER FOR DEVICE #1 LINE #17
J09      .MEXIT
J10
J11      010156-          ;=TX_CONTROL.TABLE+2000      ;MODE4
J12      010216-          ;.=.+40
J13      010216-          .BYTE BIT3+BIT0+MODE4           ;40  INC/BCC+RESERVED+MODE4
J14      010220-          .BYTE BIT3+BIT0+MODE4           ;41  INC/BCC+RESERVED+MODE4
J15      010220-          .BYTE BIT3+BIT0+MODE4           ;42  INC/BCC+RESERVED+MODE4
J16      010221-          .BYTE BIT3+BIT0+MODE4           ;43  INC/BCC+RESERVED+MODE4
J17      010222-          .BYTE BIT3+BIT0+MODE4           ;44  INC/BCC+RESERVED+MODE4
J18      010223-          .BYTE BIT3+BIT0+MODE4           ;45  INC/BCC+RESERVED+MODE4
J19      010224-          .BYTE BIT3+BIT0+MODE4           ;46  INC/BCC+RESERVED+MODE4
J20      010225-          .BYTE BIT0+MODE5           ;47  INC/BCC+RESERVED+MODE5
J21      010225-          .BYTE BIT0+MODE5           ;48  INC/BCC+RESERVED+MODE5
J22      000074-          RXB20:    .BLKB 74   ;THIS IS RECEIVER BUFFER FOR DEVICE #2 LINE #0
J23      000074-          RXB21:    .BLKB 74   ;THIS IS RECEIVER BUFFER FOR DEVICE #2 LINE #1
J24      000074-          RXB22:    .BLKB 74   ;THIS IS RECEIVER BUFFER FOR DEVICE #2 LINE #2
J25      010512-          RXB23:    .BLKB 74   ;THIS IS RECEIVER BUFFER FOR DEVICE #2 LINE #3
J26      .MEXIT
J27
J28      010556-          ;=TX_CONTROL.TABLE+2400      ;MODE5
J29      010826-          ;.=.+50
J30      010625-          .BYTE BIT3+BIT0+MODE5           ;50  INC/BCC+RESERVED+MODE5
J31      010630-          .BYTE BIT3+BIT0+MODE5           ;51  INC/BCC+RESERVED+MODE5
J32      010631-          .BYTE BIT3+BIT0+MODE5           ;52  INC/BCC+RESERVED+MODE5
J33      010632-          .BYTE BIT3+BIT0+MODE5           ;53  INC/BCC+RESERVED+MODE5
J34      010633-          .BYTE BIT3+BIT0+MODE5           ;54  INC/BCC+RESERVED+MODE5
J35      010634-          .BYTE BIT3+BIT0+MODE5           ;55  INC/BCC+RESERVED+MODE5
J36      010635-          .BYTE BIT3+BIT0+MODE5           ;56  INC/BCC+RESERVED+MODE5
J37      010635-          .BYTE BIT0+MODE6           ;57  INC/BCC+RESERVED+MODE6
J38      000074-          RXB24:    .BLKB 74   ;THIS IS RECEIVER BUFFER FOR DEVICE #2 LINE #4
J39      000074-          RXB25:    .BLKB 74   ;THIS IS RECEIVER BUFFER FOR DEVICE #2 LINE #5
J40      011122-          RXB26:    .BLKB 74   ;THIS IS RECEIVER BUFFER FOR DEVICE #2 LINE #6
J41      .MEXIT

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DVAB DEC/X11 SYSTEM EXERCISER MODULE  
XDVABo.P11 12-OCT-78 11:57

MACY11 30A(1052) 12-OCT-78 16:34 PAGE 22

SEQ 0021

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842      011156`          .=TX_CONTROL.TABLE+3000      ;MODE6
843      011236`          .=.+60
844      011237`          .BYTE BIT3+BIT0+MODE6    ,60 INC/BCC+RESERVED+MODE6
845      011237`          .BYTE BIT3+BIT0+MODE6    ,61 INC/BCC+RESERVED+MODE6
846      011240`          .BYTE BIT3+BIT0+MODE6    ,62 INC/BCC+RESERVED+MODE6
847      011240`          .BYTE BIT3+BIT0+MODE6    ,63 INC/BCC+RESERVED+MODE6
848      011242`          .BYTE BIT3+BIT0+MODE6    ,64 INC/BCC+RESERVED+MODE6
849      011243`          .BYTE BIT3+BIT0+MODE6    ,65 INC/BCC+RESERVED+MODE6
850      011245`          .BYTE BIT3+BIT0+MODE6    ,66 INC/BCC+RESERVED+MODE6
851      011245`          .BYTE BIT3+BIT0+MODE6    ,67 INC/BCC+RESERVED+MODE6
852      011245`          .BYTE BIT3+BIT0+MODE6    ,68 INC/BCC+RESERVED+MODE6
853      000074`          RXB210: .BLKB 74 ;THIS IS RECEIVER BUFFER FOR DEVICE #2 LINE #10
854      000074`          RXB211: .BLKB 74 ;THIS IS RECEIVER BUFFER FOR DEVICE #2 LINE #11
855      000074`          RXB212: .BLKB 74 ;THIS IS RECEIVER BUFFER FOR DEVICE #2 LINE #12
856      000074`          RXB213: .BLKB 74 ;THIS IS RECEIVER BUFFER FOR DEVICE #2 LINE #13
857      .MEXIT
858
859      011556`          .=TX_CONTROL.TABLE+3400      ;MODE7
860      011546`          .=.+70
861      011647`          .BYTE BIT3+BIT0+MODE7    ,70 INC/BCC+RESERVED+MODE7
862      011650`          .BYTE BIT3+BIT0+MODE7    ,71 INC/BCC+RESERVED+MODE7
863      011651`          .BYTE BIT3+BIT0+MODE7    ,72 INC/BCC+RESERVED+MODE7
864      011652`          .BYTE BIT3+BIT0+MODE7    ,73 INC/BCC+RESERVED+MODE7
865      011652`          .BYTE BIT3+BIT0+MODE7    ,74 INC/BCC+RESERVED+MODE7
866      011652`          .BYTE BIT3+BIT0+MODE7    ,75 INC/BCC+RESERVED+MODE7
867      011652`          .BYTE BIT3+BIT0+MODE7    ,76 INC/BCC+SND/BCC+MODE7
868      0001`              .BYTE BIT0+MODE0      ,77 RESERVED+MODE0
869      000074`          RXB214: .BLKB 74 ;THIS IS RECEIVER BUFFER FOR DEVICE #2 LINE #14
870      000074`          RXB215: .BLKB 74 ;THIS IS RECEIVER BUFFER FOR DEVICE #2 LINE #15
871      000074`          RXB216: .BLKB 74 ;THIS IS RECEIVER BUFFER FOR DEVICE #2 LINE #16
872      000074`          RXB217: .BLKB 74 ;THIS IS RECEIVER BUFFER FOR DEVICE #2 LINE #17
873      .MEXIT

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874 012236* 010046 TXISR: MOV R0,-(SP) ;SAVE R0 ON THE STACK
875 012236* 010046 MOV R4,(SP) ;SAVE R4 ON THE STACK
876 012240* 010446 MOV (R5)+,R0 ;GET DEVICE CSR ON INTERRUPT
877 012244* 012550 002000 BIT #DVAFRL0,@DVSCR ;REPORT OVERFLOW
878 012254* 010102 BNE 2$ ;IS 15=?
879 012254* 005710 TST @DVSCR ;BR IF YES
880 012256* 010067 165616 1$: MOV R0,CSRA ;LOAD FOR PRINT OUT
881 012256* 010046 000014 2$: MOV @DVSCR,ASTAT ;GET BAD LOC SET UP
882 012262* 010107 165616 MOV R0,ACSR ;SET FOR TYPE OUT
883 012266* 010067 165616 MOV R0,-(SP) ;CLEAR ANY SIGN EXTEND
884 012266* 010067 165616 BR 65$ ;SET 1 PROBABILITY OF GOOD RESULTS
885 012274* 010604 000014 3$: CMP R5,R4 ;ARE THEY EQUAL?
886 012300* 110405 000014 BIC #C377>R5 ;GET DVNSR REGISTER
887 012302* 024705 177400 MOV R4,R5 ;GET LINE NUMBER
888 012306* 052705 100400 BIC #BIT15+BIT8,R5 ;CLEAR ANY SIGN EXTEND
889 012312* 020504 BIS #BIT12 ;SET 1 PROBABILITY OF GOOD RESULTS
890 012312* 010146 CMP R5,R4 ;ARE THEY EQUAL?
891 012314* 010146 BEQ 4$ ;GET CHOICE OF GOOD RESULTS
892 012325* 093059 001000 BIS #BIT9,R5 ;NOW ARE THESE OK?
893 012325* 093059 001000 CMP R5,R4 ;BR IF YES
894 012324* 001412 BEQ 4$ ;SET FOR PRINTOUT
895 012326* 010067 165546 MOV R0,CSRA ;SAVE FOR PRINTOUT
896 012332* 010067 165544 MOV R0,ACSR ;SET FOR ERROR
897 012336* 020767 000014 165536 ADD R4,ASTAT ;POINTER TO DVNSR
898 012344* 000480 165534 MOV R4,STAT ;GET BAD RESULTS
899 012352* 010460 000006 BR 65$ ;CLEAR ROUTINE
900 012352* 010460 000006 MOV R4,DVSR(R0) ;LOAD LINE NUMBER
901 012356* 012760 177400 000010 4$: MOV #40,DVSA(R0) ;SET FOR 40(B) CHARS
902 012364* 042710 000060 BIC #BIT5+BIT4,@DVSCR ;CLEAR EA BITS
903 012370* 105360 000007 DECB DVSRSH(R0) ;IS THIS PBI OR ALT?
905 012375* 196710 000302 BNE S9$ ;S9 IS PBI OR ALT?
906 012402* 016760 000300 000010 BISB #NP,DVSCR ;SET EA BITS
907 012410* 000413 MOV PRI,PA,DVSA(R0) ;LOAD PBI
908 012416* 012767 000001 167456 65$: BIS 6$ ;GET OUT
909 012416* 012767 000001 BIS #MRESET,@DVSCR ;RESET DEVICE ON ERROR
910 012421* 000405 MOV #*,ABORT ;SET ERROR FLAG
911 012425* 016760 000253 5$: BISB #ALT,EA,DVSCR ;SET EA BITS
912 012433* 016760 000252 000010 MOV ALT,PA,DVSA(R0) ;LOAD ALTERNATE
914 012440* 012604 000002 6$: MOV (SP)+,R4 ;POP STACK TO R4
915 012440* 012604 000002 MOV (SP)+,R0 ;POP STACK TO R0
916 012442* 012600 000002 MOV (SP)+,R5 ;POP STACK TO R5
917 012444* 012605 000002
918 012446* 000002 RTI

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919 012450* 010046 RXISR: MOV R0,-(SP) ;SAVE R0 ON THE STACK
920 012452* 010146 MOV R1,-(SP) ;SAVE R1 ON THE STACK
921 012454* 012500 MOV (R5)+,R0 ;GET DVSCR FOR ISR USE
922 012454* 012500 000000 BIS #NP,DVSCR ;NON-POLARIZABLE BANG DV11 REGISTER
923 012456* 052710 000000 MOV DVFC(R0),R1 ;GET DVFC REGISTER
924 012466* 105710 000002 TSTB R1 ;#BIT15=?
925 012470* 010407 RMI IS ;BR IF YES
926 012472* 010067 165402 MOV R0,CSRA ;SAVE FOR TYPE OUT
927 012476* 010107 165402 MOV @DVSCR,ASTAT ;GET BAD DATA
928 012502* 010067 165374 MOV R0,ACSR ;CLEAR ROUTINE
929 012506* 000462 BR 6$ ;BR IF ERROR
930 012510* 032701 170000 65$: BIT #BIT15+BIT14+BIT13+BIT12 ;ARE ANY ERROR CODES SET?
931 012514* 001037 BNE 5$ ;BR IF YES (ECC S/B THE ONLY)
932 012516* 122701 000077 CMPB #7,R1 ;SWAB R1
933 012522* 010043 BNE 6$ ;GET LINE IN LOW BYTE
934 012524* 000301 2$: SWAB R1 ;CLEAR HIGH BYTE
935 012524* 000301 000007 BIC #377*400>R1 ;LOAD LINE NUMBER
936 012525* 042760 000006 MOV #1,DVSR(R0) ;CLEAR LINE STATE REGISTER
937 012526* 0112760 000013 000007 BIC #DVSRSH(R0) ;CLEAR TX GC
938 012526* 0112760 000013 000007 MOVB #1,DVSRSH(R0) ;CLEAR TX GC
939 012536* 012760 000002 000007 BIS #RESYNC,DVSA(R0) ;SER RX RESYNC
940 012552* 052760 000002 000010 MOVB #17,DVSRSH(R0) ;SER CTRL BYTE STORE
941 012552* 052760 000002 000010 BIS #DISCARD,DVSA(R0) ;THROW AWAY 177 CHAR
942 012556* 012760 000020 000010 BIS #PFB8,@DVSCR ;RESTART DV11 DCFU
943 012556* 012760 000020 000010 ADD R0,R5 ;PCF8 END-OF-PASS FLAG POINTER
944 012600* 062705 000010 BIS #1,R1 ;MAKE EVEN (MULT BY 2)
945 012604* 063011 012712* ASL R1 ;CONTINUE ALONG
946 012606* 056115 012712* BIS EOP,TABLE(R1),(R5) ;SST END-OF-PASS FLAG.
947 012612* 000430 BR 9$ ;CONTINUE ALONG
948 012614* 010105 5$: MOV R1,R5 ;LOAD LINE NUMBER INTO R5
949 012616* 042705 170377 BIC #BIT11+BIT10+BIT9+BIT8,R5 ;MAKE IT EQUAL TO ECC INDICATOR
950 012624* 012505 050000 BIS #BIT14+BIT12,R5 ;IS RIC OK?
951 012626* 020501 CMP R5,R1 ;IS RIC OK?
952 012630* 001417 BEQ 8$ ;SET FOR ERROR
953 012630* 001417 MOV R0,ACSR ;POINT TO DVFC
954 012632* 010067 165244 6$: ADD R2,ACSR ;LOAD BAD RESULTS
955 012636* 062767 000002 165236 7$: MOV R1,ASTAT ;SET FOR PRINT OUT
956 012644* 010167 165234 MOV DVSA(R0) ;SET FOR PRINT OUT
957 012650* 010067 165224 MOV #MRESET,@DVSCR ;SET ERROR INDICATOR.
958 012660* 012767 177777 167214 8$: MOV R0,-(SP) ;CONT
959 012666* 000402 BR 9$ ;RESTART DV11
960 012670* 052710 000400 BIS #BIT8,@DVSCR ;RESTART DV11
961 012674* 012601 9$: MOV (SP)+,R1 ;POP STACK TO R1
962 012674* 012601 MOV (SP)+,R0 ;POP STACK TO R0
963 012674* 012601 MOV (SP)+,R5 ;POP STACK TO R5
964 012676* 012600
965 012690* 024805
966 012702* 000002 RTI

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DVAB DEC/X11 SYSTEM EXERCISER MODULE  
XDVAB0.P11 12-OCT-78 11:57

MACY11 30A(1052) 12-OCT-78 16:34 PAGE 25

SEQ 0024

987	0127204	001
988	0127205	001
989	0127210	0000001
990	0127211	0000001
991		
992	0127212	00000012
993	0127213	00000013
994	0127214	00000014
995	0127215	00000015
996	0127216	00000016
997	0127217	00000017
998	0127218	00000018
999	0127219	00000019
9990	0127220	00000020
9991	0127221	00000021
9992	0127222	00000022
9993	0127223	00000023
9994	0127224	00000024
9995	0127225	01000000
9996	0127226	01000000
9997	0127227	01000000
9998	0127228	01000000
9999	0127229	01000000

DVAR DEC/X11 SYSTEM EXERCISER MODULE  
XDVA0.P11 12-OCT-78 11:57

MACV11 30A(1052) 12-OCT-78 16:34 PAGE 26

SEQ 0025

990			
991			
992			
993			
994			
995	012752	013106	
996	012754	017740	
997	012756	013146	
998	012760	017740	
999	012762	017740	
1000	012766	000000	
1002	012770	000000	
1003	012772	006156	
1004	012774	002156	
1005	012776	014013	
1006	013000	000000	
1008	013002	000000	
1009	013004	000000	
1010	013006	000000	
1011	013010	000000	

013012	000
013021	010
013030	020
013052	040
013063	050
013072	060
013077	070

  

{1}	013105	000
{2}	013116	010
{3}	013126	020
{4}	013136	030
{5}	013146	040

THIS IS AN EXACT IMAGE OF WHAT DATA SHOULD BE FOUND IN ANY COMPLETED DV11 RECEIVER BUFFER. (NOTE: DLE=30(8))

```

013012; 000 001 002 DATA.TABLE;
013021; 010 011 012 .BYTE 0,1,2,3,4,5,6
013030; 020 021 022 .BYTE 10,11,12,13,14,15,16
013034; 030 031 032 .BYTE 20,21,22,23,24,25,26
013053; 040 041 042 .BYTE DLE,30,DLE,31,DLE,32,DLE,33,DLE,34,DLE,35,36,DLE,37
013063; 050 051 052 .BYTE 40,41,42,43,44,45,46,47
013077; 070 071 072 .BYTE 50,51,52,53,54,55,56,57
013077; 070 071 072 .BYTE 70,71,72,73,74,75,76,-

```

THIS THE DATA THAT THE TRANSMITTERS SEND.

{1}	013106	000	001	002
{2}	013106	000	011	012
{3}	013106	020	011	012
{4}	013106	030	031	032
{5}	013146			
{6}	013146	040	041	042
{7}	013146	050	051	052
{8}	013176	060	061	062
{9}	013176	070	071	072

```

1013          ;RECEIVER BUFFERS FOR DEVICES #3 AND #4.
1014      RXB30:  .BLKB  74  ;THIS IS RECEIVER BUFFER FOR DEVICE #3 LINE #0
1015      013206  000074
1016      RXB31:  .BLKB  74  ;THIS IS RECEIVER BUFFER FOR DEVICE #3 LINE #1
1017      013307  000074
1018      RXB32:  .BLKB  74  ;THIS IS RECEIVER BUFFER FOR DEVICE #3 LINE #2
1019      013472  000074
1020      RXB33:  .BLKB  74  ;THIS IS RECEIVER BUFFER FOR DEVICE #3 LINE #3
1021      RXB34:  .BLKB  74  ;THIS IS RECEIVER BUFFER FOR DEVICE #4 LINE #4
1022      013392  000074
1023      RXB35:  .BLKB  74  ;THIS IS RECEIVER BUFFER FOR DEVICE #4 LINE #5
1024      014052  000074
1025      RXB36:  .BLKB  74  ;THIS IS RECEIVER BUFFER FOR DEVICE #4 LINE #6
1026      RXB37:  .BLKB  74  ;THIS IS RECEIVER BUFFER FOR DEVICE #4 LINE #7
1027      RXB38:  .BLKB  74  ;THIS IS RECEIVER BUFFER FOR DEVICE #4 LINE #8
1028      MEXIT
1029      RXB39:  .BLKB  74  ;THIS IS RECEIVER BUFFER FOR DEVICE #3 LINE #9
1030      014146  000074
1031      RXB40:  .BLKB  74  ;THIS IS RECEIVER BUFFER FOR DEVICE #3 LINE #10
1032      014242  000074
1033      RXB41:  .BLKB  74  ;THIS IS RECEIVER BUFFER FOR DEVICE #3 LINE #11
1034      014336  000074
1035      RXB42:  .BLKB  74  ;THIS IS RECEIVER BUFFER FOR DEVICE #3 LINE #12
1036      014432  000074
1037      RXB43:  .BLKB  74  ;THIS IS RECEIVER BUFFER FOR DEVICE #3 LINE #13
1038      MEXIT
1039      RXB44:  .BLKB  74  ;THIS IS RECEIVER BUFFER FOR DEVICE #4 LINE #14
1040      014526  000074
1041      RXB45:  .BLKB  74  ;THIS IS RECEIVER BUFFER FOR DEVICE #4 LINE #15
1042      014582  000074
1043      RXB46:  .BLKB  74  ;THIS IS RECEIVER BUFFER FOR DEVICE #4 LINE #16
1044      014529  000074
1045      RXB47:  .BLKB  74  ;THIS IS RECEIVER BUFFER FOR DEVICE #4 LINE #17
1046      MEXIT
1047      RXB48:  .BLKB  74  ;THIS IS RECEIVER BUFFER FOR DEVICE #4 LINE #18
1048      016045  000074
1049      RXB49:  .BLKB  74  ;THIS IS RECEIVER BUFFER FOR DEVICE #4 LINE #19
1050      016426  000074
1051      RXB50:  .BLKB  74  ;THIS IS RECEIVER BUFFER FOR DEVICE #4 LINE #20
1052      016616  000074
1053      RXB51:  .BLKB  74  ;THIS IS RECEIVER BUFFER FOR DEVICE #4 LINE #21
1054      RXB52:  .BLKB  74  ;THIS IS RECEIVER BUFFER FOR DEVICE #4 LINE #22
1055      MEXIT

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1056      017006*
1057      LNKTAB:    ALL INTERRUPTS WILL BE CHANNELLED THROUGH
1058              THE APPROPRIATE CALL.
1059      017006  004567 173436
1060      017012  000000 006206
1061      017016  004567 173214
1062      017022  000000
1063      017024  000224
1064      JSR      R5_RXISR    ;GOTO RECEIVER ISR FOR DEVICE #1.
1065      .WORD   SDVSCR,RXB10
1066      JSR      R5_TXISR    ;GOTO TRANSMITTER ISR FOR DEVICE #1
1067      .WORD   DVSCR
1068      XXX1:    .WORD   LINES1  ;END PASS FLAG.
1069      017025  004567 173416
1070      017032  000000 010226
1071      017036  004567 173174
1072      017042  000000
1073      017044  000236
1074      JSR      R5_RXISR    ;GOTO RECEIVER ISR FOR DEVICE #2.
1075      .WORD   SDVSCR,RXB20
1076      JSR      R5_TXISR    ;GOTO TRANSMITTER ISR FOR DEVICE #2
1077      .WORD   DVSCR
1078      XXX2:    .WORD   LINES2  ;END PASS FLAG.
1079      017046  004567 173376
1080      017052  000000 010228
1081      017056  004567 173154
1082      017064  000250
1083      JSR      R5_RXISR    ;GOTO RECEIVER ISR FOR DEVICE #3.
1084      .WORD   SDVSCR,RXB30
1085      JSR      R5_TXISR    ;GOTO TRANSMITTER ISR FOR DEVICE #3
1086      .WORD   DVSCR
1087      XXX3:    .WORD   LINES3  ;END PASS FLAG.
1088      017065  004567 173356
1089      017071  000000 010230
1090      017076  004567 173134
1091      017102  000000
1092      011104  000262
1093      MEXIT
1094      BUFFER_TABLE:
1095      RXB10:   .WORD   ;BUFFER FOR DEVICE #1 LINE #0
1096      RXB11:   .WORD   ;BUFFER FOR DEVICE #1 LINE #1
1097      RXB12:   .WORD   ;BUFFER FOR DEVICE #1 LINE #2
1098      RXB13:   .WORD   ;BUFFER FOR DEVICE #1 LINE #3
1099      RXB14:   .WORD   ;BUFFER FOR DEVICE #1 LINE #4
1100      RXB15:   .WORD   ;BUFFER FOR DEVICE #1 LINE #5
1101      RXB16:   .WORD   ;BUFFER FOR DEVICE #1 LINE #6
1102      RXB17:   .WORD   ;BUFFER FOR DEVICE #1 LINE #7
1103      RXB18:   .WORD   ;BUFFER FOR DEVICE #1 LINE #8
1104      RXB19:   .WORD   ;BUFFER FOR DEVICE #1 LINE #9
1105      RXB20:   .WORD   ;BUFFER FOR DEVICE #2 LINE #0
1106      RXB21:   .WORD   ;BUFFER FOR DEVICE #2 LINE #1
1107      RXB22:   .WORD   ;BUFFER FOR DEVICE #2 LINE #2
1108      RXB23:   .WORD   ;BUFFER FOR DEVICE #2 LINE #3
1109      RXB24:   .WORD   ;BUFFER FOR DEVICE #2 LINE #4
1110      RXB25:   .WORD   ;BUFFER FOR DEVICE #2 LINE #5
1111      RXB26:   .WORD   ;BUFFER FOR DEVICE #2 LINE #6
1112      RXB27:   .WORD   ;BUFFER FOR DEVICE #2 LINE #7
1113      RXB28:   .WORD   ;BUFFER FOR DEVICE #2 LINE #8

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1111	017170*	011342*	RXB211	>BUFFER FOR DEVICE #2 LINE #11
1112	017172*	011343*	RXB212	>BUFFER FOR DEVICE #3 LINE #12
1113	017174*	011345*	RXB213	>BUFFER FOR DEVICE #4 LINE #13
1114	017176*	011356*	RXB214	>BUFFER FOR DEVICE #5 LINE #14
1115	017200*	011752*	RXB215	>BUFFER FOR DEVICE #6 LINE #15
1116	017202*	0112046*	RXB216	>BUFFER FOR DEVICE #7 LINE #16
1117	017204*	0112142*	RXB217	>BUFFER FOR DEVICE #8 LINE #17
1118	017206*	0113206*	RXB30	>BUFFER FOR DEVICE #3 LINE #0
1119	017210*	0113302*	RXB31	>BUFFER FOR DEVICE #4 LINE #1
1120	017212*	0113316*	RXB32	>BUFFER FOR DEVICE #5 LINE #2
1121	017214*	0113472*	RXB33	>BUFFER FOR DEVICE #6 LINE #3
1122	017216*	0113566*	RXB34	>BUFFER FOR DEVICE #7 LINE #4
1123	017220*	0113682*	RXB35	>BUFFER FOR DEVICE #8 LINE #5
1124	017222*	0114085*	RXB36	>BUFFER FOR DEVICE #9 LINE #6
1125	017224*	0114146*	RXB37	>BUFFER FOR DEVICE #10 LINE #7
1126	017226*	0114242*	RXB310	>BUFFER FOR DEVICE #11 LINE #10
1127	017230*	0114336*	RXB311	>BUFFER FOR DEVICE #12 LINE #11
1128	017234*	0114432*	RXB312	>BUFFER FOR DEVICE #13 LINE #12
1129	017236*	0114526*	RXB313	>BUFFER FOR DEVICE #14 LINE #13
1130	017238*	0114549*	RXB314	>BUFFER FOR DEVICE #15 LINE #14
1131	017240*	0114622*	RXB315	>BUFFER FOR DEVICE #16 LINE #15
1132	017244*	0114646*	RXB316	>BUFFER FOR DEVICE #17 LINE #16
1133	017244*	0115012*	RXB317	>BUFFER FOR DEVICE #18 LINE #17
1134	017246*	0115106*	RXB40	>BUFFER FOR DEVICE #4 LINE #0
1135	017250*	0115202*	RXB41	>BUFFER FOR DEVICE #4 LINE #1
1136	017252*	0115276*	RXB42	>BUFFER FOR DEVICE #4 LINE #2
1137	017254*	0115322*	RXB43	>BUFFER FOR DEVICE #4 LINE #3
1138	017256*	0115422*	RXB44	>BUFFER FOR DEVICE #4 LINE #4
1139	017258*	0115485*	RXB45	>BUFFER FOR DEVICE #4 LINE #5
1140	017260*	0115656*	RXB46	>BUFFER FOR DEVICE #4 LINE #6
1141	017262*	0115656*	RXB47	>BUFFER FOR DEVICE #4 LINE #7
1142	017264*	0115752*	RXB410	>BUFFER FOR DEVICE #4 LINE #10
1143	017266*	0116046*	RXB411	>BUFFER FOR DEVICE #4 LINE #11
1144	017270*	0116142*	RXB412	>BUFFER FOR DEVICE #4 LINE #12
1145	017274*	0116332*	RXB413	>BUFFER FOR DEVICE #4 LINE #13
1146	017276*	0116426*	RXB414	>BUFFER FOR DEVICE #4 LINE #14
1147	017278*	0116426*	RXB415	>BUFFER FOR DEVICE #4 LINE #15
1148	017280*	0116522*	RXB416	>BUFFER FOR DEVICE #4 LINE #16
1149	017302*	0116616*	RXB417	>BUFFER FOR DEVICE #4 LINE #17

.MEXIT .END

CROSS REFERENCE TABLE -- USER SYMBOLS									
.ABORT	002102R	333*	518	520	609#	910*	959*	954*	955*
ACSR	000000R	260*	884*	896*	897#	929*			
ADDR	000000R	242*	344	370	479	539			
ADDR22=	001000								
ALT.EA	012705R	489*	912	968#					
ALT.PA	012710R	490*	913	970#					
ASB	000106R	264*	594*						
ASTAT	000104R	262*	883*	898*	928*	956*			
AWAIT	000103R	263*	595*						
BEGIN	000000R	517*	520*	488	497	516	517	531	548
BIT0 =	0000001	517*	273#	377	389	398	491	727	742
BIT0	= 0000001	748	745	750	751	752	753	754	755
BIT1	= 000002	784	785	786	787	788	802	813	814
BIT2	= 000002	820	829	830	831	832	833	834	835
BIT3	= 000002	848	849	850	852	860	861	862	863
BIT4	= 000020	217	273#	692	698	699	690	671	676
BIT5	= 002000	701	702	703	704	705	708	710	712
BIT6	= 004000	724	725	796	797	798	799	800	801
BIT7	= 010000	217	273#	398	448	451	451	950	
BIT8	= 040000	217	273#	445	448	931	931	951	
BIT9	= 040000	217	273#	448	451	931	931		
BIT10	= 002000	217	273#	398	448	451	451	950	
BIT11	= 004000	217	273#	398	445	451	451	950	
BIT12	= 010000	217	273#	445	448	931	931	951	
BIT13	= 040000	217	273#	448	451	931	931		
BIT14	= 040000	217	273#	421	442	445	448	451	889
BIT15	= 100000	217	273#	398	445	448	451	893	
BIT16	= 000004	217	273#	726	866	866	865	865	850
BIT17	= 0000010	656	657	658	659	660	661	662	663
BIT18	= 0000010	656	657	658	659	660	661	662	663
BIT19	= 0000010	702	703	704	705	706	710	711	712
BIT20	= 0000010	722	723	724	725	726	734	735	736
BIT21	= 0000010	765	766	767	768	769	770	771	772
BIT22	= 0000010	786	787	788	813	814	815	816	817
BIT23	= 0000010	832	833	834	835	844	845	846	847
BIT24	= 0000010	861	862	863	864	865	866	867	868
BIT25	= 0000010	217	273#	421	442	445	448	451	889
BIT26	= 0000010	217	273#	398	445	448	451	893	
BIT27	= 0000010	217	273#	726	866	866	865	865	850
BIT28	= 0000010	217	273#	358	517	517	517	517	517
BIT29	= 0000010	217	273#	362					
BIT30	= 0000010	217	273#						
BIT31	= 0000010	217	273#						
BIT32	= 0000010	217	273#						
BIT33	= 0000010	217	273#						
BIT34	= 0000010	217	273#						
BIT35	= 0000010	217	273#						
BIT36	= 0000010	217	273#						
BIT37	= 0000010	217	273#						
BIT38	= 0000010	217	273#						
BIT39	= 0000010	217	273#						
BIT40	= 0000010	217	273#						
BIT41	= 0000010	217	273#						
BIT42	= 0000010	217	273#						
BIT43	= 0000010	217	273#						
BIT44	= 0000010	217	273#						
BIT45	= 0000010	217	273#						
BIT46	= 0000010	217	273#						
BIT47	= 0000010	217	273#						
BIT48	= 0000010	217	273#						
BIT49	= 0000010	217	273#						
BIT50	= 0000010	217	273#						
BIT51	= 0000010	217	273#						
BIT52	= 0000010	217	273#						
BIT53	= 0000010	217	273#						
BIT54	= 0000010	217	273#						
BIT55	= 0000010	217	273#						
BIT56	= 0000010	217	273#						
BIT57	= 0000010	217	273#						
BIT58	= 0000010	217	273#						
BIT59	= 0000010	217	273#						
BIT60	= 0000010	217	273#						
BIT61	= 0000010	217	273#						
BIT62	= 0000010	217	273#						
BIT63	= 0000010	217	273#						
BIT64	= 0000010	217	273#						
BIT65	= 0000010	217	273#						
BIT66	= 0000010	217	273#						
BIT67	= 0000010	217	273#						
BIT68	= 0000010	217	273#						
BIT69	= 0000010	217	273#						
BIT70	= 0000010	217	273#						
BIT71	= 0000010	217	273#						
BIT72	= 0000010	217	273#						
BIT73	= 0000010	217	273#						
BIT74	= 0000010	217	273#						
BIT75	= 0000010	217	273#						
BIT76	= 0000010	217	273#						
BIT77	= 0000010	217	273#						
BIT78	= 0000010	217	273#						
BIT79	= 0000010	217	273#						
BIT80	= 0000010	217	273#						
BIT81	= 0000010	217	273#						
BIT82	= 0000010	217	273#						
BIT83	= 0000010	217	273#						
BIT84	= 0000010	217	273#						
BIT85	= 0000010	217	273#						
BIT86	= 0000010	217	273#						
BIT87	= 0000010	217	273#						
BIT88	= 0000010	217	273#						
BIT89	= 0000010	217	273#						
BIT90	= 0000010	217	273#						
BIT91	= 0000010	217	273#						
BIT92	= 0000010	217	273#						
BIT93	= 0000010	217	273#						
BIT94	= 0000010	217	273#						
BIT95	= 0000010	217	273#						
BIT96	= 0000010	217	273#						
BIT97	= 0000010	217	273#						
BIT98	= 0000010	217	273#						
BIT99	= 0000010	217	273#						
BIT100	= 0000010	217	273#						
BIT101	= 0000010								

DVAR DEC/X11 SYSTEM EXERCISER MODULE  
XDVAR0.P11 12-OCT-78 11:57

MACY11 30A(1052) 12-OCT-78 16:34 PAGE 32  
CROSS REFERENCE TABLE -- USER SYMBOLS

SEQ 0030

DATERS=	104404	273#	597
DEBUG =	***** U	1005	1012#
DLE =	000030	336#	340 342
DROP =	0000356R	213#	943
DSCARD=	000020	201#	334 480
DVIDR =	000014	307#	421* 422 442*
DVNSR =	000014	306#	886
DVRES =	000016	307#	
DVRIC =	000002	300#	924
DVSFR =	000012	305#	
DVSRA =	000010	304#	397* 424* 455* 463 466 469 473* 475* 476* 501* 901* 907*
DVSRS =	000006	91#	340* 941* 943* 945* 900* 938* 454* 459* 462* 465* 468* 474* 904* 939*
DVSRSR=	000007	303#	420* 425* 426 429* 454* 459* 462* 465* 468* 474* 904* 939*
EAD =	000302R	321#	471 489 500
ENDITS=	104413	273#	583
ENDS =	104410	273#	587 608
EDIT =	000014R	941#	527#
ENUTYP =	000106R	263#	529# 546*
EXITS =	104400	273#	
FUNC_X =	012752R	418#	995#
GETPAS=	104415	273#	488 497
GWBUFFS =	104414	273#	
HIBYTE=	000400	237#	1005
HULD =	0000000R	375*	391* 458
HRDENT =	000044R	543#	
HRDERS=	104405	273#	531 548
HRDPAS =	000050R	245#	
HUNG =	002106R	607#	611#
ICOUNT =	000036R	240#	
IDNUM=M=	000001	241#	
IDNUM=M=	000122R	210#	1005
INIT =	000306R	237#	
INTR =	000120R	269#	331*
LINES1 =	000224R	275#	346 371 551 562 1063
LINES2 =	000236R	281#	347 553 1069
LINES3 =	000529R	282#	348 555 1075
LINES4 =	000529R	292#	349 557 1081
LNKTRB =	017006R	346#	1055#
MAP225=	104416	273#	
MODE0 =	000000	309#	627 628 629 630 631 632 633 635 636 637 638 639
		640	641 642 643 644 645 646 647 648 649 650 727 734
		735	736 737 738 739 740 742 743 744 745 746 747 748
MODE1 =	000040	710#	750 751 752 753 754 755 756 757 758 759 760 761 762 763 764 765 766 767
MODE2 =	000100	711#	766 767 768 769 770 771 772 773 774 775 776 777 778 779 780 781 782 783
MODE3 =	000140	784#	785 786 787
MODE4 =	000200	793#	800 801 802
MODE5 =	000240	813#	863 864 865 866 867 868 869 870 871 872 873 874 875 876 877 878 879 880
		894	895 896 897 898 899 900 901 902 903 904 905 906 907 908 909 910 911 912 913

DVAR DEC/X11 SYSTEM EXERCISER MODULE  
XDVAR0.P11 12-OCT-78 11:57

MACY11 30A(1052) 12-OCT-78 16:34 PAGE 33  
CROSS REFERENCE TABLE -- USER SYMBOLS

SEQ 0031

MODE6 =	000300	832#	833 834 835
		835#	705 708 709 710 711 712 713 714 836 844 845 846
MODE7 =	000340	847#	848 849 850 851 860 861 862
MODNAM =	000000R	316#	715 720 721 722 723 724 725 726 851 860 861 862
MODSP =	000224R	863#	864 865 866
MRDSET=	004000	224#	
MSGSNS =	104403	239#	271# 909 958
MSGSS =	104402	273#	607
MSGSS =	104401	273#	
M.MODE=	014000	217#	421
NPR.IE=	020000	217#	491
NULL =	000000	217#	548
OPEN =	000000	225#	231 332 264 265 233 234 251 252 269 253# 273# 254 255 256 257 258
OTQAS =	104420	273#	
OVRFL0=	002000	217#	878
PA =	000300R	320#	472 490 501
PASCNT =	000034R	239#	
PING_P =	000000R	241#	
PIRQS =	000004R	242#	332* 364 372 379 402 563 582*
PODSP =	005726	273#	
PODSP2=	022626	273#	
PRI.EA =	012704R	471*	906 967#
PRI.PA =	01706R	472*	907 969#
PRTY0 =	001000	273#	
PRTY1 =	000000	273#	
PRTY2 =	000040	273#	
PRTY3 =	000100	273#	
PRTY4 =	000140	273#	
PRTY5 =	000200	273#	
PRTY5 =	000240	225#	
PRTY6 =	000300	273#	229 273#
PRTY7 =	000340	273#	
PS =	177776	273#	
PSW =	177776	273#	
PUSH =	005746	273#	
PUSH2 =	024646	273#	
RANDS =	000001R	273#	
RANBL =	000005R	241#	
RENABL =	020000	217#	421
RESTART =	000342R	266#	333# 550 587
RESYNC=	000002	217#	941
RES1 =	000056R	249#	
RES2 =	000060R	250#	
RSTRT =	000000R	265#	
RXBL0 =	000000	291#	1012# 1085
RXBL10 =	006206R	759#	
RXBL11 =	006302R	759#	1086
RXBL110 =	007206R	789#	1093
RXBL111 =	007302R	790#	1094
RXBL112 =	007376R	791#	1095
RXBL113 =	007412R	792#	1096
RXBL114 =	007915R	804#	1097
RXBL115 =	007712R	805#	1098

DVAB DEC/X11 SYSTEM EXERCISER MODULE  
XDVAB0.P11 12-OCT-78 11:57

MACY11 30A(1052) 12-OCT-78 16:34 PAGE 34  
CROSS REFERENCE TABLE -- USER SYMBOLS

SEQ 0032

RXB8116	01 0006R	806#	1099
RXB8117	01 01102R	807#	1100
RXB8118	01 01117R	809#	1087
RXB8119	01 00617R	810#	1088
RXB8120	01 00657R	811#	1089
RXB8121	01 00667R	812#	1090
RXB8122	01 00676R	813#	1091
RXB8123	01 00706R	814#	1092
RXB8210	01 01226R	821#	1066
RXB8211	01 01322R	822#	1103
RXB8210	01 01349R	823#	1110
RXB8212	01 01354R	824#	1111
RXB8213	01 01355R	825#	1112
RXB8214	01 01356R	826#	1113
RXB8215	01 01752R	827#	1114
RXB8216	01 02046R	828#	1115
RXB8217	01 02142R	829#	1116
RXB8220	01 02149R	830#	1117
RXB8224	01 02626R	831#	1104
RXB8225	01 02627R	832#	1105
RXB8226	01 01102R	833#	1107
RXB827	01 01122R	840#	1109
RXB830	01 03206R	101#	1072
RXB831	01 03307R	101#	1120
RXB8310	01 04159R	102#	1128
RXB8311	01 04236R	102#	1129
RXB8312	01 04336R	102#	1128
RXB8313	01 04432R	102#	1130
RXB8314	01 04526R	103#	1131
RXB8315	01 04622R	103#	1132
RXB8316	01 04716R	103#	1133
RXB8317	01 05012R	103#	1134
RXB8320	01 05375R	104#	1125
RXB8324	01 05666R	102#	1124
RXB8325	01 05667R	102#	1124
RXB8326	01 05756R	102#	1125
RXB8327	01 04052R	102#	1126
RXB840	01 05106R	103#	1078
RXB841	01 05202R	103#	1137
RXB8410	01 06046R	104#	1144
RXB8411	01 06132R	104#	1145
RXB8412	01 06132R	104#	1146
RXB8413	01 06133R	104#	1147
RXB8414	01 06422R	105#	1148
RXB8415	01 06522R	105#	1149
RXB8416	01 06616R	105#	1150
RXB8417	01 06712R	105#	1151
RXB8420	01 06757R	105#	1150
RXB8444	01 05466R	104#	1140
RXB845	01 05565R	104#	1141
RXB846	01 05565R	104#	1142
RXB847	01 05565R	104#	1143

DVAB DEC/X11 SYSTEM EXERCISER MODULE  
XDVAB0.P11 12-OCT-78 11:57

MACY11 30A(1052) 12-OCT-78 16:34 PAGE 35  
CROSS REFERENCE TABLE -- USER SYMBOLS

SEQ 0033

DVAB DEC/X11 SYSTEM EXERCISER MODULE  
 MACY11 30A(1052) 12-OCT-78 16:34 PAGE 36  
 CROSS REFERENCE TABLE -- USER SYMBOLS

SEQ 0034

WASADR	000104R	261#	593*												
WDFR	000116R	268#	330*												
WDTO	000114R	267#	329*												
XCNT	000306R	343#	405*	411*	414*										
XFLUNG	000703R	245#													
XIUNG	002103R	651#	613*												
XXX1	017034R	551#	1063*												
XXX2	017044R	553#	1069*												
XXX3	017064R	555#	1075*												
XXX4	017104R	557#	1081*												
X0	= 000100	1042#													
X1	= 000001	1035#													
X2	= 000072	1035#													
X3	= 000073	1035#													
X4	= 000074	1035#													
X5	= 000075	1035#													
X6	= 000076	1035#													
X7	= 000077	1035#													
\$DVSCR	= 000000	758#	1060#	1066#	1972#	1078#	773#	774#	775#	776#	777#	789#	790#	791#	
\$FIB	= 000005	792#	793#	804#	805#	806#	804#	805#	806#	807#	823#	824#	825#	826#	
\$QFIG	= 000005	838#	839#	840#	841#	852#	853#	854#	855#	856#	868#	869#	870#	871#	
\$ZFIG	= 000000	872#	1015#	1016#	1017#	1018#	1019#	1020#	1021#	1022#	1023#	1024#	1025#	1026#	
\$ZX	= 000004 = 017306R	1020#	1028#	1029#	1030#	1031#	1032#	1033#	1034#	1035#	1036#	1037#	1038#	1039#	
		1030#	1031#	1032#	1033#	1034#	1035#	1036#	1037#	1038#	1039#	1040#	1041#	1042#	
		1035#	1036#	1037#	1038#	1039#	1040#	1041#	1042#	1043#	1044#	1045#	1046#	1047#	

- ABS. 000000 000  
 017306 001

DVAB DEC/X11 SYSTEM EXERCISER MODULE  
 MACY11 30A(1052) 12-OCT-78 16:34 PAGE 37  
 CROSS REFERENCE TABLE -- USER SYMBOLS

SEQ 0035

ERRORS DETECTED: 0  
 DEFAULT GLOBALS GENERATED: 0  
 XDVABO,XDVABO/SOL/CRF:SYM=DDXCOM,XDVABO  
 RUN-TIME: 3.008 SECONDS  
 RUN-TIME RATIO: 25/13=1.9  
 CORE USED: 8K (15 PAGES)