

KMCB DEC/X11 SYSTEM EXERCISER MODULE
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IDENTIFICATION

PRODUCT CODE: AC-E950B-MC
PRODUCT NAME: CXKMCB0 KMC-11 MODULE
PRODUCT DATE: SEPTEMBER 1978
MAINTAINER: DEC/X11 SUPPORT GROUP

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MAIN DEC CHANGE NOTICE
MAY BE REQUIRED FOR
PROGRAM TO OPERATE

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1. ABSTRACT

KMC IS AN IOMOD THAT EXERCISES UP TO AND INCLUDING TWO CONSECUTIVELY ADDRESSED AND CONSECUTIVELY VECTORED KMC11 SYNCHRONOUS INTERFACES. IT USES NO LINE UNIT FOR RECEIVING AND TRANSMITTING DATA. DATA BUFFERS ARE TRANSMITTED AND RECEIVED FROM PDP11 MEMORY TO KMC11 & VICE VERSA. THE RECEIVER AND TRANSMITTER ISR'S ARE PERFORMED AT LEVEL 0 (PIRQ). DATA CHECKING IS PERFORMED AT LEVEL 0 AND DONE OUTSIDE THE ISRS.

2. REQUIREMENTS

HARDWARE: AT LEAST 1 KMC11

STORAGE:: KMC REQUIRES:

1. DECIMAL WORDS: 2235
2. OCTAL WORDS: 04273
3. OCTAL BYTES: 10566

3. PASS DEFINITION

ONE PASS OF THE KMAA MODULE CONSISTS OF TRANSMITTING AND RECEIVING 1 BUFFERS OF 2-512 CHARACTERS 200 TIMES FOR EACH SELECTED DEVICE.

4. EXECUTION TIME

RUNNING ALONE ON AN 11/45 ONE PASS TAKES APPROXIMATELY ONE MINUTE. IF RUN AT XX BAUD AND XX BUFFER SIZE

5. CONFIGURATION PARAMETERS.

DEFAULT PARAMETERS:

ADDR: 1 VECTOR: 1, BR1: 5, BR2: 5, DVID1: 1, SR1:0
KMAA WILL RUN UP TO TWO CONSECUTIVELY ADDRESSED AND CONSECUTIVELY VECTORED KMC11'S. THERE ARE THREE PARAMETERS WHICH CAN BE CONTROLLED IN THIS MODULE.

1.NPR RATE:- THIS CONTROLS THE RATE OF NPR'S OCCURRING FROM KMC11'S. USING MODIFY COMMAND THIS CAN BE SET TO SPECIFIC VALUE. THE ADDRESS OF THIS PARAMETER IS 226 IN KMAA MODULE.
THIS PARAMETER CAN BE CHOSEN IN TWO DIFFERENT WAYS.
I. WHEN SR1<BIT15>:=1 THEN WHATEVER IN LOC 222[RTMULV] IS LOADED INTO NPROTE LOCATION[226].
II. WHEN SR1<BIT14>:=1 THEN NPROTE BECOMES EQUAL TO RTMULV MULTIPLIED BY SR1<6:11>.
DEFAULT:: 10000(OCTAL)

RANGE:: 12-7888 USEC/NPR.
2.NPR/BR RATE:- CONTROLS THE RATE OF NPR'S PER INTERRUPT.
LIKE NPR RATE THIS PARAMETER CAN ALSO BE CHOSEN IN TWO DIFFERENT WAYS. IN THIS CASE SZMULV[220] WILL BE USED IN PLACE OF RTMULV. AND SR1<0:5> CONTENTS WILL BE USED TO MULTIPLY.
PARAMETERS :: RSIZE(202), XSIZE(204)

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DEFAULT:: 377(OCTAL)
RANGE:: 1-377(OCTAL)
CAN BE SET USING MOD COMMAND
3.DIRECTION OF NPR'S:- THE DIRECTION OF NPR'S
CAN BE CHOSEN SETTING PROPER BIT IN SR1.
EXPLANATION FOLLOWS.

6. DEVICE/OPTION SETUP

SR1(SWITCH REGISTER CONTENTS) OPTION.
BIT15:1 I.E SR1:10XXXX NPRATE:= RTMULV * SR1 <6:11>
NPR/BR:= SZMULV * SR1 <0:5>
BIT14:1 I.E SR1:04XXXX NPRATE:= RTMULV
NPR/BR:= SZMULV
BIT 15 & 14:0 DEFAULT RATE.
BIT 15 & 14:1 ILLEGAL.
BIT13:1 I.E SR1:X2XXXX XMIT ONLY.
BIT12:1 I.E SR1:X1XXXX RECEIVE ONLY
BIT13 & 12:0 DEFAULT.
BIT13 & 12:1 ILLEGAL.
SR1 BITS 6:11 NPR RATE MULTIPLIER.
SR1 BITS 5:0 NPR/PR RATE MULTIPLIER.

NOTE: SR1 CAN BE SET UP AT CONFIGURATION TIME OR
AT RUN TIME WITH A MOD COMMAND.

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7. MODULE OPERATION

1. LOAD SOFTWARE POINTERS IN LINK TABLE. SET PARAMETERS.
 2. LOAD VECTORS AND PRIORITIES IN TABLE
 3. LOAD MICRO-CODE, VERIFY IT AND INITIATE IT.
 4. ENABLE SELECTED DEVICES.
 5. SCAN FOR ALL DEVICES TO FINISH
 6. IF NOT DONE GO TO 4.
IF HUNG REPORT SO AND DROP HUNG DEVICE.
 7. CHECK DATA FOR ALL DEVICES SELECTED.
 8. DECREMENT ITERATION COUNT
 9. IF NOT = 0 GO TO 1
 10. SIGNAL ENDPASS.
- IISR: INPUT INTERRUPT SERVICE ROUTINE.
11. GET INTERRUPTING KMCSCR.
 13. IF RECEIVE BA/CC WAS REQUESTED, LOAD REC BA/CC.
 14. IF XMIT BA/CC WAS REQUESTED, LOAD XMIT BA/CC.
 15. RTI
- OISR: OUTPUT INTERRUPT SERVICE ROUTINE.
01. GET INTERRUPTING KMCSCR
 02. IF ERROR, REPORT IT AND EXIT.
 03. IF XMIT DONE OR REC DONE, SET APPROPRIATE BITS IN THE ENDPASS FLAG FOR THE DEVICE.
 04. RTI

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9. NON-STANDARD PRINTOUTS

IF THE MODULE "HANGS" IN WHICH NOT ALL SELECTED DEVICES HAVE FINISHED, THEN A "HUNG" MESSAGE IS PRINTED OUT. CHECK THE ENDPASS FLAGS FOR EACH SELECTED DEVICE IN THE LINK TABLE TO DETERMINE WHICH DEVICE FAILED TO FINISH AND HOW FAR IT GOT.

FOR EXAMPLE:

THE TWO ENDPASS FLAGS ARE LOCATED IN THE LINK TABLE (INTLNK) AT THE FOLLOWING LOCATIONS.

XX11:

XX21:

ONLY BITS 0 THRU 3 ARE USED AND ARE DEFINED AS FOLLOWS:

BIT1 = 1 RECEIVE BA/CC'S WERE LOADED.

BIT0 = 1 TRANSMIT BA/CC'S WERE LOADED.

BIT2 = 1 TRANSMIT DONE'S WERE RECEIVED.

BIT3 = 1 RECEIVE DONE'S WERE RECEIVED.

A CORRECT END PASS FLAG = 17, WHEN THE ENDPASS FLAGS = 17 FOR THE SELECTED DEVICES, THE DATA IS CHECKED. IF A "HUNG" MESSAGE IS TYPED IT IS BECAUSE ONE OR BOTH DEVICES DID NOT FINISH. TO FIND WHICH ONE, CHECK THE END PASS FLAGS, ANY THAT ARE NOT EQUAL TO 17 ARE THE HUNG DEVICES. CHECK WHICH BITS OF THE ENDPASS FLAG ARE CLEAR TO SEE WHAT IT WAS TRYING TO DO.

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178 000000 000000
180 000000
181 ;      MODULE 140000,KMCB / ;MODULE NAME
182 ;      TITLE KMCB DEC/X11 SYSTEM EXERCISER MODULE
183 ;      DDXCDM VERSION 6 23-MAY-78
184 ;      LIST RIN
185 000000
186 000000 046513 041103 040
187 000005 000
188 000006 000001
189 000010 000001
190 000015 000
191 000014 000001
192 000016 000000
193 000020 000000
194 000022 000000
195 000024 000000
196 000026 140000
197 000030 000272
198 000032 000224
199 000034 000000
200 000036 000200
201 000040 000000
202 000044 000000
203 000045 000000
204 000046 000000
205 000048 000000
206 000049 000000
207 000050 000000
208 000052 000000
209 000054 000000
210 000056 000000
211 000058 000000
212 000060 000000
213 000062 000000
214 000064 000000
215 000066 000000
216 000070 000000
217 000072 000000
218 000074 000000
219 000076 000000
220 000100 000000
221 000102 000000
222 000102 000000
223 000104 000000
224 000104 000000
225 000106 000000
226 000110 000000
227 000112 000322
228 000114 000000
229 000116 000000
230 000120 000000
231 000122 000136
232 000224
233 000224

***** BEGIN *****

BEGIN: IOMOD<KMCB>,1,1,5,5,0,200,136
MODNAME: KMCB / ;MODULE NAME
XFLAG: BYTE OPEN ;USED TO KEEP TRACK OF WBUFF USAGE
ADDR: 1+0 ;1ST DEVICE ADDR.
VECTOR: 1+0 ;1ST DEVICE VECTOR.
BR: BYTE PRTY5+0 ;2ND BR LEVEL.
DVID1: 041 ;DEVICE INDICATOR 1.
SR1: OPEN ;SWITCH REGISTER 1
SR2: OPEN ;SWITCH REGISTER 2
SR3: OPEN ;SWITCH REGISTER 3
SR4: OPEN ;SWITCH REGISTER 4
*****
STAT: 140000 ;STATUS WORD
INIT: START ;MODULE START ADDR.
SPPOINT: MODSP ;MODULE STACK POINTER.
PASCNT: 0 ;PASS COUNTER.
ICOUNT: 200 ;# OF ITERATIONS PER PASS=200
LOC TO COUNT ITERATIONS
SCNT: 0 ;LOC TO SAVE TOTAL SOFT ERRORS
HRNCNT: 0 ;LOC TO SAVE TOTAL HARD ERRORS
SOPPAS: 0 ;LOC TO SAVE SOFT ERRORS PER PASS
HRDPAS: 0 ;LOC TO SAVE HARD ERRORS PER PASS
SVSCNT: 0 ;# OF SYS ERRORS ACCUMULATED
RANNUM: 0 ;HOLDS RANDOM # WHEN RAND MACRO IS CALLED
CONFIG: ;RESERVED FOR MONITOR USE
RES1: 0 ;RESERVED FOR MONITOR USE
RES2: 0 ;RESERVED FOR MONITOR USE
SVR0: OPEN ;LOC TO SAVE R0.
SVR1: OPEN ;LOC TO SAVE R1.
SVR2: OPEN ;LOC TO SAVE R2.
SVR3: OPEN ;LOC TO SAVE R3.
SVR4: OPEN ;LOC TO SAVE R4.
SVR5: OPEN ;LOC TO SAVE R5.
SVR6: OPEN ;LOC TO SAVE R6.
CSRA: OPEN ;ADDR OF CURRENT CSP.
SBADR: ;ADDR OF GOOD DATA, OR
ACSR: OPEN ;CONTENTS OF CSR.
WASADR: ;ADDR OF BAD DATA, OR
ASTATE: OPEN ;STATUS REG CONTENTS.
ERRTY: ;TYPE OF ERROR.
AS1: OPEN ;EXPECTED DATA.
AWAS: OPEN ;ACTUAL DATA.
RSTRT: RESTRT ;RESTART ADDRESS AFTER END OF PASS
WDTO: OPEN ;WORDS TO MEMORY PER ITERATION
WDFR: OPEN ;WORDS FROM MEMORY PER ITERATION
INTK: OPEN ;# OF INTERRUPTS PER ITERATION
IDNUM: 136 ;MODULE IDENTIFICATION NUMBER=136
MODSP: ;
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244 000224 000000
245 000226 000000
246 000230 000000
247 000234 000007
248 000234 000000
249 000236 000000
250 000240 000377
251 000242 000377
252 000244 000000
253 000246 000000
254 000249 000000
255 000259 000000
256 000254 000000
257 000256 000002
258 000260 000100
259 000262 000000
260 000263 000
261 000264 010000
262 000265 000000
263 000266 000000
264 000267 000000
265 000270 000000
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281 000272 005067 177772
282 000316 001004 177774 177510
283 000304 001004 177774 177510
284 000306 016767 177502 177714
285 000314 001002
286 000316
287 000316 104410 000000
288 000322 005067 177708
289 000326 012700 005276

***** BEGIN *****

VARIABLES FOR KMC11
*****
DLV1: WORD 0 ;DEVICE 1 DELAY COUNT.
DLV2: WORD 0 ;DEVICE 2 DELAY COUNT.
SELECT: WORD 0 ;TEMPORARY SELECTED DEVICE'S
FLAG: WORD 0 ;FIRST PASS FLAG.
F1: WORD 0 ;FIRST PASS FLAG.
MASK: WORD 0 ;TEMPORARY VARIABLE.
RSTZ: WORD 377 ;RECEIVE BUFFER SIZE.
XSIZE: WORD 377 ;TRANSMIT BUFFER SIZE.
VA: WORD 0 ;VIRTUAL ADDRESS.
PA: WORD 0 ;PHYSICAL ADDRESS.
BX: WORD 0 ;TRANSMIT ADDRESS.
SARO: WORD 0 ;SAVE LOC FOR R0.
SARI: WORD 0 ;SAVE LOC FOR R1.
SZMULV: WORD 2 ;LOCATION USED TO CALCULATE NPR/BR RATE.
RTMULV: WORD 100 ;LOCATION USED TO CALCULATE NRR RATE.
TERM: 0 ;TERMINATING VALUE.
RCOLY: BYTE 0 ;XMIT ONLY FLAG=SPAD<16>
XMDLY: WORD 0 ;RCV ONLY FLAG=SPAD <15>
NPRT: WORD 10000 ;LOCATION FOR NPP RATE.
TEMP: WORD 0 ;TEMPORARY VARIABLE.
FLAG: WORD 0 ;USED TO LOAD MAIN MEMORY.
*****
BEGIN THE DEC./X11 MODULE FOR THE KMC11
*****
START: CLR FLAG ;SET FOR FIRST PASS.
BNE #CC3>,DVID1 ;DROP MODULE IF OTHER THEN
BNE DROP ;FIRST 2 DEVICES ARE SELECTED
MOV DVID1,SELECT ;SELECT=ACTIVE DEVICES.
BNE RESTRT ;DROP MODULE IF NO ACTIVE DEVICES.
DROP: ENDS,BEGIN
RESTRT: CLR FIRST ;INITIALIZE THE FIRST TIME FLAG.
LOOP: MOV #RBUF11,RO ;GET SET TO CLEAR BUFFERS.
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290 000332 005020 ;CLEAR THE LOCATION IN BUFFER.
291 000334 020027 007276* ;END OF BUFFERS?
292 000340 003774 ;NO GO CLEAR THE NEXT...
293 000342 001760 177662 ;SELECT,RO ;PROB MODULE IS NO DEVICES SELECTED.
294 000350 005767 177714 ;TST FLAG ;IS IT FIRST PASS??
295 000354 001173 ;BNE SETUP1 ;NO, THEN DONT LOAD THE MICRO-CODE...
296 000356 105067 177700 ;***** THIS PART SETS UP PARAMETERS COMMON TO ALL
297 000362 105067 177675 DEVICES LIKE: 1)NPR/BR RATE.
298 000366 005067 003642 2)NPR RATE.
299 000372 005067 003660 3)DIRECTION OF NPR.
300 000376 005767 177414 ;***** INITIATES MICRO-CODE LOADED IN KMC11.
301 000402 1050416 040000 177404 ;***** INITIALIZE THE FLAGS.
302 000404 001760 040000 177404 CLR RCOLY ;INITIALIZE THE END PASS FLAG.
303 000414 016767 177636 177616 CLR XX11 ;INITIALIZE THE END PASS FLAG.
304 000422 016767 177630 177612 TST SR1 ;IS IT MULTIPLY OPTION?
305 000430 016767 177624 177626 BIT 2$ ;YES, GO SET UP BFSIZE & NPPR RATE.
306 000436 00455 040000 177350 TST #BIT14,SR1 ;DO TAKE DEFAULT?
307 000440 032767 040000 177350 2$: MOV RCOLY ;SET UP BFSIZE & NPPR RATE.
308 000450 016767 177342 177336 CLR XX21 ;SET UP NPPR RATE.
309 000454 016702 177336 CLR R2 ;SET UP NPPR RATE MULTIPLICAND.
310 000460 006202 ASR R2 ;SET UP NPPR RATE MULTIPLICAND.
311 000462 006202 ASR R2 ;SET UP NPPR RATE MULTIPLICAND.
312 000464 006202 ASR R2 ;SET UP NPPR RATE MULTIPLICAND.
313 000466 006202 ASR R2 ;SET UP NPPR RATE MULTIPLICAND.
314 000472 006202 ASR R2 ;SET UP NPPR RATE MULTIPLICAND.
315 000474 042701 177700 BIC H177700,R1 ;CLEAR THE EXTRA BITS.
316 000500 042702 177700 BIC H177700,R2 ;CLEAR UNNECESSARY BITS.
317 000504 010103 MOV R1,R3 ;GET THE MULTIPLICAND...
318 000506 0016704 177544 MOV R5,MULPV,R4 ;GET THE MULTIPLIER.
319 000512 004567 003466 JSR R5,MULPV,R4 ;MULTIPLY & RETRIEVE THE RESULT IN TEMP.
320 000517 000377 177542 CMP #377,TEMP ;CHECK IF WITHIN LIMIT...
321 000524 006206 BLE R5 ;DO TAKE THE DEFAULT...
322 000526 016767 177534 177504 MOV TEMP,RSIZE ;RSIZE=NPR/BR RATE.
323 000534 016767 177526 177500 MOV TEMP,XSIZE ;XSIZE=NPR/BR RATE.
324 000542 010203 MOV R2,R3 ;GET THE MULTIPLICAND...
325 000544 016704 177510 177510 MOV RTMULV,R4 ;GET THE MULTIPLIER...
326 000550 003430 003430 JSR R5,MULPV,R4 ;MULTIPLY & PUT RESULT IN TEMP.
327 000552 006204 010000 177504 CMP #16000,TEMP ;CHECK IF WITHIN LIMIT...
328 000554 016767 177476 177472 BLE R5 ;DO TAKE THE DEFAULT...
329 000564 032767 010000 177216 MOV TEMP,NPRT ;NPRT=NPR/TIME.
330 000572 032767 010000 177216 3$: BIT #BIT12,SR1 ;IS IT RECEIVE ONLY?

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346 000600 001413 ;NO, CHECK IF XMITR ONLY.
347 000602 032767 020000 177206 BEQ 5$ ;IS XMITR ONLY ALSO SET?
348 000610 001404 BEQ 6$ ;NO, SETUP FOR RECEIVE ONLY
349 000612 001404 ;***** THIS PART SETS UP THE PROGRAM CONTROL
350 104403 000000 002016* 4$: MSGNS,BEGIN,SOFT1 ;ASCII MESSAGE CALL WITH COMMON HEADER
351 000620 000636 BR,DROP ;DROP THE MODULE.
352 000622 105167 177434 6$: COMB RCOLV ;SET RECEIVE ONLY FLAG.
353 000626 008406 BR,7$ ;GO SET UP OTHER VARIABLES.
354 000630 032767 020000 177160 5$: BIT #BIT13,SR1 ;IS XMITR ONLY SET?
355 000636 001402 BEQ 7$ ;NO DO BOTH.
356 000640 016167 177417 COMB XCOLY ;SET XMITR ONLY FLAG.
357 000651 006203 MOV R1,RI ;GET THE FIRST DEVICE ADDRESS.
358 000654 005067 177406 7$: MOV TEMP,R3 ;GET THE DEVICE SELECTED.
359 000660 006203 CLR TEMP ;CLEAR THE RETRY COUNT.
360 000662 103404 ASR R3 ;ANY DEVICE REMAINS.
361 000664 001427 BCS 9$ ;YES, GO AND LOAD MICRO-CODE INTO IT.
362 000665 002700 000010 BEQ SETUP1 ;SETUP THE REST.
363 000674 004767 002456 ADD #10,R1 ;UPDATE THE R1.
364 000676 004767 002546 JSR PC,WCRAM ;WRITE THE CRAM WITH MICRO-CODE.
365 000678 004767 002714 JSR PC,WMEMORY ;AND LOAD LOWER HALF OF MAIN MEMORY WITH XMITR BUFFER.
366 000680 000710 005767 177322 JSR PC,VERITFY ;VERIFY MICRO-CODE & XMITR DATA.
367 000682 001767 001764 TST MASK ;IS THERE ANY ERROR.
368 000684 001767 000010 10$: BEQ 10$ ;NO GO INITIATE IT.
369 000686 001767 002456 MSGNS,BEGIN,SOFT2 ;INCREMENT RETRY COUNT.
370 000688 004403 000000 002022* INC R1 ;INCREMENT MESSAGE CALL WITH COMMON HEADER.
371 000690 005267 107336 000003 177330 CMP #3,TEMP ;IS IT TRIED THREE TIMES?
372 000692 005267 107336 000003 177330 BGT 10$ ;NO TRY AGAIN!
373 000694 005356 001670 177352 JMB DROP ;DROP THE MODULE.
374 000740 000167 177352 ;***** THIS PART SETS UP THE PROGRAM CONTROL
375 000742 016703 177836 VARIANCES FOR THE DEVICES AND THE
376 000744 016703 004222* MODULE. EX. QUEUES....,ETC.
377 000750 016703 004222* ;***** SETUP1: MOV ADDR,R1 ;SET THE DEVICE CSR.
378 000754 012703 004222* MOV VECTOR,R2 ;SET THE VECTOR.
379 000756 016767 177246 003246 MOV #INTLINK,R3 ;SET THE POINTER TO INTERRUPT LINKAGE.
380 000760 016767 177246 003246 MOV FLAGH,XX11 ;SET THE END PASS FLAG FOR DEV#1.
381 000766 016767 177240 003262 MOV FLAGB,XX21 ;SET THE END PASS FLAG FOR DEV#2.
382 000774 012767 007276 006354 MOV #IRINQ,INQIN ;SET UP ALL QUEUES & ITS POINTERS.
383 001000 012767 007276 006354 MOV #IRINQ,INQOUT ;SET UP ALL QUEUES & ITS POINTERS.
384 001002 012767 007276 006346 MOV #IRROUTE,OUTQIN ;SET UP ALL QUEUES & ITS POINTERS.
385 001016 012767 007276 006346 MOV #IRROUTE,OUTQOUT ;SET UP ALL QUEUES & ITS POINTERS.
386 001024 012767 007336 006334 MOV #REGQ,REGQI ;SET UP ALL QUEUES & ITS POINTERS.
387 001032 012767 007336 006330 MOV #REGQ,REGQO ;SET UP ALL QUEUES & ITS POINTERS.
388 001040 016700 177164 SELECT, R0 ;R0- DEVICES SELECTED.
389 001044 006200 ASR R0 ;ANY DEVICE ACTIVE?
390 001052 002446 BCS 4$ ;YES, GO SET IT UP.
391 001054 002446 SETUP2 ;ALL DONE?
392 001056 025702 000010 ADD #10,R1 ;NO UPDATE CSR.
393 001062 025703 000022 ADD #10,R2 ;UPDATE VECTOR.
394 001062 025703 000022 ADD #22,R3 ;UPDATE LINK.

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402 001066 000766          88      25      ;GO SET UP FOR NEXT DEVICE.
403 001070 010313 176714 000002  4$:    MOV     R3,(R2)      ;LOAD INTERRUPT VECTOR ADDRESS.
404 001072 116763 176714 000002  4$:    MOVB   R3,(R2)      ;SET THE PRIORITY.
405 001100 010362 000004 176670 000006  4$:    MOV     R1,(R3)      ;SET THE DEVICE CSR.
406 001104 010362 000004          ADD    #4,(R2)      ;LOAD XMTR.
407 001110 062762 000004 000004 176670 000006  4$:    MOVB   R1,(R2)      ;INTERRUPT VECTOR.
408 001116 116762 176670 000006 176670 000006  4$:    CLR     R2,(R3)      ;CLEAR END PASS FLAG FOR DEV#1.
409 001134 005063 000005 000005 177123 000006 176670 000006  4$:    CLRB   R2,(R3)      ;CLEAR END RECEIVE BUFFER OFFSET.
410 001134 105767 177123          TSTB  XMOLY      ;IS XMIT ONLY FLAG SET?
411 001140 001404 000012 000012 177123 000006 176670 000006  4$:    BEQ    5$        ;NO, CHECK FOR RECEIVE ONLY FLAG.
412 001142 012763 000012 000012 177123 000006 176670 000006  4$:    MOV     #BIT11BIT3,12(R3) ;SET XMTR BITS IN ENDPASS FLAG.
413 001150 000740          BR    3$        ;END.
414 001150 000740          BR    3$        ;END.
415 001152 105767 177104          5$:    TSTB  RCDLY      ;IS RECEIVE ONLY FLAG SET?
416 001156 001733 000005 000005 177104 000005 177104 000005  5$:    BEQ    3$        ;NO, DON'T SET ANY BITS IN ENDPASS FLAG.
417 001162 016700 176614 000005 177104 000005 177104 000005  5$:    MOV     #DDTR,R1      ;CSR ADDRESS.
418 001172 016700 176700 177032          SETUP2:  MOV     R2,SELECT,R0 ;NO SELECT.
419 001172 016700 176700 177032          1$:    ASR     R0          ;ANY DEVICE ACTIVE?
420 001176 005200 103404          BCS    3$        ;YES, GO & INITIATE THE DEVICE DEC/X MODULE.
421 001200 001454 000010 000010 103404 000010 103404 000010 176670 000006  4$:    BEQ    SCAN      ;ALL DONE, GO AND SCAN.
422 001202 001454 000010 000010 103404 000010 103404 000010 176670 000006  4$:    ADD    #10,R1      ;UPDATE CSR ADDRESS.
423 001204 001454 000010 000010 103404 000010 103404 000010 176670 000006  4$:    BR    1$        ;CONTINUE.
424 001212 005200 105767 177016          25$:   TST    FIRST,9$      ;IS IT FIRST PASS???
425 001216 001013 040000          BEQ    3$        ;NO, THEN DON'T INITIALIZE DEVICE.
426 001220 012711 040000          MOV     #BIT14,(R1) ;MASTER CLEAR FIRST TIME ONLY.
427 001224 005011 176774 000006 176774 000006 176774 000006  4$:    CLR     (R1)        ;INITIALIZE THE UNIBUS CSR'S.
428 001245 005061 000002 176774 000002 176774 000002 176774 000006  4$:    CLR     (R2)        ;INITIALIZE THE UNIBUS CSR'S.
429 001252 005061 000004 176774 000004 176774 000004 176774 000006  4$:    CLR     (R3)        ;INITIALIZE THE UNIBUS CSR'S.
430 001252 005061 000006 176774 000006 176774 000006 176774 000006  4$:    CLR     (R4)        ;INITIALIZE THE UNIBUS CSR'S.
431 001252 005061 100000          MOV     #BIT15,(R1) ;SET THE RUN BIT...
432 001252 005061 100000          TSTB  R25         ;IS RD I SET?
433 001246 105711          9$:    TST    #BIT15,(R1) ;YES, THEN START DECX...
434 001250 100415 176716          BEQ    9$        ;SAVE REGISTER R0...
435 001252 010067 176774 000006 176774 000006 176774 000006  4$:    MOV     R0,SARO      ;SAVE REGISTER R1... NO MONITOR.
436 001256 010167 176772 000006 176772 000006 176772 000006  4$:    BREAKS,BEGIN    ;THEN CONTINUE AT NEXT INSTRUCTION.
437 001262 010407 000006 176754 000006 176754 000006 176754 000006  4$:    MOV     R0,P0        ;RESTORE REGISTER R0...
438 001262 010407 000006 176754 000006 176754 000006 176754 000006  4$:    SARO,P0        ;RESTORE REGISTER R1...
439 001276 016700 176752 000006 176752 000006 176752 000006  4$:    SAR1,R1        ;RESTORE REGISTER R1...
440 001302 000761 176752 000006 176752 000006 176752 000006  4$:    BR    9$        ;WAIT FOR RD I TO SET...
441 001304 052761 000020 000002 176620 000002 176620 000002 176620 000006  4$:    BIS    #020,(R1)  ;SET IEI.
442 001312 105767 176744 000002 176744 000002 176744 000002 176744 000006  4$:    TSTB  RCDLY      ;IS RECEIVE ONLY SFT?
443 001320 001013 040000 000002 176744 000002 176744 000002 176744 000006  4$:    BEQ    6$        ;ONLY BRANCH IF NO.
444 001320 001013 040000 000002 176744 000002 176744 000002 176744 000006  4$:    BIS    #020,(R1)  ;CONTINUE, RECEIVE PA/CC I.
445 001324 005272 000020 000020 176744 000002 176744 000002 176744 000006  4$:    BEQ    6$        ;CONTINUE.
446 001324 005272 000020 000020 176744 000002 176744 000002 176744 000006  4$:    BIS    #024,(R1)  ;SET IEI, XMIT BA/CC I.
447 001326 005271 000024 000024 176744 000002 176744 000002 176744 000006  4$:    BEQ    6$        ;CONTINUE.
448 001332 000724 176744 000002 176744 000002 176744 000002 176744 000006  4$:    BR    2$        ;CONTINUE.

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***** THIS ROUTINE SCANS ALL DEVICES END PASS FLAGS
***** UNTIL ALL ACTIVE KMC11 DEVICES ARE FINISHED.
***** UPDATES PASS COUNT AND LOOPS TILL 200 PASSES.
***** ARE DONE, CHECKS DATA AND PRINTS OUT DATA ERRORS.
***** REPORTS END OF PASS.

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461 001334 012767 000003 176674          SCAN:  MOV     #3,MASK      ;SET BIT FOR ALL DEVICES.
462 001342 012767 000010 176654 176654 000006 176654 000006  4$:    MOV     #10,DLV1      ;SET DELAY COUNT.
463 001350 005067 176652 002652 176652 002652 176652 002652  4$:    CLR     DLV2        ;CLEAR DELAY COUNT.
464 001354 026767 176652 002652 176652 002652 176652 002652  4$:    CMP     FLAG,XX11    ;IS DEVICE 1 ALL DONE?
465 001362 001003 176644 000001 176644 000001 176644 000001 176644 000006  4$:    BNE    2$        ;NO, CHECK THE NEXT ONE.
466 001362 001003 176644 000001 176644 000001 176644 000001 176644 000006  4$:    OR     #BIT0,MASK    ;CLEAR THE DEVICE BIT.
467 001362 001003 176644 000001 176644 000001 176644 000001 176644 000006  4$:    OR     #BIT0,XX21    ;CLEAR THE DEVICE BIT.
468 001400 001003 176622 000002 176622 000002 176622 000002 176622 000006  4$:    RNE    3$        ;NO, GO AND WAIT.
469 001402 043767 000002 176622 000002 176622 000002 176622 000006  4$:    BIC     #BIT1,MASK    ;CLEAR THE DEVICE BIT.
470 001410 005267 176622 000002 176622 000002 176622 000002 176622 000006  4$:    TST    MASK        ;ARE ALL DEVICES DONE?
471 001414 001064 176622 000002 176622 000002 176622 000002 176622 000006  4$:    BNE    1$        ;NO, GO AND WAIT.
472 001416 012701 040423 176602 000006 176602 000006 176602 000006  4$:    MOV     #INTLNK+10,R1 ;R1 POINTS TO DEVICE CSR.
473 001422 015700 176602 000006 176602 000006 176602 000006  4$:    SELECT,R0      ;R0 CONTAINS BITS FOR ACTIVE DEVICES.
474 001422 015700 176602 000006 176602 000006 176602 000006  4$:    MOVTAB,R3      ;R3=POINTERS TO RECEIVER BUFFER.
475 001425 005266 176602 000006 176602 000006 176602 000006  4$:    ASR     R0          ;R0=DEVS TO TEST.
476 001434 103417 000002 176602 000002 176602 000002 176602 000006  4$:    BCS    4$        ;IS ANY DEVICE ACTIVE?
477 001436 001404 000002 176602 000002 176602 000002 176602 000006  4$:    BEQ    4$        ;YES, GO AND CHECK THE DATA.
478 001440 062701 000022 176602 000002 176602 000002 176602 000006  4$:    ADD    #22,R1      ;UPDATE R1 TO NEXT DEVICE CSR.
479 001444 005723 000002 176602 000002 176602 000002 176602 000006  4$:    TST    R3+$        ;UPDATE R3 TO NEXT BUFFER.
480 001456 005723 000002 176602 000002 176602 000002 176602 000006  4$:    SETBITS,BEGIN    ;CONTINUE.
481 001456 013467 177777 176656 176656 176656 176656 176656 176656 000006  4$:    MOV     #1,FIRST    ;SET PASS FLAG.
482 001456 013467 177777 176656 176656 176656 176656 176656 176656 000006  4$:    ENDITS$,BEGIN    ;SET FLAG FOR MICRO-CODE LOADED.
483 001464 104413 000000 176656 000000 176656 000000 176656 000000 176656 000006  4$:    SIGNAL END OF ITERATION.
484 001470 000167 176632          7$:    JMP    LOOP      ;MONITOR SHALL TEST END OF PASS.
485 001470 000167 176632          7$:    LOOP      ;LOOP THE MODULE.
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493 001474 105767 176563          8$:    TSTB  XMOLY      ;IS IT XMIT ONLY??
494 001502 001302 176563 000006 176563 000006 176563 000006  4$:    BEQ    8$        ;NO, THEN DON'T CHECK THE DATA...
495 001504 013204 176563 000006 176563 000006 176563 000006  4$:    MOV     #R2,R2        ;R2=POINTS TO RECEIVER BUFFER.
496 001504 013204 176563 000006 176563 000006 176563 000006  4$:    R2=POINTS TO RECEIVE DATA.
497 001506 015205 004266 176562 004266 176562 004266 176562 004266 000006  4$:    MOV     #R5,R5        ;R5=POINTS TO GOOD DATA.
498 001512 016767 176562 004266 176562 004266 176562 004266 176562 004266 000006  4$:    R5=SIZE TEMP    ;SET THE BUFFER SIZE.
499 001520 121514 176562 004266 176562 004266 176562 004266 176562 004266 000006  4$:    CMPB  (R5),(R4)    ;COMPARE DATA.
500 001522 001414 176562 004266 176562 004266 176562 004266 176562 004266 000006  4$:    BEQ    1$        ;GOOD, COMPARE NEXT CHAR.
501 001522 001414 176562 004266 176562 004266 176562 004266 176562 004266 000006  4$:    MOV     (R1),CSRA    ;LOAD GOOD ADDRESS.
502 001524 010567 176562 004266 176562 004266 176562 004266 176562 004266 000006  4$:    MOV     (R2),SADR    ;LOAD GOOD ADDRESS.
503 001524 010567 176562 004266 176562 004266 176562 004266 176562 004266 000006  4$:    MOV     (R2),WASADR  ;LOAD BAD ADDRESS.
504 001540 111567 176344 004266 176344 004266 176344 004266 176344 004266 000006  4$:    MOV     (R5),ASB    ;LOAD GOOD DATA.
505 001544 111467 176340 004266 176340 004266 176340 004266 176340 004266 000006  4$:    MOV     (R4),WAS    ;LOAD BAD DATA.
506 001550 104404 000000 176340 000000 176340 000000 176340 000000 176340 000006  4$:    DATERS,BEGIN    ;DATA ERROR!!!
507 001554 122524 176504 000000 176504 000000 176504 000000 176504 000006  4$:    CMPB  (R5)+,(R4)+ ;POP BUFFER DATA POINTERS.
508 001556 005367 176504 000000 176504 000000 176504 000000 176504 000006  4$:    DEC    TMP        ;ALL DONE?
509 001562 001356 176504 000000 176504 000000 176504 000000 176504 000006  4$:    BNE    1$        ;NO, DO THE NEXT.
510 001564 000725 176504 000000 176504 000000 176504 000000 176504 000006  4$:    BR    5$        ;GO, AND CHECK REMAINING DEVICE.
511 001566 000725 176504 000000 176504 000000 176504 000000 176504 000006  4$:    16$:   ;END.

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514 001566* 104407 000000*           BREAKS,BEGIN      ;TEMPORARY RETURN TO MONITOR.
515 001572* 104407 000000*           BREAKS,BEGIN      ;THEN CONTINUE AT NEXT INSTRUCTION.
516                                         ;THEN CONTINUE AT NEXT INSTRUCTION.
517 001576* 005367 176424             DEC    DLV2          ;DECREMENT DELAY COUNT FOR #2.
518 001603* 001402 177544             JMP    1$          ;WAIT FOR DEVICE TO COMPLETE.
519 001610* 005367 176410             B5Q    +6          ;DECREMENT DFLAY COUNT FOR #1.
520 001614* 001402 177532             DEC    DLV1          ;WAIT FOR DEVICE TO COMPLETE.
521 001616* 000167 177532             JMP    1$          ;ROUHUNG DEVICE BITS.
522 001622* 016700 176410             MOV    MASK, R0      ;DROPPING HUNG DEVICE.
523 001632* 000167 176376             BIC    R0,SELECT    ;WAS DEV#1 HUNGT?
524 001633* 000600 176376             RDR    R0          ;BRANCH IF NO.
525 001634* 103004 176376             BCC    17$          ;TYPE ERROR MESSAGE & DROP.
526 001636* 004367 000024             JSR    R3,XERR      ;POINTER TO DEV#1 CSR.
527 001642* 004322 176440             CSRC1           ;DEVICE NUMBER FOR TYPEOUT.
528 001644* 000001                   I                 ;WAS DEV#2 HUNG?
529 001649* 000000                   17$:  RDR    R0          ;DUMP THE NEW CSR.
530 001659* 004364 000010             BOC    19$          ;TYPE ERROR MESSAGE THEN DROP.
531 001656* 004254 000010             JSR    R3,XERR      ;POINTER TO DEV#2 CSR.
532 001660* 000002                   2                 ;DEVICE NUMBER FOR TYPEOUT.
533 001662* 000167 176440             18$:  JMP    LOOP          ;REFSTART MODULE.
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***** THIS SUBROUTINE DROPS THE HUNG DEVICE.
 ***** CLEARS OUT DEVICE SELECT BITS.
 ***** PRINTS THE EXTENDED ERROR MESSAGE.

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570 002016* 002110*           SOFT1: SOFT11
571 002022* 177777*           SOFT2: SOFT21
572 002024* 177777*           ;::: ERROR MESSAGES.....
573
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578 002026* 020045 046513 030503 XDROP1: .ASCIZ /* KMC11 DEVICE # /
579 002034* 020061 042504 044526
580 002042* 042503 021440 000040
581
582 002050* 051511 044040 047125 XDROP2: .ASCIZ /IS HUNG AND HAS BEEN DROPPED.../
583 002056* 020107 047101 020104
584 002064* 040510 020123 042502
585 002072* 047105 042040 047522
586 002100* 050120 042105 027056
587 002106* 000045
588
589 002110* 020045 051105 047522 SOFT11: .ASCIZ /* ERROR IN SETTING UP SWITCH REGISTER & RESTART /
590 002116* 052022 047111 051140
591 002124* 052105 044524 043516
592 002132* 052140 020120 053523
593 002140* 052111 044103 051040
594 002148* 042505 051545 042224
595 002156* 020153 051045 020124
596 002162* 052153 051101 020124
597 002170* 000000
598
599 002171* 051511 042440 051122 SOFT21: .ASCIZ /* ERROR IN LOADING MICRO-CODE WILL RETRY.../
600 002176* 051511 044440 020116
601 002182* 051511 044516 051166
602 002190* 020107 044515 051103
603 002202* 026517 047503 042504
604 002226* 053440 046111 020114
605 002234* 042522 051124 027131
606 002242* 027056 000045
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.EVEN
 ;::: EXTENDED ERROR PRINTOUT LOCATIONS...
 ;:::
 ;::: TABLE OF ADDRESSES FOR EXTERNAL ERROR PRINT OUTS...
 ;:::
 ETABLE: DLV1
 ;::: DLV2
 ;::: ESAV1
 ;::: ESAV2
 ;::: ESAV3
 ;:::
 FTABLE: ESAV1
 ;::: ESAV2

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626 002276* 002252*          E$AV3
627 002300* 002254*          E$AV4
628 002302* 177777*          -1
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634
635
636
637
638
639
640 002304* 010577 005046  INISR: MOV R5,@INQIN    ;SAVE LINK POINTER IN QUEUE.
641 002310* 062767 000002 005040 ADD #2,INQIN    ;UPDATE THE QUEUE POINTER.
642 002316* 022267 007316* 005032 CMP #PIRING+20,INQIN ;IS IT END OF QUEUE?
643 002324* 030303           BGT 1$      ;NO, PROCEED.
644 002326* 012605 007276* 005022 MOV #PIRING,INQIN ;RESET QUEUE POINTER.
645 002334* 012605           1$:   MOV (SP)+,R5    ;RESTORE RS,IE POP THE STACK.
646
647 002336* 000004 000000* 002344* IRQ$,BEGIN_2S ; QUEUE UP TO CONTINUE AT 2S AND RTI
648
649 002344* 017705 005010 2$:   MOV #INQOUT,R5    ;RESTORE THE LINE POINTER.
650 002350* 062767 000002 005002 ADD #2,INQOUT   ;UPDATE THE QUEUE POINTER.
651 002356* 022267 007316* 004774 CMP #PIRING+20,INQOUT ;IS IT END OF QUEUE?
652 002364* 013009* 007276* 004764 BGT 3$      ;NO, CONTINUE.
653 002374* 016501 000004 3$:   MOV #PIRING,INQOUT ;RESET THE QUEUE POINTER.
654 002400* 032711 000007 MOV (R5){PC}    ;LOAD CSR ADDRESS.
655 002404* 001410           BIT #7,(R1)    ;RECEIVE BA/CCI?.
656 002404* 032711 000004 BEQ RCV      ;BRANCH IF YES.
657 002405* 032711 000004 BIT #4,(R1)    ;XMITR BA/CCI?.
658 002412* 001033           BNE XMTR      ;BRANCH IF YES.
659 002412* 010400 000000* 002554* MSGNS,BEGIN_SF1 ;ASCII MESSAGE CALL WITH COMMON HEADER
660 002426* 016504 000010 EXIT$,BEGIN    ;EXIT TO MONITOR. MODULE WAIT FOR INTERRUPT.
661 002432* 011457 175606 RECVR: MOV #10(R5),R4   ;GET RECEIVE BUFFER POINTER.
662 002432* 011457 175606 MOV (R4),VA    ;VA=RECEIVE BUFFER VIRTUAL ADDRESS..
663 002436* 004767 001510 JSR PC,ABITS   ;GET PHYSICAL ADDRESS.
664 002442* 016761 175600 000004 MOV PA,(R1)    ;SEL4=PHYSICAL REUFF.
665 002450* 016761 175574 000006 MOV EA,(R1)    ;SEL6=EXTENDED BITS OF ADDRESS.
666 002456* 056761 175506 000006 BIS #SIZE,(R1)  ;LOAD RECEIVE CHARACTER COUNTS.
667 002464* 052705 000002 BIS #BIT1,(R5)  ;SET BIT1 IN END PASS FLAG FOR
668 002464* 052705 000006           BIS #BIT1,(R5)  ;SET BIT1 IN END PASS FLAG FOR
669 002472* 142711 000220 BICB #BIT7#BIT4,(R1) ;CLEAR RD1 TO REQUEST SERVICE FROM
670
671 002476* 104400 000000* KMC11 CLEAR IEI.
672 002500* 012707 004466* 175534 XMITR: MOV BXBUF,VA ;LOAD XMIT FRESH VIRTUAL ADDRESS.
673 002506* 012707 004466* 175534 JSD PC,ABITS ;GET SELECTED PHYSICAL ADDRESS.
674 002514* 016761 175538 000004 MOV PA,(R1)    ;LOAD PHYSICAL XMTR BUFFER.
675 002522* 016761 175522 000006 MOV EA,(R1)    ;LOAD EXTENDED ADDRESS BITS XMTR BUFFER.
676 002530* 056761 175506 000006 BIS #SIZE,(R1)  ;LOAD XMTR CHARACTER COUNT.
677 002536* 052705 000001 BIS #BIT0,(R5)  ;SET BIT0 IN END PASS FLAG FOR
678 002544* 142711 000200* 1$:   BICR #BIT7,(R1) ;XMTR BA/CCI LOADED.
679 002550* 104400 000000* EXIT$,BEGIN    ;CLEAR RD1 TO INDICATE START OF OPERATION
680 002554* 002500* ILINT: MESS2 ;EXIT TO MONITOR. MODULE WAIT FOR INTERRUPT.
681 002554* 002500*

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682 002556* 177777          -1
683 002560* 044445 050116 052125 MES2: .ASCIZ /*INPUT INTERRUPT WITH NO REQUEST SET UP!!!!*/
684 002566* 044440 052116 051105
685 002574* 052522 052120 053440
686 002602* 052111 020110 047516
687 002610* 051040 050505 042525
688 002616* 052123 051440 052105
689 002624* 052440 020520 020441
690 002632 000045
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702
703 002634* 010577 004523  OUTISR: MOV R5,OUTQIN    ;SAVE LINK POINTER IN QUEUE.
704 002640* 062767 000002 004504 ADD #2,OUTQIN    ;UPDATE THE QUEUE POINTER.
705 002646* 022267 007316* 004506 CMP #PIROUTQ+20,OUTQIN ;IS IT END OF QUEUE?
706 002654* 030303           BGT 1$      ;NO, CONTINUE.
707 002656* 012605 007316* 004476 MOV #PIROUTQ,OUTQIN ;RESET THE QUEUE POINTER.
708 002664* 012605           MOV (SP)+,R5    ;POP THE STACK POINTER
709
710 002666* 000004 000000* 002674* IRQ$,BEGIN_2S ; QUEUE UP TO CONTINUE AT 2S AND RTI
711
712 002674* 017705 004464 2$:   MOV #OUTQOUT,R5    ;RESTORE THE LINK POINTER FROM QUEUE.
713 002700* 062767 000002 004456 ADD #2,OUTQOUT   ;UPDATE THE QUEUE POINTER.
714 002706* 022267 007336* 004450 CMP #PIROUTQ+20,OUTQOUT ;IS IT END OF QUEUE?
715 002714* 013009* 007316* 004440 BGT 3$      ;NO, CONTINUE.
716 002724* 011501 000002 000002 3$:   MOV #PIROUTQ,OUTQOUT ;RESET THE QUEUE POINTER.
717 002726* 032761 000002 000002 MOV #R5,(R2)    ;LOAD CSR ADDRESS.
718 002726* 032761 000002 BIT #BIT1,(R2)  ;IS CONTROL/O ERROR REPORT?.
719 002734* 001450           BEQ 4$      ;NO, THEN CHECK FOR RCV OR XMTR.
720 002736* 032761 000010 000002 BIT #BIT3,(R2)  ;IS IT SOFT ERROR.
721 002744* 001403           BEQ 5$      ;NO, THEN CHECK REMAINING.
722 002746* 032761 000000* 003156* 9$:   MSGNS,BEGIN_SF1 ;ASCII MESSAGE CALL WITH COMMON HEADER
723 002762* 032761 000100 000002 BIT #BIT6,(R2)  ;IS IT DATA ERROR.
724 002762* 032761 000100 BEQ 2$      ;NO, CHECK THE REMAINING.
725 002764* 104403 000000* 003162* MSGNS,BEGIN_DTER1 ;ASCII MESSAGE CALL WITH COMMON HEADER
726 002772* 032761 000040 000002 12$:  BIT #BIT5,(R2)  ;IS IT NON EX MEMORY ERROR.
727 003000* 001403           BEQ 5$      ;NO, THEN MUST BE DATA ERROR.
728 003000* 104403 000000* 003152* MSGNS,BEGIN_NMMRY ;ASCII MESSAGE CALL WITH COMMON HEADER
729 003006* 032761 175604 000002 MOV R5,CSR    ;LOAD DEVICE ADDRESS.
730 003014* 016167 000004 175060 MOV R5,ACSR   ;LOAD DEVICE ACSR.
731 003022* 016167 000006 175054 MOV 6(R1),ASTAT ;LOAD CONTENTS OF DEVICE CSR.
732 003022* 016167 000006 MOV #1,ERRTYPE ;DATA ERROR
733 003030* 012767 000001 175050 MOV #1,ERRTYPE ;DATA ERROR
734 003036* 104405 000000* 000000 HRSRS,BEGIN_NULL ;A CNTL/O RECEIVED
735
736 003044* 142761 000352 000002 BICB #352,(R1) ;ASTAT= ERROR BITS.
737

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738 003052* 104400 000000*      EXITS,BEGIN           ;EXIT TO MONITOR. MODULE WAIT FOR INTERRUPT.
739 003056* 032761 000004 000002 4$:    BIT    #BIT2,2,(R1)   ;IS IT XMIT DONE?
740 003064* 031411                 BEQ    6$              ;NO, RECEIVE/SERVE IT.
742 003066* 052765 000004 000002     BIS    #BIT2,2(R5)   ;SET XMIT DONE BIT IN END PASS FLAG.
743 003100* 106106 175163          TSTB   #MOLY        ;CONTINUE...
744 003102* 142711 000004          BICB   #4,(R1)      ;SET FOR RECEIVE BA/CC I...
745 003106 000403                BIS    #BIT3,2(R5)   ;CONTINUE RECEIVE
746 003110* 052765 000010 000002 6$:    BIS    #BIT3,2(R5)   ;SET RECEIVE DONE BIT IN ENDPASS FLAG.
747 003116* 026765 175110          BNE    #AGH,(R5)    ;NO CONTINUE.
748 003124* 121005 000020 000002     BICB   #BIT4,(R1)   ;CLEAR IEI.
749 003125* 121005 000020 000002     BNE    #AGH,(R5)    ;NO CONTINUE.
750 003140* 142711 000020 000002     BICB   #BIT4,(R1)   ;CLEAR THE IEI.
751 003146* 104400 000000*      EXIT,BEGIN           ;CLEAR RDO XMIT OR RECV DONE, BA/CC 0...
752 003152* 093166*             NXMMRY: MESS1      ;EXIT TO MONITOR. MODULE WAIT FOR INTERRUPT.
753 003154* 177777*             SFT1:  MES4
754 003156* 177777*             DTER1: MESS5
755 003162* 003322*             -1
756 003164* 177777*             -1
757 003166* 020045 047516 020116  MES1: .ASCII  /* NON EXISTENT MEMORY ADDRESS ERROR */
758 003174* 054105 051511 042524
759 003202* 052116 046440 046505
760 003216* 051104 051505 026123
761 003232* 022440 0000 047522 020122
762 003234* 020040 041517 052503 .ASCIZ / OCCURED WHILE DOING NPR'S..... %
763 003242* 042522 020104 041271
764 003250* 046111 020109 050154
765 003252* 023522 027123 027056
766 003254* 027056 027056 027056
767 003300* 022440 0000 .EVEN
768 003303* 045 051440 043117 MES4: .ASCIZ /* SOFT ERROR */
769 003310* 020124 051109 047522
770 003315* 020124 051109 050154
771 003320* 023522 027123 027056
772 003324* 027056 027056 027056
773 003330* 022440 0000 .EVEN
774 003334* 047440 020116 051124
775 003344* 047101 046523 052111
776 003352* 024240 0000
777 003356* 003356*             ****
778 003356* 005000*             ****
779 003356* 005000*             ****
780 003356* 005000*             ****
781 003356* 005000*             ****
782 003356* 005000*             ****
783 003356* 005000*             ****
784 003356* 005000*             ****
785 003356* 005000*             ****
786 003356* 005000*             ****
787 003356* 005000*             ****
788 003356* 005000*             ****
789 003356* 005000*             ****
790 003356* 005000*             ****
791 003356* 005000*             ****
792 003356* 005000*             ****
793 003356* 005000*             ****
794 003364* 005011*             ****
795 003364* 005011*             ****
796 003366* 005061 000004 000002 1$:    MOV    #KMAAMC,R2   ;R2 POINTS TO MICRO-CODE.
797 003372* 012261 000006 000002 1$:    CLR    (R1)        ;CLFAP SEL0.
798 003376* 012711 002000          MOV    R0,4(R1)    ;LOAD THE CRAM ADDRESS.
799 003402* 002200 002200          MOV    (R2)+,6(R1)  ;LOAD WORD TC BE WRITTEN...
800 003406* 002200 002000          MOV    #BIT16,(R1)  ;SET ROMU.
801 003410* 002200 002000          INC    R0          ;INCREMENT CRAM ADDRESS.
802 003414* 002430 000000          CMP    #2000,PO    ;OVER FLD?
803 003416* 002712 000000          BLT    5$          ;YES, REPORT AND RETURN.
804 003422* 001360 000000          CMP    #0,(R2)    ;IS IT END OF MICRO-CODE?
805 003424* 012702 000011 000002 8$:    BNE    1$          ;NO, CONTINUE LOADING.
806 003430* 002702 000262          MOV    #11,RS      ;RESET THE SCRATCH PAD ADDRESS.
807 003436* 002702 000262          CLR    (R1)        ;CLEAR SEL0.
808 003436* 002702 000077 000014 3$:    BIC    #77,4$    ;CLEAR THE ADDRESS IN INSTRUCTION..
809 003444* 050567 000010          RIS    85,4$      ;SET SCRATCH PAD ADDRESS.
810 003450* 112261 000004          MOVB   (R2)+,4(R1)  ;LOAD SEL4 WITH DATA.
811 003454* 004367 000446          JSR    R3,ROMCLK   ;CLOCK THE INSTRUCTION IN
812 003460* 123100 000000          123100          ;THIS POSITION.
813 003464* 005205 000005          INC    R5          ;INCREMENT SCRATCH PAD ADDRESS.
814 003470* 005361 000015          CMP    #15,RS    ;IS IT DONE?
815 003470* 005361 000015          BGT    5$          ;BRANCH IF NOT DONE.
816 003472* 005011 000002          5$:    CLR    (R1)        ;CLEAR SEL0.
817 003474* 000207 000002          RTS    PC          ;RETURN.
818 003476* 104403 000000*      MSGNS,BEGIN,CRMOWF ;ASCII MESSAGE CALL WITH COMMON HEADER
819 003504* 000772 000000*      BR    $5          ;RETURN.
820 003504* 000772 000000*      CRMOWF: MESS3 -1
821 003510* 177777*             ****
822 003512* 020045 044515 051103 000002 4$:    MES3: .ASCIZ /* MICRO-CODE OVER FLUWS CRAM */
823 003512* 020045 044515 051103 000002 4$:    MES3: .ASCIZ /* MICRO-CODE OVER FLUWS CRAM */
824 003520* 026517 047503 042504
825 003526* 047440 042526 020122
826 003534* 046103 052917 022123
827 003550* 051103 046501 022440
828 003550* 0000 000000*      003552* .EVEN
829 003552* 000000*      ****
830 003552* 000000*      ****
831 003552* 000000*      ****
832 003552* 000000*      ****
833 003552* 000000*      ****
834 003552* 000000*      ****
835 003552* 000000*      ****
836 003552* 000000*      ****
837 003552* 000000*      ****
838 003552* 000000*      ****
839 003552* 000000*      ****
840 003552* 004367 000350 000000*      ****
841 003552* 004367 000342 000000*      ****
842 003556* 004367 000342 000000*      ****
843 003560* 004367 000342 000000*      ****
844 003564* 004002 000000*      ****
845 003566* 012700 004266*      ****
846 003572* 012702 001000 000000*      ****
847 003576* 005061 000004 000000*      ****
848 003602* 112298 000004 000000*      ****
849 003606* 004367 000314 000000*      ****

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850 003560* 005061 000004 000002 1$:    JSR    R3,ROMCLK   ;CLOCK INSTRUCTION.
851 010000 000000          JSR    R3,ROMCLK   ;LOAD MAC2017.
852 004002 000000          JSR    R3,ROMCLK   ;CLOCK INSTRUCTION.
853 004002 000000          JSR    R3,ROMCLK   ;SET POINTER IN KMC11 MEMORY.
854 004002 000000          MOV    #XBUF,RO    ;SET POINTER TO DATA.
855 004002 000000          MOVB   #1000,R2    ;SET THE COUNT.
856 004002 000000          CLR    #4(R1)      ;BSEL4-->GOOD DATA.
857 004002 000000          JSR    R3,ROMCLK   ;
858 004002 000000          JSR    R3,ROMCLK   ;
859 004002 000000          JSR    R3,ROMCLK   ;
860 004002 000000          JSR    R3,ROMCLK   ;
861 004002 000000          JSR    R3,ROMCLK   ;
862 004002 000000          JSR    R3,ROMCLK   ;
863 004002 000000          JSR    R3,ROMCLK   ;
864 004002 000000          JSR    R3,ROMCLK   ;
865 004002 000000          JSR    R3,ROMCLK   ;
866 004002 000000          JSR    R3,ROMCLK   ;
867 004002 000000          JSR    R3,ROMCLK   ;
868 004002 000000          JSR    R3,ROMCLK   ;
869 004002 000000          JSR    R3,ROMCLK   ;
870 004002 000000          JSR    R3,ROMCLK   ;
871 004002 000000          JSR    R3,ROMCLK   ;
872 004002 000000          JSR    R3,ROMCLK   ;
873 004002 000000          JSR    R3,ROMCLK   ;
874 004002 000000          JSR    R3,ROMCLK   ;
875 004002 000000          JSR    R3,ROMCLK   ;
876 004002 000000          JSR    R3,ROMCLK   ;
877 004002 000000          JSR    R3,ROMCLK   ;
878 004002 000000          JSR    R3,ROMCLK   ;
879 004002 000000          JSR    R3,ROMCLK   ;
880 004002 000000          JSR    R3,ROMCLK   ;
881 004002 000000          JSR    R3,ROMCLK   ;
882 004002 000000          JSR    R3,ROMCLK   ;
883 004002 000000          JSR    R3,ROMCLK   ;
884 004002 000000          JSR    R3,ROMCLK   ;
885 004002 000000          JSR    R3,ROMCLK   ;
886 004002 000000          JSR    R3,ROMCLK   ;
887 004002 000000          JSR    R3,ROMCLK   ;
888 004002 000000          JSR    R3,ROMCLK   ;
889 004002 000000          JSR    R3,ROMCLK   ;
890 004002 000000          JSR    R3,ROMCLK   ;
891 004002 000000          JSR    R3,ROMCLK   ;
892 004002 000000          JSR    R3,ROMCLK   ;
893 004002 000000          JSR    R3,ROMCLK   ;
894 004002 000000          JSR    R3,ROMCLK   ;
895 004002 000000          JSR    R3,ROMCLK   ;
896 004002 000000          JSR    R3,ROMCLK   ;
897 004002 000000          JSR    R3,ROMCLK   ;
898 004002 000000          JSR    R3,ROMCLK   ;
899 004002 000000          JSR    R3,ROMCLK   ;
900 004002 000000          JSR    R3,ROMCLK   ;
901 004002 000000          JSR    R3,ROMCLK   ;
902 004002 000000          JSR    R3,ROMCLK   ;
903 004002 000000          JSR    R3,ROMCLK   ;
904 004002 000000          JSR    R3,ROMCLK   ;
905 004002 000000          JSR    R3,ROMCLK   ;
906 004002 000000          JSR    R3,ROMCLK   ;
907 004002 000000          JSR    R3,ROMCLK   ;
908 004002 000000          JSR    R3,ROMCLK   ;
909 004002 000000          JSR    R3,ROMCLK   ;
910 004002 000000          JSR    R3,ROMCLK   ;
911 004002 000000          JSR    R3,ROMCLK   ;
912 004002 000000          JSR    R3,ROMCLK   ;
913 004002 000000          JSR    R3,ROMCLK   ;
914 004002 000000          JSR    R3,ROMCLK   ;
915 004002 000000          JSR    R3,ROMCLK   ;
916 004002 000000          JSR    R3,ROMCLK   ;
917 004002 000000          JSR    R3,ROMCLK   ;
918 004002 000000          JSR    R3,ROMCLK   ;
919 004002 000000          JSR    R3,ROMCLK   ;
920 004002 000000          JSR    R3,ROMCLK   ;
921 004002 000000          JSR    R3,ROMCLK   ;
922 004002 000000          JSR    R3,ROMCLK   ;
923 004002 000000          JSR    R3,ROMCLK   ;
924 004002 000000          JSR    R3,ROMCLK   ;
925 004002 000000          JSR    R3,ROMCLK   ;
926 004002 000000          JSR    R3,ROMCLK   ;
927 004002 000000          JSR    R3,ROMCLK   ;
928 004002 000000          JSR    R3,ROMCLK   ;
929 004002 000000          JSR    R3,ROMCLK   ;
930 004002 000000          JSR    R3,ROMCLK   ;
931 004002 000000          JSR    R3,ROMCLK   ;
932 004002 000000          JSR    R3,ROMCLK   ;
933 004002 000000          JSR    R3,ROMCLK   ;
934 004002 000000          JSR    R3,ROMCLK   ;
935 004002 000000          JSR    R3,ROMCLK   ;
936 004002 000000          JSR    R3,ROMCLK   ;
937 004002 000000          JSR    R3,ROMCLK   ;
938 004002 000000          JSR    R3,ROMCLK   ;
939 004002 000000          JSR    R3,ROMCLK   ;
940 004002 000000          JSR    R3,ROMCLK   ;
941 004002 000000          JSR    R3,ROMCLK   ;
942 004002 000000          JSR    R3,ROMCLK   ;
943 004002 000000          JSR    R3,ROMCLK   ;
944 004002 000000          JSR    R3,ROMCLK   ;
945 004002 000000          JSR    R3,ROMCLK   ;
946 004002 000000          JSR    R3,ROMCLK   ;
947 004002 000000          JSR    R3,ROMCLK   ;
948 004002 000000          JSR    R3,ROMCLK   ;
949 004002 000000          JSR    R3,ROMCLK   ;
950 004002 000000          JSR    R3,ROMCLK   ;
951 004002 000000          JSR    R3,ROMCLK   ;
952 004002 000000          JSR    R3,ROMCLK   ;
953 004002 000000          JSR    R3,ROMCLK   ;
954 004002 000000          JSR    R3,ROMCLK   ;
955 004002 000000          JSR    R3,ROMCLK   ;
956 004002 000000          JSR    R3,ROMCLK   ;
957 004002 000000          JSR    R3,ROMCLK   ;
958 004002 000000          JSR    R3,ROMCLK   ;
959 004002 000000          JSR    R3,ROMCLK   ;
960 004002 000000          JSR    R3,ROMCLK   ;
961 004002 000000          JSR    R3,ROMCLK   ;
962 004002 000000          JSR    R3,ROMCLK   ;
963 004002 000000          JSR    R3,ROMCLK   ;
964 004002 000000          JSR    R3,ROMCLK   ;
965 004002 000000          JSR    R3,ROMCLK   ;
966 004002 000000          JSR    R3,ROMCLK   ;
967 004002 000000          JSR    R3,ROMCLK   ;
968 004002 000000          JSR    R3,ROMCLK   ;
969 004002 000000          JSR    R3,ROMCLK   ;
970 004002 000000          JSR    R3,ROMCLK   ;
971 004002 000000          JSR    R3,ROMCLK   ;
972 004002 000000          JSR    R3,ROMCLK   ;
973 004002 000000          JSR    R3,ROMCLK   ;
974 004002 000000          JSR    R3,ROMCLK   ;
975 004002 000000          JSR    R3,ROMCLK   ;
976 004002 000000          JSR    R3,ROMCLK   ;
977 004002 000000          JSR    R3,ROMCLK   ;
978 004002 000000          JSR    R3,ROMCLK   ;
979 004002 000000          JSR    R3,ROMCLK   ;
980 004002 000000          JSR    R3,ROMCLK   ;
981 004002 000000          JSR    R3,ROMCLK   ;
982 004002 000000          JSR    R3,ROMCLK   ;
983 004002 000000          JSR    R3,ROMCLK   ;
984 004002 000000          JSR    R3,ROMCLK   ;
985 004002 000000          JSR    R3,ROMCLK   ;
986 004002 000000          JSR    R3,ROMCLK   ;
987 004002 000000          JSR    R3,ROMCLK   ;
988 004002 000000          JSR    R3,ROMCLK   ;
989 004002 000000          JSR    R3,ROMCLK   ;
990 004002 000000          JSR    R3,ROMCLK   ;
991 004002 000000          JSR    R3,ROMCLK   ;
992 004002 000000          JSR    R3,ROMCLK   ;
993 004002 000000          JSR    R3,ROMCLK   ;
994 004002 000000          JSR    R3,ROMCLK   ;
995 004002 000000          JSR    R3,ROMCLK   ;
996 004002 000000          JSR    R3,ROMCLK   ;
997 004002 000000          JSR    R3,ROMCLK   ;
998 004002 000000          JSR    R3,ROMCLK   ;
999 004002 000000          JSR    R3,ROMCLK   ;

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050 003612* 136500      ;LOAD MEMORY AND INCREMENT ADDRESS.
051 003614* 005302      ;COUNT BY ONE.
052 003616* 001371      ;BRANCH IF NOT DONE..
053 003620* 005311      ;CLEAR THE CSR.
054 003622* 000207      ;RETURN TO MAINLAND.

*****  

*: VERIFY THE MICRO-CODE  

*: THIS ROUTINE VERIFIES THE MICRO-CODE  

*: LOADED, AND CHECKS NPR RATE PARAMETER.  

*: AND THE MAIN MEMORY CONTENTS...  

*: R1 CONTAINS THE ADDRESS OF DEVICE CSR.  

*****  

:VERIFY:  

056 003624* 005067 174406  CLR   MASK    ;CLEAR THE ERROR FLAG.  

057 003630* 012700 007372  MOV   #KNAAMC,R0 ;R0 POINTS TO SOFTWARE MICRO-CODE;  

058 003634* 005002      R0 CONTAINS CRAM ADDRESS BITS 0-7.  

059 003636* 005021      CLR   R2      ;CLEAR THE MAINTENANCE REGISTER..  

060 003638* 010211 000004  CLR   (R1)    ;SET THE CRAM ADDRESS...  

061 003644* 012111 002000  MOV   #51104(R1) ;SET ROM0.  

062 003650* 011005      MOV   (R0),R5  ;PUT "EXPECTED" IN R5.  

063 003652* 016104 000006  MOV   6(R1),R4  ;PUT "FOUND" IN R4.  

064 003656* 020504      CMP   R5,R4  ;COMPARE.  

065 003660* 014303      BEQ   3$     ;BRANCH IF O.K.  

066 003662* 005355 174350  COM   MASK    ;CLEAR THE ERROR FLAG.  

067 003670* 005350      BRK   4$     ;RETURN.  

068 003672* 005202      TST   (R0)+  ;BUMP MICRO-CODE POINTER.  

069 003674* 022110 000000  INC   R2      ;BUMP CRAM ADDRESS.  

070 003700* 001403      CMP   #0,(R0) ;IS IT DONE?  

071 003702* 022102 002000  BEQ   5$     ;YES, GO AND CHECK THE MEMORY...  

072 003704* 002353      CMP   #2000,R2 ;DONE YET??  

073 003710* 012705 000011  BGE   1$     ;NO, CONTINUE.....  

074 003714* 012704 00262*  MOV   #11,R5  ;R5=SPAD ADDRESS.  

075 003720* 005011      MOV   #R0DLV,R4 ;SET THE PARAMETER ADDRESS.  

076 003722* 042261 000077  CLR   (R1)    ;CLEAR SEL0.  

077 003730* 005057 000014  BIC   #1175  ;CLEAR THE ADDRESS FIELD...  

078 003734* 005004      BIS   #2618  ;SET SCRATCH PAD ADDRESS IN INSTR.  

079 003740* 004367 000162  JSR   R3,ROMCLK ;READ THE 4'.
080 003744* 040600      040600 000014  MOV   #11,R5  ;R5=SPAD ADDRESS.  

081 003746* 004367 000154  JSR   R3,ROMCLK ;SET THE PARAMETER ADDRESS.  

082 003752* 061224 000004  061224      CLR   SEL0. .  

083 003754* 122461      CMPB  (R4)+,4(R1) ;CLEAR THE ADDRESS FIELD...  

084 003759* 005104      BEQ   7$     ;MOVE SPADCR5->PERG.  

085 003760* 005157 174250  COM   R4      ;MOVE BREG,OUT1<CS4>.  

086 003762* 005055      BEQ   8$     ;COMPARE.  

087 003770* 005205      BIS   #2618  ;MOVE SPADCR5->PERG.  

088 003772* 022705 000015  INC   R5      ;MOVE SPADCR5->PERG.  

089 003776* 003350      BGT   6$     ;DO THE NEXT.  

090 004000* 012700 004266*  MOV   #XBUF,R0  ;SET THE ERROR FLAG.  

091 004004* 012267 00174256  MOV   #10000FLAG ;RETURN.  

092 004012* 016702 174224  MOV   XSIZE,R2 ;UPDATE R5 POINTING TO NEXT SPAD.  

093 004014* 005205      CMP   #15,R5  ;IS IT DONE?  

094 004016* 005202      BGT   6$     ;BRANCH IF NOT DONE.  

095 004018* 003350      MOV   #XBUF,R0  ;GET THE DATA ADDRESS.  

096 004020* 012700 004266*  MOV   #10000FLAG ;SET THE COUNT...  

097 004022* 016702 174224

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006 004016* 042167 000377 000026 12$: BIC   #377161$  ;CLEAR THE ADDRESS FIELD.
007 004024* 042167 000003 000026  BIS   #161$  ;CLEAR THE ADDRESS FIELD.
008 004032* 155767 174233 000012  BISB  #161$  ;ADD ADDRESS TO INSTRUCTION.
009 004040* 155767 174225 000012  BISB  #161$  ;ADD ADDRESS TO INSTRUCTION.
010 004046* 004367 000054  JSR   R3,ROMCLK ;CLEAR SEL0.
011 004052* 010000      010000 000046  010000      ;LOAD MAR_LO.
012 004054* 004367 000046  JSR   R3,ROMCLK ;LOAD MAR_HI.
013 004056* 004367 000040  040620      BREG<--MEM.
014 004062* 004367 000040  JSR   R3,ROMCLK ;BREG<--MEM.
015 004066* 040620      040620      JSR   R3,ROMCLK ;BREG<--MEM.
016 004070* 004367 000032  JSR   R3,ROMCLK ;BREG<--MEM.
017 004074* 061224      061224      061224      PSEL4<--RREC.
018 004076* 122061 000004  CMPB  (R0)+,4(R1) ;COMPARE THE DATA.
019 004102* 001403 174126  BEQ   18$     ;BRANCH IF GOOD.
020 004110* 005267 174126  COM   MASK    ;SET THE ERROR FLAG.
021 004112* 005267 174152  BEQ   21$     ;INCREMENT THE ADDRESS...
022 004116* 005302      INC   FLAG    ;DONE?
023 004116* 005302      DEC   R2      ;NO, CHECK THE NEXT.
024 004120* 001336      BNE   12$     ;CLEAR SEL0.
025 004122* 005011      CLR   (R1)    ;RETURN.
026 004124* 000207      RTS   PC      ;RETURN.

027 004126* 112761 001000 000001  ROMCLK: MOVB  #BIT9,1(R1)  ;SET ROMI.
028 004134* 012361 000006 000001  MOV   (R3)+,6(R1)  ;LOAD INSTRUCTION IN SEL6.
029 004140* 052711 001400      BIS   #BIT19BIT8(R1)  ;CLOCK INSTRUCTION.
030 004144* 042711 003400      BIC   #BIT10BIT9BIT8(R1) ;CLEAR ROM0,ROM1,NP STEP
031 004150* 000203      RTS   R3      ;RETURN.

*****  

*: SUBROUTINE EARTH:- GETS PHYSICAL 18 BITS ADDRESS FOR  

*: 16 BITS VIRTUAL ADDRESS.  

*: RETURNS: ADDRESS IN PA::ADDRESS<0:15>  

*: EA::ADDRESS<16:17>
*****  

:EBITS:  

043 004152* 104415 000000 000244* GETPA$,BEGIN, VA  ;GET PHYSICAL ADDRESS FROM 16-BIT VA
044 004152* 104415 000000 000244* SWAB  EA      ;BITS<4:5>-->BITS<12:13>
045 004160* 000367 174064      ROL   EA      ;NOW BITS<14:13>
046 004164* 006167 174060      ROL   EA      ;NOW BITS<15:14>
047 004178* 009497 174054      ROL   EA      ;CLEAR THE REST..
048 004178* 009497 003776 174046  BIC   #3776,EA  ;RETURN...
049 004202* 000207      RTS   PC      ;RETURN...

*****  

*: MULTIPLIES #'S IN R3 AND R4 AND RETURNS RESULT IN  

*: TEMP.....
*****  

:MLTPLY:  

059 004204* 005067 174056  CLR   TEMP    ;CLEAR THE RESULT...
060 004210* 060467 174052  ADD   R4,TEMP ;MULTIPLY BY ONE...
061 004214* 005303      DEC   R3      ;COUNT BY ONE...

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KMCB DEC/X11 SYSTEM EXERCISER MODULE
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SFQ 0020

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962 004216- 001374          RNE    1$      ;NOT DONE THEN CONTINUE,  
963 004220- 000205          RTS     R5      ;RETURN...  
964  
965  
966  
967  
968  
969  
970  
971 004222- 004567 176056  INTLNK: JSR     R5,INISR   ;INPUT INTERRUPT SERVICE ROUTINE.  
972 004226- 004567 176402  CSR1:  WORD    0       ;OUTPUT INTERRUPT SERVICE ROUTINE.  
973 004232- 000000          R5,OUISR  0       ;CSR ADDRESS FOR DEV #1.  
974 004234- 000000          XX11:  WORD    0       ;END PASS PAGE FOR DEV #1.  
975 004236- 000000          RBUF1   0       ;RECEIVE BUFFER POINTER FOR DEV #1.  
976 004239- 000000          XX12:  WORD    0       ;REC/XMIT COUNTERS.  
977 004242- 000000          RBUF2   0       ;REC/XMIT COUNTERS.  
978 004242- 000000          XX22:  WORD    0       ;ERROR COUNTS FOR DEV #1.  
979  
980 004244- 004567 176034  JSR     R5,INISR   ;INPUT INTERRUPT SERVICE ROUTINE.  
981 004250- 004567 176360  CSR2:  WORD    0       ;OUTPUT INTERRUPT SERVICE ROUTINE.  
982 004252- 000000          R5,OUISR  0       ;CSR ADDRESS FOR DEV #2.  
983 004256- 0005274-        XX21:  WORD    0       ;END PASS PAGE FOR DEV #2.  
984 004260- 0005274-        RBUF2   0       ;RECEIVE BUFFER POINTER FOR DEV #2.  
985 004262- 000000          XX22:  WORD    0       ;REC/XMIT COUNTERS.  
986 004264- 000000          RBUF1   0       ;REC/XMIT COUNTERS.  
987  
988  
989  
990  
991  
992  
993  
994 004266- 000400 001402 002404 XBUF:  .ASCII  <000><001><002><003><004><005><006><007><010><011><012>  
995 004274- 003406 004610 01012   .ASCII  <013><014><015><016><017><020><021><022><023><024><025>  
996 004285- 003406 010120 0113422  .ASCII  <026><027><030><031><032><033><034><035><036><037><040>  
997 004305- 010480 0113422 0124434  .ASCII  ^!"#$%&"()*,--/0123456789:<~  
998 004314- 013426 0144330 015432  .ASCII  /YZC\J^-\ABCDEFHGIJKLMNOPQR/  
999 004322- 016434 017436 040      .ASCII  /SUVWXYZC13/<177><200><201><202>  
1000 004327- 041 021442 022444  .ASCII  /=?@APCDEFGHIJKLMNOPQRSTUVWXYZ/  
1001 004334- 023446 024452 025452  .ASCII  /  
1002 004342- 026454 027456 030460  .ASCII  /  
1003 004350- 031466 032468 033466  .ASCII  /  
1004 004356- 034466 035468 036468  .ASCII  /  
1005 004362- 037075 040477 041101  .ASCII  /  
1006 004370- 042103 043103 044107  .ASCII  /  
1007 004374- 045111 046113 047115  .ASCII  /  
1008 004404- 050117 051121 052123  .ASCII  /  
1009 004412- 053125 054125 055125  .ASCII  /  
1010 004420- 056125 057125 058125  .ASCII  /  
1011 004422- 043105 044101 045111  .ASCII  /  
1012 004433- 043105 044101 045111  .ASCII  /  
1013 004440- 046113 047115 050117  .ASCII  /  
1014 004446- 051121 052125 053125  .ASCII  /  
1015 004450- 054123 053125 054127  .ASCII  /  
1016 004456- 055131 030533 077535  .ASCII  /  
1017 004464- 100600 202  
;
```

KMCB DEC/X11 SYSTEM EXERCISER MODULE
XKMCB0-P11 12-OCT-78 12:02

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SFQ 0021

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1018 004467- 203 102604 103606  .ASCII  <203><204><205><206><207><210><211><212><213><214><215>  
1019 004474- 104510 105612 106614  .ASCII  <216><217><220><221><222><223><224><225><226><227><230>  
1020 004502- 107516 110620 111622  .ASCII  <231><232><233><234><235><236><237><240><241><242><243>  
1021 004510- 112524 113626 114626  .ASCII  <244><245><246><247><250><251><252><253><254><255><256>  
1022 004515- 1231 115632 116634  .ASCII  <257><260><261><262><263><264><265><266><267><270><271>  
1023 004522- 117636 120642 121642  .ASCII  <272><273><274><275><276><277><300><301><302><303><304>  
1024 004530- 122644 123646 124650  .ASCII  <305><306><307><310><311><312><313><314><315><316><317>  
1025 004538- 125622 130680 131682  .ASCII  <320><321><322><323><324><325><326><327><330><331><332>  
1026 004543- 130680 131682 132684  .ASCII  <333><334><335><336><337><340><341><342><343><344><345>  
1027 004550- 132664 133666 134670  .ASCII  <346><347><350><351><352><353><354><355><356><357><360>  
1028 004556- 135672 136674 137676  .ASCII  <361><362><363><364><365><366><367><370><371><372><373>  
1029 004564- 140700 141702 304      .ASCII  <374><375><376><377><377><377>  
1030 004571- 143705 144710 144710  .BLKB   400      ;  
1031 004576- 145712 146714 147716  .EVEN  
1032 004604- 150740 151740 152744  .ASCII  <374><375><376><377><377><377>  
1033 004616- 153740 154740 155744  .ASCII  <374><375><376><377><377><377>  
1034 004617- 153743 154743 155743  .ASCII  <374><375><376><377><377><377>  
1035 004624- 160740 161742 162744  .ASCII  <374><375><376><377><377><377>  
1036 004632- 163746 164750 165752  .ASCII  <374><375><376><377><377><377>  
1037 004640- 166754 167756 360      .ASCII  <374><375><376><377><377><377>  
1038 004645- 171762 172764 173764  .ASCII  <374><375><376><377><377><377>  
1039 004652- 173764 174766 175766  .ASCII  <374><375><376><377><377><377>  
1040 004660- 177674 178776 179776  .ASCII  <374><375><376><377><377><377>  
1041 004665- 000400          .BLKB   400      ;  
1042  
1043 005266-          ;EVEN  
1044 005266- 005272-        BUFTAB: RBUF1           ;BUFFER POINTER FOR DEV #1.  
1045 005266- 005272-        005270- 005274-        RBUF2           ;BUFFER POINTER FOR DEV #2.  
1046  
1047  
1048  
1049  
1050 005272- 005276-        ;TABLE OF RECEIVE BUFFERS.  
1051 005272- 005276-        RBUF1:  RBUF11          ;RECEIVE BUFFERS FOR DEV #1.  
1052 005272- 005276-        RBUF2:  RBUF21          ;RECEIVE BUFFERS FOR DEV #2.  
1053  
1054 005274- 006276-        ;RECEIVE BUFFERS FOR DEVICE #1.  
1055 005274- 006276-        RBUF11: .BLKB  1000          ;RECEIVE BUFFER 11.  
1056 005274- 006276-        ;RECEIVE BUFFERS FOR DEVICE #2.  
1057 005274- 006276-        RBUF21: .BLKB  1000          ;RECEIVE BUFFER 21.  
1058 005276- 001000          BASE1:  
1059 005276- 001000          ;RECEIVE BUFFERS FOR DEVICE #1.  
1060 005276- 001000          RBUF11: .BLKB  1000          ;RECEIVE BUFFER 11.  
1061 005276- 001000          ;RECEIVE BUFFERS FOR DEVICE #2.  
1062 006276- 001000          RBUF21: .BLKB  1000          ;RECEIVE BUFFER 21.  
1063 006276- 001000          BASE1:  
1064 007276- 000000          ;QUEUES AND ITS POINTERS.  
1065  
1066  
1067  
1068  
1069  
1070 007276- 000010          PIRING: .BLKW  10          ;INPUT INTERRUPT QUEUE.  
1071 007316- 000010          PIROUTQ: .BLKW  10          ;OUTPUT INTERRUPT QUEUE.  
1072 007336- 000010          REQ:   .BLKW  0           ;REQUEST.  
1073 007356- 000000          INQIN: .WORD  0           ;INPUT QUEUE POINTER.
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1074 007360* 000000
1075 007362* 000000
1076 007364* 000000
1077 007366* 000000
1078 007370* 000000
1079 007372* 000000
1080
1081
1082
1083
1084
1085
1086
1087
1088
1089
1090
1091
1092
1093
1094
1095
1096
1097
1098
1099
1100
1101
1102
1103
1104
1105 007372* KMAAMC: MOVE #0,BREG
1106      MOVE #0,MLR ;CLEAR B REGISTER
1107      MOVE BREG,MER ;MAR <0:7>:=0.
1108      MOVE BREG,SPAD <0>;CLEAR SPAD <0>.
1109      MOVE BREG,SPAD <1>;CLEAR SPAD <1>.
1110      MOVE BREG,SPAD <2>;CLEAR SPAD <2>.
1111      MOVE BREG,SPAD <3>;CLEAR SPAD <3>.
1112      MOVE BREG,SPAD <4>;CLEAR SPAD <4>.
1113      MOVE BREG,SPAD <5>;CLEAR SPAD <5>.
1114      MOVE BREG,SPAD <6>;CLEAR SPAD <6>.
1115      MOVE BREG,SPAD <7>;CLEAR SPAD <7>.
1116      MOVE BREG,SPAD <10>;CLEAR SPAD <10>.
1117      MOVE BREG,SPAD <17>;CLEAR SPAD <17>.
1118      MOVE BREG,OUT1 <0>;CLEAR BSEL0.
1119      MOVE BREG,OUT1 <2>;CLEAR BSEL2.
1120      MOVE BREG,OUT1 <3>;CLEAR BSEL3.
1121      MOVE BREG,OUT1 <4>;CLEAR BSEL4.
1122      MOVE BREG,OUT1 <5>;CLEAR BSEL5.
1123      MOVE BREG,OUT1 <6>;CLEAR BSEL6.
1124      MOVE BREG,OUT1 <7>;CLEAR BSEL7.
1125      MOVE BREG,OUT1 <10>.
1126      MOVE BREG,OUT1 <11>.
1127      MOVE BREG,OUT0 <0>.
1128      MOVE BREG,OUT0 <1>.
1129      MOVE BREG,OUT0 <2>.

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1130 007454* MOVE BREG,OUT0 <3> ;
1131 007456* MOVE BREG,OUT0 <4> ;
1132 007460* MOVE BREG,OUT0 <5> ;
1133 007462* MOVE BREG,OUT0 <6> ;
1134 007464* MOVE BREG,OUT0 <7> ;
1135 007466* MOVE BREG,OUT0 <10> ;
1136 007468* MOVE #1,MEM,MARINC ;CLEAR MEMORY LOCATION & INC MAR
1137 007472* SINC SPAD ;INCREMENT THE COUNT
1138 007474* ADDC SPAD <1>,BREG ;ADD CARRY IN TO MSB.
1139 007476* MOVE SPAD <1>,BREG ;IS IT DONE...?
1140 007500* B81 STRTS ;RR IF DONE AND START.
1141 007502* SBR 1$ ;ELSE, CONTINUE.
1142 007504* STRTS: MOVE #200,BREG ;SET RD I BIT IN B REGISTER.
1143 007506* INP1 <CSRO>,SPAD <0>;SAVE SEL0 IN SPAD <0>.
1144 007509* OR BREG,SPAD <6>,OUT1 <0><CSRO>;SET RD I IN SEL0.
1145 007511* MCDLP: MOVE INP1 <CSR2>,BREG ;IS RD0 SET??.
1146 007514* B87 MCDLP ;WAIT TILL IT CLEARS...
1147 007516* MOVE INP1 <CSRO>,BREG ;LOAD BREG WITH SEL0.
1148 007520* B87 IS ;IF RD I SET, GO CHECK IEL.
1149 007522* PRCSBF ;IF CLEARED, PROCESS BUFFERS.
1150 007524* B84 INTRPT ;IF NOT, IS ALSO SET THEN GO INTERRUPT.
1151 007526* SBR MCDLP ;ELSE WAIT.
1152 007530* INTRPT: MOVE #0,BREG ;SET UP TO CLEAR CSRS.
1153 007532* MOVE BREG,OUT1 <CSR4> ;CLEAR BSEL4.
1154 007533* MOVE BREG,OUT1 <CSR5> ;CLEAR BSEL5.
1155 007536* MOVE BREG,OUT1 <CSR6> ;CLEAR BSEL6.
1156 007539* MOVE BREG,OUT1 <CSR7> ;CLEAR BSEL7.
1157 007540* MOVE #16,BREG ;SET BR REG WITH VECTR=XX0.
1158 007544* MOVE INP1 <CSR1>,SPAD <15>;GET MICRO MISC. REGISTER.
1159 007546* OR SPAD <0>,BREG,OUT1 <CSR1>;SET BR REQ. I.E. INTERRUPT AT XX0.
1160 007550* 1$: MOVE INP1 <CSR1>,BREG ;IS BR REQ BIT CLEARED.
1161 007551* B87 1$ ;NO, WAIT OR SPIN ON IT.
1162 007552* 2$: MOVE INP1 <CSRO>,BREG ;IS IT SERVED???
1163 007554* B87 2$ ;NO, THEN WAIT.
1164 007556* B87 MCDLP ;RETURN TO LOOP.
1165 007560*
1166
1167
1168
1169
1170
1171
1172
1173 007562* PRCSBF: MOVE INP1 <CSR6>,SPAD <15>;LOAD SPAD <15> WITH C.C LOW BYTE.
1174 007564* MOVE #77,BREG ;LOAD MASKING BITS IN B REGISTER.
1175 007566* MOVE INP1 <CSR7>,SPAD <16>;GET HIGH BYTE OF CHARACTER COUNT.
1176 007570* AND BREG,SPAD <16>,SPAD <16>;LOAD HIGH BYTE OF C.C. IN SPAD <16>.
1177 007572* 2$: MOVE #1,BREG ;LOAD MASK IN B REG.
1178 007574* MOVE INP1 <CSR0>,SPAD <0>;GET BSEL0.
1179 007576* AND BREG,SPAD <0>,SPAD <0>;MASK OUT AND LOAD IN BREG & SPAD.
1180 007600* MOVE MEM,SPAD <4> ;
1181 007602* MOVE #4,MEM ;LOAD EXPECTED IN MEM.
1182 007604* SIFEQ MEM,SPAD <0> XMIT BA/CC I LOADED!! SERVE IT!
1183 007610* MOVE #0,MEM ;LOAD EXPECTED IN MEM.
1184 007616* SIFEQ MEM,SPAD <0> RECV BA/CC I LOADED!! SERVE IT!
1185 007616* MOVE SPAD <4>,MEM ;RESTORE MEMORY LOCATION

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1186 007620* MOVE #1,BREG ;SET THE TYPE OF ERROR.
1187 007622* MOVE BREC,SPAD <0> ;NONE! REPORT ERROR!
1188 007624* CALL SFTRSPR ;NONE! REPORT ERROR!
1189 007630* SBR STRTS ;RETURN TO LOOP.

***** SERVE THE RECEIVE REQUEST.
1. DATA TRANSMIT FROM KMC11 TO PDP11. THROUGH NPR'S.
2. CONTROL NPR/RNR & NPR RATE.
3. REPORT DONE OR ERROR A: NON EX MEM.
   B: SOFT ERROR.

***** RECVE: MOVE SPAD <4>,MEM ;RESTORE MEMORY LOCATION.
1201 007632* MOVE SPAD <10>,BREG ;ICE XMIT FLAG SET?
1202 007634* BZ 1$ ;YES THEN SERVE THE REQUEST.
1203 007636* MOVE SPAD <11>,BREG ;IS RECEIVE ONLY FLAG SET?
1204 007639* BZ 1$ ;YES THEN PROCEED TO SERVE THE REQUEST.
1205 007642* MOVE #1,BREG ;SET THE TYPE OF ERROR...
1206 007644* MOVE BREC,SPAD <0> ;NO REPORT ERROR!! RECV. BEFORE XMIT.
1207 007646* CALL SFTRSPR ;WAIT FOR NEXT REQUEST.
1208 007650* SBR STRTS ;NO REPORT ERROR!! RECV. BEFORE XMIT.
1209 007652* MOVE INPI <CSR4>,OUT0 <0> ;LOAD OUTRA <0:17>
1210 007654* MOVE INPI <CSR4>,SPAD <6> ;LOAD PARALLEL COUNT..
1211 007656* MOVE INPI <CSR5>,OUT0 <7> ;LOAD OUTRA <8:15>.
1212 007658* MOVE INPI <CSR5>,SPAD <7> ;SET PARALLEL COUNT.
1213 007660* MOVE INPI <CSR7>,SPAD <0> ;GET GET OUTRA EXTENDED BITS.
1214 007662* MOVE #300,BREG ;GET THE MASK...
1215 007664* AND SPAD <0>,BR,SP ;SET IT IN RIGHT PLACE.
1216 007666* SHFBRT ;SET IT IN RIGHT PLACE.
1217 007668* SHFBRT ;SET IT IN RIGHT PLACE.
1218 007670* SHFBRT ;SET IT IN RIGHT PLACE.
1219 007672* MOVE MEM,SPAD <1> ;SAVE MEMORY LOCATION.
1220 007674* MOVE #14,MM ;GET MASK
1221 007676* MOVE MEM,SPAD <0> ;IN SPAD <0>?
1222 007678* AND SPAD <1>,BREG,OUT0 <11> ;OUT RA <16:17>
1223 007680* MOVE SPAD <1>,MEM ;RESTORE MEMORY LOCATION.
1224 007682* MOVE #0,MPLR ;SET THE BUFFER POINTER.
1225 007684* MOVE SPAD <11>,BRFG ;IS RCOLY FLAG SET?
1226 007686* BZ 2$ ;YES THEN SET LOWER BUFFER ADDRESS.
1227 007688* MOVE #0,MPPR ;CLEAR THE PAGE REGISTER.
1228 007690* SBR RCVLP ;SET THE PAGE ADDRESS.
1229 007692* RCVLP: MOVE #2,MPPR ;SET POINTER TO GOOD DATA BUFFER.
1230 007694* MOVE MEM,OUT0 <5>; MARINC ;LOAD LOW BYTE OF DATA.
1231 007696* MOVE #021,BREG ;SET
1232 007698* MOVE INPI <10>,SPAD <0> ;WORD XFR, NPR OUT
1233 007700* OR BREC,SPAD <0>,OUT1 <10> ;& NPF HQ..
1234 007702* 1$: MOVE INPI <10>,BREG ;IS NPF DINE?
1235 007704* SBR 2$ ;NO, CHCK FOR NON EX MEM.
1236 007706* 2$: MOVE SBR 2$ ;YES, PREPARE FOR NEXT.
1237 007708* 2$: MOVE INPI <11>,BREG ;IS NON EX MEM. SET?
1238 007710* 2$: MOVE RBO 4$ ;YES, REPORT FATAL ERROR.


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1243 007756* 4$: SBR 1$ ;NO, WAIT FOR NPF TO CLEAR.
1244 007760* MOVE #2,BREG ;SET THE TYPE OF ERROR...
1245 007762* MOVE BREC,SPAD <0> ;REPORT NON EX MEM. ERROR.
1246 007764* CALL NEXMEM ;WAIT FOR NEXT REQUEST.
1247 007766* SBR STRTS ;DECREMENT THE COUNT.
1248 007768* BZ 3$ ;BRANCH IF IT WAS 0.
1249 007770* 3$: BDEC SPAD <15> ;UPDATE RECEIVE BUFFER
1250 010003* 6$: SINC SPAD <0> ;POINTER.
1251 010005* 6$: SADC SPAD <0> ;UPDATE EXTENDED BITS IF CARRY SET.
1252 010007* 8$: BZ SPAD <6>,RREG ;SET THE ADDRESS.
1253 010009* 8$: MOVE BREG,OUT0 <6> ;SET THE OUTBA..
1254 010010* 8$: MOVE SPAD <7>,BREG ;SET THE OUTBA..
1255 010012* 8$: MOVE BREG,OUT0 <7> ;SET THE OUTBA..
1256 010014* 8$: CALL NPFATE ;WAIT TO MAINTAIN NPF RATE.
1257 010016* 8$: SBR RCVLP ;DO THE NEXT XFR.
1258 010022* 7$: SDEC SPAD <7> ;SET BACK TO 376.
1259 010024* 7$: MOVE BZ SPAD <7>,RREG ;SET THE ADDRESS.
1260 010026* 7$: MOVE BREG,OUT0 <7> ;SET THE OUTBA..
1261 010028* 7$: MOVE SPAD <0>,SPAD <0> ;SET EXTENDED ADDRESS BITS.
1262 010030* 7$: ADD BREG,SPAD <0>,OUT1 <11> ;INCREMENT EXTENDED BITS.
1263 010032* 7$: SBR 5$ ;WAIT TO MAINTAIN RATE THEN PROCEED.
1264 010034* 5$: SDEC SPAD <16> ;DECREMENT THE MSP'S.
1265 010040* 5$: BZ RCVDNE ;BRANCH IF DONE.
1266 010042* 5$: SBR 6$ ;SEE DO THE NEXT.
1267 010044* 5$: MOVE #201,BREG ;SET RD TO RCV DONE BITS IN BREG.
1268 010046* 5$: MOVE INPI <CSR2>,SPAD <0> ;SET THE PITS IN RSEL1.
1269 010048* 5$: OR BREG,SPAD <0>,OUT1 <CSR2>;GET RSEL2.
1270 010050* 5$: MOVE INPI <CSR2>,BREG ;DUMMY XFR TO CHECK IRO?
1271 010054* 5$: BB4 1$ ;BRANCH IF IED SET.
1272 010056* 5$: SBR STRTS ;WAIT FOR NEXT REQUEST.
1273 010060* 1$: MOVE #300,BREG ;SET BR REG. VCTE=AAA.
1274 010062* 1$: MOVE INPI <CSR11>,SPAD <0> ;SET MICROPROCESSOR MISC. REG.
1275 010064* 1$: OR BREG,SPAD <0>,OUT1 <CSR11> ;SET THE BITS IN MISC. REG.


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1279 010065: MOVE INP1 <CSR11>,BREG ;IS BR REQ CLEARED?
1279 010070: MOVE #277,BREG ;NO SPIN ON IT
1279 010074: MOVE INP1 <CSR11>,SPAD <0> ;SET TO CLEAR VCTR:=XX4...
1280 010076: AND BREG,SPAD <0>,OUT1 <CSR11> ;CLEAR VCTR:=XX4...
1281 010100: MOVE #0,BREG ;GET SET TO CLEAR XMIT FLAG.
1282 010102: MOVE BREG,SPAD <10> ;CLEAR XMIT FLAG.
1283 010104: SBR STRTS ;WAIT FOR NEXT REQUEST.

;*****TRANSMIT THE BUFFER FORM PDP11 TO KMC11
1285 010106: ;DOES ALL THE FUNCTIONS AS RECEIVE OPERATION AND
1286 010110: ;ALSO CHECKS THE DATA.
1287 010112: ;*****XMIT: MOVE SPAD <42>MEM ;RESTORE MEMORY LOCATION.
1288 010114: MOVE INP1 <CS4>,OUT0 <4> ;GET LOW BYTE OF BUFFER ADDRESS.
1289 010116: MOVE INP1 <CS4>,SPAD <6> ;SET PARALLEL ADDRESS IN SCRATCH PAD.
1290 010118: MOVE INP1 <CS5>,OUT0 <5> ;GET HIGH BYTE OF BUFFER ADDRESS.
1291 010120: MOVE INP1 <CS5>,SPAD <7> ;SET PARALLEL ADDRESS IN SCRATCH PAD.
1292 010122: MOVE INP1 <CS5>,SPAD <0> ;GET PARALLEL BITS IN BREG.
1293 010124: MOVE #300,BREG ;GET THE MASK...
1294 010126: AND BREG,SPAD <0>,BR,SP ;POSITION THE BITS.....
1295 010128: SHFBRT ;POSITION THE BITS.
1296 010130: SHFBRT ;POSITION THE BITS.
1297 010132: SHFBRT ;POSITION THE BITS.
1298 010134: SHFBRT ;POSITION THE BITS.
1299 010136: MOVE MEM,SPAD <1> ;SAVE MEMORY LOC.
1300 010138: MOVE #14,MEM ;
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1306 010142: MOVE MEM,SPAD <0> ;GET THE MASKING BITS.
1307 010144: AND SPAD <1>,OUT0 <10> ;LOAD EXTENDED ADDRESS BITS OF BUFFER
1308 010146: MOVE SPAD <1>,MEM ;RESTORE MEMORY
1309 010150: MOVE #0,MLR ;CLEAR MEMORY LOCATION REGISTER
1310 010152: MOVE ##0,NPR ;CLEAR MEMORY PAGE REGISTER
1311 010154: XMTLP: MOVE INP1 <CSR10>,SPAD <0> ;GET NPR CONTROL REGISTER.
1312 010156: MOVE #001,BREG ;SET NPR RQ BIT IN BREG.
1313 010160: OUT0 <0>,SPAD <0>,OUT1 <CSR10> ;SET NPR RQ BIT.
1314 010162: MOVE BBO 28 ;NO CHECK FOR NON EX. MEM. SET?
1315 010164: SBR 32 ;YES, PREPARE FOR NEXT.
1316 010166: BBO 45 ;REPORT FATAL ERROR IF IT IS.
1317 010170: SBR 48 ;NOT WAIT FOR NPR TO CLEAR.
1318 010172: MOVE INP1 <CSR11>,BREG ;IS NON EX. MEM. SET?
1319 010174: SBR 49 ;REPORT FATAL ERROR IF IT IS.
1320 010176: MOVE BREG,SPAD <0> ;SET THE TYPE OF ERROR...
1321 010202: MOVE BREG,SPAD <0> ;REPORT NON EX. MEM. ERROR.
1322 010204: CALL NSXMEM ;REPORT NON EX. MEM. ERROR.
1323 010206: SBR STRTS ;WAIT FOR NEXT REQUEST.
1324 010210: MOVE INPO <0>,MEM MARINC ;LOAD THE DATA IN TO MEMORY.
1325 010212: MOVE INPO <1>,MEM MARINC ;LOAD THE DATA IN TO MEMORY.
1326 010214: SDEC SPAD <15> ;DECREMENT THE COUNT
1327 010216: BZ SPAD <6> ;BRANCH IF IT HAS 0.
1328 010218: INC SPAD <6> ;UPDATE THE XMIT.
1329 010220: SADC SPAD <7> ;BUFFER POINTER.
1330 010222: BC 75 ;UPDATE EXTENDED ADDRESS IF CARRY SET.
1331 010224: MOVE SPAD <6>,BREG ;SET OUTBA ADDRESS.
1332 010226: MOVE BREG,OUT0 <4> ;SET OUTBA ADDRESS.
1333 010232: MOVE SPAD <6>,BREG ;SET OUTBA ADDRESS.
1334 010234: MOVE BREG,OUT0 <5> ;SET OUTBA ADDRESS.
1335 010236: CALL NPRATE ;WAIT TO MAINTAIN THE NPR RATE.
1336 010238: SBR XMTLP ;DO THE NEXT XTR.
1337 010240: SDEC SPAD <7> ;SET IT BACK TO 377...
1338 010242: MOVE #4,BREG ;LOAD LSB OF EXTENDED ADDRESS.
1339 010250: MOVE INP1 <CSR10>,SPAD <4> ;LOAD BREG WITH EXTENDED BITS.
1340 010252: ADD BREG,SPAD <4>,OUT1 <CSR10> ;INCREMENT THE EXTENDED BITS.
1341 010254: SDEC SPAD <16> ;INCREMENT THE MSB'S
1342 010256: BZ XMTDNE ;BRANCH IF DONE.
1343 010258: SBR 65 ;ELSE DO THE NEXT.
1344 010262: XMTDNE: MOVE INP1 <CSR6>,SPAD <15> ;GET LOW BYTE OF CHAR. COUNT.
1345 010264: MOVE INP1 <CSR6>,SPAD <15> ;LOAD THE MASK.
1346 010266: MOVE INP1 <CSR6>,SPAD <16> ;LOAD THE MASK.
1347 010270: AND BREG,SPAD <16>,SPAD <16> ;GET HIGH BYTE OF CHAR. COUNT.
1348 010272: MOVE BREG,SPAD <16>,SPAD <16> ;LOAD HIGH BYTE OF CHAR. COUNT.
1349 010274: MOVE #0,BREG ;SET POINTERS TO DATA
1350 010276: MOVE BREG,SPAD <1> ;SET POINTERS TO DATA
1351 010300: MOVE BREG,SPAD <2> ;BUFFERS.
1352 010302: MOVE BREG,SPAD <3> ;SET POINTER TO GOOD.
1353 010304: MOVE BREG,BREG ;SET POINTERS TO DATA
1354 010306: MOVE BREG,BREG ;SET POINTERS TO DATA
1355 010312: MOVE SPAD <1>,MLR ;;
1356 010314: MOVE SPAD <2>,MLR ;;
1357 010316: CKDTLP: MOVE MEM,SPAD <5> ;LOAD GOOD DATA.
1358 010318: MOVE SPAD <3>,MLR ;;
1359 010320: MOVE SPAD <4>,MLR ;;
1360 010322: MOVE SPAD <5>,MEM ;;
1361 010324: SIFEQ SPAD <5>,MEM 1$ ;COMPARE DATA. GO TO 1$ IF GOOD

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1362 010330*      MOVE   # 20,BREG      ;SET THE TYPE OF ERROR...
1363 010334*      MOVE   BREG,SPAD <0>    ;REPORT DATA ERROR.
1364 010340*      CALL   DATERR      ;DECREMENT COUNT
1365 010340*      1$:    SDEC   SPAD <15>    ;BRANCH IF LOW BYTE CIRCLED
1366 010342*      RZ    2$          ;UPDATE GOOD DATA
1367 010344*      SINC   SPAD <3>      ;BUFFER POINTER
1368 010346*      SADC   SPAD <4>      ;UPDATE DATA BUFFER POINTER
1369 010350*      SINC   SPAD <1>      ;AND LOAD IT IN MAR.
1370 010352*      SADC   SPAD <25>     ;SET THE MAR...
1371 010354*      MOVE   SPAD <1>,MLR
1372 010356*      MOVE   SPAD <2>,MFR      ;SET THE MAR...

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1373 010360*      $BR   CKDTLP      ;CHECK THE NEXT CHAR.
1374 010362*      $DEC   SPAD <16>    ;DECREMENT MSG COUNT.
1375 010364*      RZ    2$          ;BRANCH IF DONE.
1376 010366*      SBR   3$          ;PREPARE FOR THE NEXT.
1377 010368*      MOVE   # 205,BREG      ;SET RD O/N MIT DONE BA/CC O / N BREG.
1378 010372*      MOVE   INP1 <CSR2>,SPAD <0>    ;GET BSEL2.
1379 010374*      OR    BREG,SPAD <0>,OUT1 <CSR2>    ;SET BITS IN BSEL2.
1380 010376*      MOVE   INP1 <CSR2>,BREG      ;IS IEO SET?
1381 010400*      BB4    4$          ;YES GO INTERRUPT.
1382 010402*      MOVE   SPAD <300>,BREG      ;WAIT FOR NEXT INSTRUCTION. REQUEST.
1383 010404*      MOVE   INP1 <CSR11>,SPAD <0>    ;
1384 010410*      OR    BREG,SPAD <0>,OUT1 <CSR11>    ;SET BR RQ,VCTR=XX4
1385 010412*      MOVE   INP1 <CSR11>,BREG      ;IS BR RQ CLEARED
1386 010414*      BB7    5$          ;NOT SPIN ON IT.
1387 010416*      MOVE   # 277,BREG      ;SET TO CLEAR VCTR=XX4.
1388 010420*      MOVE   INP1 <CSR11>,SPAD <0>    ;GET UPMs REGISTER
1389 010422*      ANDG   SPAD <0>,OUT1 <CSR11>    ;SET VCTR=XX4.
1390 010424*      SDEC   SPAD <10>     ;SET XMIT FLAG.
1391 010426*      $BR   STRTS      ;GO TO IDLE STATE.
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1408 010430*      NPRATE: MOVE   BREG,SPAD <17>    ;SAVE THE RETURN ADDRESS.
1409 010432*      MOVE   SPAD <13>,BREG      ;GET NPR RATE COUNT...
1410 010434*      MOVE   BREG,SPAD <14>    ;COUNT MOVE BREG,SPAD <17>
1411 010436*      MOVE   SPAD <14>,BREG      ;COUNT MOVE BREG,SPAD <17>
1412 010440*      MOVE   BREG,SPAD <1>      ;DECREMENT THE COUNT
1413 010442*      SDEC   SPAD <0>      ;BRANCH IF LOW BYTE IS COUNTED
1414 010444*      RZ    1$          ;CONTINUE
1415 010446*      SBR   2$          ;COUNT HIGH BYTE BY 1.
1416 010450*      SDEC   SPAD <1>      ;RETURN IF DONE.
1417 010454*      RZ    3$          ;CONTINUE.
1418 010456*      SBR   SPAD <17>,PAGE0    ;RETURN.
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1429
1430
1431
1432 010460*
1433 010460
1434 010460
1435 010462
1436 010462
1437 010466
1438 010470
1439 010472
1440 010474
1441 010476
1442 010478
1443 010504
1444 010504
1445 010506
1446 010510
1447 010512
1448 010514
1449 010516
1450 010519
1451 010520
1452 010524
1453 010526
1454 010530
1455 010532
1456 010534
1457 010536
1458 010536
1459 010542
1460 010544
1461 010546
1462 010550
1463 010552
1464 010554
1465 010556
1466 010560
1467 010562
1468 010564 000000
1469 000001

;*****SFERR: ;ENTRY FOR SOFT ERROR.
;NEXMEM: ;ENTRY FOR NON EXISTENT MEMORY.
;DATERR: MOVE BREG,SPAD <17> ;ENTRY FOR DATA ERROR.
        MOVE SPAD <0>,BREG ;FIND THE TYPE OF ERROR.
        BBR NEXME1 ;SET UP FOR NEX MEM.
        BBR DATER1 ;SET UP FOR DATA ERRO.
        BBR SFTER1 ;SET SFTER,CNTL/O, RD 0.
        MOVE #212,BREG ;SET SFTER,CNTL/O, RD 0.
        SBR MERGE ;SET NEXMEM,CNTL/O, RD 0.
        MOVE #242,BREG ;SET BSEL1.
        SBR MERGE ;SET BSEL2.
        MOVE #172,BREG ;SET DATAERR,CNTL/O, RD 0.
        MERGE: MOVE INPI <CSR2>,SPAD <0> ;SET BSEL2.
                OR BREG,SPAD <0> OUT1 <CSR2> ;SET THE BITS IN BSEL2.
                MOVE SPAD <4>,BREG ;LOAD ERROR DATA.
                MOVE BREG,OUT1 <CSR4> ;LOAD ERROR DATA.
                MOVE SPAD <5>,BREG ;LOAD ERROR DATA.
                MOVE BREG,OUT1 <CSR5> ;LOAD ERROR DATA.
                MOVE SPAD <6>,BREG ;LOAD ERROR DATA.
                MOVE BREG,OUT1 <CSR6> ;LOAD ERROR DATA.
                MOVE SPAD <7>,BREG ;LOAD ERROR DATA.
                MOVE BREG,OUT1 <CSR7> ;LOAD ERROR DATA.
                MOVE SPAD <17>,BREG ;LOAD ERROR DATA...
                MOVE BREG,OUT1 <CSR3> ;LOAD ERROR DATA...
                MOVE INPI <CSR2>,BREG ;IS IT 0 SET?
                BBR SFTER1 ;YES GO INTERRUPT.
                SBR SFTER1 ;NO RETURN.
                MOVE #300,BREG ;SET BR REQ.
2$:    MOVE INPI <11>,SPAD <0> ;GET VCTR:=XX4.
                OR BREG,SPAD <0> OUT1 <11> ;SET BR REQ. VCTR:=XX4...
4$:    MOVE INPI <11>,BREG ;IS BR GRANTED?
                BBR 4$ ;NOT SPIN ON IT.
                MOVE #277,BREG ;SET TO CLEAR VCTR:=XX4.
                MOVE INPI <CSR11>,SPAD <0> ;SET UPM3 CLEARER...
                AND BREG,SPAD <0> OUT1 <CSR11> ;CLEAR X...
                SBR SPAD <17> PAGE0 ;RETURN TO MAINLAND.
3$:    .WORD 0 ;MICPO-CODE TERMINATOR..
.END

```

DATOUT=	000021	1#
DATO0 =	000002	1#
DATO1 =	000003	1#
DLV1	000224R	548#
DLV2	000226R	245#
DROP	000316R	283
DROP1	002006R	548
DTER1	003162R	723
DYIDI	000014R	122#
SABITS	004120R	660
ENDS=	104413	235#
ERRIYP	000106R	235#
ESAV1	002246R	558*
ESAV2	002250R	559*
ESAV3	002252R	560*
ESAV4	002254R	561#
ESTABLE	002256R	560
EXISTS =	004400	235#
FIRST	000234R	245#
FLAG	000270R	262#
FLAGB	000052	247#
PROCPW=	000005	624#
FTABLE	002252R	235#
GETPAS=	104415	944
GWSUFS=	104414	235#
HRDCNT	000044R	205#
HRDERS=	104405	235#
HRDPAS=	000040R	207#
IBAD1 =	000004	1#
IBA16 =	000004	1#
IBA17 =	000010	1#
ICOUNT	000036R	202#
ICOUNT	000040R	202#
IDC1 =	000093	1#
IDNUM	002554R	235#
ILINT	002554R	659
INISR	002304R	640#
INIT	000030R	199#
INNPR =	000000	1#
INGIN	000356R	385#
INPUT	007359R	389*
INTLINK	004359R	386
INTR	001126R	231#
INTRPT	007530R	115#
KMAAMC	007372R	1794
LDAUT =	000010	1#
LDOPO =	000310R	281#
LUDOP =	000040	485
MAINT =	000017	1#
MAB22\$=	104416	235#
MARHLD=	000000	1#

MARINC=	014000	1#
MASK	000236R	249#
MCDLP	007512R	1145#
MERGE	003054R	145#
MES1	003152R	145#
MES2	002560R	681
MES3	003512R	822
MES4	003303R	751
MESS	003322R	759
MICPC =	000474	1105#
MICKD =	000000	1075#
MILD =	010000	1107#
MLTPLY	004204R	334
MODNAM	000000R	186#
MODSP	000224R	200
MPRLD =	004000	1#
MSGNS =	104403	239#
MSGSS =	000002	239#
MSL	104401	239#
NEXMEM	010460R	1246
NEXME1	010476R	1438
NP RATE	010430R	1258
NP RBYT =	000020	1#
NP RC =	000010	1#
NP TPE	000000R	265#
NULL =	000000	251#
NXMEM =	000001	251#
NXMRRY	003152R	728
OBADO =	000006	1#
OBADI =	000007	1#
OBAL6 =	000004	1#
OBAL7 =	000019	1#
OCR	000000	1#
OPEN =	000000	187#
OTNPR =	000020	222#
OTOAS =	104420	233#
OUISR	007524R	302#
OUTAIN	007524R	973
OUTQOU	007364R	391*
PA	000246R	251#
PAGE0 =	000000	1#
PAGE1 =	001000	1#
PAGE2 =	002000	1#
PAGE3 =	003000	1#
PALMNT =	000034R	201#
PCLK =	000020	1#
PIRINO	007276R	389
PIROUT	007316R	391
PIROS	000004	235#
POPSP =	005726	235#
POUESP2=	005726	235#
PROFB =	007462R	1126#
PRTV =	000000	235#
PRTVO =	000000	235#
PRTV1 =	000040	235#

KMCB DEC/X11 SYSTEM EXERCISER MODULE
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CROSS REFERENCE TABLE -- USER SYMBOLS

SEQ 0034

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CROSS REFERENCE TABLE -- USER SYMBOLS

SFO 0035

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1437	1438	1439	1441	1443	1456	1459	1464	1465	1475	1476	1477	1478
\$\$\$\$DER= 000001		1107#	1108#	1109#	1110#	1111#	1112#	1113#	1114#	1115#	1116#	1117#
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KMCB DEC/X11 SYSTEM EXERCISER MODULE
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CROSS REFERENCE TABLE -- USER SYMBOLS

SEQ 0038

. ABS. 000000 000
010566 001

ERRORS DETECTED: 0
DEFAULT GLOBALS GENERATED: 0

XKMCBO,XKMCBO/SOL/CRF:SYM=DDXCOM,XKMCBO
RUN-TIME: 20 22 1 SECONDS

KMCB DEC/X11 SYSTEM EXERCISER MODULE
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CROSS REFERENCE TABLE -- USER SYMBOLS

SEQ 0039

DIAGNOSTIC ENGINEERING

digital**DECO DEPO SUBMISSION**

FOR RELEASE ENG. USE

 NEW CHANGE DELETE

MD	LIBRARY	PRODUCT NUMBER	REV	PATCH	ECO TALLY	PRODUCT DATE	STATUS	DISTRIBUTION	1ST COPY - RIGHT YEAR	LAST COPY - RIGHT YEAR
	ZZ	CXKMC	B	1	Q1	19 APR 79	OBsolete	X G R	1976	1979

TITLE CXKMCB1 KMC-11 MODULE

AUTHOR D. BUTENHOF MAINTAINING GROUP DEC/X SUPT GRP MAINTAINER D. BUTENHOF SUBMITTING ENGINEER D. BUTENHOF

PRODUCT COMPONENTS

CK	DESCRIPTION	PRODUCT NO.	REV	CK	DESCRIPTION	PRODUCT NO.	REV
	DOCUMENT				INDEX		
	LISTING				SOURCE MEDIA		
	OBJECT MEDIA				TEST MEDIA		
X	DECO	AF-E950B-M1					

PRODUCTS OBSOLETED (other than previous version)

LIBRARY	PRODUCT NUMBER	REV	LIBRARY	PRODUCT NUMBER	REV	LIBRARY	PRODUCT NUMBER	REV
MD		M D				M D		

PRODUCT CHARACTERISTICS

PROCESSORS PRODUCT OPERATES WITH (Enter all applicable 2-digit codes representing the Processor the product operates with. See separate instructions.)

OPERATIONAL CODES (Enter all applicable 2-digit codes that describe the product. See separate instructions.)

ACT/APT/XXDP INFORMATION FIELD	EXT	ACT SEQ NUMBER	ACT/XXDP COMPATIBLE?	APT COMPATIBLE?	1ST PASS RUN TIME SECONDS	SUBSEQUENT PASS RUN TIME SECONDS
			X Y	N X Y N		

DECO/DEPO INFORMATION

PROBLEM REPORTS CLOSED:

DEVICE AFFECTED DEC/X11 MULTIMEDIA AFFECTED? YES NO

KIT NUMBERS	ZJ130-RB						
	ZJ129-RZ, FR						

PROBLEM:

ERRORS WHEN BUFFERS OVERLAP 32K BOUNDARY, DUE TO MICROCODE BUGS.

SOLUTION:

PATCH THE FOLLOWING MODULE LOCATIONS

DEPO PATCH AREA

CHANGE LOC	FROM	TO	CHANGE LOC	FROM	TO
10024	63167	63207			
10246	63167	63207			
10254	61014	61010			

SUBMITTING ENGINEER	MANUFACTURING ENGINEER	SUPPORT ENGINEER	CHARGE DECO/DEPO TO DISCRETE PROJECT NUMBER
D. Butenhof	J. C. Casella		Q98 Q5460
DATE: 19 APR 79	DATE: 25-APR-79	DATE:	
MAINTAINER	FIELD SERVICE	WAIVERING MANAGER	COORDINATION NO.
D. Butenhof	J. C. Casella		MC 3087
DATE: 25 Apr. 79	DATE:		