

IDENTIFICATION

Product Code: Maindec 9A-D7AB-D
Product Name: PDP-9 Basic Exerciser
Date Created: November 27, 1967
Maintainer: Diagnostics Group
Author: J. W. Richardson



1. ABSTRACT

The PDP-9 Basic Exerciser is designed to exercise the CP, core memory and I/O devices associated with a basic PDP-9 configuration. Once initiated, the program will perform tests on all operate and memory reference instructions, tests on the adder, memory checkerboard patterns, tests on the real-time clock, punch, reader, Teletype and program interrupt.

The Basic Exerciser contains a condensed version of the PDP-9 Instruction Test, Parts 1 and 2, and a memory checkerboard test similar to the PDP-9 Basic Memory Checkerboard Test. These tests run continuously, and are interrupted by the punch, reader or Teletype at a device rate. The real-time clock will interrupt and suspend all operations at random time intervals. The instruction test or I/O device resumes operation after the clock interrupt has been serviced.

Nine ACS functions are provided to enable the operator to (1) inhibit the instruction and memory tests and run the real-time clock, program interrupt, and the punch, read, print sequence alone; (2) inhibit program interrupts and run the instruction and memory tests alone; (3) loop continuously on the adder test; (4) loop continuously on the memory checkerboard test; (5) inhibit program relocation; (6) inhibit the real-time clock, but continue testing with program interrupt and all other devices enabled; (7) run the instruction and memory tests, and the clock and punch with the read and print sequence inhibited; (8) run the reader, real-time clock and instruction and memory tests with the punch and Teletype inhibited; (9) run the read and print sequence, real-time clock and instruction and memory tests with the punch inhibited.

2. REQUIREMENTS

2.1 Equipment

A basic PDP-9 configuration.

2.2 Storage

The program requires all 8K of core memory to perform all tests. The program initially resides in memory locations 00000 to 7730. When the program is relocated to the higher 4K field, it occupies locations 10022 to 17730.

3. LOADING PROCEDURE

The tape supplied is punched in the HRI mode.

- a. Set the ADDRESS switches to 00000.
- b. Place all AC switches down.

- c. Place the HRI tape in the reader.
- d. Press I/O RESET, and then READ-IN.

The program is not self-starting.

4. STARTING PROCEDURE

4.1 Starting Addresses

22 or 10022 if the program is currently in the upper 4K field.

4.1.1 Restarting Addresses - 26 or 10026 if the program is currently in the upper 4K field.

4.2

Operator Action

FOR PDP-7, See below

- a. Set the ADDRESS switches to 22.
- b. Place all ACS down for normal program operation. See paragraph 5.1 for use of the ACS to inhibit certain portions of the program.
- c. Press I/O RESET, and then START.
- d. Approximately 3-1/2 feet of leader will be punched. This leader is blank except for one frame which has all channels punched.
- e. Place the punched frame directly over the reader drive sprocket, and position the tape between reader and punch for minimum binding.
- f. Press CONTINUE.
- g. The program will run until an error halt occurs, or manually stopped by the operator.
- h. Steps c through f above are similar to the procedure performed with the PDP-9 punch test.

4.2.1 Restarting Procedure -

- a. Set the ADDRESS switches to 26.
- b. Place all ACS down for normal program operation. See paragraph 5.1 for use of the ACS to inhibit certain portions of the program. Set SW's 6, 7, 8 up.
- c. If the punch, read and print sequence is not inhibited, make sure there is tape in the reader. The tape does not have to be blank leader when restarting.
- d. Press ~~I/O RESET~~, and then START.
- e. The program will run until an error halt occurs, or manually stopped by the operator.

5. OPERATING PROCEDURE5.1 Operational Switch Settings

Normal program operation is achieved by placing all ACS down before starting from locations 22 or 26.

The operator is provided nine options with which to modify the operation of the program. These may be selected by placing any one or a combination of ACS 0 through 8 up before starting from locations 22 or 26.

To make changes in the ACS settings, the program must be stopped by the operator before the changes are made. The program must then be restarted from location 26 (or 22 if new leader is desired). The program may not recognize the new ACS settings if the above procedure is not followed.

ACS Functions

- 0 (1) Run only the punch, read and print sequence plus the real-time clock. Program interrupt will be enabled.
- 1 (1) Inhibit the punch, read and print sequence. Program interrupt is disabled. The real-time clock is on. The complete instruction test and memory checkerboard test will be performed.
- 2 (1) Loop continuously on the "add random pairs" test. The real-time clock, punch, read and print sequence plus program interrupt will be enabled unless specified otherwise by an ACS.
- 3 (1) Loop continuously on the memory checkerboard test. Program relocation will not take place. Program action otherwise is the same as that described for ACS 2.
- 4 (1) Inhibit program relocation. Unless otherwise specified, the program will run in a normal way, but will not relocate from its current 4K field location to the opposite field after completing the memory checkerboard test.
- 5 (1) Inhibit clock. Unless otherwise specified, program action is normal except that the clock should always be off.
- 6 (1) Inhibit the reader and TTY. The punch will run continuously. Tape must be in the reader to prevent the no-tape indicator from being set. Program action is normal unless otherwise specified.
- 7 (1) Inhibit the punch and TTY. The reader will run continuously. A loop or fan-fold tape with any data may be used. Program action is normal unless otherwise specified.
- 8 (1) Inhibit the punch. The reader will read 52 characters at full speed and then halt. The TTY will then print the 52 characters read. Any tape loop or fan-fold tape may be used. Program action is normal unless otherwise specified.

Any combination of the nine ACS may be used, as long as the operations do not conflict; i.e., if ACS 2 and 3 are both up, the add random pairs test would be looped. Memory checkerboard would not be run unless the program is restarted with ACS 3 alone.

*Note: For PDP-7 operation,
Patch 162 to 600223.
(End of tape with section 1 finished.)*

PDP-7

*If using tape loop, use SW 8 only - Do not let
reader run out of tape.*

The I/O devices may be controlled with several combinations of ACS 6, 7 and 8. If ACS 7 and 8 are both up the reader will run continuously, as if ACS 7 only were up. If ACS 6 and 7 or 6 and 8 are up, all devices will be inhibited. Program interrupt and the real-time clock will be enabled unless otherwise specified.

5.2 Subroutine Abstracts

The PDP-9 Basic Exerciser may be thought of as three separate programs; i.e., the instruction and memory tests; punch, read and print sequence, and operation of the real-time clock. The instruction and memory tests will be interrupted, at a device rate, by the punch, reader or Teletype. The clock will randomly interrupt any of the above operations at a rate determined by the program. After each clock interrupt, the clock is reinitialized with a new number obtained by a random number generator. The clock interrupts should occur no less than 2 seconds apart, nor more than 9 seconds. The clock interrupts take first priority, followed by the Teletype, reader, and punch.

5.2.1 Instruction and Memory Tests - The instruction test portion of the Basic Exerciser performs tests on all operate group and memory reference instructions. The individual instructions are looped a random number of times before proceeding to the next test. The maximum number of loops made on any one test is 32,767.

The adder is tested using two different methods. The first performs bit by bit tests on the adder using the ADD instruction. Besides checking for correct results after an addition, the link is tested during overflow and no overflow conditions.

The second method, the "Add Random Pairs" test, tests the adder using one pair of random numbers (A and B) and their 1's complement values (-A and -B), and the ADD instruction. These four values are added in various combinations, the results of which are compared against precalculated results. The precalculated results are obtained by adding the two pairs together using the TAD instruction. Four additions are made, the results of which are used in the test. The link is tested after each addition. If it is a 1, a 1 is added to the result to simulate an end-around-carry.

The numbers added and their sums are indicated in the listing using the following symbols:

$-B + (-A)$	= SUMNEG
$A + B$	= SUMPOS
$B - A$	= BMASUM
$A - B$	= AMBSUM

The values of A, -A, B and -B plus their sums are used to test the combinations of ADD's shown below.

<u>ADD</u>	<u>SUM SHOULD EQUAL</u>	
A + B	SUMPOS	
-B + A	AMBSUM	
-B + (-A)	SUMNEG	
B - A	BMASUM	
(A + B) - A	BPOS (B)	
(B - A) - B	ANEG (-A)	
(-A-B) +A	BNEG (-B)	
(A - B) +B	APOS (A)	
777777 + A	APOS	
A + B - A	BPOS	
A + B -A -A	BMASUM (B - A)	
A + B -A -A -B	ANEG	
A + B -A -A -B -B	SUMNEG (-A -B)	
A + B -A -A -B -B + A	BNEG	
A + B -A -A -B -B + A + A	AMBSUM (-B + A)	
A + B -A -A -B -B + A +A +B	APOS	

After completing one pass of the above tests, a second pass is made on the same tests. The second pass makes all "B" constants "A", and all "A" constants "B" before repeating.

Immediately following the second pass, one random number and its 1s complement is obtained and saved in APOS and ANEG, respectively. Bit 0 of APOS is tested for equaling 0 or 1. If the value is 1, the bit remains unchanged, and the respective bit in the complement number is changed to equal a 1. The two numbers are then added together, the sum of which should equal all 0s except for bit 0. If the ADD is successful, the program continues testing all other bit positions in the same manner.

Example: (Bit 0 altered)

<u>Step</u>	<u>APOS Value</u>	<u>ANEG Value</u>
1	577776	200001
2	577776	600001 (bit 0 altered)
3	Add together. Result should = altered bit.	
	+ 101 111 111 111 111 110 110 000 000 000 000 001	
	<hr/>	
	+ 011 111 111 111 111 111 1	(end around carry)
	<hr/>	
	100 000 000 000 000 000	

The sum equals the altered bit position.

After completing the adder tests, the remaining memory reference instructions are tested. The last test performed by the exerciser is the memory checkerboard test. This routine writes and reads four different checkerboard patterns similar to the Basic Memory Checkerboard program. The memory test is looped three times before program relocation takes place.

After completing the memory checkerboard test, the program relocates the entire Basic Exerciser to the opposite 4K field. All memory reference instructions and memory locations used for testing are adjusted accordingly. All locations within the program which reference any memory location between 0 and 21 are not adjusted. These locations are used during program interrupts, autoindexing tests, etc., and must not be altered. Program interrupt is disabled while relocation is taking place.

After relocation of the program is completed, the exerciser is automatically restarted at location 70 (or 10067). This location is tagged SEQUEN. The operator is able to determine the location of the program by observing MB bit 5. This bit will glow brightly when the program is in the higher 4K field, as compared to when residing in the lower 4K field.

The Teletype BELL will ring once for each completed pass of the program. One pass is defined as the program performing all tests from each 4K field, and then relocating back to the field in which the program was first initiated.

When operating the Basic Exerciser with program interrupt inhibited (ACS 1 up), the message "COMPLETE" will be printed after five complete passes of the program. This message is printed after ten passes when ACS 4 (inhibit relocation) as well as ACS 1 is up. This feature is included as a means to determine the number of successful passes completed by the program if it is to be run for extended periods of time.

5.2.2 Punch, Read, Print Sequence - The instruction and memory tests will be interrupted, at a device rate, by the punch, reader, or Teletype. The data punched consists of the alphabet characters, followed by numbers 0 through 9, with a space character being punched between each letter or numeral character. The reader will read the tape at punch speed, storing away any punched character. Frames of all 0s are ignored. The punch and read sequence consists of 52 ASCII characters punched, read and stored away in an input buffer (tagged TTBUFA). After reading the 52nd character, the contents of TTBUFA are transferred directly to another 52 - location buffer tagged TTBUFB. This second buffer is provided to enable the operator to stop the program and compare the contents of either buffer A or buffer B with the punched data on tape. Punch and read operation is halted after the 52nd character is read and stored. The contents of TTBUFB are then printed on the Teletype. The punch and read sequence continues immediately after the 52nd character is printed. The data punched and read should appear on the Teletype as the example below.

A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T	U	V	W	X	Y	Z	0	1	2	3	4	5	6	7	8	9
A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T	U	V	W	X	Y	Z	0	1	2	3	4	5	6	7	8	9

The punch and read sequence generates 72 characters altogether, even though only 52 are punched, read and printed at one time. The alphabet and numbers sets with a space between each character enables a full line to be printed. The spacing also enables the operator to more easily detect a misprinted character. Each group of 72 characters is separated by 8 blank frames. The group which is positioned in the reader is the current line being printed. A carriage return and a line feed is punched at the end of each group. The program will punch 6 extra blank frames between two groups, approximately every fourteenth group, to enable the slack between reader and punch to remain constant.

5.2.3 Use of the Real Time Clock - The instruction and memory tests, and the punch, read and print sequence are both interrupted randomly by the real-time clock. When a clock interrupt occurs, all other operations are halted until the clock interrupt has been serviced. The program allows the clock to continue incrementing for 1/2 second after the interrupt occurs. This is evident during every clock interrupt by observing MB indicators 12 through 17 incrementing. Immediately after the clock has incremented, an additional 1/2 second, it is reset to a new random value. This value is chosen by the program to ensure that the clock interrupts no sooner than 2 seconds, nor later than 9 seconds. The clock is again enabled after being reset to a new value, and the instruction test or read, punch and print sequence is allowed to continue from the point of interrupt.

At times, the Basic Exerciser may appear to be caught in a loop after a clock interrupt occurs. The console indicators will show the clock and PIE as being disabled, and the punch, read and print sequence will be halted for several seconds. The program during this time is attempting to generate a number for the clock which falls within the 2 to 9 second limit. All operations will be resumed as soon as a suitable random value is found.

The operator may disable the clock interrupts by restarting from location 26 with ACS 5 up.

5.2.4 Interrupt Service Routine - Program interrupts by the clock, punch, reader or Teletype are all serviced by a common routine. A common routine for reentering the instruction test is also used.

Locations 0 through 6 are used to save the contents of the AC and PC immediately after an interrupt occurs. The contents of the AC are stored in the location tagged SAVAC. The contents of the link and PC are stored in the location tagged RJMP. The program then enters a routine which determines which of the four devices interrupted the program. This routine is tagged SRVINT. SRVINT will test for device flags in the following order: clock, Teletype, no-tape flags, reader, punch. The first device flag found to be set indicates the device which must be reinitiated by the program.

Immediately after selecting the proper device, a routine is entered which will restore the contents of the link and AC at the time of the program interrupt. The routine is tagged RTNIT. RTNIT first restores the AC (from SAVAC); restores the link (by testing bit 0 of RJMP); enables program interrupt; and then returns to the instruction test by a JMP indirect on the contents of RJMP.

The operator may disable program interrupts by restarting from location 26 with ACS 1 up.

5.3 Program and Operator Action

See Sections 4.2 and 5.2.

6. ERRORS

6.1 Error Halts and Description

Reference the program listing for all error halts.

All error halts are tagged EXXX, and are commented to aid debugging. Each test is self-contained, and may be looped. See Section 6.2.1 for looping instructions.

Unless a solution is obvious from following the listing, the proper MAINDEC diagnostic for the device in error should be run. This should be necessary mainly when errors are caused by one of the I/O devices. The diagnostics for the I/O devices are listed below:

<u>Device</u>	<u>Program</u>
Real-Time Clock	Instruction Test Part 1
Program Interrupt	Instruction Test Part 1
Punch	Punch Test (9A-D2DB)
Reader	Reader Test (9A-D2CB)
Teletype	TTY Test (9A-D2BB)

Incorrect operation of the real-time clock will appear as clock interrupts occurring sooner than 2 seconds apart, or greater than 9 seconds, or possibly no clock interrupts will occur. Also, after a clock interrupt, the clock should not increment further for any longer than approximately 1/2 second.

Printing of incorrect data may be caused by the data being incorrectly punched, read, or printed. Storage registers, and their locations in the program, which the punch, read, print sequence use are listed below.

<u>Tag</u>	<u>Function</u>
SAVAC (7643)	Saves contents of AC after a program interrupt.
RJMP (7644)	Saves contents of PC and link after a program interrupt.
WORK (7600)	Bit 1 if set indicates TTY is in use.
GOPNCH (6763)	Contains contents of PC at exit from punch routine.
SETCLK (6604)	Routine which sets a random value in clock register 7 when program interrupt is disabled.
CLKSET (6621)	Same function as SETCLK, but is used only after a clock interrupt.
TTOUT (7326)	Location pointer for TTBUFB when printing.
TTIN (7327)	Location pointer for TTBUFA when reading.

<u>Tag</u>	<u>Function</u>
TTBUFA (7361 to 7444)	Storage buffer for characters read.
TTBUFB (7445 to 7531)	Storage for characters to be printed. Contents should equal TTBUFA.
STORE (7336)	Contains character punched.
SETTY (7037)	Routine which is entered after 52 characters have been punched and read. Sets up TTBUFB before printing.
GENRAN (6101)	Random number generator used when PI is disabled.
RANGEN (6133)	Same as GENRAN, but used only after an interrupt.

When data is incorrectly printed, stop the program during print-out. This will enable both TTBUFA and TTBUFB to remain unchanged. TTBUFA will be changed as soon as reading begins.

The data punched is in ASCII mode, and one printed line is indicated on the paper tape by 8 blank frames separating each line. The punched data starts with character A (301) and ends with a line feed (212). A space (240) is punched between all alphabet and number characters.

The line of punched characters in the reader is the line currently being printed. The operator may inspect the tape for an incorrect character punched. If it appears correctly on the tape, it may have been read or printed incorrectly. The characters read are stored in a 52-word buffer beginning at location 7330 (tagged TTBUFA). The characters being printed are stored in a 52-word buffer beginning at location 7414 (tagged TTBUFB). If the program was stopped during printing, these two buffers should contain exactly the same information. The first character read or printed is stored in the first location of either buffer. One character is stored per location. If the data was read incorrectly, the contents of TTBUFA will not equal the last 52 characters on the tape. If the data on tape, and in TTBUFA and TTBUFB are equal, the teleprinter may be at fault.

6.2 Error Recovery

Press CONTINUE to receive further error halts or to continue testing, as indicated by the listing.

Recovery from error halts in the Add Random Pairs test is accomplished by pressing CONTINUE one or more times, depending on the type of error encountered. Pressing CONTINUE after a halt due to an incorrect sum will result in a second halt. The AC will equal the incorrect sum at the first halt, and the sum used for comparison at the second halt. If the error halt is the result of a LINK error, the next test in sequence will be executed.

Recovery from memory checkerboard errors is accomplished by pressing CONTINUE four times. The memory test is restarted after each error halt.

The contents of the AC after each halt will equal the information below.

<u>C (PC)</u>	<u>C (AC)</u>
6351	The address at which the memory error occurred.
6353	What the location should equal (000000 or 777777).
6355	The data as read.
6362	The pattern control word used.

Bit suppression may be accomplished by placing the corresponding ACS up after the halt at location 6354. Place all other ACS down, press CONTINUE, place all ACS down again, and then restore ACS 0 through 5 if needed. The bits selected will not be tested again during the memory test. The selected bits must be reselected after each error halt.

The memory checkerboard test may be continuously looped by restarting from location 26 with ACS 3 up. Program relocation will not take place.

6.2.1 Looping on Individual Tests - Looping on individual tests, except for the interrupt routines, Add Random Pairs test and Memory test, is accomplished by placing a JMP instruction in the first location of the next test in sequence. The address in the JMP instruction should equal the first location of the test to be looped. Restart the program at location 26, if program interrupt is to be enabled. Restart at the first location of the test to be looped, if interrupts are not wanted.

The complete series of tests for any one instruction may be looped by placing a NOP in the location which contains ISZ WORK3. This instruction appears at the end of each series of tests for each instruction. Restart at location 26, or at the beginning of the test to be looped.

6.2.2 Looping on Add Random Pairs - The complete series of tests may be looped by restarting from location 26 with ACS 2 up.

The individual tests may be looped by changing the LAW instruction, appearing after each test, to a JMP. For example, to loop on $(A-B) + B = A$ (tagged AMBPBT), change the LAW AMBPBT instruction to JMP AMBPBT. Restart from location 26, or AMBPBT.

6.2.3 Looping on Memory Checkerboard - Place ACS 3 up, and restart from location 26.

6.2.4 Error Print-outs - The program continually tests for reader or punch no tape indicators being set. When either indicator is set the message "R NO TAPE", or "P NO TAPE" will be printed. The program continues on in sequence after either print-out.

7. OPERATING RESTRICTIONS

All MAINDEC diagnostics which apply to a basic PDP-9 configuration should be run before attempting to run this program.

8. MISCELLANEOUS8.1 Execution Time

Approximately 1-1/2 minutes are required to execute all tests for one 4K field.

9. PROGRAM DESCRIPTION

The Basic Exerciser performs tests on all operate and memory reference instructions plus core memory.

During normal operation (all ACS down) the program exercises the real-time clock, punch, reader, Teletype and program interrupt while the instruction test portion is running. The MB indicators will change according to the portion of the instruction test currently being executed. When a clock interrupt occurs the MB bits 12 to 17 will increment for 1/2 second (due to clock counts), after which the MB indicators will return to their original state upon re-entering the instruction test.

When the punch, read and print sequence is operated with the instruction test inhibited (ACS 0 up) the MB will indicate a constant 600131 (JMP 131). On the listing this is written as JMP. at location 131. At location 130 the program interrupt is enabled, and all device interrupts will cause an interrupt immediately after the execution of the JMP. instruction. The interrupts are handled in the same manner as if the instruction test portion were operating; the difference being that the interrupt service routine (RTNIT) always returns to location 131 after reinitiating the device which caused the interrupt, instead of returning to the instruction test portion.

The instruction test portion of the program consists of MAINDEC 9A-D01A and D02A (Instruction Test, Parts 1 and 2) condensed onto one tape. Refer to the program listing or flow chart for the testing sequence.

The memory test is executed after the instruction test portion is completed. This test is similar to MAINDEC 9A-D1AA Basic Memory Checkerboard Test. This test is performed three times, after which a routine is entered which relocates the Basic Exerciser to the opposite 4K field in core memory (unless ACS 4 is up). The Exerciser is automatically restarted after relocation is completed.

If no errors occur, the Basic Exerciser will run until stopped by the operator.

10. LISTING

BASEX9 PAGE 1

```

.TITLE BASFX9
/PNP-9 BASIC EXERCISFR
/
.ABS
.LOC 0
/
/PNP-9 INSTRUCTION TEST (CONDENSED)
/
00000 0000000
00001 741000
00002 740040
00003 047643
00004 200000
00005 047644
00006 606536
00007 740000
00008 740000
00009 740000
00010 740040
00022 107171
00023 107203
00024 107171
00025 740040
00026 147635
00027 147312
00030 703302
00031 147631
00032 146763
00033 206676
00034 047327
00035 047326
00036 447326
00037 167326
00040 207326
00041 546677
00042 741000
00043 600036
00044 206676
00045 047326
00046 206124
00047 047313
00050 777737
00051 047242
00052 447313
00053 167313
00054 447242
00055 600052

.F1      HALT
        DAC SAVAC
        DAC RJMP
        JMP SRVINT
        NOP1=NOP
        NOP2=NOP
        NOP3=NOP
        HALT=HLT
        .LOC 22
        REGIN    JMS PNLEDR
        JMS PNMARK
        JMS PNLEDR
        HALT
        D2M WORK4
        D2M BREAK
        CAF
        D2M WORK
        D2M GOPNCH
        LAC DATARL
        DAC TTIN
        DAC TTOUT
        ISZ TTOUT
        D2M* TTOUT
        LAC TTOUT
        SAD ENDBIN
        SKP
        JMP .-5
        LAC DATARL
        DAC TTOUT
        LAC ENDTRL
        DAC WDCNT
        LAW -41
        DAC CRLF
        ISZ WDCNT
        D2M* WDCNT
        ISZ CRLF
        JMP .-3
        .EJECT

/ERROR. PC INCREMENTED BY
/2 AT TIME OF P. I.
/SAVE AC
/SAVE PC AND LINK
/SERVICE INTERRUPT
/PRESS CONTINUE TO
/CLEAR GOPNCH
/SETUP POINTERS
/CLEAR TTY BIN
/RESTORE POINTER
/CLEAR ERROR TABLE

```

0P056	700002	I0F	/PI OFF
0N157	750000	CLA	
0N160	700104	RSA	/INITIALIZE READER, PUNCH
0C161	750004	LAS	
0P062	507547	AND K3K	
0N063	741200	SNA	/IF ACS 7 OR 8 A 1, DON'T PUNCH
0P064	700204	PSA	
0C165	200130	LAC INTPI	/INITPI = PNSTRT
0P066	046763	DAC GUPNCH	
0P067	047644	DAC RJMP	/RJMP = PNSTRT
0P070	207617	SEQUFN	/RESTORE ADDRESS 1 (SKP)
0C071	040001	LAC KSKP	
0P072	206533	DAC 1	/RESTORE ADDRESS 3
0P073	040003	DAC 3	
0P074	207652	LAC SAV3	/RESTORE ADDRESS 4
0P075	040004	DAC 4	
0P076	206534	LAC SAV5	/RESTORE ADDRESS 5
0P077	040005	DAC 5	
0P100	206535	LAC SAV6	/RESTORE ADDRESS 6
0P101	040006	DAC 6	
0P102	207650	LAC KHALT	/RESTORE ADDRESS 2 (HALT)
0P103	040002	DAC 2	
0P104	447635	ISZ WORK4	/PASS COUNTER
0P105	750004	LAS	
0P106	507556	AND K1MK	
0P107	741200	SNA	/CHECK ACS 5 FOR INHIBIT CLOCK
0P110	106604	JMS SETCLK	
0P111	750004	LAS	
0P112	741100	SPA	
0P113	600131	JMP INHIT	/INHIBIT INST, TTEST
0P114	740010	RAL	
0P115	740100	SMA	/CHECK FOR INHIBIT PI
0P116	700042	TON	/PI ON
0P117	750004	LAS	
0P120	507321	AND K1MK	
0P121	740200	SZA	/CHECK LOOP ON RANDOM ADD
0P122	602360	JMP RANADD-2	/LOOP
0P123	750004	LAS	
0P124	507553	AND K4MK	
0P125	740200	SZA	/CHECK LOOP ON CHECKERBOARD
0P126	606055	JMP E641+3	/LOOP
0P127	600133	JMP TOTST-2	
0P130	006702	/	
		INITPI	PNSTRT
		/	
		/INHIBIT INSTRUCTION TEST	
0P131	700042	INHIT	TON
0P132	600132		JMP .
			/WAIT FOR PI
			.EJECT

/TEST CLEAR AC AT EVENT TIME 1 WITH MB 14.

/

00133	106102	JMS GENRAN	/GET NO. FOR LOOP
00134	106126	JMS CKNO	
00135	750001	TOTST	AC = 7777777
00136	700110		
00137	740200	SZA	AC = 0
00140	740040	F24 HALT	/ERROR. AC NOT 0
		/	
00141	750001	CLA!CMA	AC = 777777
00142	700210	700210	
00143	740200	SZA	AC = 0
00144	740040	F25 HALT	/ERROR. AC NOT 0
		/	
00145	750001	CLA!CMA	AC = 777777
00146	700310	700310	
00147	740200	SZA	AC = 0
00150	740040	F26 HALT	/ERROR. AC NOT 0
		/	
00151	750001	CLA!CMA	AC = 777777
00152	700010	700010	
00153	740200	SZA	AC = 0
00154	740040	F27 HALT	/ERROR. AC NOT 0
		/	
00155	447634	ISZ WORK3	/CHECK DONE LOOPING
00156	600135	JMP TOTST	/LOOP
00157	106102	JMS GENRAN	
00160	106126	JMS CKNO	
		.EJECT	/GET NO. FOR NFXT TEST

+++

```

/TTEST IOT 3344 (DBR), L= 0
/
00161 744000 TSDBR CLL /LINK = 0
00162 100204 JMS DBRX JMP 223
00163 741400 SNL
00164 740040 F28 HALT /ERROR. DRR FAILED; LINK NOT 0
/
/TTEST IOT 3344 (DBR), L = 1
/
00165 744002 CLL:CML /L = 1
00166 100204 JMS DBRX
00167 740400 SNL
00170 740040 F29 HALT /ERROR. DRR FAILED. LINK NOT 1
/
/TTEST IOT 3344 (DBR), L = 0
/
00171 754000 CLL:CLA /AC, L = 0
00172 100207 JMS DBRXX
00173 740400 SNL
00174 740040 E30 HALT /ERROR. DRR FAILED, LINK NOT 1
/
/TTEST IOT 3344 (DBR), L = 1
/
00175 754002 CLL:CML:CLA /L = 1, AC = 0
00176 100215 JMS DBRXXX
00177 751400 CLA:SZL
00200 740040 E31 HALT /ERROR. DRR FAILED, LINK NOT 0
00201 447634 TSZ WORK3 /CHECK DONE LOOPING
00202 600161 JMP TSDBR /LOOP
00203 600223 JMP OPRAT /START INSTRUCTION TST
/
00204 000000 DBRX 0 /LEAVE LINK ALONE
00205 703344 703344 /DRR
00206 620204 JMP* DBRX
/
00207 000000 DBRXX 0
00210 200207 LAC DBRXX /SET LINK TO A ONE
00211 347554 TAD K400K
00212 040207 DAC DBRXX
00213 703344 703344 /DRR
00214 620207 JMP* DBRXX
/
00215 000000 DBRXXX 0
00216 200215 LAC DBRXXX /CLEAR LINK
00217 507627 AND M400K
00220 040215 DAC DBRXXX
00221 703344 703344 /DRR
00222 620215 JMP* DBRXXX
,EJECT

```

		/TEST OPFRATF GROUP		
00223	106102	OPFRAT	JMS GENRAN	/GET NO. FOR LOOP ON TFST
00224	106126	OPFRAT	JMS CKNO	
00225	777777	OPFRAT	LAW 17777	/AC = 777777
		/		
00226	741000		SKP	/TFST SKP
00227	740040	F32	HALT	/ERROR; SKP FAILED TO SKIP
		/		
00230	750000		CLA	/AC = 0
00231	740200		SZA	
00232	740040	F33	HALT	/ERROR; CLA OR SZA FAILED TO SKIP
		/		
00233	750000		CLA	/AC = 0
00234	740100		SMA	
00235	741000		SKP	
00236	740040	F34	HALT	/ERROR; SAME SKIPPED
		/		
00237	750000		CLA	
00240	741100		SPA	
00241	740040	F35	HALT	/ERROR; SPA FAILED TO SKIP
		/		
00242	750000		CLA	
00243	741200		SNA	
00244	741000		SKP	
00245	740040	F36	HALT	/ERROR; SNA SKIPPED
		/		
00246	744000		CLL	/LINK = 0
00247	741400		SZL	
00250	740040	E37	HALT	/ERROR; SZL FAILED TO SKIP OR /CLL FAILED TO CLEAR LINK
		/		
00251	744000		CLL	/LINK = 0
00252	740400		SNL	
00253	741000		SKP	
00254	740040	F38	HALT	/ERROR; SNL SKIPPED
			.EJECT	

```

+ + +
    /TEST CLA CLL           /AC, LINK = 0
    00255 754000             CLA!CLL
    00256 740200             SZA
    00257 740040             F39   HALT
    /
    /TEST CLA CLL           /AC AND LINK = 0
    00260 754000             CLA!CLL
    00261 741400             SZL
    00262 740040             F40   HALT
    /
    /TEST SKP SPA           /ERROR; AC NOT 0
    00263 750000             CLA
    00264 741100             SKP!SPA
    00265 740040             F41   HALT
    /
    /TEST SKP SNA           /ERROR; SKP!SPA FAILED TO SKIP
    00266 750000             CLA
    00267 741200             SKP!SNA
    00270 741000             SKP
    00271 740040             F42   HALT
    /
    /TEST SKP SZL           /ERROR; SKP!SNA SKIPPED
    00272 744000             CLL
    00273 741400             SKP!SZL
    00274 740040             E43   HALT
    /
    /TEST SPA SNA           /LINK = 1
    00275 750000             CLA
    00276 741300             SPA!SNA
    00277 741000             SKP
    00300 740040             F44   HALT
    /
    /TEST SPA SZL           /ERROR; SPA!SNA SKIPPED
    00301 754000             CLA!CLL
    00302 741500             SPA!SZL
    00303 740040             F45   HALT
    /
    /TEST SNA SZL           /LINK AND AC = 0
    00304 754000             CLA!CLL
    00305 741600             SNA!SZL
    00306 741000             SKP
    00307 740040             F46   HALT
    /
    /TEST SNA, SPA, SKP, SZL /AC AND LINK = 0
    00310 754000             CLA!CLL
    00311 741700             SKP!SPA!SZL!SNA
    00312 741000             SKP
    00313 740040             F47   HALT
                                .EJECT

```

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***+
  00314 750000 /TEST SMA SZA
                  CLA
  00315 740300 SMA:SZA
  00316 740040 E48 HALT      /ERROR; SMA SZA FAILED TO SKIP

  00317 754000 /TEST SMA SNL
  00320 740500   CLA:CLL
  00321 741000   SMA:SNL
  00322 740040   SKP
                  E49 HALT      /ERROR; SMA:SNL SKIPPED

  00323 754000 /TEST SZA SNL
  00324 740600   CLA:CLL
  00325 740040   SZA:SNL
                  F50 HALT      /ERROR; SZA:SNL SKIPPED

  00326 754000 /TEST SMA SZA:SNL
  00327 740700   CLA:CLL
  00330 740040   SMA:SZA:SNL
                  F51 HALT      /ERROR; SMA:SZA:SNL FAILED TO SKIP

  00331 744000 /TEST CML - SZL
  00332 740002   CLL
  00333 741400   CML
  00334 741000   SZL
  00335 740040   SKP
                  F52 HALT      /ERROR; SZL SKIPPED OR
                                         /CML FAILED TO SET LINK

  00336 744000 /TEST CLL
  00337 740002   CLL
  00340 744000   CML
  00341 741400   CLL
  00342 740040   SZL
                  F53 HALT      /ERROR; CLL FAILED TO CLEAR LINK

  00343 744000 /TEST CML
  00344 740002   CLL
  00345 740002   CML
  00346 741400   CLL
  00347 740040   SZL
                  F54 HALT      /ERROR; CML FAILED TO SET LINK

  00350 744000 /TEST CLL CML
  00351 740002   CLL
  00352 744002   CML
  00353 741400   CLL:CML
  00354 741000   SZL
  00355 740040   SKP
                  F55 HALT      /ERROR; CLL:CML FAILED TO SET LINK
                  .EJECT

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00356	744000	/TEST CLL CML	
00357	740002	CLL	/LINK = 0
00360	744000	CML	/LINK = 1
00361	744002	CLL	/LINK = 0
00362	741400	CLL:CML	/LINK = 1
00363	741000	SZL	
00364	740040	SKP	
		F56 HALT	/ERROR; CLL:CML FAILED TO SET LINK
		/	
00365	744000	/TEST SKP SZL	
00366	741400	CLL	/LINK = 0
00367	740040	SKP!SZL	
		F57 HALT	/ERROR; SKP!SZL FAILED TO SKIP
		/	
00370	750000	/TEST SZL SNA	
00371	744002	CLA	/AC = 0
00372	741600	CLL:CML	/LINK = 1
00373	741000	SZL!SNA	
00374	740040	SKP	
		F58 HALT	/ERROR; SZL!SNA SKIPPED
		/	
00375	750000	/TEST SZL SPA	
00376	744002	CLA	/AC = 0
00377	741500	CLL:CML	/LINK = 1
00400	741000	SZL!SPA	
00401	740040	SKP	
		F59 HALT	/ERROR; SZL!SPA SKIPPED
		/	
00402	754002	/TEST CLA CLL CML	
00403	741400	CLA!CLL:CML	/AC = 0, LINK = 1
00404	741000	SZL	
00405	740040	SKP	
		E60 HALT	/ERROR; LINK NOT 1
		/	
00406	754002	/TEST CLA CLL CML	
00407	740200	CLA!CLL:CML	/AC = 0, LINK = 1
00410	740040	SZA	
		F61 HALT	/ERROR; AC NOT 0
		/	
00411	754002	/TEST SNL SZA	
00412	740600	CLA!CLL:CML	/AC = 0, LINK = 1
00413	740040	SNL!SZA	
		F62 HALT	/ERROR; SNL!SZA FAILED TO SKIP
		/	
00414	754002	/TEST SNL SMA	
00415	740500	CLA!CLL:CML	/AC = 0, LINK = 1
00416	740040	SNL!SMA	
		E63 HALT	/ERROR; SNL!SMA FAILED TO SKIP
		.EJECT	

00417	754002	/TEST SNL SZA SMA F64	CLA!CLL!CML SNI!SZA!SMA HALT	/AC = 0, LINK = 1 /ERROR; SNL!SZA!SMA FAILED TO SKIP
00420	740700			
00421	740040			
		/		
00422	750000	/TEST CMA CLA F65	CLA CMA CLA SNA SKP HALT	/AC = 0 /AC = ONES /ERROR; CLA FAILED TO CLEAR AC
00423	740001			
00424	750000			
00425	741200			
00426	741000			
00427	740040			
		/		
00430	750000	/TEST CMA SPA F66	CLA CMA SPA SKP HALT	/AC = 0 /AC = ONES /ERROR; SPA SKIPPED OR /CMA FAILED TO SET AC BIT 0
00431	740001			
00432	741100			
00433	741000			
00434	740040			
		/		
00435	750000	/TEST CMA SNA F67	CLA CMA SNA HALT	/AC = 0 /AC = ONES /ERROR; SNA FAILED TO SKIP /OR CMA FAILED TO SET ANY AC BIT
00436	740001			
00437	741200			
00440	740040			
		/		
00441	750000	/TEST CMA F68	CLA CMA CMA SNA SKP HALT	/AC = 0 /AC = ONES /AC = 0 /ERROR; CMA FAILED TO /COMPLEMENT AC TO 0
00442	740001			
00443	740001			
00444	741200			
00445	741000			
00446	740040			
		/		
00447	750001	/TEST CLA CMA E69	CLA!CMA SNA HALT	/AC = ONES /ERROR; CLA!CMA FAILED TO /SET ANY AC BIT
00450	741200			
00451	740040			

.EJECT

		/TEST SZA	
00452	750001	CLA!CMA	/AC = ONES
00453	740200	SZA	
00454	741000	SKP	
00455	740040	F70 HALT	/ERROR; SZA SKIPPED
		/	
		/TFST SMA	
00456	750001	CLA!CMA	/AC = ONES
00457	740100	SMA	
00460	740040	F71 HALT	/ERROR; SMA FAILED TO SKIP
		/	
		/TFST SKP SPA	
00461	750001	CLA!CMA	/AC = ONES
00462	741100	SKP!SPA	
00463	741000	SKP	
00464	740040	F72 HALT	/ERROR; SKP!SPA SKIPPED
		/	36001 TA2100
		/TFST SKP SNA	
00465	750001	CLA!CMA	/AC = ONES
00466	741200	SKP!SNA	
00467	740040	F73 HALT	/ERROR; SKP!SNA FAILED TO SKIP
		/	
		/TFST SPA SNA	
00470	750001	CLA!CMA	/AC = ONES
00471	741300	SPA!SNA	
00472	741000	SKP	
00473	740040	F74 HALT	/ERROR; SPA!SNA SKIPPED
		/	
		/TEST SKP SNA SPA	
00474	754003	CLA!CMA!CLL!CML	/AC = ONES, LINK = 1
00475	741700	SNA!SPA!SKP!SZN	
00476	741000	SKP	
00477	740040	F75 HALT	/ERROR; SKP!SNA!SPA!SZN SKIPPED
		/	
		/TFST SMA!SZA	
00500	750001	CLA!CMA	/AC = ONES
00501	740300	SMA!SZA	
00502	740040	E76 HALT	/ERROR; SMA!SZA FAILED TO SKIP
		/	
		/TFST SMA SZA SNI	
00503	754003	CLA!CMA!CLL!CML	/AC = ONE, LINK = 1
00504	740700	SMA!SZA!SNI	
00505	740040	F77 HALT	/ERROR; SMA!SZA!SNI
		/	
		/TEST NOP	
00506	750001	CLA!CMA	/AC = ONES
00507	740000	NOP	
00510	740001	CMA	/AC = 0
00511	740200	SZA	
00512	740040	F78 HALT	/ERROR; NOP ALTERED THE AC
		/	
		.EJECT	

```

***** /TEST NOP
00513 750000 CLA /AC = 0
00514 740000 NOP
00515 740200 SZA
00516 740040 F79 HALT /ERROR; NOP SET AN AC BIT

/ /TEST NOT
00517 744002 CLL:CML /LINK = 1
00520 740000 NOP
00521 740400 SNL
00522 740040 E80 HALT /ERROR; NO CLEARED THE LINK

/ /TEST NOP
00523 744000 CLL /LINK = 0
00524 740000 NOP
00525 741400 SZL
00526 740040 E81 HALT /ERROR; NOP SET THE LINK

/ /TEST SZA CMA
00527 750000 CLA /AC = 0
00530 740201 SZA:CMA /AC = ONES
00531 740040 F82 HALT /ERROR; SZA FAILED TO SKIP

/ /TEST SZA CLA
00532 750001 CLL:CMA /AC = ONES
00533 750200 SZA:CLA /AC = 0
00534 741000 SKP
00535 740040 E83 HALT /ERROR; SZA SKIPPED

/ /TEST SZL CML
00536 744000 CLL /LINK = 0
00537 741402 SZL:CML
00540 740040 F84 HALT /ERROR; SZL FAILED TO SKIP

/ /TEST SZL CLL
00541 744002 CLL:CML /LINK = 1
00542 745400 SZL:CLL
00543 741000 SKP
00544 740040 F85 HALT /ERROR; SZL SKIPPED

/ /TEST SKP SZL SPA CLA CLL
00545 754003 CLA:CMA:CLL:CML /AC = ONES, LINK = 1
00546 755500 SKP!SZL!SPA!CLA!CLL /AC = 0, LINK = 0
00547 741000 SKP
00550 740040 F86 HALT /ERROR; SKP!SZL!SPA SKIPPED
          .EJECT

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00551	754002	/TEST SZA SNL CMA CLL CLA!CLL!CML	/AC = 0, LINK = 1
00552	744601	SZA!SNL!CMA!CLL	/AC=ONES, LINK = 0
00553	740040	F87 HALT	/ERROR, SZA!SNL FAILED TO SKP
/			
00554	750001	/TFST CLA SKP CLA!CMA	/AC = ONES
00555	751000	SKP!CLA	/AC = 0
00556	740000	NOP	
00557	740200	SZA	
00560	740040	F88 HALT	/ERROR, CLA FAILED TO CLEAR AC
/			
00561	750000	/TEST SKP CLA CMA CLA	/AC = 0
00562	751001	SKP!CLA!CMA	/AC = ONES
00563	740000	NOP	
00564	740001	CMA	
00565	740200	SZA	
00566	740040	F89 HALT	/ERROR, CLA!CMA FAILED TO /COMPLEMENT THE AC
/			
00567	744000	/TFST SKP CLL CML CLL	/LINK = 0
00570	745002	SKP!CLL!CML	/LINK = 1
00571	740000	NOP	
00572	740400	SNL	
00573	740040	F90 HALT	/ERROR, CLL!CML FAILED TO SET THE LINK
/			
00574	750001	/TEST CMA SERIES CLA!CMA	/AC = ONES
00575	740001	CMA	/AC = 0
00576	740001	CMA	/AC = ONES
00577	740001	CMA	/AC = 0
00600	740001	CMA	/AC = ONES
00601	740001	CMA	/AC = 0
00602	740200	SZA	/AC = 0
00603	740040	F91 HALT	/ERROR, AC NOT 0 CMA FAILED
/			
00604	744002	/TEST CML SERIES CLL:CML	/LINK = 1
00605	740002	CML	/LINK = 0
00606	740002	CML	/LINK = 1
00607	740002	CML	/LINK = 0
00610	740002	CML	/LINK = 1
00611	740002	CML	/LINK = 0
00612	741400	SZL	
00613	740040	F92 HALT	/ERROR, LINK NOT 0 CML FAILED
/			
00614	447634	TS7 WORK3	/CHECK DONE LOOPING
00615	600225	JMP OPFRAT	/LOOP
00616	106102	JMS GENRAN	/GET NO, FOR NEXT LOOP
00617	106126	JMS CKNO	
		EJECT	

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††††

/TEST RAR SERIES AND LINK
RTAT CLA!CLL!CML /AC = 0, LINK = 1
00620 754002
00621 740020
00622 740020
00623 740020
00624 740020
00625 740020
00626 740020
00627 740020
00630 740020
00631 740020
00632 740020
00633 740020
00634 740020
00635 740020
00636 740020
00637 740020
00640 740020
00641 740020
00642 740020
00643 741600
00644 740040 E113
 SNA!SZL
 HALT
 .EJECT
/ERROR; AC BIT 17 NOT 1, OR LINK = 1
/AFTER ROTATE SERIES

00645	754002	/TEST RAL SERIES AND LINK	
00646	740010	CLA:CLL:CML	/AC = 0, LINK = 1
00647	740010	RAL	
00650	740010	RAL	
00651	740010	RAL	
00652	740010	RAL	
00653	740010	RAL	
00654	740010	RAL	
00655	740010	RAL	
00656	740010	RAL	
00657	740010	RAL	
00660	740010	RAL	
00661	740010	RAL	
00662	740010	RAL	
00663	740010	RAL	
00664	740010	RAL	
00665	740010	RAL	
00666	740010	RAL	
00667	740010	RAL	
00670	741600	SNA:SZL	
00671	740040	HALT	
E114 /ERROR: AC BIT 0 NOT 1, OR LINK = 1			
/AFTER ROTATE SERIES			
/			
00672	754002	/TEST RTL SERIES AND LINK	
00673	742010	CLA:CLL:CML	/AC = 0, LINK = 1
00674	742010	RTL	
00675	742010	RTL	
00676	742010	RTL	
00677	742010	RTL	
00700	742010	RTL	
00701	742010	RTL	
00702	742010	RTL	
00703	742010	RTL	
00704	741600	SNA:SZL	
00705	740040	HALT	
F115 /ERROR: AC BIT 0 NOT 1, OR LINK = 1			
/AFTER ROTATE SERIES			
/			
00706	754002	/TEST RTR SERIES AND LINK	
00707	742020	CLA:CLL:CML	/AC = 0, LINK = 1
00710	742020	RTR	
00711	742020	RTR	
00712	742020	RTR	
00713	742020	RTR	
00714	742020	RTR	
00715	742020	RTR	
00716	742020	RTR	
00717	742020	RTR	
00720	741600	SNA:SZL	
00721	740040	HALT	
F116 /ERROR: AC BIT 17 NOT 1, OR LINK = 1			
/AFTER ROTATE SERIES			
.EOT			

/PDP-9 BASIC EXERCISER - TAPE 2

/

/RAR SERIES

RTSS	CLA!CMA!CLL	/AC = ONES, LINK = 0
	RAR;	RAR;

MM722	754001	
MM723	740020	

MM724	740020	
MM725	740020	

MM726	740020	
MM727	740020	

MM728	740020	RAR;	RAR;	RAR;	RAR
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MM729	740020	
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MM730	740020	
-------	--------	--

MM731	740020	
-------	--------	--

MM732	740020	
-------	--------	--

MM733	740020	RAR;	RAR;	RAR;	RAR
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MM734	740020	
-------	--------	--

MM735	740020	
-------	--------	--

MM736	740020	
-------	--------	--

MM737	740020	RAR;	RAR;	RAR;	RAR
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MM738	740020	
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MM739	740020	
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MM740	740020	RAR;	RAR;	RAR;	RAR
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MM741	740020	
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MM742	740020	
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MM743	740020	RAR;	RAR		
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MM744	740020	
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MM745	740003	CMA!CML	/AC = 000001, LINK = 0
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MM746	741600	SNA!SZL	
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MM747	740040	F140	HALT	/ERROR; AC BIT 17 NOT 1, OR LINK = 0
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			/AFTER ROTATE SERIES
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/

/

/TFST RAL SERIES TFST

CLA!CMA!CLL	/AC = ONES, LINK = 0
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MM750	754001	RAL;	RAL;	RAL;	RAL
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MM751	740010	
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MM752	740010	
-------	--------	--

MM753	740010	
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MM754	740010	
-------	--------	--

MM755	740010	RAL;	RAL;	RAL;	RAL
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MM756	740010	
-------	--------	--

MM757	740010	
-------	--------	--

MM758	740010	
-------	--------	--

MM759	740010	RAL;	RAL;	RAL;	RAL
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MM760	740010	
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MM761	740010	
-------	--------	--

MM762	740010	RAL;	RAL;	RAL;	RAL
-------	--------	------	------	------	-----

MM763	740010	
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MM764	740010	
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MM765	740010	RAL;	RAL;	RAL;	RAL
-------	--------	------	------	------	-----

MM766	740010	
-------	--------	--

MM767	740010	
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MM768	740010	
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MM769	740010	RAL;	RAL;	CML	/AC = 377777, LINK = 0
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MM770	740010	
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MM771	740010	
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MM772	740010	
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MM773	740002	
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MM774	741500	F141	SPA!SZL	
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MM775	740040		HALT	/ERROR; AC BIT 0 NOT 0, OR LINK = 0
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			EJECT
--	--	--	-------

			/AFTER ROTATE SERIES
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***  

    UU776  754001      /Tfst RTE Series  

    UU777  742010      CLA!CMA!CLL  

    U1000  742010      RTL;      RTL;      /AC = ONES, LINK = 0  

    U1001  742010  

    U1002  742010  

    U1003  742010      RTL;      RTL;      RTL;      RTL  

    U1004  742010  

    U1005  742010  

    U1006  742010  

    U1007  742010      RTL;      CML      /LINK = 0  

    U1010  740002  

    U1011  741500      SPA!S2L  

    U1012  740040      F142      HALT      /ERROR: AC BIT 0 NOT 0, OR LINK = 0  

                                /AFTER ROTATE SERIES  

    /  

    /Tfst RTR Series  

    U1013  754001      CLA!CMA!CLL  

    U1014  742020      RTR;      RTR;      RTR;      RTR      /AC = ONES, LINK = 0  

    U1015  742020  

    U1016  742020  

    U1017  742020  

    U1020  742020      RTR;      RTR;      RTR;      RTR  

    U1021  742020  

    U1022  742020  

    U1023  742020  

    U1024  742020      RTR  

    U1025  740003      CMA!CML  

    U1026  741600      SNA!S2L  

    U1027  740040      F143      HALT      /AC = 000001, LINK = 0  

                                /ERROR: AC BIT 17 NOT 1, OR LINK = 0  

                                /AFTER ROTATE SERIES  

                                .EJECT

```

```

*****  

      /TEST RAL!SNA  

      U1030    754002      CLA!CLL!CML      /AC = 0, LINK = 1  

      U1031    741210      RAL!SNA  

      U1032    741000      SKP  

      U1033    740040      F162      HALT      /ERROR; SNA SKIPPED  

      /  

      /TEST RAR!SNA  

      U1034    754002      CLA!CLL!CML      /AC = 0, LINK = 1  

      U1035    741220      RAR!SNA  

      U1036    741000      SKP  

      U1037    740040      F163      HALT      /ERROR; SNA SKIPPED  

      /  

      /TEST RTL"SNA  

      U1040    754002      CLA!CLL!CML      /AC = 0, LINK = 1  

      U1041    743210      RTL!SNA  

      U1042    741000      SKP  

      U1043    740040      F164      HALT      /ERROR; SNA SKIPPED  

      /  

      /TEST RTR!SNA  

      U1044    754002      CLA!CLL!CML      /AC = 0, LINK = 1  

      U1045    743220      RTR!SNA  

      U1046    741000      SKP  

      U1047    740040      F165      HALT      /ERROR; SNA SKIPPED  

      /  

      /TEST RAL!SNA  

      U1050    754002      CLA!CLL!CML      /AC = 0, LINK = 1  

      U1051    740020      RAR  

      U1052    741210      SNA!RAL  

      U1053    740040      F166      HALT      /ERROR, SNA FAILED TO SKIP  

      /  

      /TEST RAR!SNA  

      U1054    754002      CLA!CLL!CML      /AC = 0, LINK = 1  

      U1055    740010      RAI  

      U1056    741220      SNA!RAR  

      U1057    740040      F167      HALT      /ERROR; SNA FAILED TO SKIP  

          .EJECT

```

01060 754002 /TEST RTL:SNA
01061 742020 CLA:CLL:CML /AC = 0, LINK = 1
01062 743210 RTR /AC = 200000
01063 740040 SNA:RTL
F168 HALT /ERROR; SNA FAILED TO SKIP

01064 754002 /TEST RTR:SNA
01065 742010 CLA:CLL:CML /AC = 0, LINK = 1
01066 743220 RTL
01067 740040 SNA:RTR
F169 HALT /ERROR; SNA FAILED TO SKIP

01070 754001 /TEST CLL:SNA!RAR
01071 751220 CLA:CMA:CLL /AC = ONES, LINK = 0
01072 740040 HALT /ERROR; SNA FAILED TO SKIP
01073 447634 ISZ WORK3 /CHECK DONE LOOPING
01074 600620 JMP RTAT /LOOP
01075 106102 JMS GENRAN /GFT NO FOR NEXT LOOP
01076 106126 JMS CKNO

/ .EJECT

```

/
V11077 754000 /TEST LAW 760000
TLAW      CLA!CLI
              LAW 00000
              RAL
              SNI!CLL
              HALT
V11104 740010   RAL
V11105 744400   SNI!CLI
V11106 740040   HALT
V11107 740010   RAL
V11110 744400   SNI!CLL
V11111 740040   HALT
V11112 740010   RAL
V11113 744400   SNI!CLI
V11114 740040   HALT
V11115 740010   RAL
V11116 744400   SNI!CLL
V11117 740040   HALT
V11120 740200   SZA
V11121 740040   HALT
V11122 754001 /TEST LAW 760000, AC = ONES
V11123 760000   CLA!CMA!CLL
V11124 741400   LAW 00000
V11125 740040   S2I
V11126 740010   HALT
V11127 744400   RAL
V11128 740040   SNI!CLL
V11129 740040   HALT
V11131 740010   RAL
V11132 744400   SNI!CLL
V11133 740040   HALT
V11134 740010   RAL
V11135 744400   SNI!CLL
V11136 740040   HALT
V11137 740010   RAL
V11140 744400   SNI!CLL
V11141 740040   HALT
V11142 740010   RAL
V11143 744400   SNI!CLL
V11144 740040   HALT
V11145 740200   SZA
V11146 740040   HALT

```

/

```

/AC = 0
/AC = 760000
/AC = 740000
/LINK = 1
/ERROR: AC NOT 0 NOT A 1,
/LAW 760000 FAILED
/AC = 700000
/LINK = 1
/ERROR: AC BIT 1 NOT A 1
/LAW 760000 FAILED
/AC = 600000
/LINK = 1
/ERROR: AC BIT 2 NOT A 1
/LAW 760000 FAILED
/AC = 400000
/LINK = 1
/ERROR: AC BIT 3 NOT A 1
/LAW 760000 FAILED
/AC = 000000
/LINK = 1
/ERROR: AC BIT 4 NOT A 1
/LAW 760000 FAILED
/AC = 000000
/LINK = 1
/ERROR: AC BITS 5-17 NOT 0
/LAW 760000 FAILED

/AC = ONES, LINK = 0
/AC = 760000
/ERROR: LINK NOT A 0, LAW SET LINK
/AC = 740000
/LINK = 1
/ERROR: AC BIT 0 NOT A 1,
/LAW 760000 FAILED
/AC = 700000
/LINK = 1
/ERROR: AC BIT 1 NOT A 1
/LAW 760000 FAILED

/AC = 600000
/LINK = 1
/ERROR: AC BIT 2 NOT A 1
/LAW 760000 FAILED
/AC = 400000
/LINK = 1
/ERROR: AC BIT 3 NOT A 1, LAW 760000 FAILED
/AC = 000000
/LINK = 1
/ERROR: AC BIT 4 NOT A 1, LAW 760000 FAILED
/AC = 000000
/LINK = 1
/ERROR: AC BITS 5-17 NOT 0

```

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EJECT

/LAW 760000 FAILED

		/TEST LAW 777777, AC=0, L=0	
01147	754000	CLA:CLL	/AC = 0
01150	777777	LAW 17777	/AC = 760200
01151	740001	CMA	
01152	740200	SZA	
01153	740040	F219 HALT	/ERROR, AC NOT 0 /LAW 17777 FAILED /AC = 400760
01154	741400	SZL	/LINK NOT 0
01155	740040	F220 /	
		/TEST LAW 777777, AC=0, L=1	
01156	754002	CLA:CLL:CML	/AC = 0
01157	777777	LAW 17777	
01160	740001	CMA	
01161	740200	SZA	
01162	740040	F221 HALT	/ERROR, LINK NOT 0
01163	740400	SNL	
01164	740040	F222 HALT	/ERROR, LINK NOT 0
		/TEST LAW 777777, AC=1, L=0	
01165	754001	CLA:CMA:CLL	/AC = 0
01166	777777	LAW 17777	
01167	740001	CMA	
01170	740200	SZA	
01171	740040	F223 HALT	/ERROR, AC NOT 0
01172	741400	SZL	/AC = 100760
01173	740040	F224 HALT	/ERROR, LINK NOT 0
		.EJECT	

W1174	754003	/TFST LAW 777777, AC=1, L=1	
W1175	777777	CLA!CMA!CLL!CML	/AC = 0
W1176	740001	LAW 17777	
W1177	740200	CMA	
W1200	740040	SZA	
W1201	740400	F225 HALT	/ERROR, AC NOT 0
W1202	740040	SNL	
W1203	447634	F226 HALT	/ERROR, LINK NOT 1
W1204	601077	TSZ WORK3	/CHECK DONE LOOPING
W1205	106102	JMP TLAW	/LOOP
W1206	106126	JMS GENRAN	
		JMS CKNO	
		.EJECT	/GET NO. FOR NEXT LOOP

/TEST LAC 0'S

01207	754000	LACK	CLA!CLL	/AC = 0, LINK = 0
01210	207531		LAC K0	/000000
01211	740200		SZA	
01212	740040	F258	HALT	/ERROR, AC NOT 0 AFTER LAC K0
01213	741400		SZL	
01214	740040	F259	HALT	/ERROR, LINK NOT 0 AFTER LAC K0
/				
01215	754002		CLA!CLL!CML	/AC = 0, LINK = 1
01216	207531		LAC K0	
01217	740200		SZA	
01220	740040	F260	HALT	/ERROR, AC NOT 0
01221	740400		SNL	
01222	740040	F261	HALT	/ERROR, LINK NOT 1 AFTER LAC K0
/				
01223	754001		CLA!CMA!CLL	/AC = 1'S, LIN = 0
01224	207531		LAC K0	
01225	740200		SZA	
01226	740040	F262	HALT	/ERROR, AC NOT 0 AFTER LAC K0
01227	741400		SZL	
01230	740040	F263	HALT	/ERROR, LINK NOT 0 AFTER LAC K0
/				
01231	754003		CLA!CMA!CLL!CML	/AC = 1'S, LINK = 1
01232	207531		LAC K0	
01233	740200		SZA	
01234	740040	F264	HALT	/ERROR, AC NOT 0
01235	740400		SNL	
01236	740040	F265	HALT	/ERROR, LINK NOT 1
/				
/TEST LAC 1'S				
/				
01237	754000		CLA!CLL	/AC = 0, LINK = 0
01240	207573		LAC K7S	/777777
01241	740001		CMA	
01242	740200		SZA	
01243	740040	F266	HALT	/ERROR, AC NOT 0 LAC K7S FAILED
01244	741400		SZL	
01245	740040	F267	HALT	/ERROR, LINK NOT 0 AFTER LAC K7S
/				
01246	754002		CLA!CLL!CML	/AC = 0, LINK = 1
01247	207573		LAC K7S	
01250	740001		CMA	
01251	740200		SZA	
01252	740040	F268	HALT	/ERROR, AC NOT 0
01253	740400		SNL	
01254	740040	F269	HALT	/ERROR, LINK NOT 1 AFTER LAC K7S
			, EJECT	

U1255	754001	CLA!CMA!CLL	/AC = 1'S, LINK = 0	
U1256	207573	LAC K7S		
U1257	740001	CMA		
U1260	740200	SZA		
U1261	740040	F270 HALT	/ERROR, AC NOT 0, LAC K7S FAILED	
U1262	741400	SZL		
U1263	740040	F271 HALT	/ERROR, LINK NOT 1 AFTER LAC K7S	
U1264	754003	/		
U1265	207573	CLA!CMA!CLL:CML	/AC = 1'S, LINK = 1	
U1266	740001	LAC K7S		
U1267	740200	CMA		
U1270	740040	F272 HALT	/ERROR, AC NOT 0	
U1271	740400	SNL		
U1272	740040	HALT	/ERROR, LINK NOT 1 AFTER LAC K7S	
U1273	750000	/		
U1274	207606	CLA		
U1275	207605	LAC K101	/AC = 525252	
U1276	207573	LAC K010	/AC = 252525	
U1277	740001	LAC K7S	/AC = 777777	
U1300	740200	CMA		
U1301	740040	F273 SZA	/ERROR, AC NOT 0	
U1302	447634	HALT		
U1303	601207	IS7 WORK3	/CHECK FOR DONE LOOPING	
U1304	106102	JMP LACK	/LOOP	
U1305	106126	JMS GENRAN	/GET NO. FOR LOOP	
		JMS CKNO		
		/		
		/TFST AND		
		/		
U1306	750000	ANDAC	CLA	/AC = 0
U1307	507531		AND K0	
U1310	740200		SZA	
U1311	740040	F274 HALT	/ERROR, AC NOT 0 AFTER AND K0	
U1312	750001	/	CLA!CMA	/AC = 1'S
U1313	507531		AND K0	
U1314	740200		SZA	
U1315	740040	F275 HALT	/ERROR, AC NOT 0 AFTER AND K0	
U1316	750000	/	CLA	/AC = 0
U1317	507573		AND K7S	
U1320	740200		SZA	
U1321	740040	E276 HALT	/ERROR, AC NOT 0 AFTER AND K7S	
U1322	750001	/	CLA!CMA	/AC = 1'S
U1323	507573		AND K7S	
U1324	740001		CMA	
U1325	740200		SZA	
U1326	740040	F277 HALT	/ERROR, AC NOT 0 AFTER AND K7S	
		,EJECT		

/SEQUENTIAL AND

U1327	754002		CLA:CLL:CML	/AC = 0, LINK = 1
U1330	507531		AND K0	
U1331	507573		AND K7S	
U1332	507606		AND K101	
U1333	507605		AND K010	
U1334	740001		CMA	
U1335	507531		AND K0	
U1336	507573		AND K7S	
U1337	507606		AND K101	
U1340	507605		AND K010	
U1341	740200		SZA	
U1342	740040	E278	HALT	/ERROR, AC NOT 0
U1343	740400		SNL	
U1344	740040	F279	HALT	/ERROR, LINK NOT 1
U1345	447634		ISZ WORK3	/CHECK FOR DONE LOOPING
U1346	601306		JMP ANDAC	/LOOP
U1347	106102		JMS GENRAN	/GET NO, FOR NEXT LOOP
U1350	106126		JMS CKNO	
<hr/>				
/TFST XOR				
<hr/>				
U1351	750000	XORAC	CLA	/AC = 0
U1352	247531		XOR K0	
U1353	740200		SZA	
U1354	740040	F280	HALT	/ERROR, AC NOT 0 AFTER XOR K0
U1355	750001		CLA:CMA	/AC = 1'S
U1356	247531		XOR K0	
U1357	740001		CMA	
U1360	740200		SZA	
U1361	740040	F281	HALT	/ERROR, AC NOT 0
U1362	750000		CLA	/AC = 0
U1363	247573		XOR K7S	/777777
U1364	740001		CMA	
U1365	740200		SZA	
U1366	740040	F282	HALT	/ERROR, AC NOT 0 AFTER XOR K7S
U1367	750001		CLA:CMA	/AC = 1'S
U1370	247573		XOR K7S	
U1371	740200		SZA	
U1372	740040	F283	HALT	/ERROR, AC NOT 0 AFTER XOR K7S
			.EJECT	

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*** /SEQUENTIAL XOR
/
01373 750000 CLA           /AC = 0
01374 247606 XOR K101      /525252
01375 247605 XOR K010      /252525
01376 247531 XOR K0         /000000
01377 247573 XOR K7S        /777777
01400 247605 XOR K010      /AC = 0
01401 247606 XOR K101      /CHECK FOR DONE LOOPING
01402 247606 XOR K101      /LOOP
01403 247605 XOR K010      /GET NO. FOR NEXT LOOP
01404 740200 SZA
01405 740040 F284 HALT     /ERROR, AC NOT 0
/
01406 447634 ISZ WORK3
01407 601351 JMP XORAC
01410 106102 JMS GENRAN
01411 106126 JMS CKNO
/
//TEST TAD
/
01412 754000 TADAC CLA:CLL /AC = 0, LINK = 0
01413 347531 TAD K0
01414 740200 SZA
01415 740040 F285 HALT    /ERROR, AC NOT 0 AFTER TAD K0
01416 741400 S7L
01417 740040 F286 HALT    /ERROR, LINK NOT 0 AFTER TAD K0
/
01420 754001 CLA:CMA:CLL   /AC = 1'S, LINK = 0
01421 347531 TAD K0
01422 740001 CMA
01423 740200 SZA
01424 740040 F287 HALT    /ERROR, AC NOT 0
01425 741400 S7L
01426 740040 F288 HALT    /ERROR, LINK NOT 0
/
01427 754002 CLA:CLL:CML   /AC = 0, LINK = 1
01430 347573 TAD K7S      /777777
01431 740001 CMA
01432 740200 SZA
01433 740040 F289 HALT    /ERROR, TAD K7S FAILED
01434 740400 SNL
01435 740040 F290 HALT    /ERROR, CARRY OUT OR OVERFLOW
                           /FAILED, LINK NOT 0
/
01436 754001 CLA:CMA:CLL   /AC = 1'S, LINK = 0
01437 347573 TAD K7S
01440 740020 RAR
01441 740001 CMA
01442 740200 SZA
01443 740040 F291 HALT    /ERROR, TAD K7S TO 1'S FAILED
01444 741400 S7L
01445 740040 F292 HALT    /ERROR, LINK NOT 0
.EJECT

```

/TEST OVERFLOW

01446	754001	CLA:CMA:CLL	/AC = 1'S, LINK = 0
01447	347532	TAD K1	/000001
01450	740200	SZA	
01451	740040	F293 HALT	/ERROR. AC NOT 0 AFTER TAD K1
01452	740400	SNL	
01453	740040	F294 HALT	/ERROR. LINK NOT 1 OVERFLOW FAILED
01454	754003	/	
01455	347532	CLA:CMA:CLL:CML	/AC = 1'S, LINK = 1
01456	740200	TAD K1	
01457	740040	SZA	
01460	741400	F295 HALT	/ERROR. AC NOT 0
01461	740040	SZL	
		F296 HALT	/ERROR. LINK NOT 0 OVERFLOW FAILED
		/	
		/TAD 525252, AC = 252525, LINK = 0	
		/	
01462	754000	CLA:CLL	
01463	347605	TAD K010	
01464	347606	TAD K101	/AC = 1'S
01465	740001	CMA	
01466	740200	SZA	
01467	740040	E297 HALT	/ERROR. AC NOT 0
01470	741400	SZL	
01471	740040	E298 HALT	/ERROR. LINK NOT 0
		/	
		/TAD 252525, AC = 525252, LINK = 1	
		/	
01472	754002	CLA:CLL:CML	
01473	347606	TAD K101	
01474	347605	TAD K010	/AC = 1'S
01475	740001	CMA	
01476	740200	SZA	
01477	740040	F299 HALT	/ERROR. AC NOT 0
01500	740400	SNL	
01501	740040	F300 HALT	/ERROR. LINK NOT 0
		/	
		/SEQUENTIAL LAC, TAD, XOR	
		/	
01502	754000	CLA:CLL	/AC = 0, LINK = 0
01503	207605	LAC K010	
01504	347606	TAD K101	/AC = 1'S
01505	247605	XOR K010	
01506	347605	TAD K010	/AC = 1'S
01507	347532	TAD K1	
01510	740400	SNL	
		.EJECT	

††††

01511	740040	F301	HALT	/ERROR, LINK NOT 1
01512	207606		LAC K1M1	
01513	247573		XOR K7S	
01514	347606		TAD K1M1	
01515	247573		XOR K7S	
01516	740200		SZA	
01517	740040	F302	HALT	/ERROR, AC NOT 0
01520	447634		ISZ WORK3 /CHECK DONE LOOPING	
01521	601412		JMP TADAC	/LOOP
01522	106102		JMS GENRAN	/GET NO. FOR NEXT LOOP
01523	106126		JMS CKNO	
		/		
		/		
		/TEST ADD		
		/TEST ADD K1S TO K6S, LINK = 0		
01524	754000	ADDAC	CLA!CLL	/AC = 0, LINK = 0
01525	307565		ADD K1S	/111111
01526	307572		ADD K6S	/666666
01527	740001		CMA	/AC = 0
01530	740200		SZA	
01531	740040	F303	HALT	/ERROR; ADD K1S TO K6S FAILED
01532	741400		SZL	
01533	740040	F304	HALT	/ERROR; LINK NOT A0
		/		
		/TEST ADD K2S TO K5S, LINK = 0		
01534	754000		CLA!CLL	/AC , LINK = 0
01535	307566		ADD K2S	/22222
01536	307571		ADD K5S	/555555
01537	740001		CMA	/AC = 0
01540	740200		SZA	
01541	740040	F305	HALT	/ERROR; ADD K2S TO K5S FAILED
01542	741400		SZL	
01543	740040	F306	HALT	/ERROR; LINK NOT A0
		/		
		/TEST ADD K3S TO K4S, LINK = 0		
01544	754000		CLA!CLL	/AC, LINK = 0
01545	307567		ADD K3S	/33333
01546	307570		ADD K4S	/444444
01547	740001		CMA	/AC = 0
01550	740200		SZA	
01551	740040	F307	HALT	/ERROR; ADD K3S TO K4S FAILED
01552	741400		SZL	
01553	740040	F308	HALT	/ERROR; LINK NOT A0
		/		
		/TEST ADD K4S TO K3S, LINK = 0		
01554	754000		CLA!CLL	/AC, LINK = 0
01555	307570		ADD K4S	/444444
01556	307567		ADD K3S	/333333
01557	740001		CMA	/AC = 0
01560	740200		SZA	
01561	740040	F309	HALT	/ERROR; ADD K4S TO K3S FAILED
01562	741400		SZL	
01563	740040	E310	HALT	/ERROR; LINK NOT A0
		.EJECT		

01610	740200		SZA	
01611	740040	F315	HALT	/ERROR; ADD K7S TO K0S FAILED
01612	741400		SZL	
01613	740040	F316	HALT	/ERROR; LINK NOT A 0
		/		
01614	754001		/TEST ADD 252525, AC = 525252, LINK = 0	
01615	207606		CLL!CLA:CMA	/AC = ONES, LINK = 0
01616	307605		LAC K101	/AC = 525252
01617	740001		ADD K010	/AC = 252525
01620	740200		CMA	/AC = 0
01621	740040	F317	SZA	
01622	741400		HALT	/ERROR; ADD K101 TO K010 FAILED
01623	740040	F318	SZL	
		/	HALT	/ERROR; LINK NOT A 0
01624	744000		/TEST ADD 525252, AC = 252525, LINK = 0	
01625	207605		CLL	/LINK = 0
01626	307606		LAC K010	/AC = 252525
01627	740001		ADD K101	/525252
01630	740200		CMA	/AC = 0
01631	740040	F319	SZA	
01632	741400		HALT	/ERROR; ADD K010 TO K101 FAILED
01633	740040	E320	SZL	
		.	HALT	/ERROR; LINK NOT A 0
			EJECT	

```

*** /TEST ADD K7S, AC = K400K, LINK = 0
01634 754001 CLA!CMA!CLL /AC = ONES, LINK = 0
01635 207554 LAC K400K /AC = 400K
01636 307573 ADD K7S /ONES
01637 507573 AND K7S /AC = 400K
01640 247554 XOR K400K
01641 740200 SZA /AC = 0
01642 740040 F321 HALT /ERROR; ADD-0 TO K400K FAILED
01643 741400 SIZ
01644 740040 F322 HALT /ERROR; LINK NOT A 0, CARRY FAILED
/
/TEST ADD K200K, AC = K200K, LINK = 0
01645 754001 CLA!CMA!CLL /AC = ONES, LINK = 0
01646 207560 LAC K200K /AC = 200K
01647 307560 ADD K200K /ONES
01650 507573 AND K7S /AC = 400K
01651 247554 XOR K400K
01652 740200 SZA /AC = 0
01653 740040 F323 HALT /ERROR; ADD K200K TO K200K FAILED
01654 740400 SNI
01655 740040 F324 HALT /ERROR; LINK NOT A ONES, CARRY FAILED
/
/TEST ADD K7S, AC = K100K, LINK = 1
01656 754003 CLA!CMA!CLL!CML /AC = ONES, LINK = 1
01657 207321 LAC K100K /AC = 100K
01660 307573 ADD K7S /ONES
01661 507573 AND K7S /AC = 100K
01662 247321 XOR K100K
01663 740200 SZA /AC = 0
01664 740040 F325 HALT /ERROR; ADD-0 TO K100K FAILED
01665 740400 SNI
01666 740040 F326 HALT /ERROR; LINK NOT A ONE, LINK RESFT
/
/TEST ADD K7S, AC = K40K, LINK = 0
01667 754001 CLA!CMA!CLL /AC = ONES, LINK = 0
01670 207553 LAC K40K /AC = 40K
01671 307573 ADD K7S /ONES
01672 507573 AND K7S /AC = 40K
01673 247553 XOR K40K
01674 740200 SZA /AC = 0
01675 740040 F327 HALT /ERROR; ADD-0 TO K40K FAILED
01676 741400 SIZ
01677 740040 F328 HALT /ERROR; LINK NOT A ZERO, CARRY FAILED
.EJECT

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01700 754001 /TEST ADD K7S, AC = K20K, LINK = 0
01701 207557 CLA:CMA:CLL /AC = ONES, LINK = 0
01702 307573 LAC K20K /AC = 20K
01703 507573 ADD K7S /ONES
01704 247557 AND K7S /AC = 20K
01705 740200 XOR K20K
01706 740040 SZA
01707 741400 E329 HALT /ERROR; ADD-0 TO K20K FAILED
01708 740040 E330 HALT /ERROR; LINK NOT A ZERO, CARRY FAILED
/
01711 754001 /TEST AND K7S, AC = K10K, LINK = 0
01712 207556 CLA:CMA:CLL /AC = ONES, LINK = 0
01713 307573 LAC K10K /AC = 10K
01714 507573 ADD K7S /ONES
01715 247556 AND K7S /AC = 10K
01716 740200 XOR K10K
01717 740040 SZA
01718 741400 E331 HALT /ERROR; ADD-0 TO K10K FAILED
01719 740040 E332 HALT /ERROR; LINK NOT A ZERO, CARRY FAILED
/
01722 754001 /TEST ADD K7S, AC = K4K, LINK = 0
01723 207550 CLA:CMA:CLL /AC = ONES, LINK = 0
01724 307573 LAC K4K /AC = 4K
01725 507573 ADD K7S /ONES
01726 247550 AND K7S /AC = 4K
01727 740200 XOR K4K
01728 740040 SZA
01729 741400 E333 HALT /ERROR; ADD-0 TO K4K FAILED
01730 740040 E334 HALT /ERROR; LINK NOT A ZERO, CARRY FAILED
/
01733 754001 /TEST ADD K7S, AC = K2K, LINK = 0
01734 207546 CLA:CMA:CLL /AC = ONES, LINK = 0
01735 307573 LAC K2K /AC = 2K
01736 507573 ADD K7S /ONES
01737 247546 AND K7S /AC = 2K
01738 740200 XOR K2K
01739 740040 SZA
01740 740040 E335 HALT /ERROR; ADD-0 TO K2K FAILED
01741 741400 E336 HALT /ERROR; LINK NOT A ZERO, CARRY FAILED
01742 740040 EJECT
01743 740040

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        /TEST ADD K7S, AC = K1K, LINK = 0  

        U1744    754101      CLA:CMA:CLI      /AC = ONES, LINK = 0  

        U1745    2075443     LAC K1K          /AC = 1K  

        U1746    3075782     ADD K7S          /ONES  

        U1747    5075782     AND K7S          /AC = 1K  

        U1750    2475443     XOR K1K  

        U1751    740200       SZA  

        U1752    740040       F337      HALT  

        U1753    741400       S2I  

        U1754    740040       F338      HALT  

        /  

        /TEST ADD K7S, AC = K400, LINK = 0  

        U1755    754101      CLA:CMA:CLI      /AC = ONES, LINK = 0  

        U1756    207545      LAC K400         /AC = 400  

        U1757    307573      ADD K7S          /ONES  

        U1760    507573      AND K7S          /AC = 400  

        U1761    247545      XOR K400  

        U1762    740200       SZA  

        U1763    740040       F339      HALT  

        U1764    741400       S2L  

        U1765    740040       F340      HALT  

        U1766    447634       ISR WORK3  

        U1767    601524       JMP ADDAC  

        U1770    106102       JMS GENRAN  

        U1771    106126       JMS CKMO  

                           .EJECT  

                           /GET NO. FOR NEXT LOOP

```

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01772 754001 /TEST ADD K7S, AC = K20, LINK = 0
01773 207541 ADDAC1 CLA!CMA!CLL /AC = ONES, LINK = 0
01774 307573 LAC K20 /AC = 20
01775 507541 ADD K7S /ONES
01776 247541 AND K20 /AC = 20
01777 740200 XOR K20
02000 740040 S2A /AC = 0
02001 741400 F347 HALT /ERROR; ADD -0 TO K20 FAILED
02002 740040 S2L
02003 754001 F348 HALT /ERROR; LINK NOT A ZERO, CARRY FAILED
02004 207535 /
02005 307573 /TEST ADD K7S, AC = K10, LINK = 0
02006 507573 CLA!CMA!CLL /AC = ONES, LINK = 0
02007 247535 LAC K10 /AC = 10
02010 740200 ADD K7S /ONES
02011 740040 AND K7S /AC = 10
02012 741400 XOR K10
02013 740040 S2A /AC = 0
F349 HALT /ERROR; ADD = 0 TO K10 FAILED
02014 754001 S2L
02015 207534 F350 HALT /ERROR; LINK NOT A ZERO CARRY FAILED
02016 307573 /
02017 507573 /TEST ADD K7S, AC = 4, LINK = 0
02020 247534 CLA!CMA!CLL /AC = ONES, LINK = 0
02021 740200 LAC K4 /AC = 4
02022 740040 ADD K7S /ONES
02023 741400 AND K7S /AC = 4
02024 740040 XOR K4
F351 HALT /AC = 0
02025 754001 /ERROR; ADD -0 TO K4 FAILED
02026 207533 S2A
02027 307573 F352 HALT /ERROR; LINK NOT A ZERO, CARRY FAILED
02030 507573 /
02031 247533 /TEST ADD K7S, AC = K2, LINK = 0
02032 740200 CLA!CMA!CLL /AC = ONES, LINK = 0
02033 740040 LAC K2 /AC = 2
02034 741400 ADD K7S /ONES
02035 740040 AND K7S /AC = 2
02036 754001 XOR K2
02037 207532 S2A /AC = 0
02040 307573 F353 HALT /ERROR; ADD -0 TO K2 FAILED
02041 507573 S2L
02042 247532 F354 HALT /ERROR; LINK NOT A ZERO, CARRY FAILED
02043 740200 /
02044 740040 /TEST ADD K7S, AC = K1, LINK = 0
02045 741400 CLA!CMA!CLL /AC = ONES, LINK = 0
02046 740040 LAC K1 /AC = 1
F355 HALT /ONES
02047 740040 AND K7S /AC = 1
02048 741400 XOR K1
02049 740040 S2A /AC = 0
02050 740040 F356 HALT /ERROR; ADD -0 TO K1 FAILED
02051 741400 S2L
02052 740040 F357 HALT /ERROR; LINK NOT A ZERO, CARRY FAILED

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EJECT

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    /TEST ADD K400K, AC = ONES, LINK = 0
    02047 744000      CLL           /LINK = 0
    02050 777777      LAW 17777    /AC = ONES
    02051 507573      AND K7S     /AC = ONES
    02052 307554      ADD K400K   /400K
    02053 247554      XOR K400K
    02054 740200      SZA          /AC = 0
    02055 740040      F357        HALT
    02056 741400      SZL          /ERROR; ADD K400K TO -0 FAILED
    02057 740040      F358        HALT
    /
    /TEST ADD K200K, AC = ONES, LINK = 1
    02060 744002      CLL!CML    /LINK = 1
    02061 777777      LAW 17777    /AC = ONES
    02062 507573      AND K7S     /AC = ONES
    02063 307560      ADD K200K   /200K
    02064 247560      XOR K200K
    02065 740200      SZA          /AC = 0
    02066 740040      F359        HALT
    02067 740400      SNL          /ERROR; ADD K200K TO -0 FAILED
    02070 740040      F360        HALT
    /
    /TEST ADD K100K, AC = ONES, LINK = 1
    02071 744002      CLL!CML    /LINK = 1
    02072 777777      LAW 17777    /AC = ONES
    02073 507573      AND K7S     /AC = ONES
    02074 307321      ADD K100K   /100K
    02075 247321      XOR K100K
    02076 740200      SZA          /AC = 0
    02077 740040      F361        HALT
    02100 740400      SNL          /ERROR; ADD K100K TO -0 FAILED
    02101 740040      E362        HALT
    /
    /TEST ADD K40K, AC = ONES, LINK = 1
    02102 744002      CLL!CML    /LINK = 1
    02103 777777      LAW 17777    /AC = ONES
    02104 507573      AND K7S     /AC = ONES
    02105 307553      ADD K40K    /40K
    02106 247553      XOR K40K
    02107 740200      SZA          /AC = 0
    02110 740040      F363        HALT
    02111 740400      SNL          /ERROR; ADD K40K TO -0 FAILED
    02112 740040      E364        HALT
    /
    /TEST ADD K20K, AC = ONES, LINK = 1
    02113 744002      CLL!CML    /LINK = 1
    02114 777777      LAW 17777    /AC = ONES
    02115 507573      AND K7S     /AC = ONES
    02116 307557      ADD K20K   /20K
    02117 247557      XOR K20K
    02120 740200      SZA          /AC = 0
    02121 740040      F365        HALT
    02122 740400      SNL          /ERROR; ADD K20K TO -0 FAILED
    02123 740040      F366        HALT
    /ERROR; LINK NOT A ONE, LINK RESET

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.ENT

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/PDP-9 BASIC EXERCISER - TAPE 3
/TEST ADD K10K, AC = ONES, LINK = 1
 02124 744002 CLL:CML /LINK = 1
 02125 777777 LAW 17777 /AC = ONES
 02126 507573 AND K7S /AC = ONES
 02127 307556 ADD K10K /10K
 02130 247556 XOR K10K
 02131 740200 SZA /AC = 0
 02132 740040 F367 HALT /ERROR; ADD K10 TO -0 FAILED
 02133 740400 SNL
 02134 740040 F368 HALT /ERROR; LINK NOT A ONE, LINK RESET
/
/TEST ADD K4K, AC = ONES, LINK = 1
 02135 744002 CLL:CML /LINK = 1
 02136 777777 LAW 17777 /AC = ONES
 02137 507573 AND K7S /AC = ONES
 02140 307550 ADD K4K /4K
 02141 247550 XOR K4K
 02142 740200 SZA /AC = 0
 02143 740040 F369 HALT /ERROR; ADD K4K TO -0 FAILED
 02144 740400 SNL
 02145 740040 F370 HALT /ERROR; LINK NOT A ONE, LINK RESET
/
/TEST ADD K2K, AC = ONES, LINK = 1
 02146 744002 CLL:CML /LINK = 1
 02147 777777 LAW 17777 /AC = ONES
 02150 507573 AND K7S /AC = ONES
 02151 307546 ADD K2K /2K
 02152 247546 XOR K2K
 02153 740200 SZA /AC = 0
 02154 740040 F371 HALT /ERROR; AC K2K TO -0 FAILED
 02155 740400 SNL
 02156 740040 F372 HALT /ERROR; LINK NOT A ONE, LINK RESET
/
/TEST ADD K1K, AC = ONES, LINK = 1
 02157 744002 CLL:CML /LINK = 1
 02160 777777 LAW 17777 /AC = ONES
 02161 507573 AND K7S /AC = ONES
 02162 307544 ADD K1K /1K
 02163 247544 XOR K1K
 02164 740200 SZA /AC = 0
 02165 740040 F373 HALT /ERROR; ADD K1K TO -0 FAILED
 02166 740400 SNL
 02167 740040 F374 HALT /ERROR; LINK NOT A ONE, LINK RESET
/
/TEST ADD K400, AC = ONES, LINK = 1
 02170 744002 CLL:CML /LINK = 1
 02171 777777 LAW 17777 /AC = ONES
 02172 507573 AND K7S /AC = ONES
 02173 307545 ADD K400 /400
 02174 247545 XOR K400
 02175 740200 SZA /AC = 0
 02176 740040 F375 HALT /ERROR; ADD K400 TO -0 FAILED
 02177 740400 SNL
 02200 740040 F376 HALT /ERROR; LINK NOT A ONE, LINK RESET

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EJECT

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    /TFST ADD K200, AC = ONES, LINK = 1
02201 744002 CLL:CML /LINK = 1
02202 777777 LAW 17777 /AC = ONES
02203 507573 AND K7S /AC = ONES
02204 307552 ADD K200 /200
02205 247552 XOR K200
02206 740200 SZA /AC = 0
02207 740040 F377 HALT /ERROR; ADD K200 TO -0 FAILED
02210 740400 SNL
02211 740040 F378 HALT /ERROR; LINK NOT A ONE, LINK RESET
/
    /TFST ADD K100, AC = ONES, LINK = 1
02212 744002 CLL:CML /LINK = 1
02213 777777 LAW 17777 /AC = ONES
02214 507573 AND K7S /AC = ONES
02215 307540 ADD K100 /100
02216 247540 XOR K100
02217 740200 SZA /AC = 0
02220 740040 F379 HALT /ERROR; ADD K100 TO -0 FAILED
02221 740400 SNL
02222 740040 F380 HALT /ERROR; LINK NOT A ONE, LINK RESET
/
    /TFST ADD K40, AC = ONES, LINK = 1
02223 744002 CLL:CML /LINK = 1
02224 777777 LAW 17777 /AC = ONES
02225 507573 AND K7S /AC = ONES
02226 307543 ADD K40 /40
02227 247543 XOR K40
02230 740200 SZA /AC = 0
02231 740040 F381 HALT /ERROR; ADD K40 TO -0 FAILED
02232 740400 SNL
02233 740040 F382 HALT /ERROR; LINK NOT A ONE, LINK RESET
/
    /TFST ADD K20, AC = ONES, LINK = 1
02234 744002 CLL:CML /LINK = 1
02235 777777 LAW 17777 /AC = ONES
02236 507573 AND K7S /AC = ONES
02237 307541 ADD K20 /20
02240 247541 XOR K20
02241 740200 SZA /AC = 0
02242 740040 F383 HALT /ERROR; ADD K20 TO -0 FAILED
02243 740400 SNL
02244 740040 F384 HALT /ERROR; LINK NOT A ONE, LINK RESET
/
    /TFST ADD K10, AC = ONES, LINK = 1
02245 744002 CLL:CML /LINK = 1
02246 777777 LAW 17777 /AC = ONES
02247 507573 AND K7S /AC = ONES
02250 307535 ADD K10
02251 247535 XOR K10
02252 740200 SZA /AC = 0
02253 740040 F385 HALT /ERROR; ADD K10 TO -0 FAILED
02254 740400 SNL
02255 740040 F386 HALT /ERROR; LINK NOT A ONE, LINK RESET

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EJECT

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    02256 744002          /TFST ADD K4, AC = ONES, LINK = 1
    02257 777777          CLL:CML           /LINK = 1
    02260 507573          LAW 17777          /AC = ONES
    02261 307534          AND K7S            /AC = ONES
    02262 247534          ADD K4             /4
    02263 740200          XOR K4             SZA
    02264 740040          HALT               /AC = 0
    02265 740400          SNL                /ERROR; ADD K4 TO -0 FAILED
    02266 740040          F388               HALT
    /
    02267 744002          /TFST ADD K2, AC = ONES, LINK = 1
    02270 777777          CLL:CML           /LINK = 1
    02271 507573          LAW 17777          /AC = ONES
    02272 307533          AND K7S            /AC = ONES
    02273 247533          ADD K2             /2
    02274 740200          XOR K2             SZA
    02275 740040          HALT               /AC = 0
    02276 740400          SNL                /ERROR; ADD K2 TO -0 FAILED
    02277 740040          F389               HALT
    /
    02300 744002          /TFST ADD K1, AC = ONES, LINK = 1
    02301 777777          CLL:CML           /LINK = 1
    02302 507573          LAW 17777          /AC = ONES
    02303 307532          AND K7S            /AC = ONES
    02304 247532          ADD K1             /1
    02305 740200          XOR K1             SZA
    02306 740040          HALT               /AC = 0
    02307 740400          SNL                /ERROR; ADD K1 TO -0 FAILED
    02310 740040          F390               HALT
    /
    02311 744000          /TFST ADD K7S, AC = ONES, LINK = 0
    02312 207573          CLL               /LINK = 0
    02313 307573          LAC K7S            /AC = ONES
    02314 740001          ADD K7S            /ONES
    02315 740200          CMA                /AC = ONES
    02316 740040          SZA                /AC = 0
    02317 741400          HALT               /ERROR; ADD K7S TO ALL ONES FAILED
    02320 740040          F393               HALT
    /
    02321 744002          /TFST ADD 525253, AC = 252525, LINK = 1
    02322 207605          CLL:CML           /LINK = 1
    02323 307607          LAC K010           /AC = 252525
    02324 247532          ADD K53             /525253
    02325 740200          XOR K1             /000001
    02326 740040          SZA                /AC = 0
    02327 740400          HALT               /ERROR; ADD K5253 TO K5252 FAILED
    02330 740040          F395               SNL
    02330 740040          F396               HALT
    .EJECT

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		/TEST ADD 252525, AC = 525253, LINK RESET	
W2331	744000	CLL	/LINK = 0
W2332	207607	LAC K53	/AC = 525253
W2333	307605	ADD K010	/252525
W2334	247532	XOR K1	/000001
W2335	740200	SZA	/AC = 0
W2336	740040	F397 HALT	/ERROR; ADD K2525 TO K5253 FAILED
W2337	741400	SZL	
W2340	740040	F398 HALT	/ERROR; LINK NOT A ZERO, CARRY FAILED
		/	
		/TEST ADD SERIES	
W2341	754000	CLA!CLL	/LINK = 0, AC = 0
W2342	307565	ADD K1S	/AC = 111111
W2343	307566	ADD K2S	/AC = 333333
W2344	307567	ADD K3S	/AC = 666666
W2345	307570	ADD K4S	/AC = 333333, LINK = 1
W2346	307571	ADD K5S	/AC = 111111
W2347	307572	ADD K6S	/AC = 777777
W2350	307573	ADD K7S	/AC = 777777
W2351	740001	CMA	/AC = 0
W2352	740200	SZA	
W2353	740040	F399 HALT	/ERROR; ADD SERIES FAILED
W2354	740400	SNI	
W2355	740040	F400 HALT	/ERROR; LINK NOT A 1
W2356	447634	TS7 WORK3	/CHECK DONE LOOPING
W2357	601772	JMP ADDAC1	/LOOP
W2360	106102	JMS GENRAN	
W2361	106126	JMS CKNO	/GET NO. FOR NEXT LOOP
		.EJECT	

/ADD RANDOM PAIRS TEST

02362	106102	RANADD	JMS GENRAN	/GET RANDOM NUMBER
02363	741100		SPA	/+ NO
02364	740001		CMA	/ - MAKE IT +
02365	741200		SNA	/0 NOT ALLOWED
02366	602362		JMP RANADD	
02367	043016		DAC APOS	/IT IS + A
02370	740001		CMA	/1 COMPLEMENT
02371	043017	MINUSA	DAC ANEG	/IT IS -A
02372	106102		JMS GENRAN	/GET NEXT RANDOM
02373	741100		SPA	/+ NO
02374	740001		CMA	/ - MAKE +
02375	741200		SNA	/0 NOT ALLOWED
02376	602372		JMP MINUSA+1	
02377	043020		DAC BPOS	/IT IS + B
02400	740001		CMA	/MAKE 1'S COMP
02401	043021	MINUSB	DAC BNEG	/IT IS - B
02402	777777		LAW -1	
02403	043027		DAC PASS2	
02404	744000		CLL	
02405	203021		LAC RNEG	/RESTART HERE TO REGENERATE NEW COMPARE
02406	343017		TAD ANEG	/-B -A
02407	741400		SZL	/EOC IF ADD
02410	347532		TAD K1	/YES MAKE CARRY
02411	043022	MINSAB	DAC SUMNEG	/SAVE -A -B
/NOW GFNFRATE A + B				
02412	203016		LAC APOS	/GET +A
02413	744000		CLL	
02414	343020		TAD BPOS	/+B
02415	741400		SZL	/EOC IF ADD
02416	347532		TAD K1	/YES ADD CARRY
02417	043023	APLUSB	DAC SUMPOS	
,EJECT				

```

/NOW GENERATE B-A
02420 203020      LAC BPOS      /GET B
02421 744000      CLL
02422 343017      TAD ANFG
02423 741400      S2L
02424 347532      TAD K1
02425 043024      RMNSA      DAC RMASUM /YFS ADD CARRY
                                         /SAVF B-A
                                         /
/NOW GENERATE A-R
02426 203016      LAC AP0S      /GET A
02427 744000      CLL
02430 343021      TAD BNFG
02431 741400      S2L
02432 347532      TAD K1
02433 043025      AMINSB      DAC AMRSUM /YFS ADD CARRY
                                         /A-B
                                         /
/IF A+B IS AN OVERFLOW SITUATION
/MAKE OFLOW TESTS THAT APPLY = SNL
/IF A+B IS NOT OVERFLOW MAKE
/OVERFLOW TEST THAT APPLY = S2L
/
02434 203023      LAC SUMPOS    /GET A+B
02435 751100      SPA!CLA     /STILL POS RRESULT
02436 207654      LAC KSNL     /NFG RESULT IS OVERFLOW
02437 741200      SNA          /AC = SNL IS OVERFLOW
02440 207653      LAC KS2L     /+ RESULT IS NO OVERFLOW
02441 042464      DAC OFLCK1   /SET UP ALL OFLOW
02442 042512      DAC OFLCK3   /TESTS WHERE OFLOW
02443 042627      DAC OFLCH1   /MAY OR MAY NOT OCCUR
02444 042644      DAC OFLCH2   /AC = SNL IS A+B OFLOW
02445 042662      DAC OFLCH3   /AC = S2L IS A+B NOT OFLOW
02446 042701      DAC OFLCH4   /IF A+R OFLOW -A-B DOES ALSO
02447 042721      DAC OFLCH5   /IF A+B NOT OFLOW
02450 042742      DAC OFLCH6   /THEN NONE OF THESE
02451 042764      DAC OFLCH7   /ADDS CAN OVERFLOW
02452 042540      DAC OFLCK5
02453 042566      DAC OFLCK7
                                         .EJECT

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/NOW DO A COMPLETE SERIES OF
/ONES COMP ADDITIONS
/SHOULD GET THE SAME RESULTS AS
/THE TAD'S WITH EOC TAD (1)

/
/FIRST TEST A+B
/

02454	744000	APLSRT	CLL	/FOR OVERFLOW CHECK
02455	203016		LAC APOS	/GET A
02456	303020		ADD RPOS	/A+B
02457	543023		SAD SUMPOS	/SHOULD = PREVIOUS A+B
02460	602464		JMP .+4	/OK
02461	740040	F401	HLT	/DISPLAY 1'S A+B
02462	203023		LAC SUMPOS	/GET 2'S COMP GEN
02463	740040		HLT	/DISPLAY 2'S A+B
02464	741400	OFLCK1	SZL	/OR SNL IF OVERFLOW
02465	740040	F402	HLT	/LINK OR OVERFLOW FAILED
02466	762454		LAW APLSRT	/MAKE JUMP FOR SCOPE LOOP
			.EJECT	

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U2467 744000 /2ND TEST -B+A
U2470 203021 AMNSRT CLL
U2471 303016 LAC BNEG /GET A
U2472 543025 ADD AP0S /A-B
U2473 602477 SAD AMRSUM /SHOULD = PREVIOUS A-B
U2474 740040 F403 HLT /OK
U2475 203025 LAC AMRSUM /DISPLAY 1'S A-B
U2476 740040 HLT /DISPLAY 2'S A-B
U2477 741400 OFLCK2 S7L /SHOULD NOT OVERFLOW
U2500 740040 F404 HLT /MAKE JMP FOR SCOPE
U2501 762467 LAW AMNSRT

/
/NOW 3RD TEST IS -A -B
U2502 744000 MAPLMB CLL
U2503 203017 LAC ANFG /GET -A
U2504 303021 ADD RNFG /PLS -B
U2505 543022 SAD SUMNEG /SHOULD = PREVIOUS -A-B
U2506 602512 JMP .+4 /OK
U2507 740040 F405 HLT /DISPLAY 1'S -A-B
U2510 203022 LAC SUMNEG /DISPLAY 2'S -A-B
U2511 740040 HLT /OR SNL
U2512 741400 OFLCK3 S7L /LINK FAILED
U2513 740040 F406 HLT /MAKE JMP FOR SCOPE
U2514 762500 LAW MAPLMB

/
/FOURTH TEST IN THIS SERIES
/U3 TEST B-A
U2515 744000 RMNSAT CLL
U2516 203020 LAC RPOS /GET B
U2517 303017 ADD ANFG /ADD -A
U2520 543024 SAD RMASUM /SHOULD = PREVIOUS B-A
U2521 602525 JMP .+4 /OK
U2522 740040 HLT /DISPLAY 1'S B-A
U2523 203024 LAC RMASUM /DISPLAY 2'S B-A
U2524 740040 HLT /CAN NOT OVERFLOW
U2525 741400 S7L /OVERFLOW FAILED
U2526 740040 HLT /MAKE JMP FOR SCOPE
U2527 762515 LAW RMNSAT
.EJECT

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|||||

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    /FIFTH TEST IN THIS SERIES
    /IS TEST (A+R)-A = B
    02530 744000 AHMATS CLL
    02531 203023 LAC SUMPOS
    02532 303017 ADD ANEG
    02533 543020 SAD RPOS
    02534 602540 JMP .+4
    02535 740040 F407 HLT
    02536 203020 LAC RPOS
    02537 740040 HLT
    02540 741400 DFLCK5 SZL      /CAN OVERFLOW SNL IF A+B OVERFLOW
    02541 740040 F408 HLT      /ILLFGAL LINK
    02542 762530 LAW ABMATS /MAKE JMP FOR SCOPE
    /

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    /SIXTH TEST IN THIS SERIFS
    /IS TEST (R-A)-B = -A
    02543 744000 RMAMRT CLL
    02544 203024 LAC RMASUM
    02545 303021 ADD RNFG
    02546 543017 SAD ANEG
    02547 602553 JMP .+4
    02550 740040 F409 HLT
    02551 203017 LAC ANEG
    02552 740040 HLT
    02553 741400 DFLCK6 SZL      /CAN NOT OVERFLOW
    02554 740040 F410 HLT      /MAKE JMP FOR SCOPE
    02555 762543 LAW RMAMRT
    /

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    /SEVENTH TEST IN THIS SERIES
    /IS (-A-R)+A = -R
    02556 744000 MARPAT CLL
    02557 203022 LAC SUMNEG
    02560 303016 ADD APoS
    02561 543021 SAD RNFG
    02562 602566 JMP .+4
    02563 740040 E411 HLT
    02564 203021 LAC RNFG
    02565 740040 HLT
    02566 741400 DFLCK7 SZL      /CAN BE OVERFLOW IF A+R OVERFLOW THEN IS
    02567 740040 F412 HLT      /ILLFGAL LINK
    02570 762556 LAW MARPAT /MAKE JMP FOR SCOPE
    .EJECT

```

/EIGHTH TEST OF THE SERIES

/IS (A-B) +H = A

W2571	744000	
W2572	203025	AMRPRT CLI
W2573	303020	LAC AMRSUM
W2574	543016	ADD AP0S
W2575	602601	SAD AP0S
W2576	740040	JMP .+4
W2577	203016	F413 HLT
W2600	740040	LAC AP0S
W2601	741400	HLT
W2602	740040	DFLCK8 S2L /CAN NOT OVERFLOW
W2603	762571	F414 HLT
		LAW AMRPRT /MAKE JMP FOR SCOPE

/

/9TH TEST OF SERIES

/NOW TEST AC = 777777 + A = A

/

W2604	754001	M0ACP8 CLL!CLA!CMA	/SET AC = 777777
W2605	303016	ADD AP0S	/+ A
W2606	543016	SAD AP0S	/SHOULD = A
W2607	602613	JMP .+4	/TEST LINK
W2610	740040	F415 HLT	/FAILED RESULTS
W2611	203016	LAC AP0S	
W2612	740040	HLT	/DISPLAY A
W2613	741400	DFLCK9 S2L	/CANNOT OVERFLOW
W2614	740040	F416 HLT	/OVERFLOW FAILED L - 1
W2615	762604	LAW M0ACP8	
		.EJECT	/MAKE JMP FOR SCOPE

/THE NEXT SERIES OF TESTS
 /ARE ADD SEQUENCES THE RESULTS
 /OF WHICH HAVE ALREADY BEEN
 /COMPUTED AND VERIFIED
 /
 /FIRST SERIES TESTS A+B OK, THEN (A+B)-A = B
 /SEE ARMTS FOR SHORTER TEST OR APLSAT OR M0ACPA
 /NOW TRY A+B-A = B

02616	754001	SERS01	CLL!CLA!CMA	
02617	303016		ADD AP0S	
02620	303020		ADD RP0S	
02621	303017		ADD ANEG	
02622	543020		SAD RP0S	
02623	602627		JMP .+4	
02624	740040	E417	HLT	
02625	203020		LAC RP0S	
02626	740040		HLT	
02627	741400	OFLCH1	SZL	/OR SNL IF A+B OVERFLOW
02630	740040	F418	HLT	/LINK FAILURE
02631	762616		LAW SERS01	/MAKE JMP FOR SCOPE
<hr/>				
/HAVE TESTED B-A PREVIOUS				
/SEE BMNSAT FOR SHORTER TEST				
/NOW TRY A+B-A-A = B-A				
02632	754001	SERS02	CLL!CLA!CMA	
02633	303016		ADD AP0S	
02634	303020		ADD RP0S	
02635	303017		ADD ANEG	
02636	303017		ADD ANFG	
02637	543024		SAD RMASUM	
02640	602644		JMP .+4	
02641	740040	F419	HLT	
02642	203024		LAC RMASUM	
02643	740040		HLT	
02644	740400	OFLCH2	SNL	/OR SNL IF NO OVERFLOW
02645	740040	F420	HLT	
02646	762632		LAW SERS02	/MAKE JMP FOR SCOPE
,EJECT				

/HAVE TESTED (B-A)-B = -A PREVIOUS
 /SEE BMAMBT FOR SHORTER TEST
 /NOW TRY A+B-A-A-B = -A

M2647	754001	SERS03	CLI !CLA !CMA
M2650	303016		ADD AP0S
M2651	303020		ADD RP0S
M2652	303017		ADD ANFG
M2653	303017		ADD ANFG
M2654	303021		ADD RNFG
M2655	543017		SAD ANFG
M2656	602662		JMP .+4
M2657	740040	F421	HLT
M2660	203017		LAC ANEG
M2661	740040		HLT
M2662	741400	0FLCH3	SZL
M2663	740040	F422	HLT
M2664	762647		LAW SERS03

/HAVE TEST -A-B NOW TRY A+B-A-A-B-R = -A-B
 /SEE MAPLMR FOR SHORTER TEST

M2665	754001	SERS04	CLI !CLA !CMA
M2666	303016		ADD AP0S
M2667	303020		ADD RP0S
M2670	303017		ADD ANFG
M2671	303017		ADD ANFG
M2672	303021		ADD RNFG
M2673	303021		ADD RNFG
M2674	543022		SAD SUMNFG
M2675	602701		JMP .+4
M2676	740040	F423	HLT
M2677	203022		LAC SUMNFG
M2700	740040		HLT
M2701	741400	0FLCH4	SZL
M2702	740040	E424	HLT
M2703	762665		LAW SERS04

,EJECT

/SNL IF A+B OVERFLOW
 /OVERFLOW FAILED OR LINK FAILED
 /MAKE JMP FOR SCOPE LOOP

/HAVE TESTED $(-A-B)+A = -B$ NOW $A+B-A-A-B+B+A = -B$
 /USE MAHPAT FOR SHORTER TEST
 SERS05 CLL:CLA:CMA
 02704 754001 ADD APOS
 02705 303016 ADD RPOS
 02706 303020 ADD ANEG
 02707 303017 ADD ANEG
 02710 303017 ADD RNEG
 02711 303021 ADD BNFG
 02712 303021 ADD APOS
 02713 303016 ADD RNEG
 02714 543021 SAD BNFG
 02715 602721 JMP .+4
 02716 740040 F425 HLT
 02717 203021 LAC RNEG
 02720 740040 HLT
 02721 740040 OFLCH5 SNL /OR S2L IF A+B DO NOT OVERFLOW
 02722 740040 E426 HLT /LINK OR OVERFLOW FAILED
 02723 762704 LAW SERS05 /MAKE JMP FOR SCOPE
 /
 /HAVE DONE $-R+A$ PREVIOUSLY
 /NOW DO $A+B-A-A-R-R+A+A = -B+A$
 /USE AMNSBT FOR SHORTER TEST
 SERS06 CLL:CLA:CMA
 02724 754001 ADD APOS
 02725 303016 ADD RPOS
 02726 303020 ADD ANEG
 02727 303017 ADD ANEG
 02730 303017 ADD ANEG
 02731 303021 ADD RNEG
 02732 303021 ADD BNFG
 02733 303016 ADD APOS
 02734 303016 ADD APOS
 02735 543025 SAD AMRSUM
 02736 602742 JMP .+4
 02737 740040 E427 HLT
 02740 203025 LAC AMRSUM
 02741 740040 HLT
 02742 741400 OFLCH6 S2L /OR SNL IF A+B OVERFLOW
 02743 740040 E428 HLT /OVERFLOW OR LINK FAILED
 02744 762724 LAW SERS06 /MAKE JMP FOR SCOPE
 .EJECT

/HAVE DONE (-B+A)+B PREVIOUSLY
 /NOW DOE A+B-A-B-B+A+A+B = A
 /USE AMRPT FOR SHORTER EST

U2745	754001	SERS07	CLL!CLA!CMA
U2746	303016		ADD AP0S
U2747	303020		ADD RP0S
U2750	303017		ADD ANEG
U2751	303017		ADD ANFG
U2752	303021		ADD RNFG
U2753	303021		ADD BNFG
U2754	303016		ADD AP0S
U2755	303016		ADD AP0S
U2756	303020		ADD RP0S
U2757	543016		SAD AP0S
U2760	602764		JMP .+4
U2761	740040	F429	HLT
U2762	203016		LAC AP0S
U2763	740040		HLT
U2764	740400	0FLCH7	SNI
U2765	740040	F430	HLT
U2766	762745		LAW SERS07

/OR SIZ IF A+B NOT OVERFLOW
 /LINK OR OVERFLOW FAILED
 /MAKE JMP FOR SCOPE LOOP

/

/AFTER ONE PASS
 /MAKE ALL B CONSTANTS A
 /AND MAKE ALL A CONSTANTS B

/

U2767	443027	CONCHG	TSZ PASS2	/2ND PASS
U2770	603011		JMP CKLP	/YES DONF 2ND
U2771	203016		LAC AP0S	/A
U2772	043020		DAC RP0S	/IS NOW B
U2773	203021		LAC BNFG	/BNFG
U2774	043017		DAC ANFG	/IS ANEG
U2775	740001		CMA	
U2776	043016		DAC AP0S	/B IS A
U2777	203020		LAC RP0S	
U2800	740001		CMA	/ANEQ
U2801	043021		DAC BNFG	/IS NEQ
U2802	203024		LAC RMASUM	
U2803	040010		DAC 10	
U2804	203025		LAC AMRSUM	/A-B
U2805	043024		DAC RMASUM	/IS NOW A-B
U2806	200010		LAC 10	
U2807	043025		DAC AMRSUM	
U2808	602454		JMP API.SRT	/IS NOW A-B
			EJECT	/OVERFLOW SERUP

03011	4476313	CKLP	IS7 WORK3	/CHECK D0NF LOOPING
03012	602362		JMP RANADD	/LOOP
03013	106102		JMS GENRAN	/GET NO. FOR NEXT LOOP
03014	106126		JMS CKNO	
03015	603030		JMP ADFDON	
		/		
03016	0000000	APOS	0	/A
03017	0000000	ANE.G	0	/-A
03020	0000000	RPOS	0	/B
03021	0000000	RNFG	0	/-B
03022	0000000	SUMNFG	0	/-A+(-B)
03023	0000000	SUMP0S	0	/A+B
03024	0000000	RMASUM	0	/B+(-A)
03025	0000000	AMPSUM	0	/A+(-B)
		/		
03026	0000000	MSKBIT	0	.EJECT

U3027 000000

PASS2 A

/GET A RANDOM NUMBER AND ITS 1'S COMPLEMENT
 /EACH BIT WILL HAVE A 0 IN ONE OF THE TWO NUMBERS
 /MAKE THE 0 BIT = 1 AND ADD THE NUMBERS BOTH WAYS
 /FIRST ADD IS THE (AC) IS THE ALTERED 0 = 1
 /SECOND ADD IS THE (MB) IS THE ALTERED 0 = 1
 /THE RESULT OF BOTH ADDS SHOULD = THE ALTERED BIT = 1

U3030 106102
 U3031 043016
 U3032 740001
 U3033 043017

ADFDON JMS GENRAN /GET RANDOM NUMBER
 DAC AP0S /SAVE IT
 CMA /MAKE ONES COMPLEMENT
 DAC ANFG /AND SAVE IT

/
 /THE FIRST BIT TO BE ALTERED IS 0 THEN CONTINUE TO 17

U3034 207554
 U3035 043026

LAC K400K
 DAC MSKBIT

/

/SET UP NEXT BIT TO TEST - ALTERED NUMBER GOES TO AP0S
 RISETU LAC AP0S
 AND MSKBIT
 SZA /DOES AP0S BIT = 0
 JMP MO0NFG /NO ALTER ANFG
 LAC AP0S
 XOR MSKBIT
 DAC RP0S /MODIFIED NUMBER GOES TO AP0S
 LAC ANFG
 DAC BNFG /UNMOD NUMBER GOES TO BNFG
 DAC BNFG
 JMP RITTS1

/
 /THE ONES COMP NUMBER HAS THE 0 BIT MODIFY IT

U3050 203017
 U3051 243026
 U3052 043020
 U3053 203016
 U3054 043021

MODNFG LAC ANFG
 XOR MSKBIT
 DAC RP0S /MOD NUMBER TO BP0S
 LAC AP0S
 DAC BNFG /UNMOD NUMBER TO BNFG

/
 /COMPLEMENTED BIT TEST1 (AC) = MODIFIED NUMBER AT ADD

U3055 744000
 U3056 203020
 U3057 303021
 U3060 543026
 U3061 603065
 U3062 740040
 U3063 203026
 U3064 740040
 U3065 741400
 U3066 740040
 U3067 763055

RITTS1 CLL /GET MODIFIED NUMBER
 LAC RP0S /ADD UNMODIFIED
 ADD BNFG /RESULT SHOULD = BIT CHANGED
 SAD MSKBIT
 JMP .+4
 F431 HLT /DISPLAY INCORRECT RESULTS
 LAC MSKBIT
 HLT /DISPLAY BIT ALTERED AND EXP
 OF1CH8 S2L /NO OVERFLOW
 F432 HLT /OVERFLOW NOT ALLOWED
 LAW RITTS1 /MAKE JMP FOR SCOPE LOOP
 .EJECT

```

/COMP BIT TEST 2 (MB) = MODIFIED NUMBER AT ADD
/
03070    744000   RITTS2    CLL           /GET UNMODIFIED NUMBER
03071    203021   LAC RNFG      /ADD MODIFIED
03072    303020   ADD RP0S      /RESULT SHOULD = BIT CHANGED
03073    543026   SAD MSKBIT
03074    603100   JMP ,+4      /OK
03075    740040   F433       HLT          /DISPLAY INCORRECT RESULTS
03076    203026   LAC MSKBIT
03077    740040   HLT          /DISPLAY BIT ALTERED AND EXP
03100    741400   OFLCH9     S2L          /SHOULD NOT OVERFLOW
03101    740040   F434       HLT          /MAKE JMP FOR SCOPE LOOP
03102    763070   LAW RITTS2

/
/POSITION MASK BIT OVER 1 PLACE
/IF 17 HAS BEEN DONE CONTINUE
/
03103    203026   LAC MSKBIT   /GET LAST
03104    744020   RCR          /POSITION
03105    043026   DAC MSKBIT   /SAVE
03106    740200   S2A          /DONE ALL BITS
03107    603036   JMP RISETU   /DO FOR NEXT BIT

/
/END OF TEST SEQUENCE
/
03110    447634   IS7 WORK3    /CHECK DONE LOOPING
03111    603030   JMP ADFDN    /LOOP
03112    106102   JMS GENRAN   /GET NO. FOR NEXT LOOP
03113    106126   JMS CKNO
03114    750004   LAS          /CHECK FOR CONTINUOUS LOOP
03115    742010   RTL          /CK ACS ?
03116    741100   SPA          /LOOP
03117    602362   JMP RANADD
                           .EOT

```

/PDP-9 BASIC EXERCISER - TAPE 4

/TEST SAD

/

03120	207531	SADAC	LAC K0	/AC = 0
03121	547531		SAD K0	
03122	741000		SKP	
03123	740040	F435	HALT	/ERROR, SAD K0 SKIPPED
03124	207531		LAC K0	/AC = 0
03125	547573		SAD K7S	
03126	740040	F436	HALT	/ERROR, SAD K7S FAILED TO SKIP
03127	207573		LAC K7S	/AC = 1'S
03130	547531		SAD K0	
03131	740040	F437	HALT	/ERROR, SAD K0 FAILED TO SKIP
03132	207573		LAC K7S	/AC = 1'S
03133	547573		SAD K7S	
03134	741000		SKP	
03135	740040	F438	HALT	/ERROR, SAD K7S SKIPPED
		/SAD, TAD		
		/		
03136	750000		CLA	/AC = 0
03137	347531		TAD K0	
03140	547531		SAD K0	
03141	741000		SKP	
03142	740040	F439	HALT	/ERROR, SAD K0 SKIPPED
03143	750000		CLA	/AC = 0
03144	347531		TAD K0	
03145	547573		SAD K7S	
03146	740040	F440	HALT	/ERROR, SAD K7S FAILED TO SKIP
03147	750000		CLA	/AC = 0
03150	347573		TAD K7S	
03151	547531		SAD K0	
03152	740040	F441	HALT	/ERROR, SAD K0 FAILED TO SKIP
03153	750000		CLA	/AC = 0
03154	347573		TAD K7S	
03155	547573		SAD K7S	
03156	741000		SKP	
03157	740040	F442	HALT	/ERROR, SAD K7S SKIPPED
		/		
		.EJECT		

/SEQUENTIAL SAD

/

UX160	207531	LAC K0	/AC = 0
UX161	547573	SAD K7S	
UX162	760001	LAW 1	/760001
UX163	547573	SAD K7S	
UX164	760002	LAW 2	/760002
UX165	547573	SAD K7S	
UX166	760004	LAW 4	/760004
UX167	547573	SAD K7S	
UX170	760010	LAW 10	/760010
UX171	547573	SAD K7S	
UX172	760020	LAW 20	/760020
UX173	547573	SAD K7S	
UX174	760040	LAW 40	/760040
UX175	740000	SZA	
UX176	740040	HALT	
	F443		/ERROR, AC NT 0. CONTENTS OF /AC = LAST SAD THAT FAILED

/TEST SAD, SKP SERIES

/

UX177	750000	- CLA	/AC = 0
UX200	54757 X2	SAD K7S	
UX201	760001	LAW 1	/760001
UX202	741000	SKP	
UX203	760002	LAW 2	/760002
UX204	54757 X2	SAD K7S ~	
UX205	760004	LAW 4	/760004
UX206	741000	SKP	
UX207	760010	LAW 10	/760010
UX210	54757 X2	SAD K7S ~	
UX211	760020	LAW 20	/760020
UX212	741000	SKP	
UX213	760040	LAW 40	/760040
UX214	740000	SZA ~	
UX215	740040	HALT	
	F444		/ERROR, AC NOT 0, CONTSNTS /OF AC = LAST SAD OR SKP

/

UX216	447634	- TSZ WORK3	/CHECK DONE LOOPING
UX217	603120	JMP SADAC	/LOOP
UX220	106102	JMS GENRAN	/GET NO. FOR NEXT LOOP
UX221	106126	JMS CKNO	

/

.EJECT

/TEST DEM

/

13222	207650	DZMAC	LAC KHALT	/AC = 740040
13223	151111		DZM 11111	/ADDR 1111 OR 01111
13224	211111		LAC 11111	
13225	740200		SZA	
13226	740040	F445	HALT	/ERROR. DZM FAILED AT 1111 /OR 01111
13227	207650		LAC KHALT	/AC = 740040
13230	152222		DZM 12222	
13231	212222		LAC 12222	
13232	740200		SZA	
13233	740040	F446	HALT	/ERROR. DZM FAILED AT 1222 OR /02222
13234	207650		LAC KHALT	/AC = 740040
13235	153333		DZM 13333	
13236	213333		LAC 13333	
13237	740200		SZA	
13240	740040	F447	HALT	/ERROR. DZM FAILED AT 13333 OR /03333
13241	207650		LAC KHALT	/AC = 740040
13242	154444		DZM 14444	
13243	214444		LAC 14444	
13244	740200		SZA	
13245	740040	F448	HALT	/ERROR. DZM FAILED AT 14444 /OR 04444
13246	207650		LAC KHALT	/AC = 740040
13247	155555		DZM 15555	
13250	215555		LAC 15555	
13251	740200		SZA	
13252	740040	F449	HALT	/ERROR. DZM FAILED AT 15555 /OR 05555
13253	207650		LAC KHALT	/AC = 740040
13254	156666		DZM 16666	
13255	216666		LAC 16666	
13256	740200		SZA	
13257	740040	F450	HALT	/ERROR. DZM FAILED AT 16666 /OR 06666
13260	207650		LAC KHALT	/AC = 740040
13261	157777		DZM 17777	
13262	217777		LAC 17777	
13263	740200		SZA	
13264	740040	F451	HALT	/ERROR. DZM FAILED AT 17777 /OR 07777

.EJECT

03265	207650	LAC KHALT	/AC = 740040
03266	152525	DZM 12525	
03267	212525	LAC 12525	
03270	740200	SZA	
03271	740040	F452 HALT	/ERROR. DZM FAILED AT 12525 /OR 02525
03272	207650	LAC KHALT	/AC = 740040
03273	155252	DZM 15252	
03274	215252	LAC 15252	
03275	740200	SZA	
03276	740040	F453 HALT	/ERROR. DZM FAILED AT 15252 OR 05252
		/	
		/TEST AC AFTER A DZM	
		/	
03277	207573	LAC K7S	/AC = 777777
03300	157777	DZM 17777	
03301	740001	CMA	
03302	740200	SZA	
03303	740040	F454 HALT	/ERROR. AC CHANGED AFTER A DZM
		/	
		//TEST AC, LINK, ADR. 17777 OR 07777 AFTER A DZM	
		/	
03304	754001	CLA!CMA!CLL	/AC = 1'S, LINK = 0
03305	307573	ADD K7S	
03306	157777	DZM 17777	
03307	740001	CMA	
03310	740200	SZA	
03311	740040	F455 HALT	/ERROR. AC NOT 1'S AFTER A DZM
03312	741400	SZL	
03313	740040	F456 HALT	/ERROR. LINK NOT 0
03314	217777	LAC 17777	
03315	740200	SZA	
03316	740040	F457 HALT	/ERROR. DZM FAILED AT 177777 OR 07777
		/	
		/	
		/SEQUENTIAL DZM	
		/	
03317	207573	LAC K7S	/AC = 1'S
03320	152525	DZM 12525	
03321	155252	DZM 15252	
03322	157777	DZM 17777	
03323	150000	DZM 10000	
03324	740001	CMA	/AC = 0
03325	750200	CLA!SZA	
03326	740040	F458 HALT	/ERROR. AC NOT 1'S AFTER /DZM SERIES
		.EJECT	

U3327	352525	TAD 12525	
U3330	355252	TAD 15252	
U3331	357777	TAD 17777	
U3332	350000	TAD 10000	
U3333	740000	SZA	
U3334	740040	F459	HALT
		/	/ERROR, DEM FAILED
U3335	447634		TS7 WORK3
U3336	603222		JMP DEMAC
U3337	106102		JMS GENRAN
U3340	106126		JMS CKNO
		/	/CHECK DONE LOOPING
		/	/LOOP
		/TEST DAC	/GET NO. FOR NEXT LOOP
U3341	207574	DACAC	LAC K51S
U3342	051111		DAC 11111
U3343	551111		SAD 11111
U3344	741000		SKP
U3345	740040	E460	HALT
			/ERROR, DAC ADR CONTENTS NOT EQUAL
U3346	207575		LAC K12S
U3347	052222		DAC 12222
U3350	552222		SAD 12222
U3351	741000		SKP
U3352	740040	F461	HALT
			/ERROR, 122222 OR 022222 CONTENTS
U3353	207576		LAC K13S
U3354	053333		DAC 13333
U3355	553333		SAD 13333
U3356	741000		SKP
U3357	740040	F462	HALT
			/ERROR, 13333 OR 03333 CONTENTS
U3360	207577		LAC K14S
U3361	054444		DAC 14444
U3362	554444		SAD 14444
U3363	741000		SKP
U3364	740040	F463	HALT
			/ERROR, 14444 OR 03333 CONTENTS
U3365	207600		LAC K15S
U3366	055555		DAC 15555
U3367	555555		SAD 15555
U3370	741000		SKP
U3371	740040	E464	HALT
			/ERROR, 15555 OR 05555 CONTENTS
			/NO = AC, DAC FAILED
			.EJECT

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*****  

03372 207601 LAC K16S /AC = 166666  

03373 056666 DAC 16666  

03374 556666 SAD 16666  

03375 741000 SKP  

03376 740040 F465 HALT /ERROR, 16666 OR 06666 CONTENTS  

03377 207602 LAC K17S /NOT = AC, DAC FAILED  

03400 057777 DAC 17777 /AC = 17777  

03401 557777 SAD 17777  

03402 741000 SKP  

03403 740040 F466 HALT /ERROR, 17777 OR 07777 CONTENTS  

03404 207605 LAC K010 /NOT = AC, DAC FAILED  

03405 052525 DAC 12525 /AC = 252525  

03406 552525 SAD 12525  

03407 741000 SKP  

03410 740040 F467 HALT /ERROR, 12525 OR 02525 CONTENTS  

03411 207606 LAC K101 /AC = AC, DAC FAILED  

03412 055252 DAC 15252 /AC = 525252  

03413 555252 SAD 15252  

03414 741000 SKP  

03415 740040 F468 HALT /ERROR, 15252 OR 05252 CONTENTS  

03416 744000 /  

03417 2075782 /  

03420 052525 /SFQUENTIAL DAC  

03421 055252 /  

03422 057777 /  

03423 051000 /  

03424 051111 /  

03425 740001 CLL /L = 0  

03426 750200 LAC K79 /AC = 1'S  

03427 740040 DAC 12525  

03428 312525 DAC 15252  

03429 315252 DAC 17777  

03430 311000 DAC 11000  

03431 311111 DAC 11111  

03432 317777 ADD 12525  

03433 311000 ADD 15252  

03434 311111 ADD 17777  

03435 740001 ADD 11000  

03436 740200 ADD 11111  

03437 740040 CMA /AC = 0  

F469 HALT /ERROR, AC NOT 1'S AFTER DAC SERIES  

03438 312525 ADD 12525  

03439 315252 ADD 15252  

03440 317777 ADD 17777  

03441 311000 ADD 11000  

03442 311111 ADD 11111  

03443 740001 CMA /AC = 0  

03444 740200 SZA /ERROR, DAC FAILED. ONE OR MORE  

03445 740040 F470 HALT /ADDRESSES NOT ONES  

03446 447634 /  

03447 603341 ISZ WORK3 /CHECK DONE LOOPING  

03448 106102 JMP DACAC /LOOP  

03449 106126 JMS GENRAN /GET NO. FOR NEXT LOOP  

03450 106126 JMS CKNO  

03451 106126 .EJECT

```

/TEST ISZ

/

U3444	207531	ISZAC	LAC K0 DAC 10100 ISZ 10100 SKP;CLA;CMA	/AC = 0
U3445	050100		HALT	/ERROR, ISZ SKIPPED
U3446	450100		AND K1	/AC = 1
U3447	751001		SAD 10100	
U3450	740040	F471	SKP	
U3451	507532		HALT	/ERROR, 10100 OR 00100 NOT 1
U3452	550100		AND K1	/ISZ FAILED
U3453	741000		SAD 10100	/AC = 377777
U3454	740040	F472	SKP	
U3455	207627		HALT	/ERROR, 10100 OR 00100 NOT 400000
U3456	050100		LAC M4'0K	/ISZ FAILED
U3457	450100		DAC 10100	/AC = 777776
U3460	751001		ISZ 10100	
U3461	740040	F473	SKP;CLA;CMA	
U3462	507554		HALT	/ERROR, ISZ SKIPPED
U3463	550100		AND K4'0K	
U3464	741000		SAD 10100	
U3465	740040	F474	SKP	
U3466	207621		HALT	/ERROR, 10100 OR 00100 NOT 400000
U3467	050100		LAC M1	/ISZ FAILED
U3470	450100		DAC 10100	/AC = 777776
U3471	751001		ISZ 10100	
U3472	740040	F475	SKP;CLA;CMA	
U3473	507573		HALT	/ERROR, ISZ SKIPPED
U3474	550100		AND K7S	
U3475	741000		SAD 10100	
U3476	740040	F476	SKP	
U3477	207621		HALT	/ERROR, 10100 OR 00100 NOT 777777
U3500	057777		LAC M1	/ISZ FAILED
U3501	457777		DAC 17777	/AC = 777776
U3502	751001		ISZ 17777	
U3503	740040	F477	SKP;CLA;CMA	
U3504	507573		HALT	/ERROR, ISZ SKIPPED
U3505	557777		AND K7S	
U3506	741000		SAD 17777	
U3507	740040	F478	SKP	
U3510	207627		HALT	/ERROR, 1777 OR 07777 NOT 777777
U3511	057777		LAC M4'0K	/ISZ FAILED
U3512	457777		DAC 17777	/AC = 377777
U3513	751001		ISZ 17777	
U3514	740040	F479	SKP;CLA;CMA	
U3515	507554		HALT	/ERROR, ISZ SKIPPED
U3516	557777		AND K4'0K	
U3517	741000		SAD 17777	
			SKP	
			EJECT	

03520	740040	F480	HALT	/ERROR, 17777 OR 07777 NOT 400000 /ISZ FAILED
03521	207531		LAC K0	
03522	057777		DAC 17777	
03523	457777		ISZ 17777	
03524	751001		SKP!CLA!CMA	
03525	740040	F481	HALT	/ERROR, ISZ SKIPPED
03526	507532		AND K1	
03527	557777		SAD 17777	
03530	741000		SKP	
03531	740040	F482	HALT	/ERROR, 17777 OR 07777 NOT 1 /ISZ FAILED
03532	750000		CLA	/AC = 0
03533	247573		XOR K7S	/AC = 1'S
03534	051111		DAC 11111	
03535	451111		ISZ 11111	
03536	740040	F483	HALT	/ERROR, ISZ FAILED TO SKIP
03537	211111		LAC 11111	
03540	740200		SZA	
03541	740040	F484	HALT	/ERROR, 11111 OR 01111 NOT 0 /ISZ FAILED
03542	750000		CLA	/AC = 0
03543	247573		XUR K7S	/AC = 1'S
03544	052222		DAC 12222	
03545	452222		ISZ 12222	
03546	740040	F485	HALT	/ERROR, ISZ FAILED TO SKIP
03547	212222		LAC 12222	
03550	740200		SZA	
03551	740040	F486	HALT	/ERROR, 12222 OR 02222 NOT 0 /ISZ FAILED
03552	750000		CLA	
03553	247573		XOR K7S	/AC = 1'S
03554	053333		DAC 13333	
03555	453333		ISZ 13333	
03556	740040	F487	HALT	/ERROR, ISZ DID NOT SKIP
03557	213333		LAC 13333	
03560	740200		SZA	
03561	740040	F488	HALT	/ERROR, 13333 OR 03333 NOT 0 /ISZ FAILED
03562	750000		CLA	
03563	247573		XUR K7S	/AC = 1'S
03564	054444		DAC 14444	
03565	454444		ISZ 14444	
03566	740040	F489	HALT	/ERROR, ISZ DID NOT SKIP
03567	214444		LAC 14444	
03570	740200		SZA	
03571	740040	F490	HALT	/ERROR, 14444 OR 04444 NOT 0 /ISZ FAILED
			.EJECT	

U3572	7500000	CLA	
U3573	247573	XOR K7S	/AC = 1'S
U3574	0555555	DAC 15555	
U3575	4555555	ISZ 15555	
U3576	7400040	F491 HALT	/ERROR, ISZ DID NOT SKIP
U3577	2155555	LAC 15555	
U3600	7402000	SZA	
U3601	7400040	F492 HALT	/ERROR, 15555 OR 05555 NOT 0 /ISZ FAILED
U3602	7500000	CLA	
U3603	247573	XOR K7S	/AC = 1'S
U3604	0566666	DAC 16666	
U3605	4566666	ISZ 16666	
U3606	7400040	F493 HALT	/ERROR, ISZ DID NOT SKIP
U3607	2166666	LAC 16666	
U3610	7402000	SZA	
U3611	7400040	F494 HALT	/ERROR, 16666 OR 06666 NOT 0 /ISZ FAILED
U3612	7500000	CLA	
U3613	247573	XOR K7S	/AC = 1'S
U3614	0577777	DAC 17777	
U3615	4577777	ISZ 17777	
U3616	7400040	F495 HALT	/ERROR, ISZ DID NOT SKIP
U3617	2177777	LAC 17777	
U3620	7402000	SZA	
U3621	7400040	F496 HALT	/ERROR, 17777 OR 07777 NOT 0 /ISZ FAILED
U3622	7500000	CLA	
U3623	247573	XOR K7S	/AC = 1'S
U3624	052525	DAC 12525	
U3625	452525	ISZ 12525	
U3626	7400040	F497 HALT	/ERROR, ISZ DID NOT SKIP
U3627	212525	LAC 12525	
U3630	7402000	SZA	
U3631	7400040	F498 HALT	/ERROR, 12525 OR 02525 NOT 0 /ISZ FAILED
U3632	7500000	CLA	
U3633	247573	XOR K7S	/AC = 1'S
U3634	055252	DAC 15252	
U3635	455252	ISZ 15252	
U3636	7400040	F499 HALT	/ERROR, ISZ DID NOT SKIP
U3637	215252	LAC 15252	
U3640	7402000	SZA	
U3641	7400040	F500 HALT	/ERROR, 15252 OR 05252 NOT 0 /ISZ FAILED

.EJECT

/TFST ISZ, SKP

/

03642	2075782	LAC K7S	/AC = 1'S
03643	052525	DAC 12525	/12525 OR 02525
03644	055252	DAC 15252	/15252 OR 05252
03645	057777	DAC 17777	/17777 OR 07777
03646	051000	DAC 11100	/11000 OR 01000
03647	050100	DAC 10100	/10100 OR 00100
03650	452525	ISZ 12525	
03651	741000	SKP	
03652	455252	ISZ 15252	
03653	741000	SKP	
03654	457777	ISZ 17777	
03655	741000	SKP	
03656	451000	ISZ 11100	
03657	741000	SKP	
03660	450100	ISZ 10100	
03661	740040	F501 HALT	/ERROR. ISZ DID NOT SKIP
03662	312525	ADD 12525	
03663	315252	ADD 15252	
03664	317777	ADD 17777	
03665	311000	ADD 11100	
03666	310100	ADD 10100	
03667	740001	CMA	
03670	740200	SZA	
03671	740040	F502 HALT	/ERROR. ALL ADDRS. NOT 0
		/	
		/	

/SEQUENTIAL ISZ, NO-SKIP

/

03672	2076216	LAC M400K	/AC = 377777
03673	052525	DAC 12525	/OR 02525 -400000
03674	055252	DAC 15252	/OR 05252 "
03675	057777	DAC 17777	/OR 07777 "
03676	051000	DAC 11100	/OR 01000 -377777
03677	050100	DAC 10100	/OR 00100 "
03700	452525	ISZ 12525	
03701	455252	ISZ 15252	
03702	457777	ISZ 17777	
03703	451000	ISZ 11100	
03704	450100	ISZ 10100	
03705	750000	CLA	/AC = 0
03706	312525	ADD 12525	
03707	315252	ADD 15252	
03710	317777	ADD 17777	
03711	311000	ADD 11100	
03712	310100	ADD 10100	
03713	247554	XOR K402K	/RESULT = 400002
03714	740200	SZA	
03715	740040	F503 HALT .EJECT	/ERROR. ALL ADDRS. NOT 400000

/TEST ISZ-SKP, SKIP

/

U3716	207573	LAC K7S	/AC = 1'S
U3717	055252	DAC 15252	
U3720	455252	ISZ 15252	
U3721	741000	SKP	
U3722	741000	SKP	
U3723	740040	E504 HALT	/ERROR, ISZ-SKP DID NOT SKIP

/TEST SKP-ISZ, SKIP

/

U3724	207573	LAC K7S	/AC = 17S
U3725	055252	DAC 15252	
U3726	741000	SKP	
U3727	740000	NOP	
U3730	455252	ISZ 15252	
U3731	740040	E505 HALT	/ERROR, SKP-ISZ DID NOT SKIP

/TEST SKP-ISZ, NO-SKIP

/

U3732	207531	LAC K0	/AC = 0
U3733	055252	DAC 15252	
U3734	741000	SKP	
U3735	740000	NOP	
U3736	455252	ISZ 15252	
U3737	741000	SKP	
U3740	740040	E506 HALT	/ERROR, SKP-ISZ SKIPPED

/

ISZ WORK3	
JMP ISZAC	
JMS GENRAN	
JMS CKNO	
.EJECT	

/CHECK DONE LOOPING

/LOOP

/GET NO, FOR NEXT LOOP

/TEST JMP

/

03745	207667		LAC JMPRET	
03746	740200		SZA	
03747	740040	F507	HALT	/ERROR JMP .-7, .+4 OR .+5 FAILED
03750	204130	INIT4K	LAC JMPSEQ	
03751	047667		DAC JMPRET	/LOAD 4K WITH JMP TO 22
03752	203752		LAC ,	
03753	507556		AND K10K	
03754	740200		SZA	
03755	603761		JMP .+4	
03756	207556		LAC K10K	
03757	047630		DAC RJCNT	
03760	603765		JMP .+5	
03761	207541		LAC K20	
03762	047630		DAC RJCNT	
03763	741000		SKP	
03764	147667		D2M JMPRET	/CLEAR ERROR TABLE
03765	204130		LAC JMPSEQ	
03766	067630		DAC* RJCNT	
03767	447630		IS7 RJCNT	
03770	207630		LAC RJCNT	
03771	546061		SAD K17777	
03772	741000		SKP	
03773	603764		JMP .-7	
03774	207667		LAC JMPRFT	
03775	740200		SZA	
03776	740040	F508	HALT	/ERROR, JMP .+4, .+5 OR .-7 FAILED
03777	204131		LAC MON	/PRESS CONTINUE TO DETERMINE
04000	043745		DAC E517-2	/JMP FAILURE
04001	740200	MONX	NOP	
		/		
04002	207670		LAC J111	
04003	740200		SZA	
04004	740040	F509	HALT	/ERROR, RJMP OR JMP TO 11111
				/OR 01111 FAILED
04005	204132		LAC RJ111	
04006	047670		DAC J111	/STORE JMP ADDRESS IN TABLE
04007	051111		DAC 11111	
04010	611111		JMP 11111	/JMP TO 11111 OR 01111
04011	741000		SKP	
04012	147670	RJMP1	D2M J111	/CLEAR ERROR WORD TABLE
04013	207671		LAC J222	
04014	740200		SZA	
04015	740040	F510	HALT	/ERROR, RJMP OR JMP TO 12222
				/OR 02222 FAILED
04016	204133		LAC RJ222	
04017	047671		DAC J222	
04020	052222		DAC 12222	
04021	612222		JMP 12222	/JMP 1222 OR 02222
04022	741000		SKP	
04023	147671	RJMP2	D2M J222	/CLEAR ERROR TABLE
			,EJECT	

W4024	207672		LAC J333	
W4025	740200		SZA	
W4026	740040	F511	HALT	/ERROR, RJMP OR JMP TO 13333 /OR 03333 FAILED
W4027	204134		LAC RJ33	
W4030	047672		DAC J333	
W4031	053333		DAC 13333	
W4032	613333		JMP 13333	
W4033	741000		SKP	/JMP TO 13333 OR 03333
W4034	147672	RJMP3	D2M J333	/CLEAR ERROR TABLE
W4035	207673	/	LAC J444	
W4036	740200		SZA	
W4037	740040	F512	HALT	/ERROR, RJMP OR JMP TO 14444 /OR 04444 FAILED
W4040	204135		LAC RJ444	
W4041	047673		DAC J444	
W4042	054444		DAC 14444	
W4043	614444		JMP 14444	
W4044	741000		SKP	/JMP TO 14444 OR 04444
W4045	147673	RJMP4	D2M J444	/CLEAR ERROR TABLE
W4046	207674	/	LAC J555	
W4047	740200		SZA	
W4050	740040	F513	HALT	/ERROR, RJMP OR JMP TO 15555 /OR 05555 FAILED
W4051	204136		LAC RJ555	
W4052	047674		DAC J555	
W4053	055555		DAC 15555	
W4054	615555		JMP 15555	
W4055	741000		SKP	/JMP TO 15555 OR 05555
W4056	147674	RJMP5	D2M J555	/CLEAR ERROR TABLE
W4057	207675	/	LAC J666	
W4060	740200		SZA	
W4061	740040	F514	HALT	/ERROR, RJMP OR JMP TO 16666 /OR 06666 FAILED
W4062	204137		LAC RJ666	
W4063	047675		DAC J666	
W4064	056666		DAC 16666	
W4065	616666		JMP 16666	
W4066	741000		SKP	/JMP TO 16666 OR 06666
W4067	147675	RJMP6	D2M J666	/CLEAR ERROR TABLE
		/	.EJECT	

04070	207676		LAC J777	
04071	740200		SZA	
04072	740040	F515	HALT	/ERROR, RJMP OR JMP TO 17777 /OR 07777 FAILED
04073	204140		LAC RJ777	
04074	047676		DAC J777	
04075	057777		DAC 17777	
04076	617777		JMP 17777	/JMP TO 17777 OR 07777
04077	741000		SKP	
04100	147676	RJMP7	DZM J777	/CLEAR ERROR TABLE
04101	207700	/	LAC J252	
04102	740200		SZA	
04103	740040	F516	HALT	/ERROR, RJMP OR JMP TO 12525 /OR 02525 FAILED
04104	204141		LAC RJ252	
04105	047700		DAC J252	
04106	052525		DAC 12525	
04107	612525		JMP 12525	/JMP TO 12525 OR 02525
04110	741000		SKP	
04111	147700	RJMPH	DZM J252	/CLEAR ERROR TABLE
04112	207677	/	LAC J525	
04113	740200		SZA	
04114	740040	E517	HALT	/ERROR, RJMP OR JMP TO 15252 /OR 05252 FAILED
04115	204142		LAC RJ525	
04116	047677		DAC J525	
04117	055252		DAC 15252	
04120	615252		JMP 15252	/JMP TO 15252 OR 05252
04121	741000		SKP	
04122	147677	RJMP9	DZM J525	/CLEAR ERROR TABLE
04123	447634		TSZ WORK3	/CHECK DONE LOOPING
04124	604002		JMP MODX+1	/LOOP
04125	106102		JMS GENRAN	/GET NO. FOR NEXT LOOP
04126	106126		JMS CKNO	
04127	604143		JMP TSCAL	/TFST CAL
		/	JMP CONSTANTS. THESE ARE MODIFIED WHEN IN HI 4K	
		/		
04130	6000070	JMPSFQ	JMP SEQUNF	
04131	604002	MOD	JMP MODX+1	
04132	604012	RJ111	JMP RJMP1	
04133	604023	RJ222	JMP RJMP2	
04134	604034	RJ333	JMP RJMP3	
04135	604045	RJ444	JMP RJMP4	
04136	604056	RJ555	JMP RJMP5	
04137	604067	RJ666	JMP RJMP6	
04140	604100	RJ777	JMP RJMP7	
04141	604111	RJ252	JMP RJMP8	
04142	604122	RJ525	JMP RJMP9	
			.EJECT	

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/TFST CAL
/
W4143 2077040 TSCAL LAC CAL0
W4144 740200 SZA
W4145 740040 F518 HALT
W4146 707704 LEM
W4147 754000 CLL:CLL
W4150 770020 LAW 10020
W4151 150704 D2M 10704
W4152 207563 LAC K2021
W4153 047701 DAC CAL0
W4154 204215 LAC RCAL0
W4155 040021 DAC 21
W4156 610704 JMP 10704
W4157 147701 D2M CAL0
W4160 200020 LAC 20
W4161 544216 SAD KCAL0
W4162 741000 SKP
W4163 740040 F519 HALT
W4164 210704 LAC 10704
W4165 740200 SZA
W4166 740040 F519A HALT
/
/TFST CAL LINK = 1
/
W4167 2077041 LAC CAL1
W4170 740200 SZA
W4171 740040 F520 HALT
W4172 744002 CLL:CM1
W4173 2075643 LAC K2120
W4174 0477041 DAC CAL1
W4175 204217 LAC RCAL1
W4176 040021 DAC 21
W4177 610704 JMP 10704
W4200 1477041 D2M CAL1
W4201 200020 LAC 20
W4202 544220 SAD KCAL0
W4203 741000 SKP
W4204 740040 F521 HALT
W4205 210704 LAC 10704
W4206 740200 SZA
W4207 740040 F521A HALT
/
TS7 WORK3
JMP TSCAL
JMS GENRAN
JMS CKNO
JMP TSJMS
/
.EJECT

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/CAL CONSTANTS. THESE ARE MODIFIED WHEN IN HI 4K

/

04215	604157	RCAL0	JMP RCALS0
04216	010705	KCAL0	10705
04217	604200	PCAL1	JMP RCALS1
04220	410705	KCALF	410705

.EJECT

/TEST JMS					
/					
U4221	207703	TSJMS	LAC	JSM71	
U4222	740200		SZA		/ERROR, JMS FROM 07777 TO 11111
U4223	740040	F522	HALT		/OR FROM 17777 TO 01111
U4224	744000		CLL		/LINK = 0
U4225	204537		LAC	RJSM71	/JMP TO RJMS71
U4226	051112		DAC	11112	/RJMP FROM JMS DEST'N
U4227	204540		LAC	RSM71	/JMS 11111
U4230	047777		DAC	07777	
U4231	047703		DAC	JSM71	
U4232	771112		LAW	11112	/AC = 771112
U4233	607777		JMP	07777	
U4234	147703	RJMS71	DZM	JSM71	/CLEAR ERROR TABLE
U4235	211111		LAC	11111	
U4236	544541		SAD	K10000	
U4237	741000		SKP		
U4240	740040	F523	HALT		/ERROR, (11111 OR 01111) NOT /10000 OR 00000
U4241	207704		LAC	JSM72	
U4242	740200		SZA		/ERROR, JMS FROM 07776 TO 12222
U4243	740040	F524	HALT		/OR FROM 17776 TO 02222
U4244	707704		LEM		
U4245	744000		CLL		
U4246	204542		LAC	RJSM72	/JMP TO RJMS72
U4247	052223		DAC	12223	
U4250	204543		LAC	RSM72	/JMS 12222
U4251	047776		DAC	07776	
U4252	047704		DAC	JSM72	
U4253	772223		LAW	12223	/AC = 772223
U4254	607776		JMP	07776	
U4255	147704	RJMS72	DZM	JSM72	/CLEAR ERROR TABLE
U4256	212222		LAC	12222	
U4257	544544		SAD	K77	
U4260	741000		SKP		
U4261	740040	F525	HALT		/ERROR, (12222 OR 02222) NOT /07777 OR 17777
U4262	207705		LAC	JSM73	
U4263	740200		SZA		/ERROR, JMS FROM 07775 TO 13333
U4264	740040	F526	HALT		/OR FROM 17775 TO 03333
U4265	707704		LEM		
U4266	744000		CLL		
U4267	204545		LAC	RJSM73	/JMP TO RJMS73
U4270	053334		DAC	13334	
U4271	204546		LAC	RSM73	/JMS 13333
U4272	047775		DAC	07775	
U4273	047705		DAC	JSM73	
U4274	773334		LAW	13334	/AC = 773334
U4275	607775		JMP	07775	
				EJECT	

↑↑↑↑

U4276	147705	RJMS73	DZM JSM73 LAC 13333 SAD K76 SKP	/CLEAR ERROR TABLE
U4277	213333			
U4300	544547			
U4301	741000			
U4302	740040	F527	HALT	/ERROR, (13333 OR 03333) NOT /07776 OR 17776
U4303	207706		LAC JSM74	
U4304	740200		SZA	
U4305	740040	F528	HALT	/ERROR, JMS FROM 07774 TO 14444 /OR FROM 17774 TO 04444
U4306	707704		LEM	
U4307	744000		CLL	
U4310	204550		LAC RJMS74	/JMP RJMS74
U4311	054445		DAC 14445	
U4312	204551		LAC RSM74	
U4313	047774		DAC 07774	
U4314	047706		DAC JSM74	
U4315	774445		LAW 14445	/AC = 774445
U4316	607774		JMP 07774	
U4317	147706	RJMS74	DZM JSM74	/CLEAR ERROR TABLE
U4320	214444		LAC 14444	
U4321	544552		SAD K75	
U4322	741000		SKP	
U4323	740040	F529	HALT	/ERROR, (14444 OR 04444) NOT /07775 OR 17775
U4324	207707		LAC JSM75	
U4325	740200		SZA	
U4326	740040	F530	HALT	/ERROR, JMS FROM 07773 TO 15555 /OR FROM 17773 TO 05555
U4327	707704		LEM	
U4330	744000		CLL	
U4331	204553		LAC RJMS75	/JMP TO RJMS75
U4332	055556		DAC 15556	
U4333	204554		LAC RSM75	
U4334	047773		DAC 07773	
U4335	047707		DAC JS475	
U4336	775556		LAW 15556	/AC = 775556
U4337	607773		JMP 07773	
U4340	147707	RJMS75	DZM JSM75	/CLEAR ERROR TABLE
U4341	215555		LAC 15555	
U4342	544555		SAD K74	
U4343	741000		SKP	
U4344	740040	F531	HALT	/ERROR, (15555 OR 05555) NOT /07774 OR 17774
U4345	207710		LAC JSM76	
U4346	740200		SZA	
U4347	740040	F532	HALT	/ERROR, JMS FROM 07772 TO 16666 /OR 17772 TO 06666

.EJECT

U4350	707704		LEM	
U4351	744000		CLL	
U4352	204556		LAC RJSM76	/JMP TO RJMS76
U4353	056667		DAC 16667	
U4354	204557		LAC RSM76	/JMS 16666
U4355	047772		DAC 07772	
U4356	047710		DAC JSM76	
U4357	776667		LAW 16667	/AC = 776667
U4360	607772		JMP 07772	
U4361	147710	RJMS76	DZM JSM76	/CLEAR ERROR TABLE
U4362	216666		LAC 16666	
U4363	544560		SAD K73	
U4364	741000		SKP	
U4365	740040	F533	HALT	/ERROR. (16666 OR 06666) NOT /07773 OR 17773
U4366	207711		LAC JSM77	
U4367	740200		SZA	
U4370	740040	F534	HALT	/ERROR. JMS FROM 07771 TO 17777 /OR 17771 TO 07777
U4371	707704		LEM	
U4372	744000		CLL	
U4373	700004		CLOF	
U4374	700002		TOF	/PI OFF FOR THIS TEST
U4375	204561		LAC RJSM77	/JMP TO RJMS77
U4376	040000		DAC 00000	
U4377	204562		LAC RSM77	/JMS 17777
U4400	047771		DAC 07771	
U4401	047711		DAC JSM77	
U4402	760000		LAW 0	/AC = 760000
U4403	607771		JMP 07771	
U4404	147711	RJMS77	DZM JSM77	/CLEAR ERROR TABLE
U4405	217777		LAC 17777	
U4406	544563		SAD K72	
U4407	741000		SKP	
U4410	740040	F535	HALT	/ERROR (17777 OR 07777) NOT /07772 OR 17772
U4411	750004		LAS	
U4412	740010		PAL	
U4413	740100		SMA	
U4414	700042		TON	/PI BACK ON
U4415	507557		AND K2AK	
U4416	741200		SNA	/CHECK ACS 5
U4417	106604		JMS SETCLK	/CLOCK BACK ON
U4420	207712		LAC JS252	
U4421	740200		SZA	
U4422	740040	F536	HALT	/ERROR. JMS FROM 12525 TO 15252 /OR FROM 02525 TO 05252
			EJECT	

```

***+
04423 744002 CLLICML
04424 204564 LAC RJSM25 /LINK = 1 /JMP TO RJMS14
04425 055253 DAC 15253
04426 204565 LAC RSM25
04427 052525 DAC 12525 /JMS 15252
04430 047712 DAC JS252
04431 775253 LAW 15253 /AC = 775253
04432 612525 JMP 12525
04433 147712 RJMS14 DZM JS252
04434 215252 LAC 15252 /CLEAR ERROR TABLE
04435 544566 SAD K426 /412526 OR 402526
04436 741000 SKP
04437 740040 F537 HALT /ERROR, (15252 OR 05252) NOT
                           /412526 OR 402526

04440 207713 LAC JS255
04441 740200 SZA
04442 740040 F538 HALT /ERROR, JMS FROM 15252 TO 12525
                           /OR 05252 TO 02525
                           /LINK = 1 /RJMP TO RJMS15

04443 744002 CLLICML
04444 204567 LAC RJSM25
04445 052526 DAC 12526 /JMS 12525
04446 204570 LAC RSM25
04447 055252 DAC 15252
04450 047713 DAC JS255
04451 772526 LAW 12526 /AC = 772526
04452 615252 JMP 15252
04453 147713 RJMS15 DZM JS255
04454 212525 LAC 12525 /CLEAR ERROR TABLE
04455 544572 SAD K415 /415253 OR 405253
04456 741000 SKP
04457 740040 F539 HALT /ERROR, (12525 OR 02525) NOT
                           /415253 OR 405253

.EOT

```

/PDP-9 BASIC EXERCISER - TAPF 5

/TFST JMS SERIFS

/

U4460	207714	LAC JSSS	
U4461	740200	SZA	
U4462	740040	F540 HALT	/ERROR, JMS SERIES FAILED
		/	
U4463	744002	CLL!CML	/LINK = 1
U4464	104465	JS1 JMS .+1	
U4465	740040	E541 HALT	
U4466	104467	JS2 JMS .+1	/ERROR, JMS SERIES
U4467	740040	F542 HALT	
U4470	104471	JS3 JMS .+1	/ERROR, JMS SERIES
U4471	740040	F543 HALT	/ERROR, JMS SERIES
U4472	707704	IEM	
U4473	744000	CLL	
U4474	104475	JS4 JMS .+1	
U4475	740040	F544 HALT	
U4476	147714	RJMSS D2M JSSS	/CLEAR ERROR TABLE
U4477	204573	LAC KJS1	/TFST JS1, LINK = 1
U4500	347554	TAD K400K	
U4501	740001	CMA	
U4502	344465	TAD JS1+1	
U4503	740001	CMA	
U4504	740200	SZA	
U4505	740040	F545 HALT	/ERROR, JS1+1
U4506	204574	LAC KJS2	/TEST JS2, LINN = 1
U4507	347554	TAD K400K	
U4510	740001	CMA	
U4511	344467	TAD JS2+1	
U4512	740001	CMA	
U4513	740200	SZA	
U4514	740040	F546 HALT	/ERROR, JS2+1
U4515	204575	LAC KJS3	/TEST JS3, LINK = 1
U4516	347554	TAD K400K	
U4517	740001	CMA	
U4520	344471	TAD JS3+1	
U4521	740001	CMA	
U4522	740200	SZA	
U4523	740040	E547 HALT	/ERROR, JS3+1
U4524	204576	LAC KJS4	/TEST JS4, EXT = 0, LINK = 0
U4525	740001	CMA	
U4526	344475	TAD JS4+1	
U4527	740001	CMA	
U4530	740200	SZA	
U4531	740040	F548 HALT	/ERROR, JS4+1
		/	
U4532	447634	TS7 WORK3	/CHECK DONE LOOPING
U4533	604221	JMP TSJMS	/LOOP
U4534	106102	JMS GENRAN	
U4535	106126	JMS CKNO	
U4536	604577	JMP TSXCT	
		.EJECT	/TEST XCT

/CONSTANTS FOR JMS. MODIFIED WHEN IN HI 4K

/

U4537	6U4234	RJSM71	JMP RJMS71
U4540	111111	RSM71	JMS 11111
U4541	U100000	K100000	100000
U4542	604255	RJSM72	JMP RJMS72
U4543	112222	RSM72	JMS 12222
U4544	U07777	K77	U7777
U4545	604276	RJSM73	JMP RJMS73
U4546	113333	RSM73	JMS 13333
U4547	U07776	K76	U7776
U4550	6U4317	RJSM74	JMP RJMS74
U4551	114444	RSM74	JMS 14444
U4552	U07775	K75	U7775
U4553	6U4340	RJSM75	JMP RJMS75
U4554	115555	RSM75	JMS 15555
U4555	U07774	K74	U7774
U4556	6U4361	RJSM76	JMP RJMS76
U4557	116666	RSM76	JMS 16666
U4560	U07773	K73	U7773
U4561	6U4404	RJSM77	JMP RJMS77
U4562	117777	RSM77	JMS 17777
U4563	U07772	K72	U7772
U4564	6U4433	RJSM25	JMP RJMS14
U4565	115252	RSM25	JMS 15252
U4566	412526	K426	412526
U4567	604463	RJSM52	JMP RJMS15
U4570	112525	RSM52	JMS 12525
U4571	U07771	K71	U7771
U4572	415253	K415	415253
U4573	U04465	KJS1	JS1+1
U4574	U04467	KJS2	JS2+1
U4575	U04471	KJS3	JS3+1
U4576	U04475	KJS4	JS4+1

/

.EJECT

```

*** /Tfst XCT
/
U4577 754003      TSXCT    CLA!CMA!CLL!CML   /AC = ONES, LINK = 0
U4600 404601      XCT .+1      NOP
U4601 740000      NOP
U4602 740040      SNL
U4603 740040      HALT     /ERROR; XCT NOP; LINK WAS RESET
U4604 740001      CMA
U4605 740200      SZA
U4606 740040      F550     HALT     /ERROR; XCT NOP; AC NOT ONES
/
/Tfst EXECUTE NOP, AC = 0, LINK ≠ 0
U4607 754000      CLA!CLL   /AC = 0, LINK = 0
U4610 404611      XCT .+1
U4611 740000      NOP
U4612 741400      S2L
U4613 740040      HALT     /ERROR; XCT NOP; LINK WAS SET
U4614 740200      S2A
U4615 740040      F552     HALT     /ERROR; XCT NOP; AC NOT 0
/
/Tfst XCT SKP
U4616 407617      XCT KSKP
U4617 740040      HALT     /ERROR; XCT SKP FAILED
U4620 750001      CLA!CMA
U4621 407620      XCT KCLA
U4622 740200      S2A
U4623 740040      F554     HALT     /ERROR; XCT CLA FAILED
/
/Tfst XCT LAW
U4624 750000      CLA
U4625 407573      XCT K7S   /LAW = 17777
U4626 740001      CMA
U4627 740200      S2A
U4630 740040      F555     HALT     /ERROR; XCT LAW FAILED
/Tfst XCT ISZ
U4631 750001      CLA!CMA
U4632 057777      DAC 17777 /ISZ 17777
U4633 405052      XCT XCTISZ
U4634 740040      F556     HALT     /ERROR; XCT ISZ FAILED TO SKP
/
/Tfst XCT TAD
U4635 744002      CLL!CML   /LINK = 1
U4636 777777      LAW 17777 /AC = ONES
U4637 057777      DAC 17777 /17777=777777
U4640 405054      XCT XCTTAD /TAD K1
U4641 740200      S2A
U4642 740040      F557     HALT     /ERROR; XCT TAD FAILED, AC NOT 0
U4643 741400      S2L
U4644 740040      F558     HALT     /ERROR; XCT TAD FAILED LINK
.EJECT

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		/TFST XCT RAL, AC = ONES, LINK = 1
U4645	754003	CLA!CMA!CLL!CML /AC = ONES, LINK = 1
U4646	407641	XCT XCTR AL /RAL
U4647	740001	CMA /AC = 0
U4650	740200	SZA
U4651	740040	F559 HALT /ERROR; XCT RAL FAILED AC DROPPED A BIT
U4652	744400	SNL!CLL
U4653	740040	F560 HALT /ERROR; XCTR AL FAILED LINK DROPPED
		/
		/TFST XCT DAC
U4654	207567	LAC K3S /AC = 333333
U4655	405053	XCT XCTDAC /DAC 17777
U4656	347570	TAD K4S /AC = 777777
U4657	740001	CMA /AC = 0
U4660	740200	SZA
U4661	740040	F561 HALT /ERROR; XCT DAC FAILED, K3S
		/NOT STORED AT 17777
		.EJECT

*** /TEST XCT JMS /

U4662	207715	LAC XCT11	
U4663	740200	S2A	
U4664	740040	F562 HALT	/ERROR, XCT (16666 OR 06666) /FROM 11111 OR 01111 /XCT (16666 OR 06666)
U4665	205030	LAC XT11S	
U4666	047715	DAC XCT11	
U4667	051111	DAC 11111	/OR 01111
U4670	205031	LAC XTR11	/JMS 11111 OR 01111
U4671	056666	DAC 16666	/OR 06666
U4672	205032	LAC XT1R	/RJMP TO RXCT1
U4673	051112	DAC 11112	/OR 01112
U4674	611111	JMP 11111	/OR 01111 AND XCT (16666 OR 06666)
U4675	147715	D2M XCT11	/CLEAR ERROR TABLE
U4676	211111	LAC 11111	/OR 01111
U4677	545033	SAD K12	
U4700	741000	SKP	
U4701	740040	F563 HALT	/ERROR, RJMP ADR. NOT 1112 /OR 01112
U4702	207716	LAC XCT12	
U4703	740200	S2A	
U4704	740040	F564 HALT	/ERROR, XCT (15555 OR 05555) /FROM 12222 OR 02222 /XCT (15555 OR 05555)
U4705	205034	LAC XT12S	
U4706	047716	DAC XCT12	
U4707	052222	DAC 12222	
U4710	205035	LAC XTR12	/JMS 12222 OR 02222
U4711	055555	DAC 15555	
U4712	205036	LAC XT2R	/RJMP TO RXCT2
U4713	052223	DAC 12223	
U4714	612222	JMP 12222	
U4715	147716	D2M XCT12	/CLEAR ERROR TABLE
U4716	212222	LAC 12222	
U4717	545037	SAD K23	
U4720	741000	SKP	
U4721	740040	F565 HALT	/ERROR, RJMP NOT 12223 /OR 02223
U4722	207717	LAC XCT13	
U4723	740200	S2A	
U4724	740040	F566 HALT	/ERROR, XCT (14444 OR 04444) /FROM 13333 OR 03333

.EJECT

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***  

  04725 205040      LAC XT13S          /XCT (14444 OR 04444)  

  04726 047717      DAC XCT13            

  04727 053333      DAC 13333            

  04730 205041      LAC XTR13          /JMS 13333 OR 03333  

  04731 054444      DAC 14444            

  04732 205042      LAC XT3R           /RJMP TO RXCT3  

  04733 053334      DAC 13334            

  04734 613333      JMP 13333            

  04735 147717      RXCT3             /CLEAR ERROR TABLE  

  04736 213333      LAC 13333            

  04737 545043      SAD K34             

  04740 741000      SKP                  

  04741 740040      F567               /ERROR, RJMP NOT 13334 OR 03334  

  04742 207720      LAC XCT17            

  04743 740200      SZA                  

  04744 740040      F568               HALT  

                                         LAC XT17S          /ERROR, XCT (17776 OR 07776)  

  04745 205044      DAC XCT17          /FROM 07776 OR 17776  

  04746 047720      DAC 07776          /XCT (17776 OR 07776)  

  04747 047776      DAC 07776          /OR 17776  

  04750 205045      LAC XTR17          /JMS 07776 OR 17776  

  04751 057776      DAC 17776          /OR 07776  

  04752 205046      LAC XT4R           /RJMP TO RXCT4  

  04753 047777      DAC 07777          /OR 17777  

  04754 607776      JMP 07776          /OR 17776  

  04755 147720      RXCT4             /CLEAR ERROR TABLE  

  04756 207776      LAC 07776          /FOR 17776  

  04757 544544      SAD K77             

  04760 741000      SKP                  

  04761 740040      E569               /ERROR, RJMP NOT 07777 OR 17777  

  04762 207721      LAC XCT125           

  04763 740200      SZA                  

  04764 740040      F570               HALT  

                                         LAC XCT125         /ERROR, XCT (12525 OR 02525)  

  04765 205047      DAC XCT125         /FROM 15252 OR 05252  

  04766 047721      DAC 15252          /XCT (12525 OR 02525)  

  04767 055252      DAC 15252          /OR 05252  

  04770 205050      LAC XTR12          /JMS 15252 OR 05252  

  04771 052525      DAC 12525          /RJMP TO RXCT5  

  04772 205051      LAC XT5R             

  04773 055253      DAC 15253            

  04774 615252      JMP 15252          /CLEAR ERROR TABLE  

  04775 147721      RXCT5               

  04776 215252      LAC 15252            

  04777 545435      SAD K15253           

  0E000 741000      SKP                  

  0E001 740040      F571               HALT  

                                         .EJECT             /ERROR, RJMP NOT 15253 OR 05253

```

/TEST XCT SERIES

UF002	754003		CLA!CMA!CLL!CML /AC = ONES, LINK = 1
UF003	405004		XCT .+1; XCT .+1; XCT .+1
UF004	405005		
UF005	405006		
UF006	405007		XCT .+1; XCT .+1; XCT .+1
UF007	405010		
UF010	405011		
UF011	405012		XCT .+1; XCT .+1; XCT .+1
UF012	405013		
UF013	405014		
UF014	405015		XCT .+1
UF015	740000		NOP
UF016	740001		CMA
UF017	740200		SZA
UF020	740040	F572	HALT /ERROR, XCT SERIES FAILED AC NOT ONES
UF021	740400		SNL
UF022	740040	F573	HALT /ERROR, LINK CHANGED
UF023	447634		ISZ WORK3 /CHECK DONE LOOPING
UF024	604577		JMP TSXCT /LOOP
UF025	106102		JMS GENRAN /GET NO FOR NEXT LOOP
UF026	106126		JMS CKNO
UF027	605055		JMP TSAUTO /TEST AUTO-INDEXING

/XCT CONSTANTS, MODIFIED WHEN IN UPPER 4K

UF030	416666	XT11S	XCT 16666
UF031	111111	XTR11	JMS 11111
UF032	604675	XT1R	JMP RXCT1
UF033	011112	K12	11112
UF034	415555	XT12S	XCT 15555
UF035	112222	XTR12	JMS 12222
UF036	604715	XT2R	JMP RXCT2
UF037	012223	K23	12223
UF040	414444	XT13S	XCT 14444
UF041	113333	XTR13	JMS 13333
UF042	604735	XT3R	JMP RXCT3
UF043	013334	K34	13334
UF044	417776	XT17S	XCT 17776
UF045	107776	XTR17	JMS 07776
UF046	604755	XT4R	JMP RXCT4
UF047	412525	XCT12S	XCT 12525
UF050	115252	XCTR12	JMS 15252
UF051	604775	XT5R	JMP RXCT5
UF052	457777	XCT15Z	ISZ 17777
UF053	057777	XCTDAC	DAC 17777
UF054	347532	XCTTAD	TAD K1
			.EJECT

```

*** /AUTO-INDEX
/
UF055 200020   TSAUTO LAC 20
UF056 047637   DAC AUTNOT
UF057 220020   LAC* 20
UF060 207637   LAC AUTNOT
UF061 540020   SAD 20
UF062 741000   SKP
UF063 740040   F574 HALT
UF064 040020   DAC 20
/
UF065 200030   LAC 30
UF066 047637   DAC AUTNOT
UF067 220030   LAC* 30
UF070 207637   LAC AUTNOT
UF071 540030   SAD 30
UF072 741000   SKP
UF073 740040   F575 HALT
UF074 040030   DAC 30
/
/TFST AUTO-INDEX 50
UF075 200050   LAC 50
UF076 047637   DAC AUTNOT
UF077 220050   LAC* 50
UF100 207637   LAC AUTNOT
UF101 540050   SAD 50
UF102 741000   SKP
UF103 740040   F576 HALT
UF104 040050   DAC 50
/
/TFST AUTO-INDEX 110
UF105 200110   LAC 110
UF106 047637   DAC AUTNOT
UF107 220110   LAC* 110
UF110 207637   LAC AUTNOT
UF111 540110   SAD 110
UF112 741000   SKP
UF113 740040   F577 HALT
UF114 040110   DAC 110
/
/TFST AUTO-INDEX 210
UF115 200210   LAC 210
UF116 047637   DAC AUTNOT
UF117 220210   LAC* 210
UF120 207637   LAC AUTNOT
UF121 540210   SAD 210
UF122 741000   SKP
UF123 740040   F578 HALT
UF124 040210   DAC 210
.EJECT

```

UR125	200410		/TFST AUTO-INDEX 410	
UR126	047637		LAC 410	/STORE (410) AT AUTNOT
UR127	220410		DAC AUTNOT	/FALSE AUTOINDEX 410
UR130	207637		LAC* 410	
UR131	540410		LAC AUTNOT	/COMPARE (AUTNOT) WITH (410)
UR132	741000		SAN 410	
UR133	740040		SKP	
UR134	040410	F579	HALT	/ERROR; (410) AUTO-INDEXED
			DAC 410	/RESTORE 410
			/	
UR135	201010		/TFST AUTO-INDEX 1010	
UR136	047637		LAC 1010	/STORE (1010) AT AUTNOT
UR137	221010		DAC AUTNOT	/FALSE AUTO-INDEX 1010
UR140	207637		LAC* 1010	
UR141	541010		LAC AUTNOT	/COMPARE (AUTNOT) WITH (1010)
UR142	741000		SAN 1010	
UR143	740040	F580	SKP	
UR144	041010		HALT	/ERROR; (1010) AUTO-INDEXED
			DAC 1010	/RESTORE 1010
			/	
UR145	202010		/TFST AUTO-INDEX 2010	
UR146	047637		LAC 2010	/STORE (2010) AT AUTNOT
UR147	222010		DAC AUTNOT	/FALSE AUTO INDEX 2010
UR150	207637		LAC* 2010	
UR151	542010		LAC AUTNOT	/COMPARE (AUTNOT) WITH (2010)
UR152	741000		SAN 2010	
UR153	740040	F581	SKP	
UR154	042010		HALT	/ERROR; (2010) AUTO-INDEXED
			DAC 2010	/RFSTORE 2010
			/	
UR155	204010		/TFST AUTO-INDEX 4010	
UR156	047637		LAC 4010	/STORE (4010) AT AUTNOT
UR157	224010		DAC AUTNOT	/FALSE AUTO INDEX 4010
UR160	207637		LAC* 4010	
UR161	544010		LAC AUTNOT	/COMPARE (AUTNOT) WITH (4010)
UR162	741000		SAN 4010	
UR163	740040	F582	SKP	
UR164	044010		HALT	/ERROR; (4010) AUTO-INDEXED
			DAC 4010	/RFSTORE 4010
			/	
UR165	447634		ISZ WORK3	/CHECK DONE LOOPING
UR166	605055		JMP TSAUTO	/LOOP
UR167	106102		JMS GENRAN	
UR170	106126		JMS CKNO	
			.EJECT	/GET NO. FOR NEXT LOOP

```

*** /TFST LAC INDIRECT
/
UF171 204544 LACIN LAC K77
UF172 057777 DAC 17777 /((17777 OR 07777) = 07777 OR 17777
UF173 207627 LAC M400K /377777
UF174 047777 DAC 07777 /OR 17777
UF175 237777 LAC* 17777 /OR 07777
UF176 347554 TAD K400K /AC = 777777
UF177 740001 CMA
UF200 740200 SZA
UF201 740040 F584 HALT /ERROR, LAC* 17777 OR 07777 FAILED
/
UF202 204547 LAC K76
UF203 056666 DAC 16666 /((16666 OR 06666) = 07776 OR 17776
UF204 207626 LAC M40K /737777
UF205 047776 DAC 07776
UF206 236666 LAC* 16666 /AC = 737777
UF207 347553 TAD K40K /AC = 777777
UF210 740001 CMA
UF211 740200 SZA
UF212 740040 F585 HALT /ERROR, LAC* 16666 OR 06666 FAILED
/
UF213 204552 LAC K75
UF214 055555 DAC 15555 /((15555 OR 05555) = 07775 OR 17775
UF215 207625 LAC M4K /AC = 773777
UF216 047775 DAC 07775
UF217 235555 LAC* 15555 /AC = 773777
UF220 347550 TAD K4K /AC = 777777
UF221 740001 CMA
UF222 740200 SZA
UF223 740040 F586 HALT /ERROR, LAC* 15555 OR 05555 FAILED
/
UF224 204555 LAC K74
UF225 054444 DAC 14444 /((14444 OR 04444) = 07774 OR 17774
UF226 207624 LAC M400 /AC = 777377
UF227 047774 DAC 07774
UF230 234444 LAC* 14444 /AC = 777377
UF231 347545 TAD K400 /AC = 777777
UF232 740001 CMA
UF233 740200 SZA
UF234 740040 F587 HALT /ERROR, LAC* 14444 OR 04444 FAILED
/
UF235 204560 LAC K73
UF236 053333 DAC 13333 /((13333 OR 03333) = 07773 OR 17773
UF237 207623 LAC M40 /AC = 777737
UF240 047773 DAC 07773
UF241 233333 LAC* 13333 /AC = 777737
UF242 347543 TAD K40 /AC = 777777
UF243 740001 CMA
UF244 740200 SZA
UF245 740040 F588 HALT /ERROR, LAC* 13333 OR 03333 FAILED
.EJECT

```

```

*****  

UF 246 204563 LAC K72  

UF 247 052222 DAC 12222 / (12222 OR 02222) = 07772 OR 17772  

UF 250 207622 LAC M4 /AC = 777773  

UF 251 047772 DAC 07772  

UF 252 232222 LAC* 12222 /AC = 777773  

UF 253 347534 TAD K4 /AC = 777777  

UF 254 740001 CMA  

UF 255 740200 SZA  

UF 256 7400040 HALT /ERROR, LAC* 12222 OR 02222 FAILED  

/  

UF 257 204571 LAC K71  

UF 260 051111 DAC 11111 / (11111 OR 01111) = 07771 OR 17771  

UF 261 207621 LAC M1 /AC = 777776  

UF 262 047771 DAC 07771  

UF 263 231111 LAC* 11111 /AC = 777776  

UF 264 347532 TAD K1 /AC = 777777  

UF 265 740001 CMA  

UF 266 740200 SZA  

UF 267 7400040 HALT /ERROR, LAC* 11111 OR 01111 FAILED  

/  

UF 270 205427 LAC 1NK52  

UF 271 055252 DAC 15252 / (15252 OR 05252) = 02525 OR 12525  

UF 272 207606 LAC K111 /AC = 525252  

UF 273 052525 DAC 12525  

UF 274 235252 LAC* 15252 /AC = 525252  

UF 275 347605 TAD KU10 /AC = 777777  

UF 276 740001 CMA  

UF 277 740200 SZA  

UF 300 7400040 HALT /ERROR, LAC* 15252 OR 05252 FAILED  

/  

UF 301 447634 IS7 WORK3  

UF 302 605171 JMP LACIN /CHECK DONE LOOPING  

UF 303 106102 JMS GENRAN /LOOP  

UF 304 106126 JMS CKNO /GET NO. FOR NEXT LOOP  

.EJECT

```

/TFST XCT JMS INDIRECT

/

05305	744000	XIJMSI	CLL	
05306	750000		CLA	
05307	547722		SAD JST77	
05310	741000		SKP	
05311	740040	F592	HALT	/ERROR, JMS DEST'B ERROR
		/		
05312	206060		LAC K17776	/DIRECT ADDRESS
05313	047777		DAC 07777	
05314	205430		LAC JMSI1	/JMS# 07777 OR 17777
05315	047722		DAC JST77	/ERROR TABLE
05316	055252		DAC 15252	
05317	205431		LAC RJMI1	/JMP RJSI1
05320	057777		DAC 17777	
05321	415252		XCT 15252	/XCT TFST
05322	741000		SKP	
05323	147722	RJSI1	D2M JST77	
05324	217776		LAC 17776	
05325	545432		SAD RJSI1X	/RJSI1X = RJSI1-1
05326	751000		CLA:SKP	
05327	740040	F593	HALT	/ERROR (17776 OR 07776) NOT = /TO RJSI1-1
		/		
		/		
		/TEST JMS INDIRECT		
		/		

05330	744000		CLL	
05331	750000		CLA	
05332	547723		SAD JST66	
05333	741000		SKP	
05334	740040	F594	HALT	/ERROR, JMS DEST'N ERROR
		/		
05335	206075		LAC K11111	/DIRECT ADR. 11111 OR 01111
05336	056666		DAC 16666	/OR 06666
05337	205433		LAC JST66	/JMS# 16666 OR 06666
05340	047723		DAC JST66	
05341	055252		DAC 15252	/JMS# 16666 OR 06666 AT 15252 /OR 05252
05342	205434		LAC RJMI2	/JMP RJSI2
05343	051112		DAC 11112 /OR 01112	
05344	615252		JMP 15252	/OR 05252
05345	741000		SKP	
05346	147723	RJSI2	D2M JST66	/CLEAR ERROR TABLE
05347	211111		LAC 11111	
05350	545435		SAD K15253	
05351	751000		CLA:SKP	
05352	740040	E595	HALT	/ERROR, RJMP ADR. (11111 OR 01111) /NOT 15253 OR 05253
			.EJECT	

UF353	744000		CLL	
UF354	750000		CLA	
UF355	547724		SAD JST55	
UF356	741000		SKP	
UF357	740040	F596	HALT	/ERROR, JMS DEST'N ERROR
UF360	206073		LAC K12222	/DIRECT ADR. 12222 OR 02222
UF361	055555		DAC 15555 /OR 055555	
UF362	205436		LAC JST55	/JMS# 15555 OR 05555
UF363	047724		DAC JST55	
UF364	055252		DAC 15252	/JMS# 15555 OR 05555 AT /15252 OR 05252
UF365	205437		LAC RJM13	/JMP RJSI3
UF366	052223		DAC 12223	/OR 02223
UF367	615252		JMP 15252	/OR 05252
UF370	741000		SKP	
UF371	147724	RJSI3	DZM JST55	/CLEAR ERROR TABLE
UF372	212222		LAC 12222	/OR 02222
UF373	545435		SAD K15253	
UF374	751000		CLA!SKP	
UF375	740040	F597	HALT	/ERROR, RJMP ADR. (12222 OR 02222) /NOT 15253 OR 05253
UF376	744000		CLL	
UF377	750000		CLA	
UF400	547725		SAD JST44	
UF401	741000		SKP	
UF402	740040	F598	HALT	/ERROR, JMS DEST'N ERROR
UF403	206071	/	LAC K13333	/DIRECT ADR. 13333 OR 03333
UF404	054444		DAC 14444	/OR 04444
UF405	205440		LAC JST44	/JMS# 14444 OR 04444
UF406	047725		DAC JST44	
UF407	055252		DAC 15252	/OR 05252
UF410	205441		LAC RJM14	
UF411	053334		DAC 13334	/OR 03334
UF412	615252		JMP 15252	/OR 05252
UF413	741000		SKP	
UF414	147725	RJSI4	DZM JST44	/CLEAR ERROR TABLE
UF415	213333		LAC 13333	/OR 03333
UF416	545435		SAD K15253	
UF417	751000		CLA!SKP	
UF420	740040	F599	HALT	/FRROR, RJMP ADR (13333 OR 03333) /NOT 15253 OR 05253
UF421	447634		IS7 WORK3	/CHECK DONE LOOPING
UF422	605305		JMP XTJMSI	/LOOP
UF423	106102		JMS GENRAN	/GET NO. FOR NEXT LOOP
UF424	106126		JMS CKNO	
UF425	700004		CLOF	
UF426	605442		JMP XTJCT	/TEST XCT INDIRECTS
			EJECT	

††††

/CONSTANTS FOR LAC*, XCT JMS* MODIFIED
/WHEN IN UPPFR 4K

/
UF427 012525 INK52 12525
UF430 127777 JMSI1 JMS* 07777
UF431 605323 RJMI1 JMP RJSI1
UF432 005322 RJSI1X RJSI1-1
UF433 136666 JSI66 JMS* 16666
UF434 605346 RJMI2 JMP RJSI2
UF435 015253 K15253 15253
UF436 135555 JSI55 JMS* 15555
UF437 605371 RJMI3 JMP RJSI3
UF440 134444 JSI44 JMS* 14444
UF441 605414 RJMI4 JMP RJSI4
.EOT

/PDP-9 BASIC EXERCISER - TAPE 6
 /TFST XCT INDIRECT

/

UF 442	700002	XTXCT	TOF	/PI OFF FOR XCT* TO USE /LOCATION 0
UF 443	207531		LAC K0	
UF 444	057777		DAC 17777	/OR 07777
UF 445	205516		LAC XCTDZM	
UF 446	040000		DAC 0	/DZM 12525 OR 02525
UF 447	777777		LAW 17777	
UF 450	052525		DAC 12525	/((12525 OR 02525) = 777777
UF 451	437777		XCT* 17777	/OR 07777
UF 452	212525		LAC 12525	/OR 02525
UF 453	740200		SZA	
UF 454	740040	F600	HALT	/ERROR, XCT* A DZM FAILED /((12525 OR 02525) NOT 777777
		/		
		/TFST ISZ INDIRECT. PI IS OFF		
		/		
UF 455	207531		LAC K0	
UF 456	057777		DAC 17777	/OR 07777
UF 457	777777		LAW 17777	
UF 460	040000		DAC 0	/((0) = 777777
UF 461	477777		ISZ* 17777	
UF 462	740040	F601	HALT .EJECT	/ERROR ISZ* FAILED TO SKIP/

††††

/TFST XCT* AND* PI IS OFF

/

UF463	207531	LAC K0	
UF464	057777	DAC 17777	/OR 07777, INDIRECT ADR FOR XCT*
UF465	207532	LAC K1	/DIRECT ADR, FOR AND*
UF466	057776	DAC 17776	/OR 07776
UF467	205517	LAC ANDI	
UF470	040000	DAC 0	/AND* 17776 OR 07776
UF471	207603	LAC K2525	
UF472	040001	DAC 1	/(1) = 2525
UF473	750001	CLA:CMIA	/AC = ONES
UF474	437777	XCT* 17777	/OR 07777
UF475	547603	SAD K2525	
UF476	741000	SKP	
UF477	740040	F602 HALT	/ERROR, XCT* 17777 OR 07777 /FOLLOWED BY AND* 17776 /OR 07776 FAILED /CHECK DONE LOOPING
UF500	447634	ISZ WORK3	
UF501	605442	JMP XTXCT	/LOOP
UF502	106102	JMS GENRAN	/GET NO. FOR NEXT LOOP
UF503	106126	JMS CKNO	
UF504	207617	LAC KSKP	
UF505	040001	DAC 1	/RESTORE LOC 1
UF506	750004	LAS	
UF507	740010	RAL	
UF510	740100	SMA	
UF511	700042	ION	/PI RACK ON
UF512	507557	AND K20K	
UF513	741200	SNA	/TEST ACSS
UF514	106604	JMS SETCLK	/CLOCK BACK ON
UF515	605520	JMP AUTOIN	/TEST INDEX REGISTERS
		.EJECT	

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***** /CONSTANTS FOR PRECEDING LOOPS, MODIFIED WHEN IN UPPER 4K
/
UF516    152525   XCTDZM    DZM 12525
UF517    537776   ANDI      AND# 17776
/
/
/TEST AUTO-INDEX (XOR# 10)
/
UF520    206060   AUTOIN    LAC K17776
UF521    040010   DAC 10      /(10) = 17776 OR 07776
UF522    207565   LAC K1S     /AC = 11111
UF523    057777   DAC 17777  /OR 07777
UF524    260010   XOR# 10
UF525    740200   SZA
UF526    740040   F603      HALT      /ERROR, XOR# 10 FAILED
                                         /AC NOT 11111
UF527    200010   LAC 10
UF530    546061   SAD K17777
UF531    741000   SKP
UF532    740040   F604      HALT      /ERROR, (10) NOT INCREMENTED+1
/
UF533    206062   LAC K16665
UF534    040010   DAC 10      /(10) = 16665 OR 06665
UF535    207566   LAC K2S     /AC = 22222
UF536    056666   DAC 16666  /OR 06666
UF537    260010   XOR# 10
UF540    740200   SZA
UF541    740040   F605      HALT      /ERROR, XOR# 10 FAILED
                                         /AC NOT 22222
UF542    200010   LAC 10
UF543    546063   SAD K16666
UF544    741000   SKP
UF545    740040   F606      HALT      /ERROR, (10) NOT INCREMENTED+1
/
UF546    206064   LAC K15554
UF547    040010   DAC 10      /(10) = 15554 OR 05554
UF550    207567   LAC K3S     /AC = 333333
UF551    055555   DAC 15555
UF552    260010   XOR# 10
UF553    740200   SZA
UF554    740040   F607      HALT      /ERROR, XOR# 10 FAILED, AC NOT 333333
UF555    200010   LAC 10
UF556    546065   SAD K15555
UF557    741000   SKP
UF560    740040   F608      HALT      /ERROR, (10) NOT INCREMENTED +1
                                         .EJECT

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UF561    206066      LAC K14443
UF562    040010      DAC 10
UF563    207570      LAC K4S
UF564    054444      DAC 14444
UF565    260010      XOR# 10
UF566    740200      SZA
UF567    740040      HALT
UF570    200010      LAC 10
UF571    546067      SAD K14444
UF572    741000      SKP
UF573    740040      HALT
UF574    206070      LAC K13332
UF575    040010      DAC 10
UF576    207571      LAC K5S
UF577    053333      DAC 13333
UF600    260010      XOR# 10
UF601    740200      SZA
UF602    740040      HALT
UF603    200010      LAC 10
UF604    546071      SAD K13333
UF605    741000      SKP
UF606    740040      HALT
/
UF607    206072      LAC K12221
UF610    040010      DAC 10
UF611    207572      LAC K6S
UF612    052222      DAC 12222
UF613    260010      XOR# 10
UF614    740200      SZA
UF615    740040      HALT
UF616    200010      LAC 10
UF617    546073      SAD K12222
UF620    741000      SKP
UF621    740040      HALT
UF622    206074      LAC K11110
UF623    040010      DAC 10
UF624    207573      LAC K7S
UF625    051111      DAC 11111
UF626    260010      XOR# 10
UF627    740200      SZA
UF630    740040      HALT
UF631    200010      LAC 10
UF632    546075      SAD K11111
UF633    741000      SKP
UF634    740040      HALT
.EJECT

```

/ (10) = 14443 OR 04444
 / AC = 444444

/ERROR, XOR# 10 FAILED AC NOT 444444

/ (10) = 13332 OR 03332
 / AC = 55555

/ERROR, (10) NOT INCREMENTED +1

/ (10) = 12221 OR 02221
 / AC = 666666

/ERROR, XOR# 10 FAILED AC NOT 666666

/ (10) = 11110 OR 01110
 / AC = 777777

/ERROR, (10) NOT INCREMENTED +1

/ERROR, XOR# 10 FAILED AC NOT 777777

/ERROR, (10) NOT INCREMENTED +1

/TFST ISZ* 11

UF635	206076	LAC K15252	
UF636	040011	DAC 11	/ (11) = 15252 OR 05252
UF637	207573	LAC K7S	/OR 05253
UF640	055253	DAC 15253	
UF641	460011	ISZ* 11	
UF642	740040	F617 HALT	/ERROR, ISZ FAILED TO SKIP /AUTO-INDEX 11 FAILED
UF643	215253	LAC 15253	
UF644	740200	SZA	
UF645	740040	F618 HALT	/ERROR, (15253 OR 05253) NOT 0 /ISZ FAILED
UF646	200011	LAC 11	
UF647	545435	SAD K15253	
UF650	741000	SKP	
UF651	740040	F619 HALT	/ERROR, (11) NOT INCREMENTED+1
/			
/AUTON-INDEX JMP* 12.			
/			
UF652	207726	LAC AUTJMP	
UF653	740200	SZA	
UF654	740040	F620 HALT	/ERROR, JMP* 12 FAILED TO REACH 15253 /JMP* 12
UF655	207651	LAC JMPAUT	
UF656	047726	DAC AUTJMP	
UF657	206077	LAC AUTRET	
UF660	055253	DAC 15253	
UF661	206076	LAC K15252	
UF662	040012	DAC 12	/ (12) = 15252
UF663	620012	JMP* 12	
UF664	741000	SKP	
UF665	147726	AUTR 02M AUTJMP	/CLEAR ERROR TABLE
UF666	200012	LAC 12	
UF667	545435	SAD K15253	
UF670	741000	SKP	
UF671	740040	F621 HALT	/ERROR, (12) NOT INCREMENTED+1
/			
/AUTON-INDEX (DAC* 13).			
/			
UF672	204547	LAC K76	
UF673	040013	DAC 13	/ (13) = 07776 OR 17776
UF674	204547	LAC K76	
UF675	047777	DAC 07777	/ (07777) = 07776
UF676	207531	LAC K0	
UF677	060013	DAC* 13	
UF700	207777	LAC 07777	
UF701	740200	SZA	
UF702	740040	F622 HALT	/ERROR, (07777) NOT 0, DAC* 13 FAILED
UF703	200013	LAC 13	
UF704	544544	SAD K77	
UF705	741000	SKP	
UF706	740040	F623 HALT	/ERROR (13) NOT INCREMENTED+1
,EJECT			

/AUTO-INDEX (XCT# 14),			
/			
05707	204571	LAC K71	
05710	040014	DAC 14	/ (14) = 07771
05711	207642	LAC AUTCMA	
05712	047772	DAC 07772	/ (07772) = CMA
05713	750001	CLL!CMA	
05714	420014	XCT# 14	
05715	740200	SZA	
05716	740040	F624 HALT	/ ERROR. AC NOT 0 (XCT# 14) A CMA
05717	200014	LAC 14	
05720	544563	SAD K72	
05721	741000	SKP	
05722	740040	F625 HALT	/ ERROR. (14) NOT INCREMENTED+1
/			
/AUTO-INDEX (TAD# 15),			
/			
05723	206060	LAC K17776	
05724	040015	DAC 15	/ (15) = 17776 OR 07776
05725	207532	LAC K1	
05726	057777	DAC 17777	/OR 07777
05727	754001	CLL!CLA!CMA	
05730	360015	TAD# 15	
05731	740200	SZA	
05732	740040	F626 HALT	/ ERROR. AC NOT 0 (TAD# 15)
05733	740400	SNL	
05734	740040	F627 HALT	/ ERROR. LINK NOT 1 (TAD# 15)
05735	200015	LAC 15	
05736	546061	SAD K17777	
05737	741000	SKP	
05740	740040	F628 HALT	/ ERROR. (15) NOT INCREMENTED+1
05741	217777	LAC 17777	
05742	547532	SAD K1	
05743	741000	SKP	
05744	740040	F629 HALT	/ ERROR. (17777 OR 07777) NOT 1
/			
/AUTO-INDEX (SAD# 16),			
/			
05745	204555	LAC K74	
05746	040016	DAC 16	/ (16) = 07774
05747	207604	LAC K5252	
05750	047775	DAC 07775	/ (07775) = 5252
05751	560016	SAD# 16	
05752	741000	SKP	
05753	740040	F630 HALT	/ ERROR. SAD SKIPPED (SAD# 16)
05754	207775	LAC 07775	
05755	547604	SAD K5252	
05756	741000	SKP	
05757	740040	F631 HALT	/ ERROR. (07775) NOT 5252
05760	200016	LAC 16	
05761	544552	SAD K75	
05762	741000	SKP	
05763	740040	F632 HALT	/ ERROR. (16) NOT INCREMENTED+1
		.EJECT	

```

**** /AUTO-INDEX (JMS# 17),
/
UF764 207727 LAC AUTJMS
UF765 744200 SZA:CLL
UF766 740040 F633 HALT /ERROR, ERROR TABLE NOT 0
UF767 206100 LAC AUTRJM
UF770 047773 DAC 07773 /JMP AUTRE1
UF771 204571 LAC K71
UF772 040017 DAC 17 / (17) = 07771
UF773 207647 LAC JMSAUT
UF774 047727 DAC AUTJMS /JMS# 17 IN ERROR TABLE
UF775 120017 JMS# 17
UF776 741000 SKP
UF777 147727 AUTRF1 DZM AUTJMS /CLEAR ERROR TABLE
UF000 207772 LAC 07772
UF001 546101 SAD AURJMP (015776)
UF002 741000 SKP
UF003 740040 F634 HALT /ERROR, (07772) STORED WRONG
UF004 200017 LAC 17 / (07772) SHOULD = AUTRE1-1
UF005 544563 SAD K72
UF006 741000 SKP
UF007 740040 F635 HALT /ERROR, (17) NOT INCREMENTED+1
/
/AUTO-INDEX (ISZ# 10) (10) = 10.
/
UF010 207535 LAC K1A
UF011 040010 DAC 10
UF012 207573 LAC K7S
UF013 040011 DAC 11 / (11) = 777777
UF014 460010 ISZ# 1A
UF015 740040 F636 HALT /ERROR, ISZ FAILED TO SKIP
UF016 200010 LAC 10
UF017 547536 SAD K11
UF018 741000 SKP
UF021 740040 F637 HALT /ERROR, (10) NOT 11
UF022 200011 LAC 11
UF023 740200 SZA
UF024 740040 F638 HALT /ERROR, (11) NOT 0
/
/AUTO-INDEX (ISZ# 11) (11) = 10.
/
UF025 207535 LAC K1A
UF026 040011 DAC 11 / (11) = 10
UF027 460011 ISZ# 11
UF030 200011 LAC 11
UF031 547537 SAD K12A
UF032 741000 SKP
UF033 740040 F639 HALT /ERROR, ISZ# 11 FAILED TO
.EJECT /INCREMENT +2 (11)

```

/AUTO-INDEX (XCT# 15),

/

06034	204560	LAC K73	
06035	040015	DAC 15	/ (15) = 07773
06036	207645 4	LAC LAWAUT	
06037	047774	DAC 07774	/ (07774) = XCT# 15
06040	207646 5	LAC LAWFUL	
06041	047775	DAC 07775	/ (07775) = LAW 17777
06042	750000	CLA	/AC = 0
06043	420015	XCT# 15	
06044	740001	CMA	
06045	740200	SZA	
06046	740040	HALT	
			/ERROR, AC NOT ONES
			/LAW 1777 DID NOT OCCUR
06047	200015	LAC 15	
06050	544552	SAD K75	
06051	741000	SKP	
06052	740040	HALT	
06053	447634 3	ISZ WORK3	/ERROR, (15) NOT 7775 (XCT# 15)
06054	605520	JMP AUTOIN	/CHECK DONE LOOPING
06055	777775	LAW -3	/LOOP
06056	047634 3	DAC WORK3	
06057	606154	JMP CHKBRD	
		.EJECT	/BASIC MEMORY CHECKERBOARD

(51)

/CONSTANTS FOR AUTO-INDEXING, MODIFIED WHEN IN HI 4K

WFO60	017776	K17776	17776
WFO61	017777	K17777	17777
WFO62	016665	K16665	16665
WFO63	016666	K16666	16666
WFO64	015554	K15554	15554
WFO65	015555	K15555	15555
WFO66	014443	K14443	14443
WFO67	014444	K14444	14444
WFO70	013332	K13332	13332
WFO71	013333	K13333	13333
WFO72	012221	K12221	12221
WFO73	012222	K12222	12222
WFO74	011110	K11110	11110
WFO75	011111	K11111	11111
WFO76	015252	K15252	15252
WFO77	605665	AUTRET	JMP AUTR
WF100	605777	AUTRJM	JMP AUTRE1
WF101	005776	AURJMP	AUTRE1-1
			.EJECT

/RANDOM NUMBER GENERATORS

/

0F102	000000	GENRAN	0	/CHECK FOR END OF TABLE
0F103	206123		LAC RANDFX	
0F104	546124		SAD ENOTRL	
0F105	741000		SKP	
0F106	606116		JMP RANTAD-1	
0F107	206125		LAC TBLTOP	
0F110	046123		DAC RANDEX	
0F111	207655		LAC RANCON	
0F112	744010		CLL!RAL	
0F113	741400		SZL	
0F114	347532		TAD K1	
0F115	047655		DAC RANCON	
0F116	226123	RANTAD	LAC* RANDEX	
0F117	347655		TAD RANCON	
0F120	066123		DAC* RANDEX	
0F121	446123		ISZ RANDFX	
0F122	626102		JMP* GENRAN	
0F123	007666	RANDEX	RANTRL+10	
0F124	007666	ENOTRL	RANTRL+10	
0F125	007656	TBLTOP	RANTRL	
0F126	000000	/		
0F127	507610	CKNO	0	
0F130	740001		AND K37S	
0F131	047634		CMA	
0F132	626126		DAC WQRK3	
0F133	000000	RANGEN	JMP* CKNO	
0F134	206123			
0F135	546124			
0F136	741000			
0F137	606147			
0F140	206125			
0F141	046123			
0F142	207655			
0F143	744010			
0F144	741400			
0F145	347532			
0F146	047655	TADRAN		
0F147	226123			
0F150	347655			
0F151	066123			
0F152	446123			
0F153	626133			

507601

(7654) - 000000

(7655) - (1665) = 0

/END
 /GENFRATE RANDOM
 /RFSET INDEX TO FIRST
 /POSITION MODIFIER
 /1 LEFT
 /WAS BIT 0 A 1
 /YES MAKE 17 A 1
 /RFSTORE MODIFIER
 /GET FIRST CONTROL
 /ADD MODIFIER
 /NEW CONTROL = RANDOM
 /STEP POINTER
 /EXIT

/MAKE 65K OR LESS
 /LOOP COUNTER
 /EXIT

/CHECK FOR TABLE END
 /END
 /GENFRATE RANDOM
 /RESET INDEX TO FIRST
 /POSITION MODIFIER
 /1 LFTT
 /WAS BIT 0 A 1
 /MAKE 17 A 1
 /RESTORE MODIFIER
 /GET FIRST CONTROL
 /ADD MODIFIER
 /NEW CONTROL = RANDOM
 /STEP POINTER
 /EXIT

UF154	700002	CHKBRD	I0F	
UF155	750004		LAS	
UF156	740010		RAL	
UF157	740100		SMA	/CHECK PI INHIBITED
UF160	700042		ION	/PI ON
UF161	777777		LAW -1	
UF162	047317		DAC BITSUP	
UF163	206165		LAC .+2	
UF164	046332		DAC NEXPAT	
UF165	207301		LAC KPAT	
UF166	047305		DAC MPAT	
UF167	147632		D2M WORK1	
UF170	707704		LEM	
 /LOAD CHECKERBOARD				
 /				
UF171	106312		JMS ADJUST	
UF172	207305	LOAD	LAC MPAT	
UF173	047307		DAC PATWD	
UF174	777776		LAW -2	
UF175	047314		DAC WC256	
UF176	777770	LCNTA	LAW -1*	
UF177	047277		DAC WC128	
UF200	777760	LCNTR	LAW -2*	/-16 DECIMAL
UF201	047276		DAC WC16	
UF202	207307		LAC PATWD	
UF203	047306		DAC PATR	
UF204	207306	WC100P	LAC PATR	
UF205	744010		RCL	
UF206	047306		DAC PATR	
UF207	751400		SZL:CLA	/TEST FOR A 1 OR 0
UF210	740001		CMA	
UF211	067300		DAC* LLREG	/STORE WORD
UF212	207300		LAC LLREG	
UF213	546261		SAD K17777	/DONE LOADING?
UF214	606234		JMP READ	/YES
UF215	447300		TS7 LLREG	/INCR. ADR
UF216	447276		TS7 WC16	/16 WORDS?
UF217	606204		JMP WC100P	/NO
UF220	447277		TS7 WC128	
UF221	606200		JMP LCNTR	
UF222	207305		LAC MPAT	
UF223	744020		RCR	
UF224	740400		SNL	
UF225	606230		JMP .+3	
UF226	447314		TS7 WC256	
UF227	606176		JMP LCNTR	
UF230	207307		LAC PATWD	
UF231	740001		CMA	
UF232	047307		DAC PATWD	
UF233	606174		JMP LOAD+2	
			.EJECT	

06234	207305	/READ CHECKERBOARD	
06235	047633	READ	LAC MPAT
06236	106312		DAC WORK2
06237	777776		JMS ADJUST
06240	047314	RCNTA	LAW -2
06241	777770		DAC WC256
06242	047277		LAW -10
06243	207633	RCNTR	DAC WC128
06244	047306		LAC WORK?
06245	777760		DAC PATR
06246	047276		LAW -20
06247	777772	RCLLOOP	DAC WC16
06250	047242		LAW -6
06251	207306		DAC CRLF
06252	744010		LAC PATR
06253	047306		RCL
06254	751400		DAC PATR
06255	740001		SZL:CLA
06256	047307		CMA
06257	227300		DAC PATWD
06260	740001		LAC* LLREG
06261	067300		CMA
06262	447242		DAC* LLREG
06263	606257		ISZ CRLF
06264	227300		JMP .-4
06265	547307	RDRTN	LAC* LLREG
06266	741000		SAD PATWD
06267	606340		SKP
06270	207300		JMP ERROR
06271	546061		LAC LLREG
06272	606325		SAD K17777
06273	447300		JMP NXTST
06274	447276		ISZ LLREG
06275	606247		ISZ WC16
06276	447277		JMP RCLLOOP
06277	606243		ISZ WC128
06300	207305		JMP RCNTA
06301	744020		LAC MPAT
06302	740400		RCR
06303	606306		SNL
06304	447314		JMP .+3
06305	606241		ISZ WC256
06306	207633		JMP RCNTA+?
06307	740001		LAC WORK?
06310	047633		CMA
06311	606237		DAC WORK2
			JMP RCNTA
			.EJECT

/RESTORE PATTERN GEN

/-26 DECIMAL

/SAVE FOR COMPARE

/COMPARE

/OK

/DONE READING?

/YES

/INCR. ADR

/16 WORDS?

/NO,

0F312	000000	ADJUST	0	
0F313	206313		LAC .	
0F314	507556		AND K10K	
0F315	740200		SZA	
0F316	606322		JMP ULADJ	
0F317	207556		LAC K10K	/SA = 010000 FOR PATTERN
0F320	047300		DAC LLREG	
0F321	626312		JMP# ADJUST	
0F322	207545	/ULADJ	LAC K400	/SA = 400 FOR PATTERN
0F323	047300		DAC LLREG	
0F324	626312		JMP# ADJUST	
0F325	207632	/NXTST	LAC WORK1	
0F326	247561		XOR K6M0K	
0F327	741200		SNA	
0F330	606363		JMP CKLOOP	
0F331	446332		TSZ .+1	
0F332	207301	NEXPAT	LAC KPAT	
0F333	047305		DAC MPAT	
0F334	207632		LAC WORK1	
0F335	347560		TAD K210K	
0F336	047632		DAC WORK1	
0F337	606171		JMP LOAD-1	
			.EJECT	

/BASIC MEMORY CHECKERBOARD ERROR ROUTINE.

06340	047634	FRRUR	DAC WORK3	
06341	507317		AND BITSUP	
06342	740200		SZA	
06343	740001		CMA	
06344	507317		AND BITSUP	
06345	741200		SNA	
06346	606270		JMP RDRTN	
06347	207300		LAC LLREG	/DISPLAY ADDRESS
06350	740040		HLT-	
06351	207307	F642	LAC PATWD	/GOOD DATA
06352	740040		HLT	
06353	207634	GDATA	LAC WORK3	/BAD DATA
06354	740040	HDATA	HLT	
06355	750004		LAS	/SUPPRESS HERE
06356	740001		CMA	
06357	047317		DAC BITSUP	
06360	207633	PATT	LAC WORK2	/PATTERN CONTROL WORD
06361	740040		HLT	
06362	606055		JMP E641+3	/START OVER
<hr/>				
06363	750004	CKLOOP	LAS	
06364	742010		RTL	
06365	740010		RAL	
06366	741100		SPA	
06367	606154		JMP CHKBRD	/LOOP ON MEMORY TEST
06370	447634		IS7 WORK3	/CHECK DONE LOOPING
06371	606154		JMP CHKBRD	/LOOP
06372	700002		TOF	
06373	750004		LAS	
06374	507557		AND K20K	/CHECK ACS 4 FOR /INHIBIT RELOCATION
06375	741200		SNA	
06376	606406		JMP FNTST	/RFLOCATE
06377	760207		LAW 207	
06400	107211		JMS TLSSF	/BELL FOR ONE PASS
06401	750004		LAS	
06402	740010		RAL	
06403	741100		SPA	
06404	107251		JMS PINOT	/CHECK FOR PI INHIBITED
06405	600070		JMP SEQUEN	/INHIBITED
			.EOT	/START OVER IN THIS 4K

/PDP-9 BASIC EXERCISER - TAPE 7

/ROUTINE FOR PROGRAM RELOCATION

06406	7000002	FNTST	TOF	/PI OFF DURING RELOCATION
06407	770023		LAW -7755	
06410	047313		DAC WDCNT	
06411	206411		LAC .	
06412	507556		AND K10K	
06413	740200		SZA	/SEE IF IN LO OR HI 4K
06414	606510		JMP MVRK	/HI 4K
06415	740001		CMA	
06416	047633		DAC WORK?	/SOURCE ADDRESS
06417	207320		LAC K7777	/DEST'N ADR. TO HI 4K
06420	047634		DAC WORK3	
06421	207633	MOVE	LAC WORK2	
06422	047315		DAC MOVES	
06423	207634		LAC WORK3	
06424	047316		DAC MOVED	
06425	447316		ISZ MOVED	
06426	167316		DZM* MOVFD	/CLEAR DEST'N TO 0'S
06427	447313		ISZ WDCNT	
06430	606425		JMP .-3	
06431	207634		LAC WORK3	
06432	047316		DAC MOVED	
06433	447315	RFROM	ISZ MOVES	/RESTORE DEST'N S.A.
06434	227315		LAC* MOVES	
06435	047634		DAC WORK3	/SOURCE ADR.
06436	507562		AND K700K	/SAVE INSTRUCTION
06437	247562		XOR K700K	
06440	740200		SZA	
06441	606515		JMP MRINS	/OPERATE INST. IF 0
06442	207634		LAC WORK3	/MEMORY REF
06443	447316	MVRTN	ISZ MOVED	
06444	067316		DAC* MOVED	
06445	547275		SAD LIMITA	
06446	741000		SKP	
06447	606433		JMP RFROM	
06450	147634		DZM WORK3	
06451	447315	MVCST	ISZ MOVES	
06452	227315		LAC* MOVES	/SOURCE
06453	447316		ISZ MOVED	
06454	067316		DAC* MOVFD	
06455	547275		SAD LIMITA	
06456	741000		SKP	
06457	606451		JMP MVCST	
			.EJECT	

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06460	204541	LAC K10000
06461	247556	XOR K10K
06462	054541	DAC K10000+10000
06463	204376	LAC E534+6
06464	247556	XOR K10K
06465	054376	DAC E534+6+10000
06466	207327	LAC TTIN
06467	247556	XOR K10K
06470	057327	DAC TTIN+10000
06471	207326	LAC TTOUT
06472	247556	XOR K10K
06473	057326	DAC TTOUT+10000
06474	750004	LAS /LOWFR
06475	740010	RAL
06476	741100	SPA
06477	107251	JMS PINOT .EJECT /CHECK FOR INHIBIT PT /INHIBITED

```

*****  

06500 206500 RGNAGN LAC .  

06501 507556 AND K10K  

06502 740200 SZA  

06503 741000 SKP  

06504 627311 JMP* BGNHI /SEE WHICH 4K  

06505 760207 LAW 207 /START OVER IN HI 4K  

06506 107211 JMS TLSSF /BELL  

06507 627310 JMP* BGNLO /START IN LOW 4K  

/  

/SETUP TO MOVE TO LOW 4K  

/  

06510 777777 MVRK LAW 17777 /DEST'N  

06511 047634 DAC WORK3  

06512 207320 LAC K7777 /SOURCE  

06513 047315 DAC MOVES  

06514 606423 JMP MOVE+2 /MOVE PROGRAM  

/  

/  

/ADJUST MEMORY REF. INSTRUCTIONS. DO NOT ADJUST IF  

/ADR. PORTION=ANY ADR. FROM 0 TO 21.  

/  

06515 147313 MRINS DZM WDCNT /ADR. COMPARE WORD  

06516 207634 LAC WORK3 /INST. TO BE MODIFIED  

06517 507320 AND K7777 /CLEAR BITS 0-5  

06518 047633 DAC WORK2 /SAVE  

06519 207313 LAC WDCNT  

06520 547542 SAD K22 /DONF IF EQUAL TO 22  

06521 606530 JMP ,+5  

06522 547633 SAD WORK2 /COMPARE  

06523 606442 JMP MVRTN-1 /ADR. IS SOME REG. FROM 0  

06524 547633 /TO 21, MOVE WITHOUT ADJUSTING  

06525 606442 /ADR. COUNT+1  

06526 447313 TSZ WDCNT  

06527 606521 JMP , -6  

06528 207556 LAC K10K /10000  

06529 247634 XOR WORK3 /ADJUST INST. BY 10000  

06530 606443 JMP MVRTN /MOVEF  

/  

06531 047643 SAV3 DAC SAVAC /THESE ARE MODIFIED FOR  

06532 047644 SAV5 DAC RJMP /RELLOCATION  

06533 606536 SAV6 JMP SRVINT  

, EJECT

```

```

/
/SERVICE ALL INTERRUPTS
/
06536 700001    SRVINT    CLSF      /CHECK FOR PI FROM CLOCK
06537 741000    SKP       /SOME OTHER DEVICE
06540 606575    JMP CLKINT
06541 700314    IORS
06542 741100    SPA       /STATUS WORD BIT 0 MUST = 0
06543 740040    HALT
06544 207631    LAC WORK
06545 740010    RAL
06546 741100    SPA       /SEE IF TTY IN USE AT TIME
                           /OF PI
06547 606636    JMP TTYINT
06550 700314    IORS
06551 507322    AND K1400
06552 740200    SZA
06553 107147    JMS RNFLG
06554 700101    RSF
06555 741000    SKP
06556 606776    JMP READA
06557 700201    PSF
06560 606562    JMP RTNIT
06561 626763    JMP* GOPNCH
                           /CONTINUE PRINTING
                           /I/O STATUS WORD
                           /CHECK FOR NO TAPE FLAGS
                           /EITHER READER OR PUNCH NO TAPE
                           /CHECK FOR PI FROM READER
                           /READ MORE
                           /CHECK PUNCH PI
                           /SOME OTHER DEVICE
                           /PUNCH MORE
/
/SETUP TO RETURN TO INSTRUCTION TEST
/
06562 744000    RTNIT     CLL      /C(0) AT PI
06563 207644    LAC RJMP
06564 741100    SPA
06565 744002    STL
06566 507602    AND K17S
06567 546574    SAN ILINT
06570 740040    F644     HALT
                           /RESTORE LINK
06571 207643    LAC SAVAC
06572 700042    PION     ION
06573 627644    JMP* RJMP
                           /AC AT TIME OF PI
                           /PI ON
                           /CONTENTS OF (0) AFTER PI
06574 006572    ILINT     PION
06575 200007    CLKINT    LAC 7
06576 547540    SAN K100
06577 741000    SKP
06600 606575    JMP .-3
06601 700004    CLOF
06602 106621    JMS CLKSET
06603 606562    JMP RTNIT
                           .EJECT
                           /LEFT CLOCK CONTINUE FOR 1/2 SEC
                           /RESET CLOCK TO RANDOM VALUE
                           /RETURN TO INST. TEST

```

```

*****  

/SETUP CLOCK VALUES  

/  

U6604    000000      0  

U6605    106102      JMS GENRAN      /GET A NO. FOR CLOCK  

U6606    047313      DAC WDCNT      /SAVF  

U6607    507324      AND K777      /MAX. TIME = 9 SECS.  

U6610    047313      DAC WDCNT      /SAVE  

U6611    347325      TAD M167      /MIN. TIME = 2 SEC.  

U6612    741100      SPA          /POS.=? SECS. OR MORE  

U6613    606605      JMP SETCLK+1  /NEG = LESS THAN 2 SEC.  

U6614    207313      LAC WDCNT  

U6615    740001      CMA  

U6616    040007      DAC 7          /PUT VALUE IN (7)  

U6617    700044      CLON  

U6620    626604      JMP* SETCLK  /CLOCK ON  

                           /EXIT  

/  

CLKSET      0  

U6621    000000      JMS RANGEN      /GET A NO. FOR CLOCK  

U6622    106133      DAC WDCNT      /SAVF  

U6623    047313      AND K777      /MAX. TIME = 9 SECS.  

U6624    507324      DAC WDCNT      /SAVF  

U6625    047313      TAD M167      /MIN. TIME = 2 SECS.  

U6626    347325      SPA          /POS. = 2 SECS. OR MORE  

U6627    741100      JMP CLKSET+1  /NEG. = LESS THAN 2 SECS.  

U6630    606622      LAC WDCNT  

U6631    207313      CMA  

U6632    740001      DAC 7          /PUT VALUE IN (7)  

U6633    040007      CLON  

U6634    700044      JMP* CLKSET  /CLOCK ON  

U6635    626621      .EJECT      /EXIT

```

/SFTUP FOR READ, PUNCH, OR PRINT

/

06636	207326	TTYINT	LAC TTOUT	
06637	546701		SAD ENDOUT	/IF EQUAL GO PUNCH AND READ
06640	606657		JMP PREADY	
06641	207631		LAC WORK	
06642	507531		AND K0	
06643	247560		XOR K200K	
06644	047631		DAC WORK	
06645	700101		RSF	
06646	741000		SKP	
06647	740040	F645	HALT	/ERROR. READ FLAG UP
06650	700201		PSF	
06651	741000		SKP	
06652	740040	F646	HALT	/ERROR. PUNCH FLAG UP
06653	447326		ISZ TTOUT	/TTOUT = CHAR. BIN POINTER
06654	227326		LAC* TTOUT	/GET CHAR. FROM TTY BIN
06655	700406		TLS	/PRINT ONE CHARACTER
06656	606562		JMP RTNIT	/RETURN TO INST. TEST
06657	147631	PREADY	D2M WORK	
06660	147330		D2M CNTA	
06661	147331		D2M CNTB	
06662	700402		TCF	/CLEAR TTY FLAG
06663	750004		LAS	
06664	507544		AND K1K	
06665	741200		SNA	/TEST ACS 8 A 1
06666	606671		JMP .+3	
06667	700104		RSA	/SELECT READER
06670	606562		JMP RTNIT	/RETURN TO INST. TEST
06671	206763		LAC GOPNCH	
06672	741200		SNA	/0=1ST TIME THRU
06673	606702		JMP PNSTRT	/START SEQUENCE
06674	700104		RSA	/SELECT READER
06675	626763		JMP* GOPNCH	/CONTINUE SEQUENCE
06676	007360	DATARL	TTRUFA-1	
06677	007444	ENDBIN	TTRUFA+63	
06700	007444	OUTTOP	TTRUFB-1	
06701	007530	ENDOUT	TTRUFB+63	
			,EJECT	

```

/
/PUNCH DATA
/
06702 107242 PNSTRT JMS CRLF /CR,LF
06703 700402 TCF /CLEAR TTY FLAG
06704 750004 LAS
06705 507547 AND K3K /MASK ACS 7 AND 8
06706 741200 SNA /IF EITHER IS A 1, DON'T PUNCH
06707 606712 JMP .+3 /PUNCH DATA
06710 700202 PCF /CLEAR PUNCH FLAG, NO MORE
                     /PI'S FROM PUNCH SHOULD OCCUR,
                     /RETURN TO INST. TEST

06711 606562 JMP RTNIT /SELECT READER AND PUNCH
06712 750000 CLA /TO INITIATE SEQUENCE
06713 700104 RSA
06714 106763 JMS GOPNCH
06715 207334 LAC K300
06716 047336 DAC STORE
06717 447336 ISZ STORE
06720 207336 LAC STORE
06721 106763 JMS GOPNCH /PUNCH CHAR. IN AC 10-17
06722 207337 LAC SPCE ///(SRCE)=240
06723 106763 JMS GOPNCH /PUNCH SPACE
06724 207336 LAC STORF /DONF WITH ALPHABET IF EQUAL
06725 547340 SAD K332
06726 741000 SKP /PUNCH MORE CHARS.
06727 606717 JMP PNXT
06730 207332 LAC K257
06731 047336 DAC STORF
06732 447336 ISZ STORE
06733 207336 LAC STORE
06734 106763 JMS GOPNCH
06735 207337 LAC SPCE
06736 106763 JMS GOPNCH
06737 207336 LAC STORE
06740 547333 SAD K271
06741 741000 SKP
06742 606732 JMP PNXTA ///(KCRLF)=CR,LF
06743 207341 LAC KCRLF
06744 047336 DAC STORE
06745 106763 JMS GOPNCH /PUNCH CR
06746 207336 LAC STORE /ROTATE 9 RIGHT
06747 107233 JMS ROTAT9 /PUNCH LF
06750 106763 JMS GOPNCH
06751 777770 LAW -1a /AC = 777777
06752 047336 DAC STORE /CLEAR AC WITH MB 14
06753 750001 CLA!CMA
06754 700010 700010
06755 740200 SZA
06756 740040 HALT /ERROR. EVENT TIME 1 DIDN'T
                     /CLEAR AC,
                     /PUNCH 8 FRAMES OF 0'S

06757 106763 JMS GOPNCH
06760 447336 ISZ STORE
06761 606753 JMP .-6
06762 606715 JMP PNXT-2 /START NEW LINE

```

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EJECT

```

*** 06763 0000000 GOPNCH 0
      06764 700204 PSA
      06765 750004 LAS
      06766 507550 AND K4K
      06767 740200 SZA
      06770 606562 JMP RTNIT
      06771 447330 ISZ CNTA
      06772 207331 LAC CNTB
      06773 740200 SZA
      06774 607031 JMP SUR1
      06775 606562 JMP RTNIT
      /READ PUNCHED INFO
      /
06776 750004 READA LAS
06777 507547 AND K3K
07000 740200 SZA
07001 607113 JMP READR
07002 750004 LAS
07003 507550 AND K4K
07004 741200 SNA
07005 607010 JMP .+3
07006 700112 RRR
07007 606562 JMP RTNIT
07010 700112 RRR
07011 740200 SZA
07012 607017 JMP ZRONOT
07013 207330 LAC CNTA
07014 740200 SZA
07015 607031 JMP SUR1
07016 607027 JMP TADD1
07017 447327 ISZ TTIN
07020 067327 DAC# TTIN
07021 207327 LAC TTIN
07022 546677 SAD FNDBIN
07023 607037 JMP SETTY
07024 207330 LAC CNTA
07025 740200 SZA
07026 607031 JMP .+3
07027 447331 TADD1 ISZ CNTB
07030 606562 JMP RTNIT
07031 777777 SUB1 LAW +1
07032 347330 TAD CNTA
07033 047330 DAC CNTA
07034 147331 D2M CNTB
07035 700104 RSA
07036 606562 JMP RTNIT
      /
      ,EJECT
      /
      /MASK ACS 6
      /IF A 1, DON'T USE CNTA OR CNTB
      /RETURN TO INST. TEST
      /CNTA=PUNCH SELECTED
      /0=WAIT FOR PI
      /1=SFLCT READER AGAIN
      /RETURN TO INST. TEST

      /MASK ACS 7 AND 8
      /IF EITHER IS A 1, READ FULL SPEFD

      /MASK ACS 6
      /IF A 1, CLEAR READER FLAG, NO
      /MORE PI'S FROM READER
      /CLEAR READER FLAG
      /RETURN TO INST. TEST AND
      /WAIT FOR PUNCH PI
      /READ ONE
      /0=NO DATA IN READER YET
      /SEE IF PUNCH IS SELECTED
      /YES, SUBTRACT FROM CNTA
      /READER SELECTED
      /STORE CHAR. IN TTY BIN

      /CHECK FOR 52 CHARACTERS STORED
      /DONF. SETUP TO PRINT

      /RETURN TO INST. TEST
      /(CNTA)-1
      /RETURN TO INST. TEST
  
```

```

**** 07037 700201 SETTY PSF /WAIT FOR PUNCH
07040 607037          JMP .-1
07041 700202          PCF
07042 700402          TCF
07043 207312          LAC BREAK
07044 547537          SAD K12A
07045 607070          JMP PUN6
07046 447312          ISZ BREAK
07047 206676          LAC DATABL
07050 047327          DAC TTIN
07051 206700          LAC OUTTOP
07052 047326          DAC TTOUT
07053 447327          ISZ TTIN
07054 227327          LAC TTIN
07055 447326          ISZ TTOUT
07056 067326          DAC TTOUT
07057 207326          LAC TTOUT
07060 546701          SAD FNDOUT
07061 741000          SKP
07062 607053          JMP XFR1
07063 206676          LAC DATAFL
07064 047327          DAC TTIN
07065 206700          LAC OUTTOP
07066 047326          DAC TTOUT
07067 606641          JMP TTYINT+3 /BEGIN PRINTING

07070 206763          /PUNG LAC GOPNCH
07071 547112          SAD K647 /PUNCH 0'S ONLY AT END OF BLOCK
07072 741000          SKP
07073 607047          JMP XFR1-4 / (GOPNCH) NOT=E647+2
07074 147312          DZM BRFK
07075 777777          LAW -6
07076 047336          DAC STORE /FRAME COUNTER
07077 777777          LAW -1 /AC=777777
07100 700010          700010 /CLEAR AC WITH BIT 14
07101 740200          SZA
07102 740040          HALT
07103 700204          PSA
07104 700201          PSF
07105 607104          JMP .-1
07106 447336          ISZ STORE
07107 607077          JMP E648-3
07110 700202          PCF
07111 607047          JMP XFR1-4 /CLEAR PUNCH FLAG
07112 006760          /SETUP TO PRINT

07112          E647+2
07112          /EJECT

```

/RFADB ROUTINE IS USED ONLY WHEN PUNCH IS INHIBITED
 /BY ACS 7 OR 8, OR BOTH, READER RUNS AT FULL SPEED.
 /

07113	750004	READR	LAS	
07114	507550		AND K4K	/MASK ACS 6
07115	741200		SNA	/IF A 1, DON'T READ
07116	607121		JMP .+3	
07117	700112		RRR	
07120	606562		JMP RTNIT	/CLEAR READER FLAG
07121	700112		RRR	/RETURN TO INST. TEST
07122	741200		SNA	/GET CHAR. FROM BUFFER.
07123	607131		JMP SELECT	/0 = NO DATA IN READER YET.
07124	447327		ISZ TTIN	/SELECT READER AGAIN
07125	067327		DAC* TTIN	/BUFFER POINTER +1
07126	207327		LAC TTIN	/STORE CHAR. IN TTBUFA
07127	546677		SAD ENDBIN	
07130	607133		JMP .+3	/CHECK FOR 52 CHARS. STORED
07131	700104	SELECT	RSA	/TTBUFA IS FULL
07132	606562		JMP RTNIT	/SELECT READER
				/RETURN TO INST. TEST
07133	206676		LAC DATABL	
07134	047327		DAC TTIN	/RESTORE TTBUFA POINTER
07135	750004		LAS	
07136	507551		AND K6K	/MASK ACS 6 AND 7
07137	740200		SZA	/IF EITHER A 1, DON'T PRINT
07140	607131		JMP SELECT	/SELFC READER AGAIN
07141	607042		JMP SETTY+3	/SETUP TO PRINT
			/SFERVICE NO TAPE CONDITIONS	
			/	
07142	000000	RNFLG	A	
07143	507544		AND K1K	/CHECK FOR READER NO TAPE
07144	740200		SZA	
07145	607152		JMP .+5	/READER
07146	760320		LAW 320	/PUNCH. NO TAPE
07147	247342		XOR K520K	
07150	047352		DAC NTFLG+1	
07151	607154		JMP OUTFLG	/PRINT R OR P NO TAPE
07152	760322		LAW 322	/READER NO TAPE
07153	607147		JMP .-4	
			/	
07154	207351	OUTFLG	LAC NTFLG	
07155	047313		DAC WDCNT	
07156	107242		JMS CRLF	/CR,LF
07157	447313		ISZ WDCNT	
07160	227313		LAC* WDCNT	
07161	741200		SNA	
07162	607167		JMP CLRFLG	
07163	107211		JMS TLSSF	
07164	107233		JMS ROTAT9	
07165	107211		JMS TLSSF	
07166	607157		JMP OUTFLG+3	
07167	107242	CLRFLG	JMS CRLF	/CR,LF
07170	627142		JMP* RNFLG	/RETURN TO SEQUENCE

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.EJECT

/PUNCH LEADER

/

07171	000000	PNLEDR	A
07172	777440		LAW -340
07173	047313		DAC WDCNT
07174	750000		CLA
07175	700204		PSA
07176	700201		PSF
07177	607176		JMP .-1
07200	447313		IS7 WDCNT
07201	607175		JMP .-4
07202	627171		JMP* PNLEDR

/

07203	000000	PNMARK	A
07204	777777		LAW -1
07205	700204		PSA
07206	700201		PSF
07207	607206		JMP .-1
07210	627203		JMP* PNMARK

/EXIT
.EJECT

```

/PRINT A CHARACTER
/
07211 000000
07212 047316
07213 207631
07214 740010
07215 740100
07216 607221
07217 700401
07220 607217
07221 207316
07222 700406
07223 700401
07224 607223
07225 207631
07226 740010
07227 740100
07230 700402
07231 207316
07232 627211

/JMP* TLSSF      /CHECK TTY FLAG
/                  /WAIT FOR FLAG
/                  /CLEAR TTY IF BIT 1 = 0

/ROTATE 9 RIGHT
/
07233 000000
07234 742020
07235 742020
07236 742020
07237 742020
07240 740020
07241 627233

RTR;      RTR;      RTR
RTR;      RAR
JMP*      ROTAT9

/CARRIAGE RETURN, LINEFEED
/
07242 000000
07243 760215
07244 107211
07245 547247
07246 627242
07247 760212
07250 607244

CRLF      0
          LAW 215      /CR
          JMS TLSSF
          SAD .+2
          JMP* CRLF      /EXIT
          LAW 212      /LF
          JMP CRLF+2    .EJECT

```

/PRINT "COMPLETE"
/
07251 000000 PINOT 0
07252 207635 LAC WORK4 /PASS COUNTER
07253 547537 SAD K12A /PRINT IF EQUAL TO 10
07254 741000 SKP
07255 627251 JMP* PINOT /START PROGRAM
07256 147635 DZM WORK4
07257 157635 DZM WORK4+10000
07260 207274 LAC COMPA
07261 040014 DAC 14 /PRINT COMPLETE
07262 107242 JMS CRLF
07263 220014 LAC* 14
07264 741200 SNA /DONF PRINTING IF 0
07265 607272 JMP .+5
07266 107211 JMS TLSSF /PRINT 1 CHAR
07267 107233 JMS ROTAT9
07270 107211 JMS TLSSF /PRINT 2ND
07271 607263 JMP .-6 /GET NEXT PAIR
07272 107242 JMS CRLF /CR, LF
07273 627251 JMP* PINOT
/
07274 007343 COMPA COMP
.EJECT

/CONSTANT TABLE FOR CHECKERBOARD AND PI /SERVICE ROUTINES			
/			
17275	742929	LIMITA	752525
17276	0000000	WC16	0
17277	0000000	WC12H	0
17300	0000000	ILREG	0
17301	0377000	KPAT	A37700
17302	7400076		7400076
17303	037701		037701
17304	7400077		7400077
17305	0377000	MPAT	0377000
17306	0000000	PATH	0
17307	0000000	PATWD	0
17310	0000070	HGNLO	SEQUEN
17311	0100070	HGNHI	SEQUEN+100000
17312	0000000	RREAK	0
17313	0000000	WDUNT	0
17314	7777776	WC256	7777776
17315	0000000	MOVES	0
17316	0000000	MOVED	0
17317	7777777	BITSUP	7777777
17320	0000000	K7777	7777
17321	1000000	K100K	1000000
17322	0014000	K1400	1400
17323	5000000	K500K	5000000
17324	0000000	K777	777
17325	777611	M167	777611
17326	0000000	TTOUT	0
17327	0000000	TTIN	0
17330	0000000	CNTA	0
17331	0000000	CNTB	0
17332	0000257	K257	257
17333	0000271	K271	271
17334	0003000	K300	300
17335	0003001	K301	301
17336	0000000	STORE	0
17337	0002400	SPCE	240
17340	000332	K332	332
17341	212215	KCRLF	212215
17342	5200000	K5200K	5200000
		EJECT	

/PRINT ROUTINE CONSTANTS
/"COMPLETE"

/COMP

. 317303; 320315; 305314; 305324; 0

07343 007343
07344 317303
07345 320315
07346 305314
07347 305324
07350 000000

/R OR P NO TAPE

/NTFLG

. 0 317316; 324240; 320301

07351 007351
07352 000000
07353 317316
07354 324240
07355 320301
07356 240305
07357 207207
07360 000000

/TTY BIN

/

TTRUFA .BLOCK 64

/READER BUF = 52 LOCS. (DECIMAL)

/

TTRUFB .BLOCK 64

/TTY BUF = 52 LOCS. (DECIMAL)

.EJECT

/CONSTANT AND ERROR TABLES, NOT MODIFIED WHEN IN HI 4K

		/	
07531	000000	K0	0
07532	000001	K1	1
07533	000002	K2	2
07534	000004	K4	4
07535	000010	K10	10
07536	000011	K11	11
07537	000012	K12A	12
07540	000040	K100	40
07541	000020	K20	20
07542	000022	K22	22
07543	000040	K40	40
07544	001000	K1K	1000
07545	000400	K400	400
07546	002000	K2K	2000
07547	003000	K3K	3000
07550	004000	K4K	4000
07551	006000	K6K	6000
07552	000200	K200	200
07553	040000	K40K	40000
07554	400000	K400K	400000
07555	400002	K402K	400002
07556	010000	K10K	10000
07557	020000	K20K	20000
07560	200000	K200K	200000
07561	600000	K600K	600000
07562	700000	K700K	700000
07563	002021	K2021	2021
07564	002120	K2120	2120
07565	111111	K1S	111111
07566	222222	K2S	222222
07567	333333	K3S	333333
07570	444444	K4S	444444
07571	555555	K5S	555555
07572	666666	K6S	666666
07573	777777	K7S	777777
07574	011111	K15S	11111
07575	012222	K12S	12222
07576	013333	K13S	13333
07577	014444	K14S	14444
07600	015555	K15S	15555
07601	016666	K16S	16666
07602	017777	K17S	17777
07603	002525	K2525	2525
07604	005252	K5252	5252
07605	252525	K010	252525
07606	525252	K101	525252
07607	525253	K53	525253
07610	077777	K37S	077777

.EJECT

07611	700042	K7X42	700042
07612	700002	K7XX2	700002
07613	760002	K76X2	760002
07614	100002	K1XX2	100002
07615	604002	K6X42	604002
07616	344002	K344X2	344002
07617	741000	KSKP	SKP
07620	750000	KCLA	CLA
07621	777776	M1	777776
07622	777773	M4	777773
07623	777737	M40	777737
07624	777377	M400	777377
07625	773777	M4K	773777
07626	737777	M40K	737777
07627	377777	M400K	377777
		/	
07630	000000	RJCNT	0
07631	000000	WORK	0
07632	000000	WORK1	0
07633	000000	WORK2	0
07634	000000	WORK3	0
07635	000000	WORK4	0
07636	000000	IIADR	0
07637	000000	AUTNOT	0
07640	000000	TCLK	0
07641	740010	XCTRAL	RAL
07642	740001	AUTCMA	CMA
07643	000000	SAVAC	0
07644	000000	RJMP	0
07645	420015	LAWAUT	XCT* 15
07646	777777	LAWFUL	LAW 17777
07647	120015	JMSAUT	JMS* 15
07650	740040	KHALT	740040
07651	620012	JMPAUT	JMP* 12
07652	200000	SAV4	LAC 0
07653	741400	KS7L	741400
07654	740400	KSNL	740400
07655	123456	RANCON	123456
07656	654321	RANTRL	654321
07657	361416		361416
07660	055363		055363
07661	546060		546060
07662	243035		243035
07663	762572		762572
07664	453237		453237
07665	150214		150214
07666	000000		0
		.EJECT	

/ERROR TABLES

07667	000000	JMPRFT	0	/JMP 22
07670	000000	J111	0	/JMP 11111 (E509)
07671	000000	J222	0	/JMP 12222 (F510)
07672	000000	J333	0	/JMP 13333 (E5111)
07673	000000	J444	0	/JMP 14444 (F512)
07674	000000	J555	0	/JMP 15555 (F513)
07675	000000	J666	0	/JMP 16666 (F514)
07676	000000	J777	0	/JMP 17777 (E515)
07677	000000	J525	0	/JMP 15252 (F516)
07700	000000	J252	0	/JMP 12525 (F517)
07701	000000	CAL0	0	/CAL FROM 17757 EXT, LINK = 0 (E518)
07702	000000	CAL1	0	/CAL FROM 17757, LINK = 1 (E520)
07703	000000	JSM71	0	/JMS FROM 07777 TO 11111 (E522)
07704	000000	JSM72	0	/JMS FROM 07776 TO 12222 (E524)
07705	000000	JSM73	0	/JMS FROM 07775 TO 13333 (E526)
07706	000000	JSM74	0	/JMS FROM 07774 TO 14444 (F528)
07707	000000	JSM75	0	/JMS FROM 07773 TO 15555 (F530)
07710	000000	JSM76	0	/JMS FROM 07772 TO 16666 (E532)
07711	000000	JSM77	0	/JMS FROM 07771 TO 17777 (E534)
07712	000000	JS252	0	/JMS FROM 12525 TO 15252 (F536)
07713	000000	JS525	0	/JMS FROM 15252 TO 12525 (E538)
07714	000000	JSSS	0	/JMS SERIES TEST (E540)
<hr/>				
07715	000000	XCT11	0	/XCT JMS, FROM 11111 XCT (16666) (E562)
07716	000000	XCT12	0	/XCT JMS, FROM 12222 XCT (15555) (F564)
07717	000000	XCT13	0	/XCT JMS, FROM 13333 XCT (14444) (F566)
07720	000000	XCT17	0	/XCT J.S FROM 07776 XCT (17776) (E568)
07721	000000	XCT125	0	/XCT JMS, FROM 12525 XCT (15252)
<hr/>				
07722	000000	JST77	0	/JMS* 07777 (E592)
07723	000000	JST66	0	/JMS* 16666 (E594)
07724	000000	JST55	0	/JMS* 15555 (E596)
07725	000000	JST44	0	/JMS* 14444 (E598)
07726	000000	AUTJMP	0	/JMP* 12 (AUTO-INDEX) (E620)
07727	000000	AUTJMS	0	/JMS* 17 (AUTO-INDEX) (E633)
07730	752525		752525	
	000000		.END	

ABMATS	02530
ADDAC	01524
ADDAC1	01772
ADEDDN	03030
ADJUST	06312
AMRPRT	02571
AMRSUM	03025
AMINSB	02433
AMNSRT	02467
ANDAC	01306
ANDI	05517
ANEQ	03017
APLSRT	02454
APLUSB	02417
APOS	03016
AURJMP	06101
AUTCMA	07642
AUTJMP	07726
AUTJMS	07727
AUTNBT	07637
AUTOIN	05520
AUTR	05665
AUTRF1	06077
AUTRF1	05777
AUTRJM	06100
BDATA	06354
BEGIN	00022
BGNAGN	06500
BGNH1	07311
BGNLO	07310
BISETU	03036
BITSUP	07317
BITTS1	03055
BITTS2	03070
BMAMRT	02543
BMASUM	03024
BMINSA	02425
HMNSAT	02515
BNEG	03021
BPOS	03020
BREAK	07312
CAL0	07701
CAL1	07702
CHKBRD	06154
CKLOOP	06363
CKLP	03011
CKNO	06126
CLKINT	06575
CLKSFT	06621
CLRFIG	07167
CNTA	07330
CNTB	07331
COMP	07343
COMPA	07274
CONCHG	02767

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CRLF	07242
UACAC	03341
DATARL	06676
DBRX	00204
DBRXX	00207
DBRXXX	00215
UZMAC	03222
ENDBTN	06677
ENDOUT	06701
ENDTRL	06124
ENTST	06406
FRROR	06340
E1	00002
E113	00644
E114	00671
E115	00705
E116	00721
E140	00747
E141	00775
E142	01012
E143	01027
E162	01033
E163	01037
E164	01043
E165	01047
E166	01053
E167	01057
E168	01063
E169	01067
E170	01072
E206	01103
E207	01106
E208	01111
E209	01114
E210	01117
E211	01121
E212	01125
E213	01130
E214	01133
F215	01136
F216	01141
F217	01144
E218	01146
E219	01153
E220	01155
E221	01162
E222	01164
E223	01171
E224	01173
E225	01200
E226	01202
E24	00140
E25	00144
F258	01212
E259	01214

E26	00150
E260	01220
E261	01222
E262	01226
E263	01230
E264	01234
E265	01236
E266	01243
E267	01245
E268	01252
E269	01254
E27	00154
E270	01261
E271	01263
E272	01270
E273	01301
E274	01311
E275	01315
E276	01321
E277	01326
E278	01342
E279	01344
E28	00164
E280	01354
E281	01361
E282	01366
E283	01372
E284	01405
E285	01415
E286	01417
E287	01424
E288	01426
E289	01433
E29	00170
E290	01435
E291	01443
E292	01445
E293	01451
E294	01453
E295	01457
E296	01461
E297	01467
E298	01471
E299	01477
E30	00174
E300	01501
E301	01511
E302	01517
E303	01531
E304	01533
E305	01541
E306	01543
E307	01551
E308	01553
E309	01561

E31	00200
E310	01563
E311	01571
E312	01573
E313	01601
E314	01603
E315	01611
E316	01613
E317	01621
E318	01623
E319	01631
F32	00227
F320	01633
F321	01642
F322	01644
E323	01653
E324	01655
E325	01664
F326	01666
E327	01675
E328	01677
E329	01706
F33	00232
E330	01710
E331	01717
E332	01721
E333	01730
E334	01732
F335	01741
F336	01743
E337	01752
E338	01754
E339	01763
F34	00236
F340	01765
E347	02000
E348	02002
E349	02011
E35	00241
E350	02013
E351	02022
E352	02024
E353	02033
E354	02035
E355	02044
F356	02046
E357	02055
E358	02057
F359	02066
F36	00245
E360	02070
E361	02077
E362	02101
E363	02110
E364	02112

E 365	02121
E 366	02123
E 367	02132
E 368	02134
F 369	02143
E 37	00250
E 370	02145
E 371	02154
E 372	02156
E 373	02165
E 374	02167
F 375	02176
E 376	02200
E 377	02207
E 378	02211
F 379	02220
E 38	00254
F 380	02222
E 381	02231
E 382	02233
F 383	02242
E 384	02244
E 385	02253
E 386	02255
E 387	02264
E 388	02266
E 389	02275
E 39	00257
E 390	02277
E 391	02306
E 392	02310
E 393	02316
E 394	02320
E 395	02326
F 396	02330
E 397	02336
E 398	02340
E 399	02353
E 40	00262
E 400	02355
E 401	02461
E 402	02465
E 403	02474
E 404	02500
F 405	02507
E 406	02513
E 407	02535
E 408	02541
E 409	02550
E 41	00265
E 410	02554
E 411	02563
E 412	02567
E 413	02576
E 414	02602

E415	02610
E416	02614
E417	02624
E418	02630
E419	02641
E420	02671
E421	02645
E422	02657
E423	02663
E424	02676
E425	02702
E426	02716
E427	02722
E428	02737
E429	02743
E430	02761
E431	02774
E432	02765
E433	03062
E434	03066
E435	03075
E436	03101
E437	03123
E438	03126
E439	03131
E440	03135
E441	03142
E442	03146
E443	03152
E444	03157
E445	03176
E446	03215
E447	03226
E448	03233
E449	03240
E450	03245
E451	03252
E452	03263
E453	03271
E454	03276
E455	03303
E456	03311
E457	03313
E458	03326
E459	03334
E460	03345
E461	03352
E462	03357
E463	03364
E464	03371

E 465	03376
E 466	03403
E 467	03410
E 468	03415
E 469	03427
E 47	00313
E 470	03437
E 471	03450
E 472	03454
E 473	03461
E 474	03465
E 475	03472
E 476	03476
E 477	03503
E 478	03507
E 479	03514
E 48	00316
E 480	03520
E 481	03525
E 482	03531
E 483	03536
E 484	03541
E 485	03546
E 486	03551
E 487	03556
E 488	03561
E 489	03566
E 49	00322
E 490	03571
E 491	03576
E 492	03601
E 493	03606
E 494	03611
E 495	03616
E 496	03621
E 497	03626
E 498	03631
E 499	03636
E 50	00325
E 500	03641
E 501	03661
E 502	03671
E 503	03715
E 504	03723
E 505	03731
E 506	03740
E 507	03747
E 508	03776
E 509	04004
E 51	00330
E 510	04015
E 511	04026
E 512	04037
E 513	04050
E 514	04061

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E516	04103
E517	04114
E518	04145
E519	04163
E519A	04166
E52	00335
E520	04171
E521	04204
E521A	04207
E522	04223
E523	04240
E524	04243
E525	04261
E526	04264
E527	04302
E528	04305
E529	04323
E53	00342
E530	04326
E531	04344
E532	04347
E533	04365
E534	04370
E535	04410
E536	04422
E537	04437
E538	04442
E539	04457
F54	00347
E540	04462
E541	04465
E542	04467
E543	04471
E544	04475
F545	04505
E546	04514
E547	04523
E548	04531
E549	04603
F55	00355
F550	04606
E551	04613
E552	04615
E553	04617
E554	04623
E555	04630
E556	04634
E557	04642
E558	04644
E559	04651
E56	00364
F560	04653
E561	04661
F562	04664

E563	04701
E564	04704
E565	04721
E566	04724
E567	04741
E568	04744
E569	04761
E571	00367
E570	04764
E571	05001
E572	05020
E573	05022
E574	05063
E575	05073
E576	05103
E577	05113
E578	05123
E579	05133
E581	00374
E580	05143
E581	05153
E582	05163
E584	05201
E585	05212
E586	05223
E587	05234
E588	05245
E589	05256
E591	00401
E590	05267
E591	05300
E592	05311
E593	05327
E594	05334
E595	05352
E596	05357
E597	05375
E598	05402
E599	05420
E601	00405
E600	05454
E601	05462
E602	05477
E603	05526
E604	05532
E615	05541
E606	05545
E607	05554
E608	05560
E609	05567
E61	00410
E610	05573
E611	05602
E612	05606
E613	05615

E 614	05621
E 615	05630
E 616	05634
F 617	05642
E 618	05645
E 619	05651
E 62	00413
F 620	05654
E 621	05671
E 622	05702
E 623	05706
E 624	05716
E 625	05722
F 626	05732
E 627	05734
F 628	05740
E 629	05744
E 63	00416
E 630	05753
E 631	05757
F 632	05763
E 633	05766
F 634	06003
E 635	06007
E 636	06015
E 637	06021
E 638	06024
F 639	06033
E 64	00421
E 640	06046
E 641	06052
E 642	06350
E 643	06543
E 644	06570
E 645	06647
E 646	06652
E 647	06756
E 648	07102
F 65	00427
E 66	00434
F 67	00440
E 68	00446
F 69	00451
F 70	00455
E 71	00460
F 72	00464
F 73	00467
E 74	00473
E 75	00477
F 76	00502
F 77	00505
E 78	00512
E 79	00516
E 80	00522
E 81	00526

F82	00531
F83	00535
F84	00540
F85	00544
F86	00550
F87	00553
F88	00560
F89	00566
F90	00573
L91	00603
F92	00613
GDATA	06352
GENRAN	06102
GPNCH	06763
HALT	740040
IIADR	07636
ILINT	06574
INHIT	00131
INITPI	00130
INIT4K	03750
INK52	05427
IOTST	00135
IS^AC	03444
JMPAUT	07651
JMPRF7	07667
JMPSFQ	04130
JMSAUT	07647
JMS11	05430
JST44	05440
JST55	05436
JST66	05433
JSM71	07703
JSM72	07704
JSM73	07705
JSM74	07706
JSM75	07707
JSM76	07710
JSM77	07711
JSSS	07714
JST44	07725
JST55	07724
JST66	07723
JST77	07722
JS1	04464
JS2	04466
JS252	07712
JS3	04470
JS4	04474
JS525	07713
J111	07670
J222	07671
J252	07700
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J666	07675
J777	07676
KCALF	04220
KCALR	04216
KCLA	07620
KCRLF	07341
KHALT	07650
KJS1	04573
KJS2	04574
KJS3	04575
KJS4	04576
KPAT	07301
XSKP	07617
KSNL	07654
KSQL	07653
K0	07531
K010	07605
K1	07532
K1K	07544
K1S	07565
K1XX2	07614
K1W	07535
K1'K	07556
K1'0	07540
K100K	07321
K10000	04541
K1W1	07606
K11	07536
K11110	06074
K11111	06075
K12	05033
K12A	07537
K12S	07575
K12221	06072
K12222	06073
K13S	07576
K13332	06070
K13333	06071
K14S	07577
K1400	07322
K14443	06066
K14444	06067
K15S	07600
K15252	06076
K15253	05435
K15554	06064
K15555	06065
K16S	07601
K16665	06062
K16666	06063
K17S	07602
K17776	06060
K17777	06061
K2	07533

K2K	07546
K2S	07566
K2A	07541
K2IK	07557
K2AO	07552
K2AIK	07560
K2A21	07563
K2120	07564
K22	07542
K23	05037
K2525	07603
K257	07332
K271	07333
K3K	07547
K3S	07567
K3A0	07334
K3A1	07335
K3S2	07340
K34	05043
K344X2	07616
K37S	07610
K4	07534
K4K	07550
K4S	07570
K4A	07543
K4IK	07553
K4AO	07545
K4AOK	07554
K4A2K	07555
K415	04572
K426	04566
K5S	07571
K5AOK	07323
K51S	07574
K52OK	07342
K5252	07604
K53	07607
K6K	07551
K6S	07572
K6X42	07615
K6AOK	07561
K647	07112
K7S	07573
K7XX2	07612
K7X42	07611
K7AOK	07562
K71	04571
K72	04563
K7S	04560
K71	04555
K75	04552
K76	04547
K76X2	07613
K77	04544
K777	07324

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K7779 07320
LACIN 05171
LACK 01207
LAWAUT 07645
LAWFUL 07646
LCNTA 06176
LCNTR 06200
LIMITA 07275
LLREG 07300
LOAD 06172
MAPAT 02556
MAPLM8 02502
MINSAB 02411
MINUSA 02371
MINUSB 02401
MOD 04131
MONNFG 03050
MONX 04001
MOVE 06421
MOVED 07316
MOVES 07315
MPAT 07305
MRINS 06515
MSKB1T 03026
MVRK 06510
MVCST 06451
MVRTN 06443
MUCPA 02604
M1 07621
M167 07325
M4 07622
M4K 07625
M4P 07623
M4VK 07626
M400 07624
M400K 07627
MEXPAT 06332
NOP1 740000
NOP2 740000
NOP3 740000
NTFLG 07351
NXTST 06325
OFLCH1 02627
OFLCH2 02644
OFLCH3 02662
OFLCH4 02701
OFLCH5 02721
OFLCH6 02742
OFLCH7 02764
OFLCH8 03065
OFLCH9 03100
OFLCK1 02464
OFLCK2 02477
OFLCK3 02512
OFLCK5 02540

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IFLCK7	02566
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IFLCK9	02613
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OPIRAT	00223
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OUTTOP	06700
PASS2	03027
PATR	07306
PATT	06361
PATWD	07307
PINOT	07251
PION	06572
PNLEDR	07171
PNMARK	07203
PNSTRT	06702
PNXT	06717
PNXTA	06732
PREADY	06657
PUNK	07070
RANADD	02362
RANCON	07655
RANDFX	06123
RANGEN	06133
RANTAD	06117
RANTRBL	07656
RCALSO	04157
RCALS1	04200
RCAL0	04215
RCAL1	04217
RCLOOP	06247
RCNTA	06237
RCNTR	06243
RIBRTN	06270
READ	06234
READA	06776
READR	07113
REFROM	06433
RJDNFT	07630
RJMI1	05431
RJMI2	05434
RJMI3	05437
RJMI4	05441
RJMP	07644
RJMP1	04012
RJMP2	04023
RJMP3	04034
RJMP4	04045
RJMP5	04056
RJMP6	04067
RJMP7	04100
RJMP8	04111
RJMP9	04122
RJMSS	04476

RJMS14	04433
RJMS15	04453
RJMS71	04234
RJMS72	04255
RJMS73	04276
RJMS74	04317
RJMS75	04340
RJMS76	04361
RJMS77	04404
RJSI1	05323
RJSI1X	05432
RJSI2	05346
RJSI3	05371
RJSI4	05414
RJSM25	04564
RJSM52	04567
RJSM71	04537
RJSM72	04542
RJSM73	04545
RJSM74	04550
RJSM75	04553
RJSM76	04556
RJSM77	04561
RJ111	04132
RJ222	04133
RJ252	04141
RJ333	04134
RJ444	04135
RJ525	04142
RJ555	04136
RJ666	04137
RJ777	04140
RNFGLG	07142
ROTAT9	07233
RSM25	04565
RSM52	04570
RSM71	04540
RSM72	04543
RSM73	04546
RSM74	04551
RSM75	04554
RSM76	04557
RSM77	04562
RTAT	00620
RTINIT	06562
RTSS	00722
RXCT1	04675
RXCT2	04715
RXCT3	04735
RXCT4	04755
RXCT5	04775
SADAC	03120
SAVAC	07643
SAV3	06533
SAV4	07652

SAVS	06534
SAV6	06535
SELECT	07131
SEDOFN	00070
SERS01	02616
SERS02	02632
SERS03	02647
SERS04	02665
SERS05	02704
SERS06	02724
SERS07	02745
SETCLK	06604
SETTY	07037
SPCE	07337
SRVINT	06536
STORE	07336
SUR1	07031
SUMNFG	03022
SUMPOS	03023
TADAC	01412
TADD1	07027
TAIRAN	06150
TRLTOP	06125
TCLK	07640
TLAW	01077
TLSSF	07211
TSAUTO	05055
TSCL	04143
TSDBR	00161
TSJMS	04221
TSXCT	04577
TTHUF4	07361
TTAUFH	07445
TTIN	07327
TTOUT	07326
TTYINT	06636
ULADJ	06322
WCLLOOP	06204
WC128	07277
WC16	07276
WC256	07314
WICNT	07313
WORK	07631
WORK1	07632
WORK2	07633
WORK3	07634
WORK4	07635
XCTD4C	05053
XCTD7M	05516
XCTISZ	05052
XCTRAL	07641
XCTR12	05050
XCTTAD	05054
XCT11	07715
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XCT125	07721
XCT13	07717
XCT17	07720
XFR1	07053
XORAC	01351
XTJMSI	05305
XTR11	05031
XTR12	05035
XTR13	05041
XTR17	05045
XTXCT	05442
XT1R	05032
XT11S	05030
XT12S	05034
XT13S	05040
XT17S	05044
XT2R	05036
XT3R	05042
XT4R	05046
XT5R	05051
ZRONOT	07017

E1	00002
BEGIN	00022
SEQUFN	00070
INITPI	00130
INHIT	00131
LOTST	00135
E24	00140
F25	00144
E26	00150
E27	00154
TSNBR	00161
E28	00164
E29	00170
F30	00174
E31	00200
DBRX	00204
DBRXX	00207
DBRXXX	00215
OPRAT	00223
OPERAT	00225
E32	00227
E33	00232
E34	00236
E35	00241
E36	00245
E37	00250
E38	00254
E39	00257
F40	00262
E41	00265
F42	00271
E43	00274
F44	00300
E45	00303
E46	00307
E47	00313
E48	00316
E49	00322
E50	00325
F51	00330
E52	00335
L53	00342
E54	00347
E55	00355
E56	00364
E57	00367
E58	00374
E59	00401
E60	00405
E61	00410
E62	00413
E63	00416
E64	00421
E65	00427
E66	00434

E67	00440
E68	00446
E69	00451
E70	00455
E71	00460
E72	00464
E73	00467
E74	00473
E75	00477
E76	00502
E77	00505
E78	00512
E79	00516
E80	00522
E81	00526
E82	00531
E83	00535
E84	00540
E85	00544
E86	00550
E87	00553
E88	00560
E89	00566
E90	00573
E91	00603
E92	00613
RTAT	00620
E113	00644
E114	00671
E115	00705
E116	00721
RTSS	00722
E140	00747
E141	00775
E142	01012
E143	01027
E162	01033
E163	01037
E164	01043
E165	01047
E166	01053
E167	01057
E168	01063
E169	01067
E170	01072
TL^W	01077
E206	01103
E207	01106
E208	01111
E209	01114
E210	01117
E211	01121
E212	01125
E213	01130
E214	01133

E215	01136
E216	01141
E217	01144
E218	01146
E219	01153
E220	01155
E221	01162
E222	01164
E223	01171
E224	01173
E225	01200
E226	01202
LACK	01207
E258	01212
E259	01214
E260	01220
F261	01222
E262	01226
E263	01230
E264	01234
E265	01236
F266	01243
E267	01245
E268	01252
F269	01254
E270	01261
E271	01263
E272	01270
E273	01301
ANDAC	01306
E274	01311
E275	01315
E276	01321
E277	01326
E278	01342
E279	01344
XORAC	01351
E280	01354
E281	01361
E282	01366
E283	01372
F284	01405
TADAC	01412
E285	01415
E286	01417
E287	01424
E288	01426
F289	01433
E290	01435
E291	01443
E292	01445
E293	01451
E294	01453
E295	01457
E296	01461

E297	01467
E298	01471
E299	01477
E300	01501
E301	01511
E302	01517
ADDA1	01524
E303	01531
E304	01533
E305	01541
E306	01543
E307	01551
E308	01553
E309	01561
E310	01563
E311	01571
E312	01573
E313	01601
E314	01603
E315	01611
E316	01613
E317	01621
E318	01623
E319	01631
E320	01633
E321	01642
E322	01644
E323	01653
E324	01655
E325	01664
E326	01666
E327	01675
E328	01677
E329	01706
E330	01710
E331	01717
E332	01721
E333	01730
E334	01732
E335	01741
E336	01743
E337	01752
E338	01754
E339	01763
E340	01765
ADDA1	01772
E347	02000
E348	02002
E349	02011
E350	02013
E351	02022
E352	02024
E353	02033
E354	02035
E355	02044

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E357	02055
E358	02057
E359	02066
E360	02070
E361	02077
E362	02101
E363	02110
E364	02112
E365	02121
E366	02123
E367	02132
E368	02134
E369	02143
E370	02145
E371	02154
E372	02156
E373	02165
E374	02167
E375	02176
E376	02200
E377	02207
E378	02211
E379	02220
E380	02222
E381	02231
E382	02233
E383	02242
E384	02244
E385	02253
E386	02255
E387	02264
E388	02266
E389	02275
E390	02277
E391	02306
E392	02310
E393	02316
E394	02320
E395	02326
E396	02330
E397	02336
E398	02340
E399	02353
E400	02355
RANAND	02362
MINUSA	02371
MINUSB	02401
MINSAB	02411
MINUSB	02417
MINSA	02425
MINSB	02433
APLSRT	02454
E401	02461
DFLCK1	02464

:412	02465
:MNSRT	02467
:4V3	02474
:FLCK2	02477
:4V4	02500
:APLMB	02502
:4V5	02507
:FLCK3	02512
:4V6	02513
:MNSAT	02515
:BMATS	02530
:4V7	02535
:FLCK5	02540
:4V8	02541
:MAMRT	02543
:4V9	02550
:FLCK6	02553
:410	02554
:ARPAT	02556
:411	02563
:FLCK7	02566
:412	02567
:MRPRT	02571
:413	02576
:FLCK8	02601
:414	02602
:04CPA	02604
:415	02610
:FLCK9	02613
:416	02614
:ERS01	02616
:417	02624
:FLCH1	02627
:418	02630
:ERS02	02632
:419	02641
:FLCH2	02644
:420	02645
:ERS03	02647
:421	02657
:FLCH3	02662
:422	02663
:ERS04	02665
:423	02676
:FLCH4	02701
:424	02702
:ERS05	02704
:425	02716
:FLCH5	02721
:426	02722
:ERS06	02724
:427	02737
:FLCH6	02742
:428	02743
:ERS07	02745

429	02761
IFLCH7	02764
431	02765
IOACHG	02767
IKLP	03011
IPDS	03016
INFG	03017
IPCS	03020
INFQ	03021
SUMNFG	03022
SUMPOS	03023
IMASUM	03024
IMRSUM	03025
ISKBIT	03026
ASSP	03027
DFDQN	03030
ISETU	03036
IOINFG	03050
ITTS1	03055
431	03062
IFLCH8	03065
432	03066
ITTS2	03070
433	03075
IFLCH9	03100
434	03101
ADAC	03120
435	03123
436	03126
437	03131
438	03135
439	03142
440	03146
441	03152
442	03157
443	03176
444	03215
ZMAC	03222
445	03226
446	03233
447	03240
448	03245
449	03252
450	03257
451	03264
452	03271
453	03276
454	03303
455	03311
456	03313
457	03316
458	03326
459	03334
ACAC	03341
460	03345

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:4*2	02465
:MNSRT	02467
:4*3	02474
:FLCK2	02477
:4*4	02500
:APLMB	02502
:4*5	02507
:FLCK3	02512
:4*6	02513
:MNSAT	02515
:BMATS	02530
:4*7	02535
:FLCK5	02540
:4*8	02541
:MAMRT	02543
:4*9	02550
:FLCK6	02553
:4*10	02554
:ARPAT	02556
:4*11	02563
:FLCK7	02566
:4*12	02567
:MBPRT	02571
:4*13	02576
:FLCK8	02601
:4*14	02602
:0ACPA	02604
:4*15	02610
:FLCK9	02613
:4*16	02614
:ERS01	02616
:4*17	02624
:IFLCH1	02627
:4*18	02630
:ERS02	02632
:4*19	02641
:IFLCH2	02644
:4*20	02645
:ERS03	02647
:4*21	02657
:IFLCH3	02662
:4*22	02663
:ERS04	02665
:4*23	02676
:IFLCH4	02701
:4*24	02702
:ERS05	02704
:4*25	02716
:IFLCH5	02721
:4*26	02722
:ERS06	02724
:4*27	02737
:IFLCH6	02742
:4*28	02743
:ERS07	02745

429	02761
IFLCH7	02764
430	02765
IONCHG	02767
IKLP	03011
IPIS	03016
INF G	03017
IPIS	03020
INF G	03021
IUMNFG	03022
IUMPOS	03023
IMASUM	03024
MRSUM	03025
ISKBIT	03026
ASS2	03027
UDFDON	03030
ILSETU	03036
IOINFG	03050
ITTS1	03055
431	03062
IFLCH8	03065
432	03066
ITTS2	03070
433	03075
IFLCH9	03100
434	03101
ADAC	03120
435	03123
436	03126
437	03131
438	03135
439	03142
440	03146
441	03152
442	03157
443	03176
444	03215
7MAC	03222
445	03226
446	03233
447	03240
448	03245
449	03252
450	03257
451	03264
452	03271
453	03276
454	03303
455	03311
456	03313
457	03316
458	03326
459	03334
ACAC	03341
460	03345

JMS76	04361
533	04365
534	04370
JMS77	04404
535	04410
536	04422
JMS14	04433
537	04437
538	04442
JMS15	04453
539	04457
540	04462
S1	04464
541	04465
S2	04466
542	04467
S3	04470
543	04471
S4	04474
544	04475
JMS8	04476
545	04505
546	04514
547	04523
548	04531
JSM71	04537
S ^m 71	04540
1 ^a 000	04541
JSM72	04542
S ^m 72	04543
77	04544
JSM73	04545
S ^m 73	04546
76	04547
JSM74	04550
S ^m 74	04551
75	04552
JSM75	04553
S ^m 75	04554
74	04555
JSM76	04556
S ^m 76	04557
73	04560
JSM77	04561
S ^m 77	04562
72	04563
JSM52	04564
S ^m 25	04565
426	04566
JSM52	04567
S ^m 52	04570
71	04571
415	04572
JS1	04573
JS2	04574

J43	04575
J44	04576
SCT	04577
549	04603
550	04606
551	04613
552	04615
553	04617
554	04623
555	04630
556	04634
557	04642
558	04644
559	04651
560	04653
561	04661
562	04664
I ^X CT1	04675
563	04701
564	04704
I ^X CT2	04715
565	04721
566	04724
I ^X CT3	04735
567	04741
568	04744
I ^X CT4	04755
569	04761
570	04764
I ^X CT5	04775
571	05001
572	05020
573	05022
(T11S	05030
(T11	05031
(T1R	05032
(12	05033
(T12S	05034
(T12	05035
(T2R	05036
(23	05037
(T13S	05040
(TR13	05041
(T3R	05042
(34	05043
(T17S	05044
(T17	05045
(T4R	05046
(CT12S	05047
(CTR12	05050
(T5R	05051
(CT15Z	05052
(CTD4C	05053
(CTTAD	05054
(SAUTO	05055

E574	05063
E575	05073
E576	05103
E577	05113
E578	05123
E579	05133
E580	05143
E581	05153
E582	05163
LACIN	05171
E584	05201
E585	05212
E586	05223
E587	05234
E588	05245
E589	05256
E590	05267
E591	05300
KTJMSI	05305
E592	05311
RJSI1	05323
E593	05327
E594	05334
RJSI2	05346
E595	05352
E596	05357
RJSI3	05371
E597	05375
E598	05402
RJSI4	05414
E599	05420
INK52	05427
JMSI1	05430
RJMI1	05431
RJSI1X	05432
JSI66	05433
RJMI2	05434
K15253	05435
JSI55	05436
RJMI3	05437
JSI44	05440
RJMI4	05441
XTXCT	05442
E610	05454
E611	05462
E612	05477
XCTD7M	05516
ANDI	05517
AUTOTN	05520
E613	05526
E614	05532
E615	05541
E616	05545
E617	05554
E618	05560

E619	05567
E610	05573
E611	05602
E612	05606
E613	05615
E614	05621
E615	05630
E616	05634
E617	05642
E618	05645
E619	05651
E620	05654
AUTR	05665
E621	05671
E622	05702
E623	05706
E624	05716
E625	05722
E626	05732
E627	05734
E628	05740
E629	05744
E630	05753
E631	05757
E632	05763
E633	05766
AUTRF1	05777
E634	06003
E635	06007
E636	06015
E637	06021
E638	06024
E639	06033
E640	06046
E641	06052
K17776	06061
K17777	06061
K1665	06062
K1666	06063
K16554	06064
K16555	06065
K14443	06066
K14444	06067
K1332	06070
K1333	06071
K12221	06072
K12222	06073
K11110	06074
K11111	06075
K12252	06076
AUTRFT	06077
AUTRJM	06100
AURJMP	06101
GENRAN	06102
RANTAD	06117

RANDFX	06123
ENITRL	06124
THLTOP	06125
CKNO	06126
RANGFN	06133
TADRAN	06150
CHKRD	06154
LOAD	06172
LCNTA	06176
LCNTR	06200
NCLOOP	06204
READ	06234
RCNTA	06237
RCNTR	06243
RCLLOOP	06247
RDRTN	06270
ADJUST	06312
JLOAD	06322
NXTST	06325
NEXPAT	06332
FRHOR	06340
E642	06350
GDATA	06352
BDATA	06354
PATT	06361
CKLOOP	06363
ENTST	06406
MOVE	06421
RFROM	06433
MVRTN	06443
MVOST	06451
BGNAGN	06500
MVRK	06510
MRTNS	06515
SAV3	06533
SAV5	06534
SAV6	06535
SRVINT	06536
E643	06543
RTAIT	06562
E644	06570
PION	06572
ILINT	06574
CLKINT	06575
SETCLK	06604
CLKSFT	06621
TTYINT	06636
E645	06647
E646	06652
PREADY	06657
DATARL	06676
ENDRTN	06677
OUTTOP	06700
ENDOUT	06701
PNSTRT	06702

PNXT	06717
PNXTA	06732
E647	06756
GOINCH	06763
READA	06776
ARONOT	07017
TAUDI	07027
SUW1	07031
SETTY	07037
XEW1	07053
PUN6	07070
E648	07102
K617	07112
READR	07113
SELECT	07131
RNF LG	07142
OUTFLG	07154
CL.FLG	07167
PNLDR	07171
PINMARK	07203
TLSSE	07211
ROTATY	07233
CRLF	07242
PINOT	07251
COMP A	07274
LIMITA	07275
NC16	07276
NC128	07277
LLREG	07300
KPAT	07301
IPAT	07305
PA TR	07306
PA TWO	07307
IGNLO	07310
CGHT	07311
BREAK	07312
ADCNT	07313
RCPS6	07314
MOVES	07315
MOVED	07316
ITSUP	07317
K777	07320
K10K	07321
K1400	07322
K500K	07323
K777	07324
1167	07325
TTOUT	07326
TTIN	07327
CNTA	07330
CNTB	07331
K257	07332
K271	07333
K370	07334
K371	07335

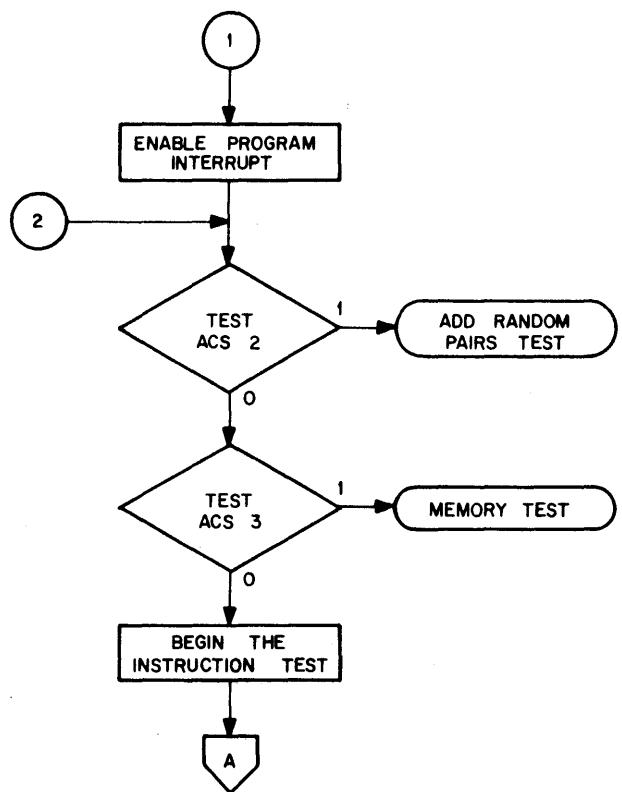
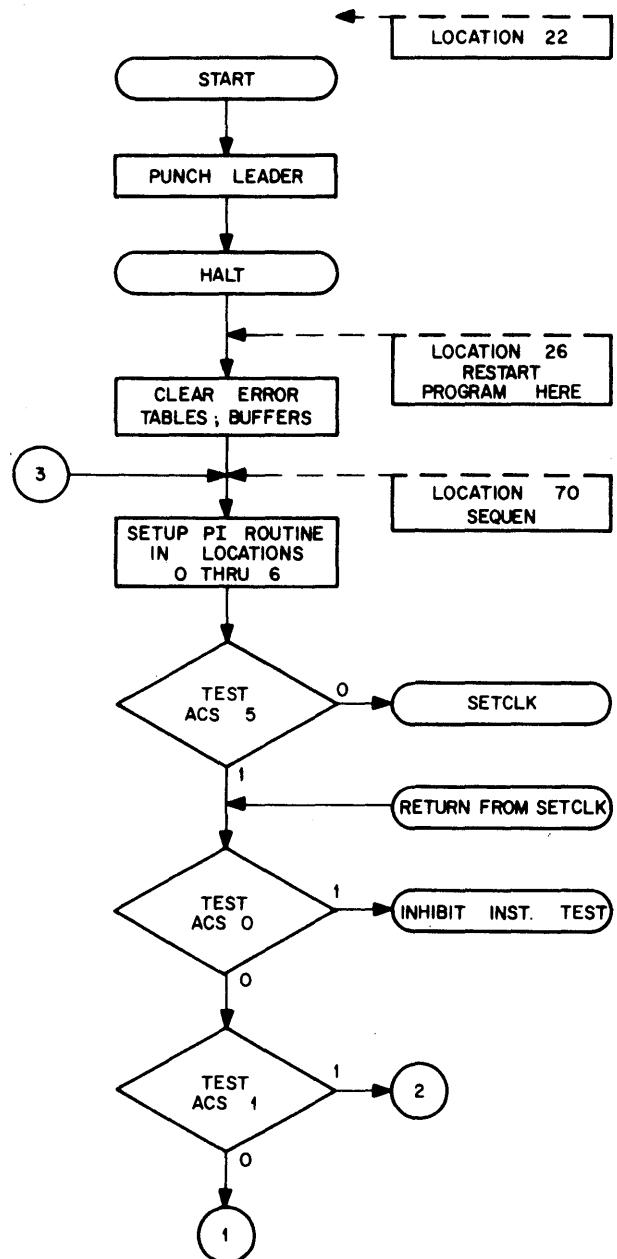
STORF	07336
SPCE	07337
K332	07340
KCRLF	07341
K520K	07342
COMP	07343
NTFLG	07351
TTIHF	07361
TTTUFH	07445
K0	07531
K1	07532
K2	07533
K4	07534
K1^	07535
K11	07536
K12A	07537
K1^0	07540
K2^	07541
K22	07542
K4^	07543
K1K	07544
K4^0	07545
K2K	07546
K3K	07547
K4K	07550
K6K	07551
K2^0	07552
K4^K	07553
K4^0K	07554
K4^2K	07555
K1^K	07556
K2^K	07557
K2^0K	07560
K6^0K	07561
K7^0K	07562
K2^21	07563
K2120	07564
K1S	07565
K2S	07566
K3S	07567
K4S	07570
K5S	07571
K6S	07572
K7S	07573
K51S	07574
K12S	07575
K1^S	07576
K14S	07577
K15S	07600
K16S	07601
K17S	07602
K2525	07603
K5252	07604
K010	07605
K1^1	07606

K53	07607
X37S	07610
X7X42	07611
X7XX2	07612
X7~X2	07613
X1~X2	07614
X6X42	07615
X3~4X2	07616
XSKP	07617
KCL A	07620
M1	07621
M4	07622
M4~	07623
M4~0	07624
M4~	07625
M4~K	07626
M4~0K	07627
RJENT	07630
NDK	07631
NDK1	07632
NDK2	07633
NDK3	07634
NDK4	07635
LIAOR	07636
AUTNOT	07637
TOK	07640
XCTRAL	07641
AUTCMA	07642
SAVAC	07643
RJUP	07644
LAVAUT	07645
LAFUL	07646
JMSAUT	07647
XHALT	07650
JMSAUT	07651
SAV4	07652
XSL	07653
XSL	07654
ZACON	07655
RATRL	07656
JMFRFT	07667
J111	07670
J222	07671
J333	07672
J444	07673
J555	07674
J666	07675
J777	07676
J525	07677
J252	07700
CAL0	07701
CAL1	07702
JS~71	07703
JS~72	07704
JS~73	07705

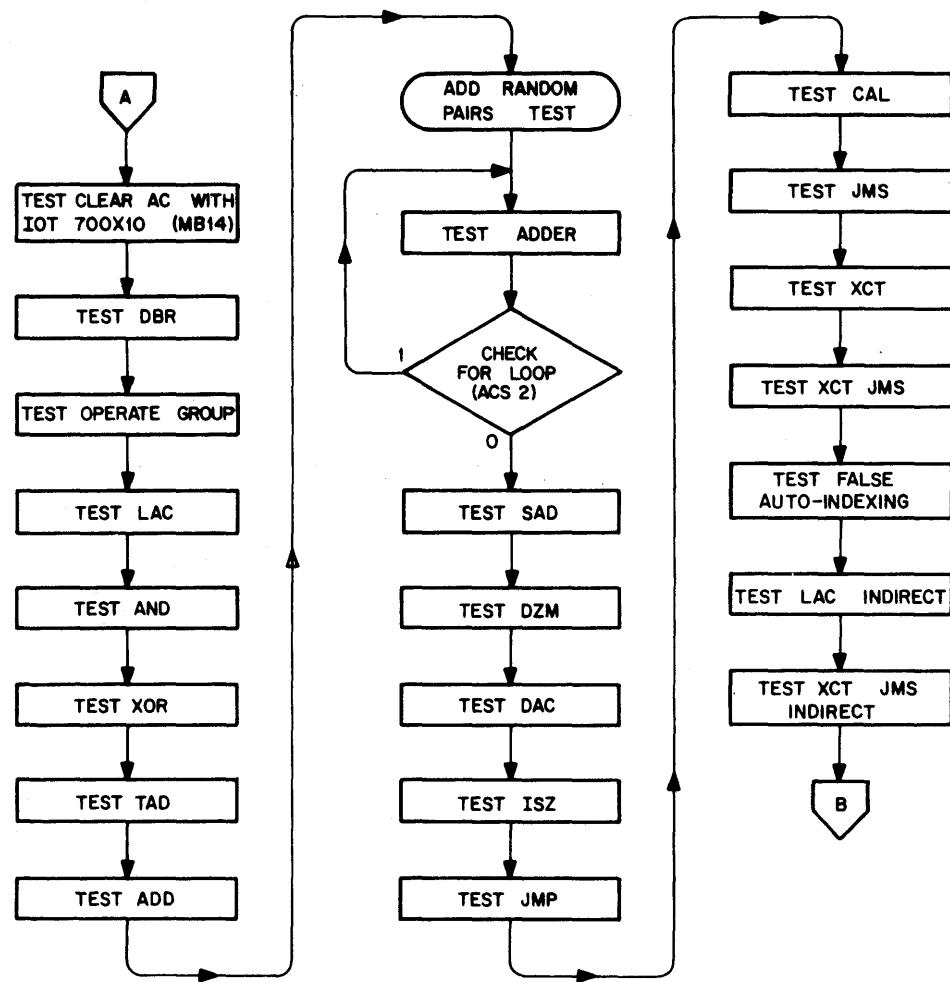
BASEX9 PAGE 159

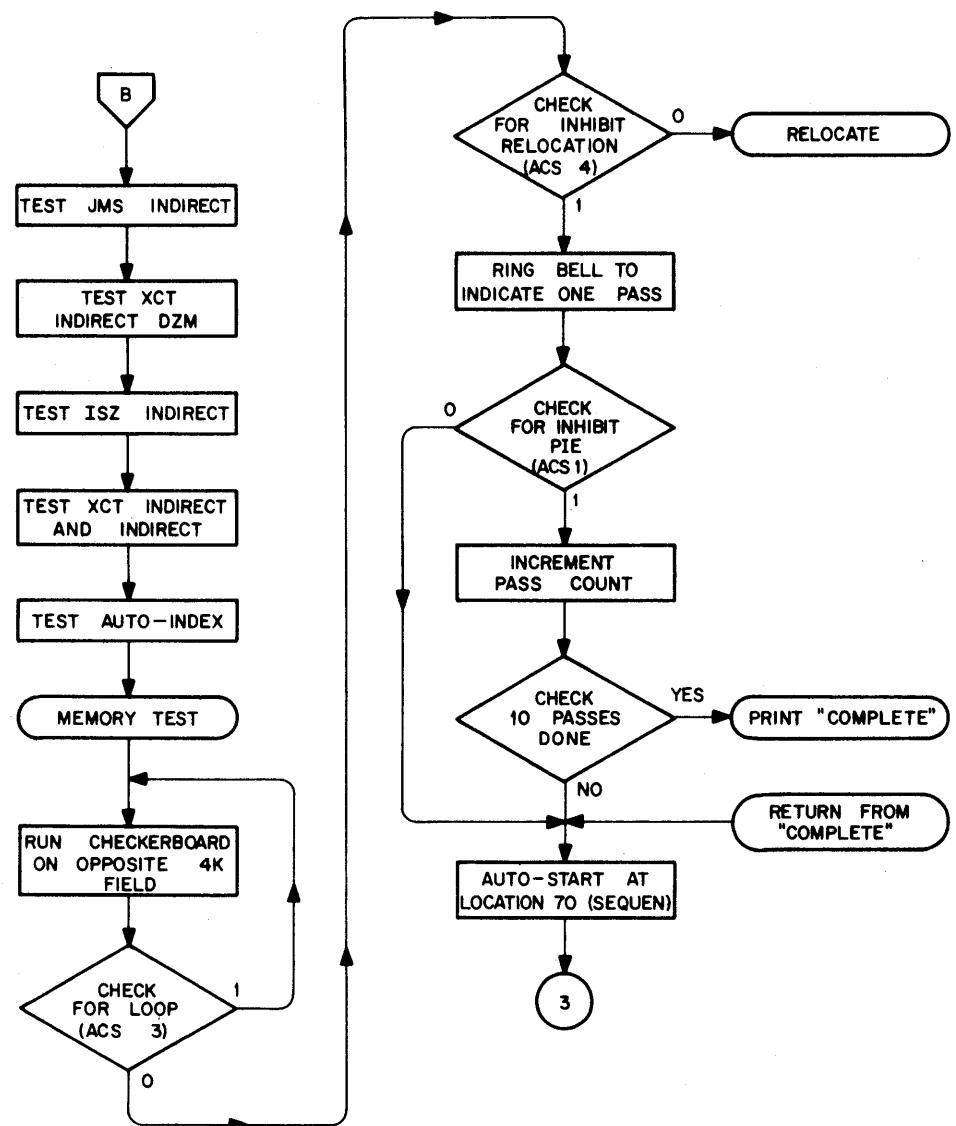
JSM74	07706
JSM75	07707
JSM76	07710
JSM77	07711
JSP52	07712
JSP25	07713
JSSS	07714
XCT11	07715
XCT12	07716
XCT13	07717
XCT17	07720
XCT125	07721
JST77	07722
JST66	07723
JST55	07724
JST44	07725
AUTJMP	07726
AUTJMS	07727
NOP1	740000
NOP2	740000
NOP3	740000
HALT	740040

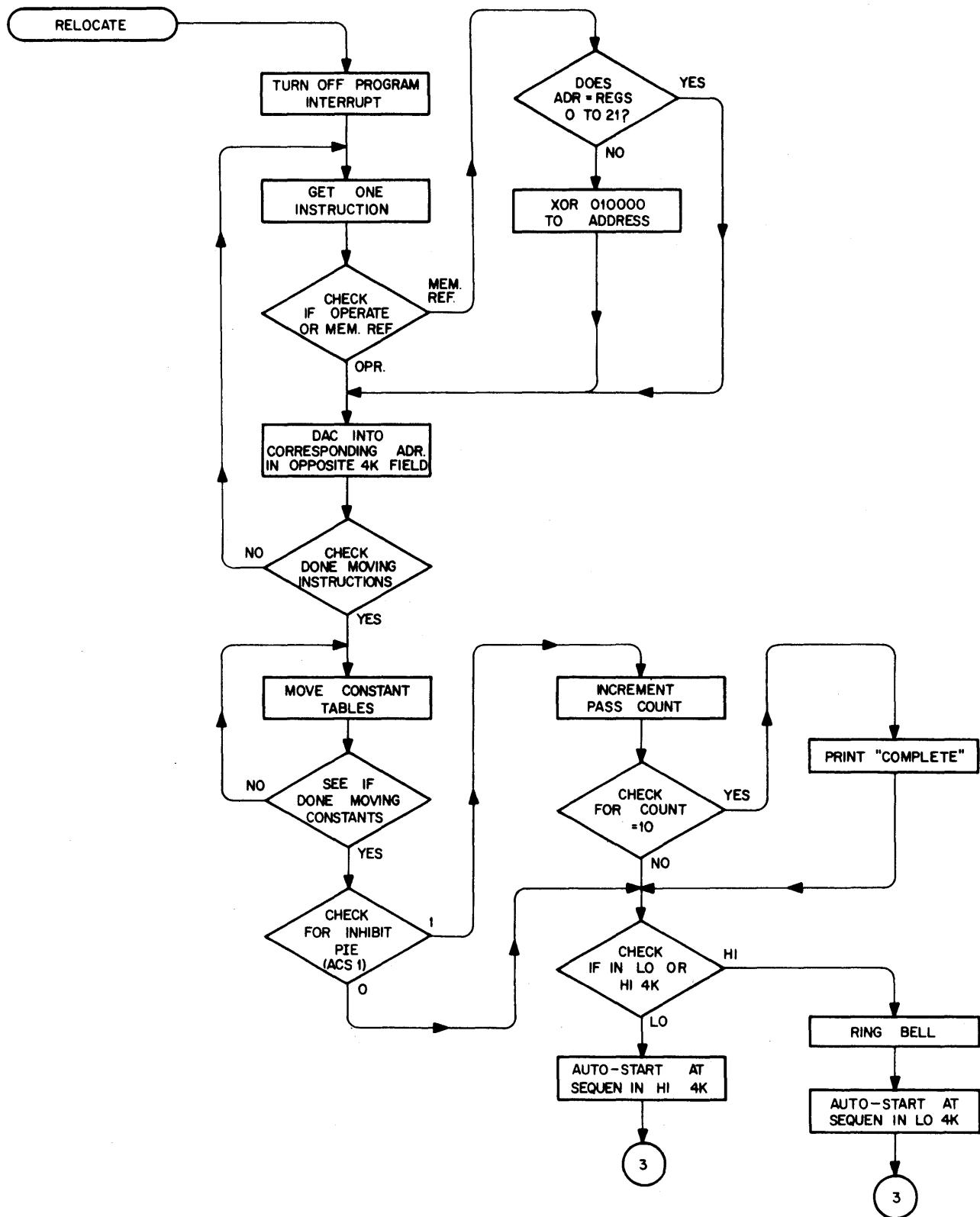
11. FLOW CHARTS



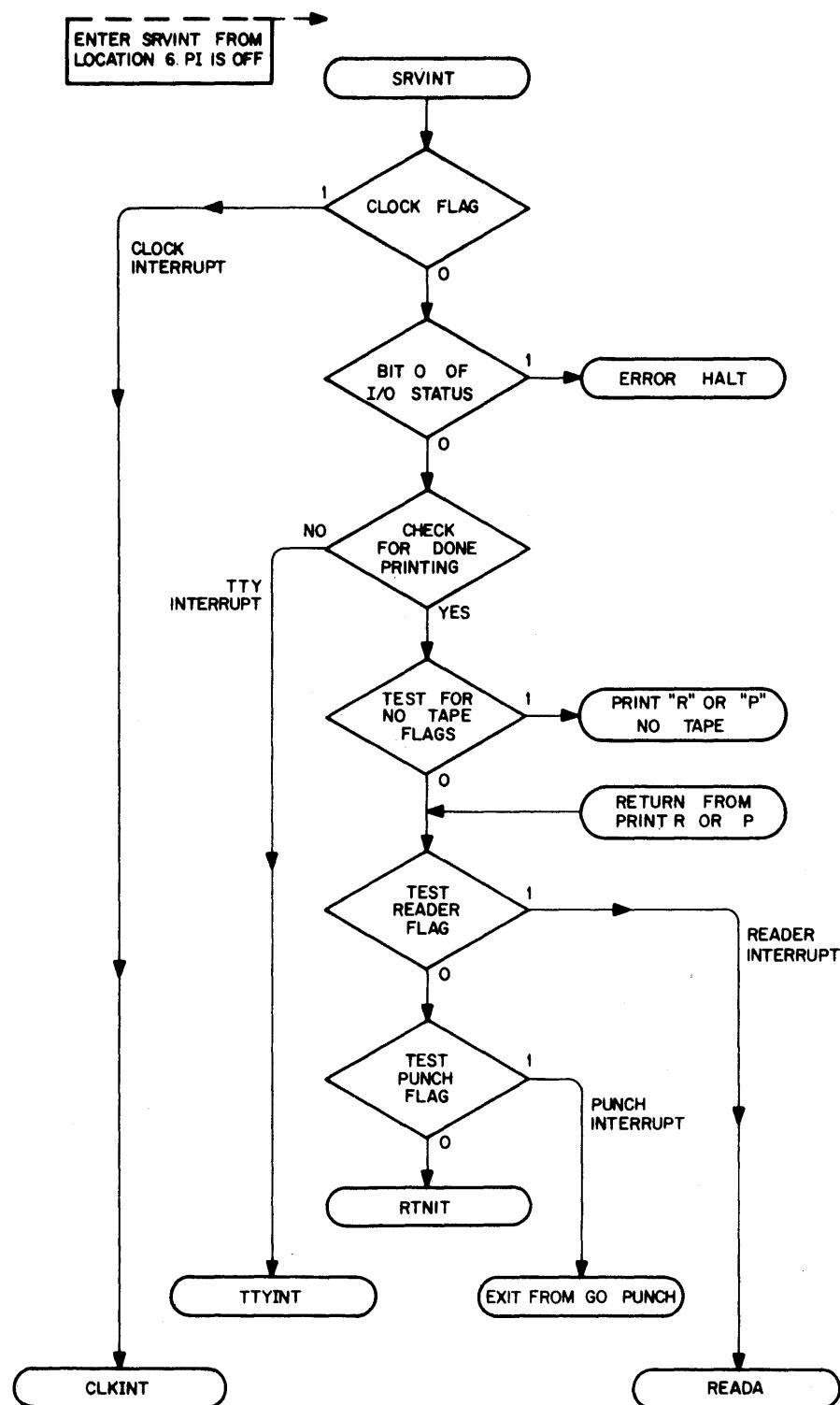
Generalized Flow of PDP-9 Basic Exerciser



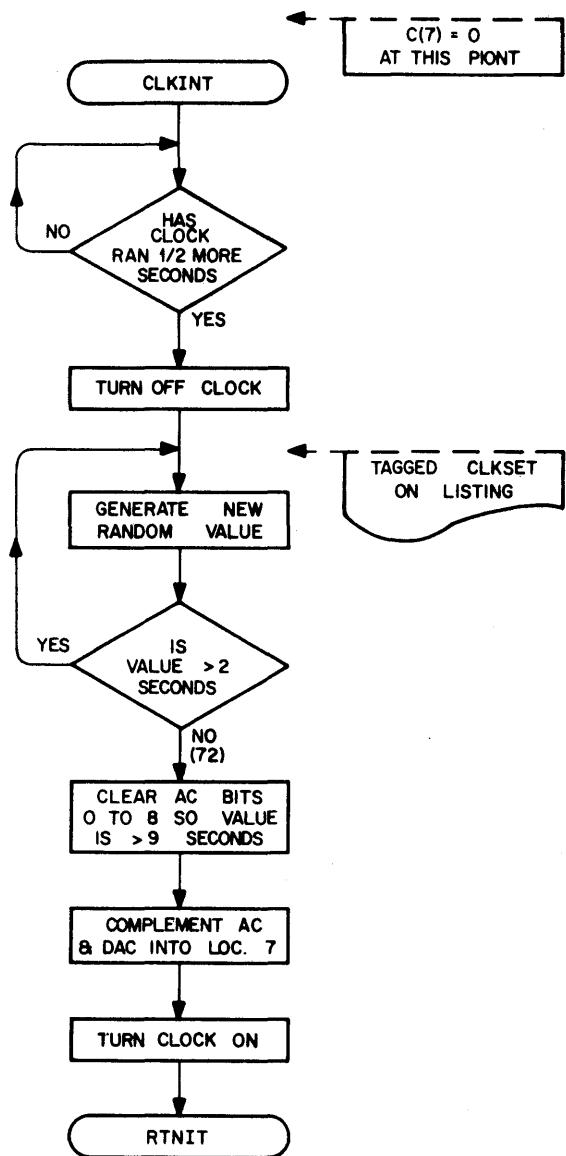




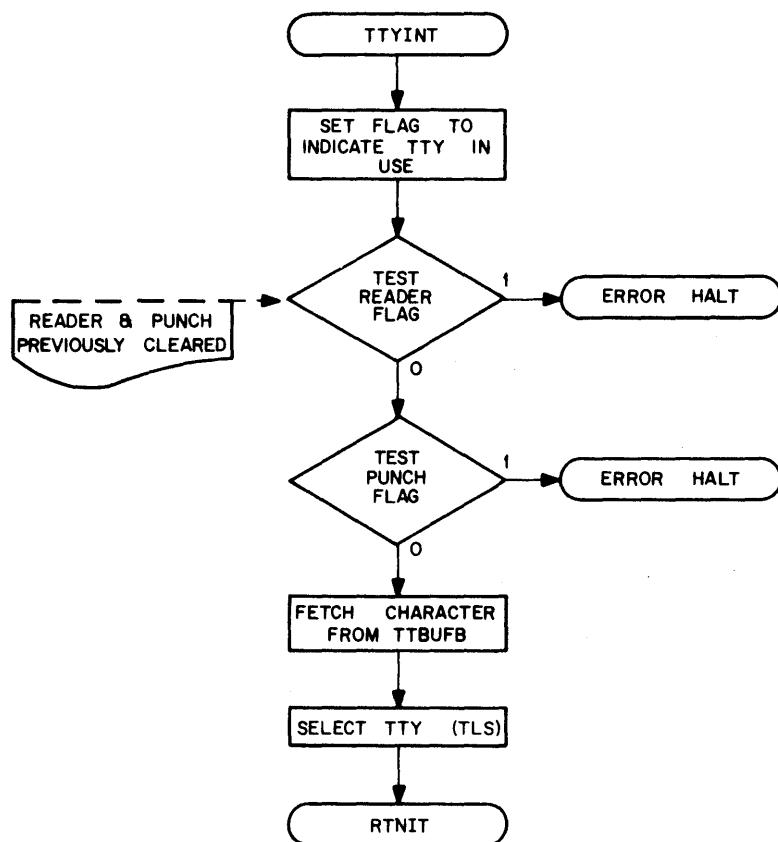
Program Relocation Routine



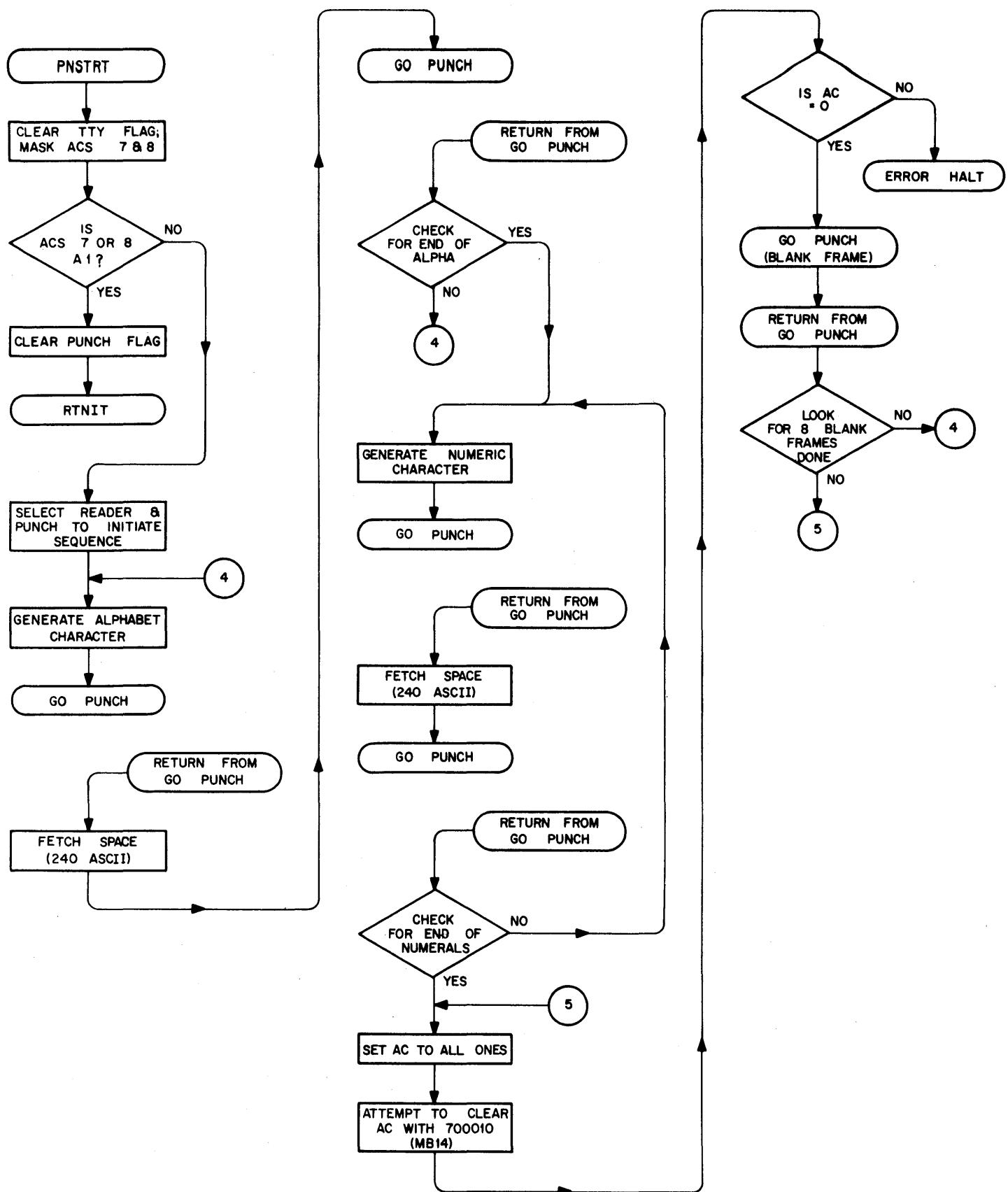
Interrupt Service Routine



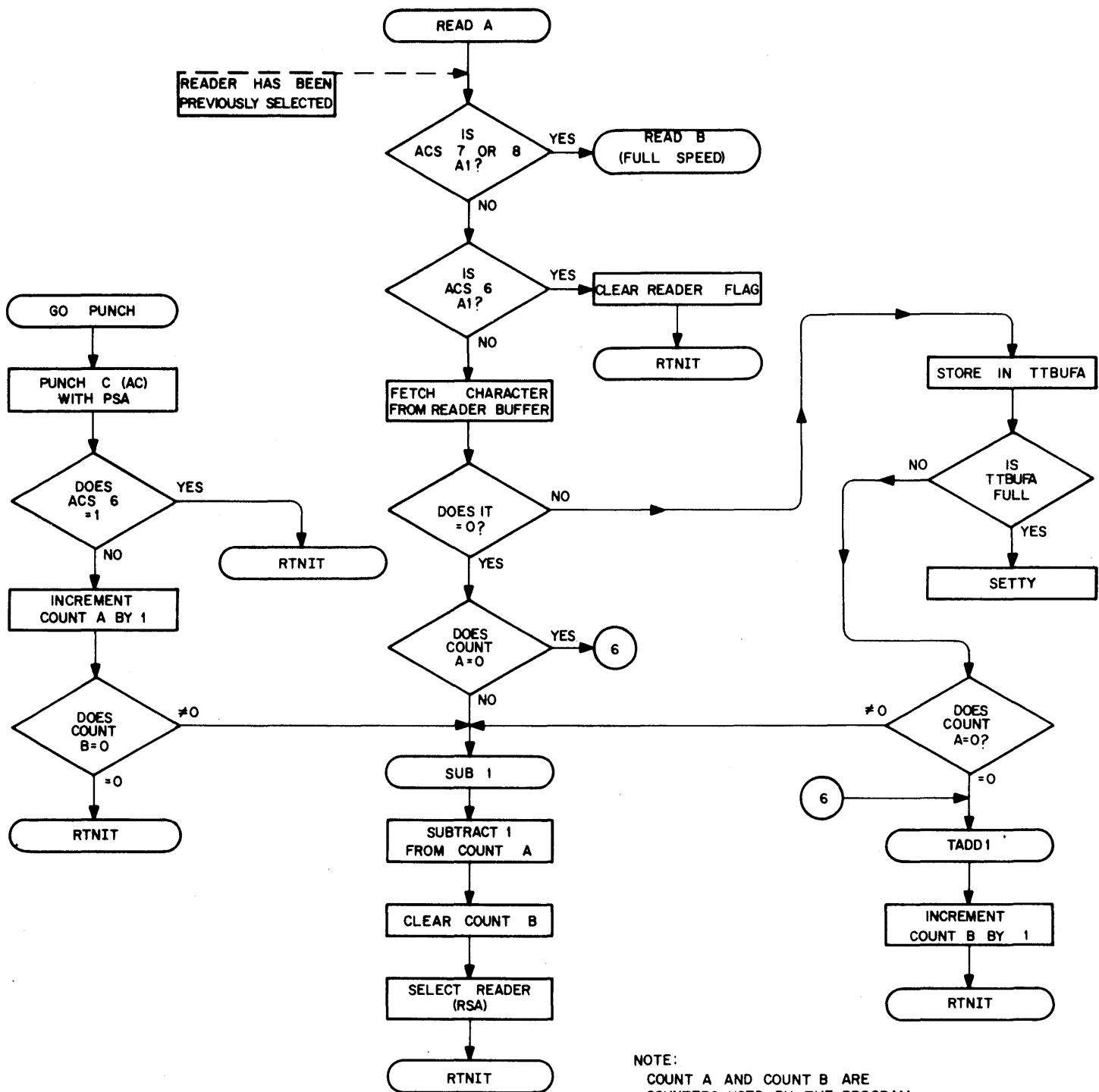
Service Clock Interrupt



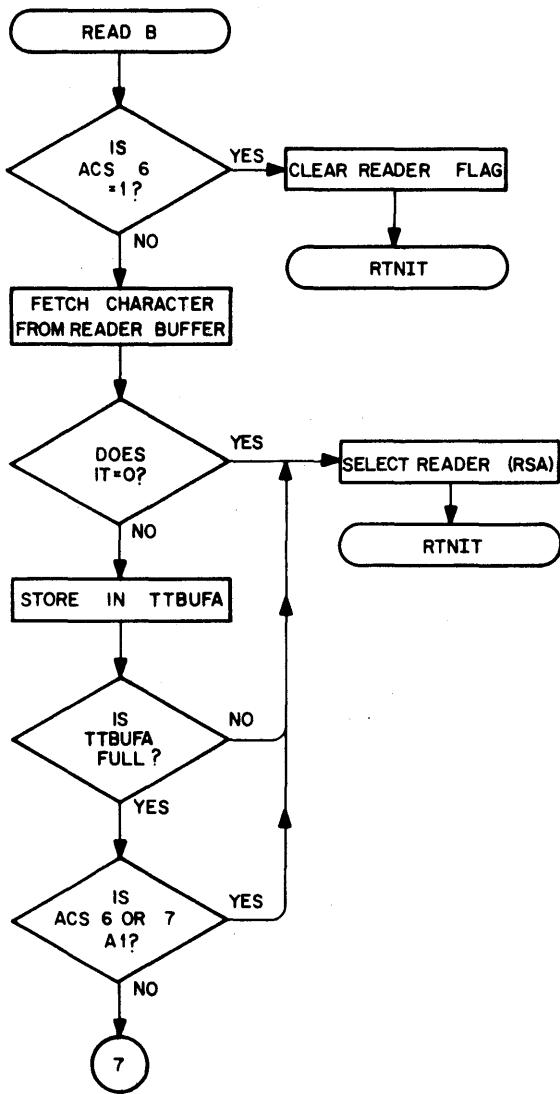
Service TTY Interrupt



Punch Routine



NOTE:
COUNT A AND COUNT B ARE
COUNTERS USED BY THE PROGRAM
TO ENABLE THE READER TO
OPERATE AT PUNCH SPEED.



Read full speed ACS 7 A 1

