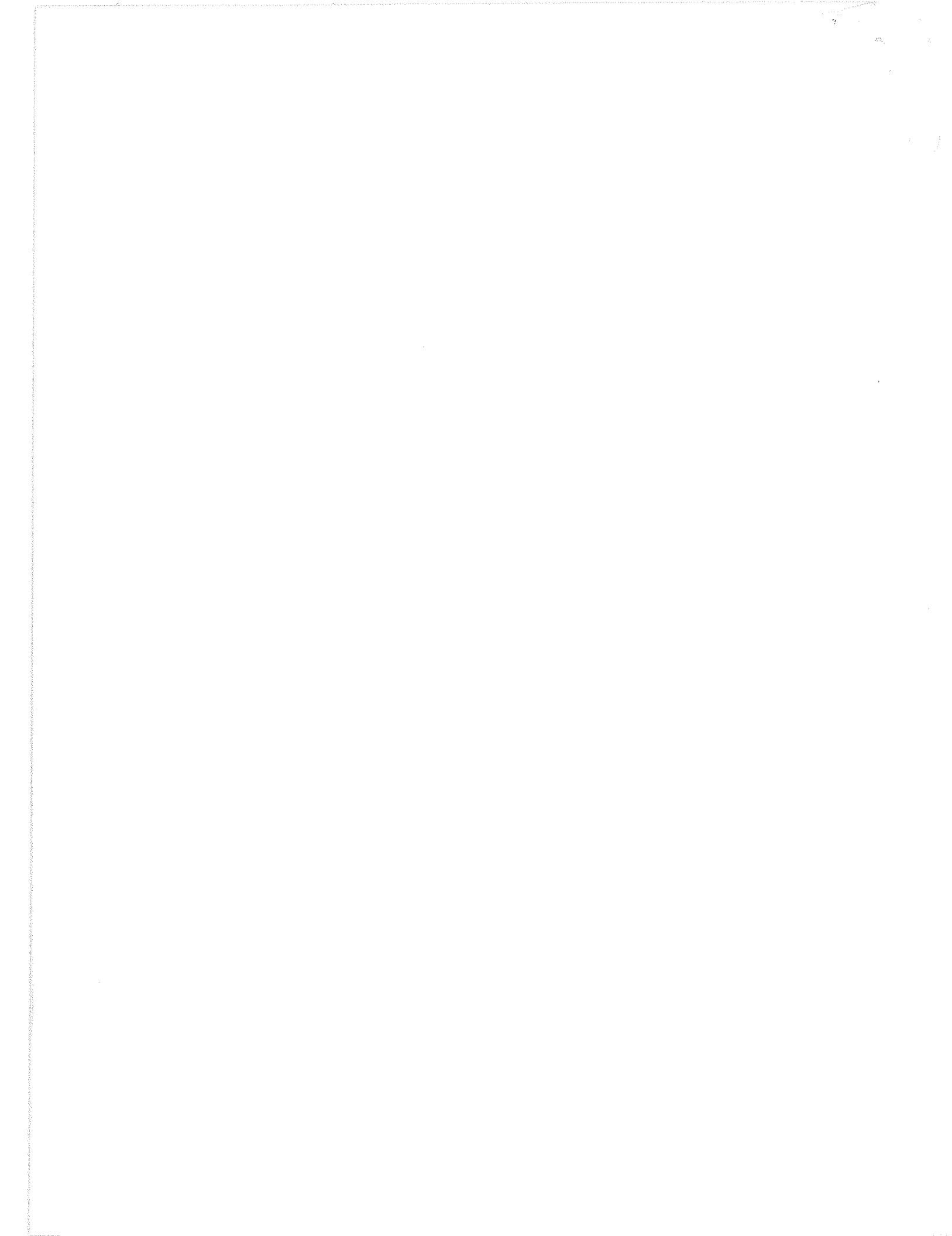


IDENTIFICATION

Product Code: MAINDEC-9A-D2BA-D
Product Name: PDP-9 TTY Test
Date Created: May 1, 1967
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Author: J. W. Richardson



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1. ABSTRACT

The PDP-9 TTY Test is designed to verify the operational status of a KSR 33 or KSR 35 Teletype and associated control logic.

The program is divided into two separate parts. Part 1 tests the teleprinter control logic, the complete character set, carriage return, line feed, space, right hand margins, and a mechanical worst case. The mechanical worst-case test provides a pattern for either a Model 33 or 35 KSR. The appropriate pattern is selected by the operator with an AC switch.

Part 2 of the program tests the keyboard input control logic, character input/output, and ability to interrupt the teleprinter from the keyboard without losing the input character.

2. REQUIREMENTS

2.1 Equipment

A standard PDP-9 equipped with a Model 33 or 35 KSR Teletype.

2.2 Storage

The program requires 4510 words of core memory. Locations 00000 through 04510 are used.

2.3 Preliminary Programs

(None required.)

3. LOADING PROCEDURE

3.1 Method

The paper tape supplied is punched in HRI mode.

Place the HRI tape in the reader.

Set the ADDRESS SWITCHES to 00000.

Press I/O RESET, and then READ-IN.

At the completion of loading, the PC should equal 4465 and the MB 740040.

4. STARTING PROCEDURES

4.1 Control Switch Settings

Part 1 - ACS 2 up if a KSR 35 is being used. All other ACS down.

Part 2 - All ACS down.

4.2 Starting Addresses

Part 1 - 100

Part 2 - 2500

Starting addresses for individual tests may be found in table 2 appearing at the end of this document.

4.3 Program and Operator Action**4.3.1 Part 1 - Teleprinter Tests -**

Set the ADDRESS SWITCHES to 100.

Place ACS 2 up if the PDP-9 being used is equipped with a Model 35 KSR.

Press I/O RESET, and then START.

Part 1 consists of seven tests, of which the first five test the teleprinter control logic. As one test is completed, the next is automatically started. The Mechanical Worst Case test is the last test performed in part 1, and will run until manually stopped by the operator. This portion of test 7 also provides variable speed control of the teleprinter using ACS 3 through 17. See paragraph 8.2, Applications.

4.3.2 Part 2 - Keyboard Tests -

Set the ADDRESS SWITCHES to 2500.

Place all ACS down.

Press I/O RESET, and then START.

Part 2 consists of six tests, of which the first four test the keyboard input control logic. As one test is completed, the next is automatically started. Tests 5 and 6 must be started by the operator. At the completion of test 4 a halt will occur (unless looping on tests 2 through 4) with the PC = 3221 (PRHLT2). Pressing CONTINUE will provide a header for test 5. The header may be omitted by starting test 5 from location 3300 (KTS5).

Test 6 may be started at location 3374 or 3400. Starting at location 3374 will provide a header with operating instructions. The header and instructions may be omitted by starting from location 3300 (KTS5).

5. OPERATING PROCEDURE**5.1 Operational Switch Settings****5.1.1 Part 1 - Teleprinter Control Logic Tests 1-5 -**

ACS 1 (1) - 'Scope mode for current test.

ACS 2 (1) - Loop on tests 2-5.

5.1.2 Part 1 - Teleprinter Tests 6 and 7 - 7K -

ACS 0 (1) - Halt current test at location 2076.

ACS 1 (1) - Loop on current test.

ACS 2 (0) - Model 33 KSR being used.

(1) - Model 35 KSR being used.

ACS 2 is used for test 7K, Mechanical Worst Case, only.

ACS 9 (1) - Print character in ACS 10-17. Start from location 600.

- For 7G, repeat a specific line length.
- For 7H, repeat a specific number of line feeds.
- For 7I, repeat a specific number of spaces.

ACS 3-17 - Used with test 7K (Mechanical Worst Case) for variable printing speed control.

Refer to section 8.2 (Applications) for detailed operating instructions for any of the above ACS functions.

5.1.3 Part 2 - Keyboard Control Logic Tests 1-4

ACS 1 (1) - 'Scope mode for current test.

ACS 2 (1) - Loop on tests 2-4.

5.1.4 Part 2 - Keyboard Input Tests 5 and 6

ACS 0 (1) - Halt current test at location 2076.

ACS 1 (1) - Simulate keyboard buffer character shift with character specified in ACS 10-17.
(See section 8.2.5).

5.2 Subroutine Abstracts - Part 1

5.2.1 Test 1 - Illegal Instruction - Test 1 first issues a CAF instruction, clears the AC, and then attempts to execute an IOT of 700400. The program then stalls for approximately 150 msec and then tests for the teleprinter flag set. The flag should be clear, and if not, an error halt occurs with the PC = 122 (E01). Press CONTINUE to re-execute the test. Test 1 is repeated 7 times. If no error occurs test 2 is executed.

5.2.2 Test 2 - Test For Flag Off - Test 2 makes sure the teleprinter flag is clear before going on to tests 3, 4, and 5. A 150 msec stall is first done, followed by a test of the teleprinter flag. If the flag has set, an error halt will occur with the PC = 205 (E02). Press CONTINUE to re-execute the test. If no error, test 3 is executed.

5.2.3 Test 3 - Set Flag With TLS - Test 3 attempts to set the teleprinter flag using the IOT TLS. A carriage return character is used for output. A TLS is first executed, followed by a 150 msec stall, after

which the teleprinter flag is tested. If the flag is not set an error halt occurs with the PC = 316 (E03). Press CONTINUE to re-execute the test. Test 3 is repeated 13 times. If no error occurs test 4 is executed.

5.2.4 Test 4 - Clear the Flag with TLS and TCF - The teleprinter flag is first set using TLS and a carriage return character for output. After the flag is set another TLS is immediately executed.

The teleprinter flag should be cleared by the second TLS. If the test is successful, the flag is again set using the same procedure as before, and then an attempt to clear the flag with TCF is executed. If successful, test 5 is executed.

If the teleprinter flag cannot be cleared using TLS, an error halt occurs with PC = 411 (E04). If TCF will not clear the flag, an error halt occurs with PC = 426 (E04A). With either error halt, press CONTINUE to re-execute the test which failed.

5.2.5 Test 5 - Interrupt Test - Test 5 first issues a CLOF IOT followed by a CAF. The program interrupt is then enabled (ION) and a 150 msec stall is done. No interrupt should occur. If an interrupt does occur, the program will determine if it was caused by the teleprinter, or by some other external device. If another device caused the interrupt, a halt occurs with PC = 536 (E05A). If the teleprinter caused the interrupt, a halt occurs with PC = 540 (E05B). At either error halt the AC will contain the I/O status word. Press CONTINUE to re-execute this portion of test 5.

If no illegal interrupt occurs, a carriage return character is used as output to cause an interrupt. After output, a 150 msec stall is done. If no interrupt occurs within this period an error halt occurs with PC = 522 (E05). Press CONTINUE to re-execute this portion of test 5.

If no errors occur test 6 is executed.

5.2.6 Test 6 - Basic Test of Complete Character Set - Test 6 prints the alphabet, digits, and punctuation on one line. Four lines are printed and then test 7 is executed.

The sequence of characters is the same for either the Model 33 or 35 Teleprinter as follows.

ABCDEFHIJKLMNOPQRSTUVWXYZ0123456789! "#\$%&!*+, -./@:;<=> ? [] t←BELL CR LF

A single character may be continuously printed by placing ACS 9 up and the desired character in ACS 10-17. Start the program from location 600. A full line of one character will be repeated, with an automatic carriage return/line feed at the end of each line. The routine may be halted by placing ACS 0 up. ACS 10-17 may be changed while the program is running.

The operator may continuously loop on test 6 by placing ACS 1 up, any time before the four lines are completed, or by restarting from location 600 with ACS 1 up.

ACS 0 in the up position will immediately halt test 6, looping or not.

5.2.7 Test 7 - Test Pairs of Characters - Test 7 tests pairs of characters starting with the alphabet, numbers, and punctuation marks. After testing the character string, tests are made on carriage return, line feed, space, right hand margins, and mechanical worst case.

Test 7 divides the character string into six groups of tests, mainly for the convenience of restarting the program at a point which tests a particular group. The six groups are designated as test 7, 7B, 7C, 7D, 7E, and 7F. Tests 7 through 7E print three pairs of characters per line, printing each pair eight times. Three lines are printed before going on to the next test. Test 7F is similar with the exception that only two pairs are printed. The format will appear as in the example below.

```
ABABABABABABABABCDCDCDCDCDCDEFEFEFEFEFEFEF
ABABABABABABABABCDCDCDCDCDCDEFEFEFEFEFEF
ABABABABABABABABCDCDCDCDCDCDEFEFEFEFEFEF
```

Each of the six groups is preceded by a short header identifying the particular group. The header will be the figure 7, followed by a letter which identifies the group.

The six groups and the pairs they test are as follows:

Test 7 - Alphabet and Numbers Set - A-Z and 0-9.

7B - ! " # \$ % &

7C - ' () * + ,

7D - - . / @ : ;

7E - < = > ? [

7F -] ↑ ← BELL

Any pair of characters may be printed by manually placing the two characters into location 1012 (tagged TEMP), and placing ACS 9 up. The specific characters will be printed, until stopped by placing ACS 0 up. Placing ACS 9 down, while the specific pair is being printed, will cause the program to start printing where it left off before ACS 9 was raised. See section 8.2.7 for detailed operating instructions.

Placing ACS 1 up at any time will allow the program to continuously print the three pairs of current characters. See the table of restarting addresses appearing at the end of this document.

Immediately after the completion of 7F, 7G is executed. Test 7G tests the carriage return function for bounce, and for binding. The test starts by printing a space, followed by a line of 71 O's. At the end of the line two carriage returns are issued. After the second carriage return an X is printed. The X should fall directly in the space at the beginning of the line. This procedure is repeated 36 times, shortening the length of each line by 2 characters. By observing the left hand margins, the operator may easily detect any variation in spacing between the X and first O of each line. There should be no overprinting.

Placing ACS 0 up will halt the test immediately.

Placing ACS 1 up will cause the entire test to be repeated.

Manually depositing the complement of a desired line length into location 1133, and then restarting 7G with ACS 9 up, will allow a specific line length to be continuously repeated. See section 8.2.8 for detailed operating instructions.

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Test 7H is automatically executed. Test 7H tests the line feed functions of the teleprinter. The main function tested for is the ability of the roller to lock firmly into place after each line feed. Roll-back, or double spacing spacing will be apparent with the pattern provided.

Sequences of from 1 to 9 line feeds are given, with a number (1 to 9) being first printed, along with a stall of approximately 1-sec, then the sequence of line feeds. Between each line feed a slash is printed. Any variation of spacing between the slashes indicates some adjustment is necessary to correct the variation.

The 1-sec stall is included to give the operator time to examine the number printed before the sequence begins.

ACS 0 placed up will halt the test after completion of the current line feed sequence.

ACS 1 up will allow the complete test to loop continuously.

By placing the octal equivalent of the desired number of line feeds into location 1013, and placing ACS 9 up, a specific number of line feeds may be continuously tested. See section 8.2.9 for detailed operating instructions.

Test 7I is executed next. This is a test of the space functions. One line consisting of alternate spaces and O's is printed, followed by a carriage return. An X is printed in the first space, followed by two carriage returns. A count for the spacing is incremented by two. The teleprinter then spaces according to this count, and prints an X followed by two carriage returns. This sequence is repeated until the line is composed of alternate X and O's. Any overprint indicates a spacing error. Only one line is printed.

Placing ACS 0 up will halt the program.

Placing ACS 1 up will allow the complete test to be repeated continuously.

Placing ACS 9 up, and the octal equivalent of any desired number of spaces in location 1012, will allow the specific number to be repeated on one line. See section 8.2.10 for detailed operating instructions.

The next test executed is test 7J. This test makes sure the right hand margin is set for a line length of 72 characters.

The program first spaces 69 positions. Four characters are then printed, in order, = 0 / . If the right hand margin is set correctly, the first slash will be printed in position 72 with the second slash over-printing to form a X, i.e., = OX. No overprint of the slashes indicates a right margin adjusted for greater than 72 characters. Any overprint other than the slashes indicates a right margin adjusted for less than 72 characters.

Two lines are printed before going on to test 7K.

ACS 0 up will halt the program after completing one line.

ACS 1 up will allow the test to loop continuously.

ACS 9 is not used with test 7J.

Test 7K is performed next, and is the last test performed on the teleprinter. ACS 2 should be set, to indicate whether a KSR 33 or 35 is being used, before test 7K is executed. The mechanical worst case for the KSR 33 or KSR 35 consists of a group of four characters repeated continuously in a fixed sequence for three full lines, and then a reverse sequence for another three lines. The test should be allowed to run for no less than twelve lines.

The pattern for a KSR 33 consists of the characters ' ~ W / . This pattern produces maximum rotation and vertical movement of the type-wheel.

The pattern for a KSR 35 consists of the characters ' [? C . This pattern produces maximum horizontal and vertical movement of the type-box.

The type of errors to be expected with either printer is that of character spacing.

ACS 0 up will cause a program halt at the end of the current line.

ACS 3-17 will provide variable printing-speed control. Any combination of these ACS may be set while the program is running. All of the ACS up gives the slowest speed, and all down gives normal speed.

5.2.8 Part 2 - Keyboard Control Logic Tests 1-4 - Each of the control logic tests is preceded by a printout of a number to identify the test. Tests 2 through 4 require the operator to input any one character from the keyboard. This is done immediately following the message PRESS A KEY, during tests 2,3, and 4. After test 4 is completed successfully the message, OK, is printed, followed by a halt with PC = 3221 (PRHLT2). This message is omitted if the program is looping on tests 2-4.

5.2.9 Test 1 - Illegal Instruction - The header, 1, is printed, followed by CAF IOT. The AC is cleared, and an IOT of 700300 is issued. This IOT should not cause any change in the contents of the AC, nor should it effect the keyboard flag. If no errors are detected, test 2 is executed.

If the contents of the AC are changed an error halt occurs with PC = 2522 (KE01).

If the keyboard flag is set an error halt occurs with PC = 2517 (KE01A)

5.2.10 Test 2 - Set Keyboard Flag - The header, 2, is printed followed by the message, PRESS A KEY, after which the program immediately begins a 5 sec timing loop to wait for input. Test 3 is immediately executed after the keyboard flag is set.

If the keyboard flag is not set within the 5 sec period, an error halt occurs with PC = 2573 (KE02).

5.2.11 Test 3 - Clear Flag with KRB - The header, 3, is printed followed by the message, PRESS A KEY. The program will wait in a loop until the keyboard flag is set by an input. As soon as the flag is set, the input buffer is read using the KRB IOT. This instruction should clear the flag immediately. If the flag has been cleared, test 4 is executed.

If the keyboard flag is not clear following a KRB, an error halt occurs with PC = 3120 (KE03).

5.2.12 Test 4 - Clear Buffer to Ones with CAF - The header, 4, is printed followed by the message, PRESS A KEY. The program then issues a CAF instruction, followed by a KRB. The contents of the input buffer must equal 377. If successful, the program then loops, waiting for an input from the keyboard. When an input is detected, the buffer is read and another CAF instruction issued. The buffer is read a second time, and the contents should equal 377. If both tests are successful, the message, OK, is printed and a halt occurs with PC = 3221 (PRHLT2). Press CONTINUE to receive the header for test 5, or start test 5 from location 3300 to omit the header.

If the buffer contents do not equal 377 after either of the two CAF instructions, an error halt occurs with PC = 3202 (KEO4)

5.2.13 Test 5 - Keyboard Character Input Test - Test 5 provides the operator with means for testing all keyboard characters, plus checking the character shift functions of the input buffer at the same time.

Immediately following the start of test 5, the program enters a 2 sec timing loop to wait for input. As soon as an input is detected, the input character is stored and the input buffer is continuously read for approximately 150 msec to see if the buffer contents continue shifting after the flag has been set. If all is in order, the character read, plus a space, is printed and the 2-sec timing loop is again initiated. See the example below which shows the resulting format.

An automatic carriage return, line feed is issued after the operator has typed 22 characters.

If the program does not detect an input after 2 secs the message, NO INPUT, is printed. The 2-sec loop is again initiated and a new line started.

If the contents of the buffer continue shifting after the flag has been set, the message, CONTENTS OF BUFFER CONT'D SHIFTING AFTER FIRST READ. will be printed. The 2-sec loop is again entered and a new line started. If the operator double-strokes a key, the program will interpret this as an error and print the above message.

Example of format:

AA BB CC DD EE FF GG HH II JJ KK LL MM NN OO PP QQ RR SS TT UU VV

ACS 0 up will halt the program at the end of the 2-sec timing loop. Press CONTINUE to re-execute.

5.2.14 Test 6 - Keyboard Interrupt Test - Test 6 may be started from location 3374 to receive a header and instructions, or from location 3400 (KTS6) to omit the header and instructions.

The purpose of this test is to test the ability of the teletype control logic to accept an input from the keyboard while the teleprinter is operating. Program interrupt is enabled during this test.

The program continuously prints the message, PRESS A KEY, with interrupt enabled. When an input from the keyboard is detected, the program immediately stops printing, and waits for further input from the keyboard. The operator indicates end of keyboard input by pressing control D (EOT). The program will output the character(s) received. After the last character has been printed the program continues with the message, PRESS A KEY.

Only one or two characters from the keyboard are sufficient to perform this test. However, the entire upper 4K field of core memory is reserved for one input message. Input messages longer than one character are stored away with two characters per word. This provides a maximum message length of 8K characters. If the message length reaches 8K characters, the message, FULL INPUT BUFFER, will be printed, followed by a dump of the message. Regardless of the message length the program interrupt is enabled even when the input message is being printed.

ACS 0 up will halt the test immediately. Press CONTINUE to re-execute.

5.3 Operating Instructions

- a. Place the HRI tape in the reader.
- b. Press I/O RESET, and then READ-IN.
- c. The program will halt with PC = 4465 (PRHLT4), and the MB = 740040.

5.3.1 Part 1 - Teleprinter Tests -

- a. Set the ADDRESS SWITCHES to 100.
- b. If control logic tests 2-5 are to be looped, raise ACS 2. If not, go on to step c.
- c. Press I/O RESET, and then START.
- d. When test 7K (Mechanical Worst Case) is reached, the program will run continuously until stopped by the operator.

5.3.2 Part 2 - Keyboard Input Tests

- a. Set the ADDRESS SWITCHES to 2500.
- b. If control logic tests 2-4 are to be looped, place ACS 2 up. If not, go on to step c.
- c. Press I/O RESET, and then START.
- d. If not looping on tests 2-4, OK will be printed, followed by a halt with PC = 3321 (PRHLT2). Press CONTINUE to receive test 5 header, or set the ADDRESS SWITCHES to 3300 (KTS5), press I/O RESET, and then START.
- e. During test 5, type in all keyboard characters. When satisfied that the input logic and keyboard are operating correctly, go on to f.
- f. Set the ADDRESS SWITCHES to 3374 to receive test 6 header and instructions, or to 3400 (KTS6) to omit header and instructions.

- g. Press I/O RESET, and then START.
- h. The message, PRESS A KEY, will be continuously printed.
- i. Press any key and wait for the program to halt. The output message will be garbled after the key is pressed.
- j. To verify that the input character was properly received, press CTRL D keys. The character inputted will be printed followed by the message, PRESS A KEY, being resumed.
- k. Test 6 is the last test performed by the program.

6. ERRORS

6.1 Error Halts and Description

All program halts and their descriptions are listed in Table 1 which appears at the end of this document. The modules listed for each error halt were chosen as being the most likely at fault.

6.2 Error Recovery

Pressing CONTINUE after an error halt will cause the program to re-execute the test which failed.

Placing ACS 1 up before pressing CONTINUE will enable 'scope mode for the failing test.

Recovery from program halts, either at the end of a test or as a result of ACS 0 up, may be done by pressing CONTINUE. The program will continue on in sequence.

7. RESTRICTIONS

7.1 Starting Restrictions

(None)

7.2 Operating Restrictions

Both parts 1 and 2 enable program interrupt at certain times. Because of this, the user must make sure that any external devices which may cause unwanted interrupts are turned off.

8. MISCELLANEOUS

8.1 Execution Time

Part 1 - When looping on test 2-5 the required time per pass is approximately 8 seconds.

The teleprinter tests 7 through 7J require approximately 10-1/2 minutes.

Part 2 - If looping on tests 2-4 the execution time is approximately 10 to 15 seconds, depending on the rate which input characters are received.

No time can be computed for tests 5 and 6 since manual operation is required.

8.2 Applications

8.2.1 'Scope Mode - Parts 1 and 2 Control Logic Tests - 'Scope mode for any of the keyboard or teleprinter control logic tests may be entered by placing ACS 1 up, and pressing CONTINUE after an error halt.

Any control logic test may be restarted with ACS 1 up to initiate 'scope mode. The restarting addresses are listed below.

Part 1 - Tests 1-5

<u>Test</u>	<u>Starting Address</u>
1	100
2	200
3	300
4	400
5	500

Part 2 - Tests 1-4

<u>Test</u>	<u>Starting Address</u>
1	2500
2	2550
3	3100
4	3150

8.2.2 "Fast" 'Scope Mode Loop - A short loop is provided which accepts input from the keyboard. The loop may be halted by placing ACS 0 up. The halt will occur after the next input character is received. The PC will equal 2076, and the AC will contain the last character received.

Set the ADDRESS SWITCHES to 4200.

Press I/O RESET and then START.

A CAF instruction is issued at the beginning of the loop.

8.2.3 Print-out of Buffer Contents After Each Shift - If 'scoping the keyboard logic does not provide sufficient information for debugging, a routine is provided which will print the contents of the input buffer after each TTI clock shift time.

The routine may be used to test a single character suspected of malfunctioning, the buffer itself, to make certain the contents stop shifting properly, or the contents of the buffer with no input.

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Set the ADDRESS SWITCHES to 3700, and all ACS down.

Press I/O RESET and then START.

The header shown below will be printed, and the routine immediately enters a 5-sec timing loop to wait for input.

I	0	1	2	3	4	5	6	7	8	9
---	---	---	---	---	---	---	---	---	---	---

Where: I = Initial contents of buffer before input.

This value should always = 377.

0 = Contents of buffer after first shift.

This value should always = 177, as a result of the start pulse entering the buffer.

1 through 8 = Will vary according to the character typed. However, column 1 should equal one of two values; either 077, or 277. Column 8 will equal the octal value of the character typed.

9 = The contents of the buffer after the shifting should have stopped. The octal value under 9 must always equal the value under 8. If not, the contents continued shifting when they should have stopped. The value printed is the value as read from the buffer at the end of the 5 sec period.

An example of the result of the character A being typed is shown below.

I	0	1	2	3	4	5	6	7	8	9
---	---	---	---	---	---	---	---	---	---	---

A

377 177 277 137 057 027 013 005 202 301 301

If no key is pressed before the 5 sec period is timed out, the value in each column should equal 377. This may be valuable if the content of the buffer logic is suspected of changing intermittently.

Immediately after an input is detected the 5 sec timing loop is re-initiated. The information printed will therefore occur 5 sec after an input.

If a key is double-struck the printout occurs immediately, and the information will be erroneous.

A halt occurs after the information is printed with PC = 4037 (PRHLT3). Press CONTINUE to re-execute the routine.

8.2.4 Simulation of Characters - The user may simulate any character by placing ACS 1 up, and the octal equivalent of the desired character in ACS 10 - 17. Start from location 3700, or press CONTINUE if halted at location 4037 (PRHLT3). The results printed will appear in the format shown in the example above. A halt occurs with PC = 4037 (PRHLT3) after simulation is completed. Unless another simulation is desired, place ACS 1 down before pressing CONTINUE to re-execute the program.

8.2.5 Output Printing Speed in msec -

- a. Set the ADDRESS SWITCHES to 1700.
- b. Press I/O RESET and then START.
- c. A carriage return is used for output.
- d. The values printed should be near 110 msec.

Upon completion of loading the program, a routine is executed which determines whether the PDP-9 being used has a 1.0 or 1.2 μ sec memory cycle time. The accuracy of the printed times in msec is directly dependent on the accuracy of the clock being used. The accuracy of the loop itself is $\pm 8 \mu$ sec if a 1.0 μ sec cycle time is being used, and $\pm 16 \mu$ sec if a 1.2 μ sec cycle time is used.

ACS 0 placed up will halt the program with PC = 2076. Press CONTINUE to re-execute.

8.2.6 Tests 7 through 7F - Specific Pairs of Characters

- a. Set the ADDRESS SWITCHES to 1012.
- b. Place the first character to be printed in ACS 10-17. Place the second character in ACS 1-8.
- c. Press DEPOSIT (up).
- d. Place all ACS down, and then place ACS 9 up.
- e. Set the ADDRESS SWITCHES to 700.
- f. Press I/O RESET and then START.

The program will continuously print the selected pair. ACS 0 placed up will halt the program with PC = 2076 (PRHLT1).

8.2.7 Test 7G - Specific Line Length

- a. Set the ADDRESS SWITCHES to 1133. This location contains 777671.
- b. Place the complement of the octal equivalent of the desired line length in the ACS.
- c. Press DEPOSIT (up).
- d. Place all ACS down, and then place ACS 9 up.
- e. Set the ADDRESS SWITCHES to 1126.
- f. Press I/O RESET and then START.

The program will continuously perform the carriage return test with the specified line length. ACS 0 placed up will halt the program with PC = 2076 (PRHLT1).

Location 1133 must be restored to 777671, if normal operation is desired.

8.2.8 Test 7H - Specific Number of Line Feeds

- a. Set the ADDRESS SWITCHES to 1013.
- b. Place in the ACS the octal equivalent of the desired number of line feed.

- c. Press DEPOSIT (up).
- d. Place ACS down, and then ACS 9 up.
- e. Set the ADDRESS SWITCHES to 1202.
- f. Press I/O RESET and then START.

The program will continuously print the desired number of line feeds, with a 1-sec (approximately) stall between sequences. The number preceding the first sequence will be 1, but no number will be printed thereafter, unless the test is restarted.

Placing ACS 0 up will halt the routine at the completion of one sequence. PC will = 2076 (PRHLT1).

8.2.9 Test 7I - Specific Number of Spaces -

- a. Set the ADDRESS SWITCHES to 1012.
- b. Place in the ACS the octal equivalent of the desired spacing on the line (including the position of X), minus 1. The positions are referenced to position 1, the first space before the first O.
- c. Press DEPOSIT (up).
- d. Place all ACS down, then place ACS 9 up.
- e. Set the ADDRESS SWITCHES to 1265.
- f. Press I/O RESET and then START.

ACS 0 may be placed up to halt the program. PC will equal 2076 (PRHLT1).

Press CONTINUE to re-execute.

8.2.10 Looping on Control Logic Tests

Part 1 - Test 2-5

- a. Set the ADDRESS SWITCHES to 200.
- b. Place ACS 2 up.
- c. Press I/O RESET and then START.

The program will loop continuously until an error is detected or until stopped with PROGRAM STOP.

Part 2 - Tests 2-4

- a. Set the ADDRESS SWITCHES to 2550.
- b. Place ACS 2 up.
- c. Press I/O RESET and then START.

The program will loop continuously until an error is detected, or until stopped with PROGRAM STOP.

8.2.11 Variable Printing Speed Control - ACS 3-12 provide the printing speed control during teleprinter test 7K (mechanical worst case). The ACS may be changed while the program is running. All ACS up will provide the slowest speed, and all down provide normal speed. ACS 0 may still be used to halt the test.

9. PROGRAM DESCRIPTION

The PDP-9 TTY Test is divided into two unique parts, part 1 begins by testing the teleprinter control logic. Provision is made to loop on four of the five control logic tests in order that the control logic may be tested with marginal power-supply voltages. If the control logic tests run without error, the remaining errors will be in the individual data paths. The rest of part 1 consists of various tests on the teleprinter itself. Any errors during this portion of part 1 must be detected by the operator while watching the printout.

Part 2 begins by testing the keyboard control logic. Provision is made to loop on three of the four control logic tests in order that the control logic may be tested with marginal power-supply voltages. The rest of part 2 consists of tests on the keyboard itself. The operator is required to provide inputs from the keyboard. The operator must watch the printed information to determine if any errors are present.

TABLE 1 ERROR HALTS AND DESCRIPTIONS

C(PC)	Tag	Test Number	Identification	Suggested Type	Module Location
122	E01	1	Teleprinter flag set after IOT 700400. If character "A" printed If no character printed	S603 S202	C39 D38
205	E02	2	Flag should not be set	S202	D38
316	E03	3	Flag did not set after TLS issued If teleprinter responded If no teleprinter response	R111 S202 S107 S205 S202 S205	C35 D38 C33 D32 D38 D37
411	E04	4	Flag did not clear after a TLS	R450 S202	C40 D38
426	E04A	4	Flag did not clear after TCF	S202	D38
522	E05	5	No interrupt occurred, waited 150 msec	R111	D39
536	E05A	5	Spurious interrupt, AC=I/O status Turn off device(s)		
540	E05B	5	Illegal flag from teleprinter	R111	D39

TABLE 1 ERROR HALTS AND DESCRIPTIONS (continued)

C(PC)	Tag	Test Number	Identification	Suggested Type	Module Location
552	E05C	5	Spurious interrupt, AC=I/O status Turn off device(s)		
2076	PRHLT1		Program halt as a result of ACS 0 up		
2517	KE01A	1	Keyboard flag set after IOT 700300.	S202	B38
2522	KE01	1	C(AC) changed after IOT 700300. Check wiring between C40-J and B38-T	S202	D40
2573	KE02	2	Keyboard flag not set, waited 5 seconds	S202 R111	B38 C35
3120	KE03	3	Keyboard flag did not clear after KRB	S202	B38
3201	KE04	4	Buffer did not clear to 377 after a CAF. AC = Contents of buffer	S202 S202 S202 S603	B34 B35 B36 B37 C39
3467	ILLGAL	6	Spurious interrupt. Turn off device(s)		
3743	ERR7		Keyboard flag should not be set. Press CONTINUE to ignore, or restart from location 3700.		
4037	PRHLT3		Program halt which occurs after contents of input buffer are printed. Press CONTINUE to re-execute.		
4465	PRHLT4		Program halt for routine which checks for 1.0 or 1.2 μ sec memory cycle time immediately after loading program.		

TABLE 2 RESTARTING ADDRESSES

Test No.	ADDRESS
<u>Part 1</u>	
1 - Illegal Instruction	100
2 - Test for Flag Off	200
3 - Set Flag With TLS	300
4 - Clear Flag With TLS and TCF	400
5 - Interrupt Test	500
6 - Character Set Test	600
7 - Alphabet and Numbers	700
7B - Test ! " # \$ % &	1020
7C - Test ^ () * + ,	1036
7D - Test = ./ @ : ;	1054
7E - Test < = > ? [\	1072
7F - Test] ← BELL	1110
7G - Test Carriage Return	1126
7H - Test Line Feed	1202
7I - Test Space	1265
7J - Test Right Margin	1362
7K - Mechanical Worst Case	1500
Print Output Clock Time	1700
<u>Part 2</u>	
1 - Illegal Instruction	2500
2 - Set Keyboard Flag	2550
3 - Clear Flag With KRB	3100
4 - Clear Buffer to Ones With CAF	3150
5 - Character Input Test	3300
6 - Interrupt Test	3374 (Header) 3400 (No Header)
Print Contents of Buffer After Shift	3700
"Fast" 'Scope Mode Loop	4200

MAINDEC-9A-D2BA-LA

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```

        .TITLE TTY-9
/PDP-9 TTY DIAGNOSTIC
/PART 1. TEST TELEPRINTER
/
/INTERRUPT ROUTINE
/
        .ARS
        .LOC 0
        @
        IORS
        LOC A3    HLت      /INTERRUPT ON ILLEGALLY
        /AC = I/O STATUS
/
/
/TEST 1. ILLEGAL INSTRUCTION
/
        .LOC 1A0
        TTST1    CAF
        CLOF
        LAW -7
        DAC 13
        LAW 301
        700400      /ILLEGAL INSTRUCTION
        JMS STL150
        TSF
        SKP
        JMP FLGERR   /FLAG = @
        JMS SCOPE
        ISZ 13
        JMP TTST1+4   /ERROR PATH
        JMS SCOPE
        JMP TTST2
        JMP TTST1+4   /SUCCESS PATH
        JMS SCOPE
        EXIT
        JMS SCOPE
        EJECT
        .EJECT

```

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00200

00200 102100
00201 700401
00202 600206
00203 102112
00204 740040
00205 600201
00206 102112
00207 600300
00210 600201

.LOC 200
/TEST 2. TEST FOR FLAG OFF
TTST2 JMS STL150 /WAIT 150 MS
TSF /FLAG SHOULD BE OFF
JMP OK2
JMS SCOPE /ERROR PATH
F02 HLT /TTY FLAG ON ILLEGALLY
OK2 JMP TTST2+1 /SCOPE
JMS SCOPF /CHECK FOR SCOPE MODE
JMP TTST3 /SUCCESS PATH
JMP TTST2+1 /SCOPE

/
/
/TEST 3. SET FLAG WITH TLS

/

.LOC 300

/

00300 777763
00301 040013
00302 760215
00303 700406
00304 102100
00305 700401
00306 600314
00307 440013
00310 600302
00311 102112
00312 600400
00313 600302
00314 102112
00315 740040
00316 600302

TTST3 LAW -15 /USE CARRIAGE RETURN FOR OUTPUT
DAC 13
LAW 215
TLS /WAIT 150 MS FOR FLAG
JMS STL150 /FLAG SHOULD BE SET
TSF /ERROR PATH
JMP ERR3
ISZ 13 /SUCCESS PATH
JMP TTST3+2 /EXIT
JMS SCOPE /SCOPE
JMP TTST4 /CHECK FOR SCOPE MODE
JMP TTST3+2 /FLAG NOT SET
ERR3 JMS SCOPE
E03 HLT
JMP TTST3+2 .EJECT

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/TEST 4. CLEAR THE FLAG WITH TLS AND TCF

/

A420 .LOC 410

/

AV420	TTST4	LAW 215	/SET FLAG TO START WITH
		TLS	
		TSF	/WAIT FOR FLAG
		JMP .-1	
		TLS	/TRY TO CLEAR FLAG
		TSF	
		JMP OK4	/FLAG CLEARED OK WITH TLS
		JMS SCOPE	
	E04	HLT	/FLAG NOT CLEARED IMMEDIATELY
		JMP TTST4	/AFTER TLS
	OK4	JMS SCOPE	/CHECK FOR SCOPE MODE
		SKP	
		JMP TTST4	/SCOPE
		LAW 215	
		TLS	/SET THE FLAG FOR TCF TEST
		TSF	/WAIT FOR FLAG
		JMP .-1	
		TCF	/TRY TO CLEAR
		TSF	
		JMP OK4A	/FLAG CLEARED OK WITH TCF
		JMS SCOPE	
	F04A	HLT	/TCF DID NOT CLEAR FLAG
		JMP OK4+3	/SCOPE
	OK4A	JMS SCOPE	/CHECK FOR SCOPE MODE
		JMP TTST5	/EXIT
		JMP OK4+3	/SCOPE
		.EJECT	

/TEST 5. INTERRUPT TEST

/ .LOC 500
/ TTST5 LAC JMP5
0V500 200523 DAC 1
0V501 040001 CLOF
0V502 700004 CAF
0V503 703302 LAW 215
0V504 760215 ION /TEST FOR ILLEGAL INT.
0V505 700042 JMS STL150 /WAIT 150 MS
0V506 102100 JMS SCOPE /SUCCESS PATH
0V507 102112 SKP
0V510 741000 JMP TTST5 /SCOPE
0V511 600500 LAC JMP6 /TEST TELEPRINTER INT
0V512 200524 DAC 1
0V513 040001 TPINT LAW 215
0V514 760215 ION /OUTPUT A CARRIAGE RETURN
0V515 700042 TLS /WAIT 150 MS
0V516 700406 JMS STL150 /ERROR PATH - NO INTERRUPT
0V517 102100 JMS SCOPE
0V520 102112 E05 HLT
0V521 740040 JMP TPINT /SCOPE
0V522 600514 .EJECT
/ JMP TLLINT
0V523 600525 JMP TSTFLG
0V524 600541 .EJECT

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/INTERRUPT SERVICE ROUTINES

/

W525	142112	TLLINT	JMS SCOPE	/CHECK FOR SCOPE MODE
W526	741707		SKP	
W527	644500		JMP TTSTS	/SCOPE
W530	744314		IORS	/READ STATUS
W531	544467		AND (020000)	
W532	7441200		SZA	
W533	620537		JMP F05B	/TEST FOR TELEPRINTER FLAG
W534	724314		IORS	/PRINTER FLAG ON ILLEGALLY
W535	744442	F05A	HLT	/ERROR FROM SPURIOUS INT
W536	644500		JMP TTSTS	/AC = I/O STATUS
W537	744440	F05B	HLT	
W540	644500		JMP TTSTS	/ILLEGAL FLAG FROM PRINTER
W541	142112	TSTFLG	JMS SCOPE	/CHECK FOR SCOPE MODE
W542	741707		SKP	
W543	644514		JMP TPINT	/SCOPE
W544	744314		IORS	/READ I/O STATUS
W545	544467		AND (020000)	
W546	744442		SZA	
W547	644503		JMP OKS	
W550	744314		IORS	
W551	744442	F05C	HLT	/SUCCESS PATH
W552	644514		JMP TPINT	
W553	244500	OKS	LAC F05C-1	/INTERRUPT FROM DEVICE OTHER
W554	844441		DAC 1	/THAN PRINTER. AC = I/O STATUS
W555	724402		IOP	/SUCCESS
W556	743302		CAF	
W557	750404		LAS	
W560	742410		RTL	
W561	741130		SPA	/CHECK FOR LOOP ON TESTS 2-5
W562	644500		JMP TTST2	/LOOP
W563	644500		JMP TST6	/ONLY EXIT
			EJECT	

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```

/TEST 6. BASIC TEST OF COMPLETE CHARACTER SFT
/BELL CR LF
/PLACE ACS9 UP AND ACS 10-17 SET TO A SPECIFIC
/CHARACTER IF DESIRED.  ACS1 SFT = LOOP ON TST
/
TST6      .LOC 620
          JMS CRLF
          LAW -4
          DAC LOOPX           /INITIALIZE LOOP COUNTER
          LAS
          SPA                  /CHECK FOR HALT
          JMS HLTST             /HALT
          AND (440              /CHECK FOR SPECIFIC CHAR
          SZA
          JMP PRSPEC            /SPECIFIC
          LAC DOTS6              /TOP OF CHAR TABLE MINUS 1
          DAC 12
          JMS PHDR
          LAS
          RAL
          SPA                  /CHECK FOR LOOP ON TEST
          JMP TST6+1             /LOOP
          ISZ LOOPX              /TEST FOR 4 LINES
          JMP TST6+3             /NOT 4 YET
          JMP TTST7              /EXIT
/
LOOPX     0
/
PRSPEC    LAW -110
          DAC LINE               /INITIALIZE LINE COUNT
          LAS
          SPA                  /CHECK FOR HALT
          JMS HLTST             /HALT
          LAS
          JMS TLSSF              /PRINT
          ISZ LINE               /CHECK FOR 72 CHARS
          JMP .-6
          JMS CRLF
          JMP PRSPEC             /CR, LF
          ,EJECT

```

```

A 7200 /TFST 7. TEST PAIRS OF CHARACTERS
A 7201 .LOC 740
A 7202 /
A 7203 TTST7 LAC JMSS
A 7204 DAC GETNXT
A 7205 LAC HDR7A
A 7206 DAC 12
A 7207 JMS PHDR
A 7208 LAC DUTS6
A 7209 DAC 11
A 7210 DAC TEMP+1
A 7211 LAS
A 7212 AND (440
A 7213 SZA
A 7214 SKP
A 7215 GETNXT JMS NEWRD
A 7216 LAW -3
A 7217 DAC CNTLNE
A 7218 JMS CRLF
A 7219 LAW -3
A 7220 DAC LINE
A 7221 LAW -1^
A 7222 DAC GROUP
A 7223 DAC TEMP
A 7224 PRINT2 JMS TLSSF
A 7225 JMS ROTAT9
A 7226 JMS TLSSF
A 7227 LAS
A 7228 SPA
A 7229 JMS HLTST
A 7230 ISZ GROUP
A 7231 JMP PRINT2
A 7232 ISZ LINE
A 7233 JMP NOT3G
A 7234 LAS
A 7235 AND (440
A 7236 SZA
A 7237 JMP GETNXT+3
A 7238 ISZ CNTLNE
A 7239 JMP NOT3L
A 7240 LAS
A 7241 RAL
A 7242 SPA
A 7243 JMP LOOP7
A 7244 LAC 11
A 7245 DAC TEMP+1
A 7246 JMS CRLF
A 7247 JMP GETNXT
A 7248 EJECT
A 7249 /PRINT TEST7 ALPHABET AND
A 7250 NUMBERS SET
A 7251 /CHAR TABLE POINTER
A 7252 /CHECK FOR SPECIFIC PAIR
A 7253 /SPECIFIC PAIR
A 7254 /COUNTS 3 LINES
A 7255 /CR,LF
A 7256 /COUNTS GROUPS PER LINE
A 7257 /COUNTS CHAR PER GROUP
A 7258 /PRINT FIRST
A 7259 /POSITION NEXT CHAR.
A 7260 /PRINT SECOND
A 7261 /CHECK FOR HALT
A 7262 /CHECK FOR 16 CHARS
A 7263 /NOT 16 YET
A 7264 /CHECK FOR 3 GROUPS
A 7265 /NOT 3 YET
A 7266 /CHECK FOR SPECIFIC PAIR
A 7267 /SPECIFIC
A 7268 /CHECK FOR 3 LINES
A 7269 /NOT 3 LINS YET
A 7270 /CHECK FOR LOOP ON TEST
A 7271 /REPEAT
A 7272 /UPDATE POINTER STORAGE
A 7273 /CR,LF
A 7274 /GET A NEW PAIR

```

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††††

AV755	201013	NOT3L	LAC TEMP+1 DAC 11 JMS NEWRD JMP GETNXT+3	/RESTORE POINTER /GET A NEW PAIR
AV756	040011			
AV757	100772			
AV760	600717			
AV761	777775	LOOP7	LAW -3 DAC CNTLNE JMP NOT3L	
AV762	041017			
AV763	600755			
AV764	750004	NOT3G	LAS AND (400	
AV765	504470		SZA	
AV766	7400200		JMP PRINT2-2	/CHECK FOR SPECIFIC PAIR
AV767	600722	JMSS	JMS NEWRD	/SPECIFIC PAIR
AV770	100772		JMP PRINT2-2	/GET A NEW PAIR
AV771	600722			
AV772	0000000	NEWRD	0 LAC* 11 SAD (212215	
AV773	220011		JMP CKLINE	/CHECK FOR END OF TABLE
AV774	544471		DAC TEMP	
AV775	600737		SAD (242241	
AV776	041012		SKP	
AV777	544472		JMP* NEWRD	/CHECK FOR START OF PUNCTUATION
01000	741000		LAC GETNXT	
01001	620772		SAD JMSS	/EXIT
01002	200714		SKP	
01003	540770		JMP* NEWRD	
01004	741000		LAC HDR7R	
01005	620772		DAC 12	
01006	202215		JMS PHDR	
01007	040012		JMP TS7B	/PRINT TEST PUNCTUATION SET
01010	102274			
01011	601020			
01012	0000000	TEMP	0	
01013	0000000		0	
01014	0000000		0	
01015	0000000	GROUP	0	
01016	0000000	LINE	0	
01017	0000000	CNTLNE	0	
			.EJECT	

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↑↑↑↑

/
/TEST 7B. TEST : " # \$ % &
/
A1020 102754 TS7B JMS CRLF /CR, LF
A1021 202236 LAC PR7B /7B
A1022 040712 DAC 12
A1023 102274 JMS PHDR
A1024 102754 JMS CRLF /CR,LF
A1025 202163 LAC PUNCT /CHAR TABLE POINTER
A1026 040711 DAC 11
A1027 041013 DAC TEMP+1
A1030 220711 LAC* 11 /GET FIRST PAIR
A1031 041012 DAC TEMP
A1032 201735 LAC RJMP1 /SET RETURN FOR 7C
A1033 040714 DAC GETNXT
A1034 600715 JMP GETNXT+1 /EXIT
/
A1035 601036 RJMP1 JMP TS7C
/
/
/TEST 7C. TEST ' () * + ,
/
A1036 102754 TS7C JMS CRLF /CR, LF
A1037 202241 LAC PR7C /7C
A1040 040712 DAC 12
A1041 102274 JMS PHDR
A1042 102754 JMS CRLF /CR,LF
A1043 202164 LAC PUNCT+1
A1044 040011 DAC 11
A1045 041013 DAC TEMP+1
A1046 220711 LAC* 11 /GET FIRST PAIR
A1047 041012 DAC TEMP
A1050 201753 LAC RJMP2 /SET RETURN FOR 7D
A1051 040714 DAC GETNXT
A1052 600715 JMP GETNXT+1
/
A1053 601054 RJMP2 JMP TS7D
.EJECT

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↑↑↑↑

/TEST 7D. TST - . / @ : ;

/

01054	102054	TS7D	JMS CRLF	/CR, LF
01055	202244		LAC PR7D	
01056	040012		DAC 12	
01057	102274		JMS PHDR	/PRINT 7D
01060	102054		JMS CRLF	/CR,LF
01061	202165		LAC PUNCT+2	
01062	040011		DAC 11	
01063	041013		DAC TEMP+1	
01064	220011		LAC* 11	/GET FIRST PAIR
01065	041012		DAC TEMP	
01066	201071		LAC RJMP3	/SET RETURN FOR 7E
01067	040714		DAC GETNXT	
01070	600715		JMP GETNXT+1	

/

01071	601072	RJMP3	JMP TS7E	
-------	--------	-------	----------	--

/

/

/TEST 7E. TEST < = > ? [\

/

01072	102054	TS7E	JMS CRLF	/CR, LF
01073	202247		LAC PR7E	
01074	040012		DAC 12	/PRINT 7F
01075	102274		JMS PHDR	
01076	102054		JMS CRLF	/CR,LF
01077	202166		LAC PUNCT+3	
01100	040011		DAC 11	
01101	041013		DAC TEMP+1	
01102	220011		LAC* 11	/GET FIRST PAIR
01103	041012		DAC TEMP	
01104	201107		LAC RJMP4	/SET RETURN FOR 7F
01105	040714		DAC GETNXT	
01106	600715		JMP GETNXT+1	

/

01107	601110	RJMP4	JMP TS7F	
			.EJECT	

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↑↑↑↑

/TFST 7F. TEST J + LEFT ARROW BELL

/

A1110	102154	TS7F	JMS CRLF	/CR, LF
A1111	202252		LAC PR7F	
A1112	040012		DAC 12	
A1113	102274		JMS PHDR	/PRINT 7F
A1114	102154		JMS CRLF	
A1115	202167		LAC PUNCT+4	
A1116	040011		DAC 11	
A1117	041013		DAC TEMP+1	
A1120	220011		LAC# 11	
A1121	041112		DAC TEMP	
A1122	201125		LAC RJMP5	/GET FIRST PAIR /SET RETURN FOR CR, LF TESTS
A1123	040714		DAC GETNXT	
A1124	600715		JMP GETNXT+1	

/

A1125	601126	RJMP5	JMP TS7G	
-------	--------	-------	----------	--

/

/

/TFST 7G. TEST CARRIAGE RETURN

/

A1126	102154	TS7G	JMS CRLF	/CR, LF
A1127	202255		LAC PR7G	
A1130	040012		DAC 12	
A1131	102274		JMS PHDR	/PRINT 7G
A1132	102154		JMS CRLF	
A1133	777671		LAW -1#7	/-71
A1134	041012		DAC TEMP	/INITIALIZE CHAR COUNTERS
A1135	041013		DAC TEMP+1	
A1136	760240	ANOTHER	LAW 24^	/SPACE
A1137	101175		JMS TLSF	
			.EJECT	

```

****

01140 760317 ANTHRO LAW 317 /0
01141 101175 JMS TLSSF
01142 750004 LAS
01143 741100 SPA
01144 102074 JMS HLTST
01145 441012 ISZ TEMP
01146 601140 JMP ANTHRO
01147 760215 LAW 215
01150 101175 JMS TLSSF
01151 101175 JMS TLSSF
01152 760330 LAW 330
01153 101175 JMS TLSSF
01154 750004 LAS
01155 504470 AND (400
01156 740200 SZA
01157 601132 JMP TS7G+4
01160 441013 ISZ TEMP+1
01161 741000 SKP
01162 601170 JMP .+6
01163 441013 ISZ TEMP+1
01164 201013 LAC TEMP+1
01165 041012 DAC TEMP
01166 102054 JMS CRLF
01167 601136 JMP ANOTH R
01170 750004 LAS
01171 740010 RAL
01172 741100 SPA
01173 601132 JMP TS7G+4
01174 601202 JMP TS7H
01175 000000 /
01176 700406 TLSSF 0
01177 700401 TLS
01200 601177 TSF
01201 621175 JMP .-1
                           JMP* TLSSF
                           .EJECT

```

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/TEST 7H, TEST LINE FEED

/

A1242	102054	TS7H	JMS CRLF	/CR, LF
V1243	202260		LAC Pn7H	
A1244	040012		DAC 12	
A1245	102274		JMS PHIR	/PRINT 7H
A1246	102154		JMS CRLF	
A1247	750004		LAS	
A1248	504470		AND C4 10	
A1249	740200		SZ#	
A1249	601224		JMP LNFEFD+5	/CHECK FOR SPECIFIC NUMBER
A1249	141112		DZM TEMP	/SPECIFIC
V1249	141113		DZM TEMP+1	/INITIALIZE COUNTERS
A1249	201264		LAC N260	
A1249	041114		DAC TEMP+2	
A1249	441114	LNFEED	ISZ TEMP+2	
V1249	201114		LAC TEMP+2	
A1249	544473		SAD C272	
A1249	501257		JMP LNPFLF	/CHECK LOOP ON TEST
A1249	1011175		JMS TLSSF	/PRINT NUMBER
A1249	777773		LAK -5	
A1249	041116		DAC LINE	
V1249	102100		JMS ST115H	
A1249	441116		ISZ LINE	
A1249	601226		JMP , -2	
A1249	201113		LAC TEMP+1	
A1249	740401		CMA	
A1249	441112		DAC TEMP	
V1249	102054		JMS CRLF	/CR,LF
A1249	441112	INCR	ISZ TEMP	/COUNT LINE FEEDS
A1249	601247		JMP FEED	
A1249	764215		LAK 215	
V1249	1011175		JMS TERSE	
A1249	750004		LAS	
V1249	50447A		AND C4 10	
A1249	740200		SZ#	
A1249	601224		JMP LNFEFD+5	/CHECK FOR SPECIFIC NUMBER
A1249	441113		ISZ TEMP+1	/SPECIFIC
A1249	601217		JMP LNFEFD	/INCREMENT COUNT
			.EJECT	/MAIN LOOP

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↑↑↑↑

01247	760334	FEED	LAW 334	
01250	101175		JMS TLSSF	/SLASH
01251	750004		LAS	
01252	741100		SPA	/CHECK FOR HALT
01253	102074		JMS HLTST	/HALT
01254	760212		LAW 212	/LINE FEED
01255	101175		JMS TLSSF	
01256	601235		JMP INCR	
01257	750004	LOOPLF	LAS	
01260	740010		RAL	
01261	741100		SPA	/CHECK FOR LOOP ON TEST
01262	601213		JMP LNFEED-4	
01263	601265		JMP TS7I	/TEST SPACE
01264	000260	/		
		N260	260	
			.EJECT	

/TEST 71. TEST SPACE

/

A1265	102054	TS7I	JMS CRLF	/CR,LF
A1266	202263		LAC PR7I	
A1267	040712		DAC 12	
A1270	102274		JMS PHDR	/PRINT 71
A1271	777777		LAW -1	
A1272	041016		DAC LINE	
A1273	102054		JMS CRLF	/INITIALIZE LINE COUNT
A1274	204474		LAC (31724)	/SPACE, 0
A1275	041013		DAC TEMP+1	
A1276	777735		LAW -43	/-36
A1277	041014		DAC TEMP+2	
A1300	201013	SPA0H	LAC TEMP+1	
A1301	101175		JMS TLSSF	/PRINT SPACE, 0
A1302	102765		JMS ROTAT9	/ROTATE 9 RIGHT
A1303	101175		JMS TLSSF	
A1304	750004		LAS	
A1305	741100		SPA	
A1306	102074		JMS HLTST	
A1307	441014		ISZ TEMP+2	/CHECK FOR 72
A1310	601300		JMP SPA0H	
A1311	760215		LAW 215	/DONE WITH SPACE, 1
A1312	101175		JMS TLSSF	/CARRIAGE RETURN
A1313	101175		JMS TLSSF	
A1314	750004		LAS	
A1315	504470		AND (4)0	
A1316	740200		SZA	/CHECK FOR SPECIFIC COUNT
A1317	741000		SKP	/SPECIFIC
A1320	141012		OZM TEMP	/INITIALIZE COUNTER
A1321	201012	RESTRT	LAC TEMP	
A1322	041361		SAP ONE10	/CHECK FOR 71 CHARS
A1323	601352		JMP LU0PSP	/CHECK FOR LOOP ON TEST
A1324	740001		CMA	
A1325	041017		DAC CNTLINE	
A1326	760330		LAW 334	/X
A1327	441017	SPACNT	ISZ CNTLINE	/PRINT X WHEN 0
A1330	601347		JMP SPACF	
A1331	101175		JMS TLSSF	/PRINT X
A1332	760215		LAW 215	
A1333	101175		JMS TLSSF	/CARRIAGE RETURN X 2
A1334	101175		JMS TLSSF	
A1335	750004		LAS	
A1336	741100		SPA	
A1337	102074		JMS HLTST	
A1340	750004		LAS	
A1341	504470		AND (4)0	
A1342	740200		SZA	/CHECK FOR SPECIFIC COUNT
A1343	601321		JMP RESTRT	/SPECIFIC
A1344	441012		ISZ TEMP	/INCR SPACE COUNT BY 2
A1345	441012		ISZ TEMP	
A1346	601321		JMP RESTRT	
			EJECT	

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††††

01347	760240	SPACE	LAW 240	/SPACE
01350	101175		JMS TLSSF	
01351	601326		JMP SPACNT-1	
01352	441016	LOOPSP	ISZ LINE	/CHECK FOR 1 LINE
01353	601273		JMP TS7I+6	/DO ANOTHER LINE
01354	750004		LAS	
01355	740010		RAL	/CHECK FOR LOOP ON TEST
01356	741100		SPA	
01357	601271		JMP TS7I+4	/LOOP
01360	601362		JMP TS7J	/TEST RIGHT MARGIN
01361	000110	/ ONE10	110 .EOT	

```

/PDP-9 TTY TEST - TAPE 2
/
/TFST 7J. TEST RIGHT MARGIN
/PRINTED DATA SHOULD EQUAL =0X FOR 72 CHAR. LINE
/ = 0 / \ EQUALS > 72, ANY OVERPRINT EQUALS < 72
/
A1362    102454      TS7J      JMS CRLF           /CR, LF
A1363    202266      LAC PR7J
A1364    040012      DAC 12
A1365    102274      JMS PHOR
A1366    102454      JMS CRLF           /PRINT 7J
A1367    777776      LAW -2
A1370    041V17      DAC CNTLNE        /INITIALIZE LINE COUNT
A1371    777673      SPAC69   LAW -1*5          /INITIALIZE SPACE COUNT
A1372    041M15      DAC GROUP
A1373    760240      LAW 24*
A1374    101175      JMS TLSSF
A1375    441M15      ISZ GRUP          /CHECK FOR 60 SPACES
A1376    601374      JMP .-2
A1377    760275      LAW 275
A1400    101175      JMS TLSSF
A1401    760317      LAW 317
A1402    101175      JMS TLSSF
A1403    760257      LAW 257
A1404    101175      JMS TLSSF
A1405    760334      LAW 334
A1406    101175      JMS TLSSF
A1407    102454      JMS CRLF           /CR, LF
A1410    441V17      ISZ CNTLNE        /CHECK FOR 2 LINES
A1411    601422      JMP CKHLT
A1412    750P04      LAS
A1413    740010      RAL
A1414    741100      SPA
A1415    671366      JMP TS7J+4        /CHECK FOR LOOP ON TEST
A1416    750P04      LAS
A1417    741100      SPA
A1420    102V74      JMS HALTST        /HALT
A1421    601500      JMP TS7K          /TEST MECH. WORST CASE
A1422    750P04      LAS
A1423    741100      SPA
A1424    102V74      JMS HALTST        /HALT
A1425    6V1371      JMP SPAC69        /NEW LINE
                           .EJECT
CKHLT

```

/TEST 7K. MECHANICAL WORST CASE
 /ACS2 UP FOR MODEL 35, DOWN FOR MODEL 33
 /AC3-17 CONTROL PRINTING SPEED
 /MODEL 33 WORST CASE IS ' LEFT ARROW W /
 /MODEL 35 IS ' [QUESTION MARK C

J	01426	004470		.SPACE 2
			/	
01500			.LOC 1500	
01500	102054	TS7K	JMS CRLF	/CR,LF
01501	202271		LAC PR7K	
01502	040012		DAC 12	
01503	102274		JMS PHDR	/PRINT 7K
01504	102054		JMS CRLF	
01505	750004		LAS	
01506	742010		RTL	
01507	741100		SPA	/CHECK FOR MODEL 33 OR 35
01510	601540		JMP MOOL35	/MODEL 35
01511	204475		LAC (337247	
01512	041012		DAC TEMP	/CHAR. STORAGE
01513	777775		LAW -3	/INITIALIZE LINE COUNT
01514	041017		DAC CNTLNE	
01515	777734	PRNT33	LAW -44	/INITIALIZE CHAR,COUNT
01516	041016		DAC LINE	
01517	750004		LAS	
01520	741100		SPA	/CHECK FOR HALT
01521	102074		JMS HLTST	/HALT
01522	101570		JMS PRNTWC	/PRINT 2 CHARS
01523	201012		LAC TEMP	
01524	244476		XOR (160160	
01525	041012		DAC TEMP	
01526	441016		ISZ LINE	/CHECK FOR 72 CHARS
01527	601522		JMP .-5	
01530	041012		DAC TEMP	/SAVE DATA
01531	102054		JMS CRLF	/CR,LF
01532	441017		ISZ CNTLNE	/CHECK FOR 3 LINES
01533	601515		JMP PRNT33	/NOT 3 YET
01534	201012		LAC TEMP	/REVERSE CHAR SEQUENCE
01535	244476		XOR (160160	
01536	041012		DAC TEMP	
01537	601513		JMP PRNT33-2	
			.EJECT	

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↑↑↑↑

V1540	204477	MODL35	LAC (333247	
V1541	041012		DAC TEMP	
V1542	777775		LAC -3	/INITIALIZE LINE COUNT
V1543	041017		DAC CNTLNE	
V1544	777734	PRNT35	LAC -44	/INITIALIZE CHAR COUNT
V1545	041016		DAC LINE	
V1546	750004		LAS	
V1547	741100		SPA	/CHECK FOR HALT
V1550	102074		JMS HLTST	/HALT
V1551	201012		LAC TEMP	
V1552	101570		JMS PRNTWC	/PRINT 2CHARS
V1553	201012		LAC TEMP	
V1554	244500		XOR (030030	
V1555	041012		DAC TEMP	
V1556	441016		ISZ LINE	/CHECK FOR 72 CHARS
V1557	601552		JMP .-5	/NOT 72 YET
V1560	041012		DAC TEMP	/SAVE DATA
V1561	102054		JMS CRLF	/CR,LF
V1562	441017		ISZ CNTLNE	/CHECK FOR 3 LINES
V1563	601544		JMP PRNT35	/NOT 3 YET
V1564	201012		LAC TEMP	
V1565	244500		XOR (030030	/REVERSE CHAR SEQUENCE
V1566	041012		DAC TEMP	
V1567	601542		JMP PRNT35-2	
/				
V1570	400000	PRNTWC	0	
V1571	750004		LAS	
V1572	504501		AND (77777	
V1573	740001		CMA	
V1574	041013		DAC TEMP+1	/SPEED CONTROL
V1575	441013		ISZ TEMP+1	
V1576	601575		JMP .-1	
V1577	201012		LAC TEMP	
V1600	700406		TLS	/FIRST CHAR
V1601	700401		TSF	
V1602	621601		JMP .-1	
V1603	102065		JMS ROTAT9	
V1604	041014		DAC TEMP+2	
V1605	750004		LAS	
V1606	504501		AND (77777	
V1607	740001		CMA	
V1610	041013		DAC TEMP+1	/SPEED CONTROL
V1611	441013		ISZ TEMP+1	
V1612	601611		JMP .-1	
V1613	201014		LAC TEMP+2	
V1614	700406		TLS	/SECOND CHARACTER
V1615	700401		TSF	
V1616	601615		JMP .-1	/EXIT
V1617	621570		JMP* PRNTWC	
			.EJECT	

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/CALCULATE TTY OUTPUT CLOCK TIME IN MSEC.

/

01700		.LOC 1700	
01700	102054	JMS CRLF	
01701	141016	DZM LINE	/CLEAR LOOP COUNTER
01702	760215	LAW 215	/USE CARRIAGE RETURN
01703	700406	TLS	/SELECT PRINTER
01704	700401	TSF	/START 8 USEC LOOP
01705	601707	JMP TIME	
01706	601711	JMP .+3	
01707	441016	TIME ISZ LINE	
01710	601704	JMP .-4	
01711	201016	LAC LINE	/CONTENTS OF LINE = NO. OF /8 USEC LOOPS BEFORE FLAG SET

01712	742010	RTL	
01713	740010	RAL	/MULTIPLY RESULT BY 8
01714	041016	DAC LINE	/TO GET TOTAL USEC.
01715	101720	JMS GODEC	/CONVERT TO DECIMAL
01716	101751	JMS OUTMS	
01717	601701	JMP TMEAGN	/LOOP

/

/

/BINARY TO DECIMAL ROUTINE

01720	000000	GODEC	0	
01721	142014	DZM PCW		/CLEAR PARTIAL CONVERSION WORD
01722	442004	ISZ TABLE		/INCR ADDRESSES
01723	442015	ISZ PWRTEM		
01724	201016	LAC LINE		/BINARY
01725	041012	DAC TEMP		/SAVE
01726	744000	SUBAGN	CLL	
01727	362015	TAD* PWRTEM		/SUBTRACT ONE POWER OF TEN
01730	740400	SNL		/DONE ONE POWER IF LINK = 0
01731	601735	JMP DONCON		
01732	041012	DAC TEMP		/SAVE RESULTS
01733	442014	ISZ PCW		/INCREMENT PARTIAL CONVERSION WORD
01734	601726	JMP SURAGN		/SUBTRACT AGAIN
01735	202014	DONCON	LAC PCW	
01736	341264		TAD N260	/MAKE ASCII
01737	062004		DAC* TABLE	/STORE
01740	201012		LAC TEMP	
01741	041016		DAC LINE	
01742	222015		LAC* PWRTEM	
01743	542022		SAD PWRTEM+5	/SEE IF CONVERSION COMPLETED
01744	741000		SKP	/DONE
01745	601721		JMP GODEC+1	/SUBTRACT NEXT POWER OF TEN
01746	202023		LAC PWRTEM+6	
01747	042015		DAC PWRTEM	
01750	621720		JMP* GODEC	
			.EJECT	/EXIT

		/PRINT OUTPUT TIME IN MSEC		
		/		
41751	000000	OUTMS	0	
41752	202012		LAC TABLE+6	
41753	042004		DAC TARLF	
41754	442004		ISZ TABLE	
41755	222004		LAC* TABLE	
41756	041012		DAC TEMP	
41757	542012		SAD TARLF+6	
41760	601773		JMP PRTMS	
41761	202004		LAC TARLF	
41762	542013		SAD TARLF+7	
41763	601767		JMP ,+4	
41764	201012		LAC TEMP	
41765	101175	OUTPRT	JMS TLSSF /PRINT	
41766	601754		JMP OUTMS+3	
41767	760256		LAW 256	
41770	101175		JMS TLSSF /PERIOD	
41771	201012		LAC TEMP	
41772	601765		JMP OUTPRT	
41773	042004	PRTMS	DAC TABLE	
41774	202024		LAC MSEC	
41775	040012		DAC 12	
41776	102274		JMS PHIR	/PRINT MSEC
41777	102254		JMS CRLF	
42000	750004		LAS	
42001	741100		SPA	/CHECK FOR HALT
42002	102074		JMS HLST	/HALT
42003	621751		JMP* OUTMS	
		/		
42004	002004	TABLE	.	
42005	000100		0	/100 K
42006	100100		0	/TEN K
42007	620000		0	/THOUSANDS
42010	000000		0	/HUNDREDS
42011	000000		0	/TENS
42012	002004		TABLE	
42013	702010		TABLE+4	
42014	600000	PCW	0	
		/		
42015	002015	PWRTE	.	
42016	474540		474540	/-100000 DECIMAL
42017	754360		754360	/-10000 DECIMAL
42020	776030		776030	/-1000 DECIMAL
42021	777634		777634	/-100 DECIMAL
42022	777766		777766	/-10 DECIMAL
42023	602015	PWRTE	.EJECT	

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††††

02024	002024	MSEC	.	
02025	315240		315240	
02026	305323		305323	
02027	256303		256303	
02030	000000		000000	
		/		
02031	760215	ALTIM	LAW 215	
02032	141016		DZM LINE	/CLEAR LOOP COUNTER
02033	700406		TLS	/START 16 US LOOP
02034	700401		TSF	
02035	741000		SKP	
02036	602044		JMP .+6	
02037	500000		AND 0	
02040	500000		AND 0	
02041	740000		NOP	
02042	441016		ISZ LINE	/CONTENTS OF LINE = NO. OF
02043	602034		JMP ALTIM+3	/16 US LOOPS BEFORE FLAG SET
02044	201016		LAC LINE	
02045	744000		CLL	
02046	742010		RTL	/MULT RESULT BY 16
02047	742010		RTL	
02050	041016		DAC LINE	
02051	101720		JMS GODEC	/CONVERT TO DECIMAL
02052	101751		JMS OUTMS	
02053	602031		JMP ALTIM	
		,EJECT		/REPEAT

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/CARRIAGE RETURN, LINE FEED
/
A2054 000000 CRLF 0
A2055 760215 LAW 215
A2056 700406 TLS
A2057 700401 TSF
A2060 602157 JMP .-1
A2061 542063 SAD .+2
A2062 622054 JMP* CRLF
A2063 760212 LAW 212
A2064 622056 JMP CRLF+2
/
/
/ROTATE 9 RIGHT
/
A2065 000000 ROTAT9 0
A2066 742020 RTR; RTR; RTR
A2067 742020
A2070 742020
A2071 742020 RTR; RAR
A2072 740120
A2073 622065 JMP* ROTAT9 /EXIT
/
/
/PROGRAM HALT FOR ALL SUR-TESTS
/
A2074 000000 HLTST 0
A2075 740040 PRHLT1 HLT
A2076 750104 LAS
A2077 622074 JMP* HLTST /CONTINUE
/
/
/150 MSEC STALL
/
A2100 000000 STL150 0
A2101 777750 LAW -3H
A2102 042065 DAC ROTAT9
A2103 774000 LAW -4000
A2104 042054 DAC CRLF
A2105 442054 ISZ CRLF
A2106 602105 JMP .-1
A2107 442065 ISZ ROTAT9
A2110 602103 JMP .-5
A2111 622100 JMP* STL150 /EXIT
/
/
/CHECK FOR SCOPE MODE
/
A2112 000100 SCOPE 0
A2113 750004 LAS
A2114 740010 RAL
A2115 741100 SPA
A2116 442112 ISZ SCOPF
A2117 622112 JMP* SCOPF /1 = SCOPE

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.EJECT

/CHARACTER TABLE FOR TEST 6

/

02120	AN2120	.			
02121	302301	302301;	304303;	306305	
02122	304303				
02123	306305				
02124	310307	310307;	312311;	314313	
02125	312311				
02126	314313				
02127	316315	316315;	320317;	322321	
02130	320317				
02131	322321				
02132	324323	324323;	326325;	330327	
02133	326325				
02134	330327				
02135	332331	332331;	261260;	263262	
02136	261260				
02137	263262				
02140	265264	265264;	267266;	271270	
02141	267266				
02142	271270				
02143	242241	/ TST7B	242241;	244243;	246245 /!"#\$%&
02144	244243				
02145	246245				
02146	250247	TST7C	250247;	252251;	254253 /'()*+,
02147	252251				
02150	254253				
02151	256255	TST7D	256255;	300257;	273272 /-.@:;
02152	300257				
02153	273272				
02154	275274	TST7E	275274;	277276;	334333 /<=>?[]
02155	277276				
02156	334333				
02157	336335	TST7F	336335;	207337;	212215 /]↑ LEFT ARROW, BELL, CR LF
02160	212215				
02161	212215				
02162	2000000				
02163	AN2142	/ PUNCT	TST7B-1		
02164	002145		TST7C-1		
02165	002150		TST7D-1		
02166	AN2153		TST7E-1		
02167	AN2156		TST7F-1		
02170	AN2170	/			
02171	212215	/			
02172	305324	/			
02173	324323	/			
02174	267240	/ HEADER 7A TEXT	212215;	305324;	324323
02175	240256	/			
02176	240256				

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02176	301240			
02177	320314	320314;	301310;	305302
02200	301310			
02201	305302			
02202	240324	240324;	316301;	240304
02203	316301			
02204	240304			
02205	325316	325316;	302315;	322305
02206	302315			
02207	322305			
02210	240323	240323;	305323;	240324
02211	305323			
02212	240324			
02213	212215	212215;	000000	
02214	000000			

/

/

/HEADER 7B TEST

/

02215	002215	HDR7B	.	
02216	212215		212215;	305324;
02217	305324			324323
02220	324323			
02221	267240		267240;	256302;
02222	256302			240240
02223	240240			
02224	325320		325320;	303316;
02225	303316			325324
02226	325324			
02227	324301		324301;	317311;
02230	317311			240316
02231	240316			
02232	305323		305323;	240324;
02233	240324			212215
02234	212215			
02235	000000		000000	

/

02236	002236	PR7B	.	
02237	302267		302267	/7B
02240	000000		000000	

/

02241	002241	PR7C	.	
02242	303267		303267	/7C
02243	000000		000000	
			.EJECT	

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42244 302244 PR7D . 304267 /7D
42245 304267 000000
42246 000000 /
42247 302247 PR7E . 305267 /7E
42250 305267 000000
42251 000000 /
42252 302252 PR7F . 306267 /7F
42253 306267 000000
42254 000000 /
42255 302255 PR7G . 307267 /7G
42256 307267 000000
42257 000000 /
42260 302260 PR7H . 310267 /7H
42261 310267 000000
42262 000000 /
42263 302263 PR7I . 311267 /7I
42264 311267 000000
42265 000000 /
42266 302266 PR7J . 312267 /7J
42267 312267 000000
42270 000000 /
42271 302271 PR7K . 313267 /7K
42272 313267 000000
42273 000000 /
/HEADER ROUTINE
/
42274 000000 PHER Ø
42275 223712 LAC# 10
42276 741200 SVA
42277 622274 JMP# PHDR
42300 1W1175 JMS TLSF
42301 102265 JMS ROTAT9
42302 1W1175 JMS TLSF
42303 622275 JMP PHDR+1
.END

```

/
/PDP-9 TTY DIAGNOSTIC - PART 2
/TEST KEYBOARD
/TEST 1. ILLEGAL INSTRUCTION
/
02500      .LOC 2500
02500      102054      KTS1      JMS CRLF
02501      760261      LAW 261
02502      101175      JMS TLSSF
02503      777770      LAW -10
02504      041012      DAC TEMP
02505      703302      CAF
02506      750000      CLA
02507      700300      700300
02510      740200      SZA
02511      602520      JMP ACVN
02512      102100      JMS STL150
02513      700301      KSF
02514      602523      JMP OKS1
02515      102112      JMS SCOPE
02516      740040      KEM1A   HLT
02517      602505      JMP KTS1+5
02520      102112      ACVN
02521      740040      KE01    HLT
02522      602505      JMP KTS1+5
02523      441012      OKS1    ISZ TEMP
02524      602505      JMP KTS1+5
02525      102112      JMS SCOPE
02526      602550      JMP KTS2
02527      602503      JMP KTS1+3
/
/TEST 2. SET KEYBOARD FLAG
/
02550      .LOC 2550
02550      102054      KTS2      JMS CRLF
02551      760262      LAW 262
02552      101175      JMS TLSSF
02553      102054      JMS CRLF
02554      204212      DAC PRSKEY
02555      040012      DAC 12
02556      102274      JMS PHDR
02557      102054      JMS CRLF
/
02560      703302      CKFLG   CAF
02561      777742      LAW -36
02562      041016      DAC LINE
02563      700301      KSF
02564      741000      SKP
02565      602574      JMP OKS2
02566      102100      JMS STL150
02567      441016      ISZ LINE
02570      602563      JMP -.5
02571      102112      JMS SCOPE
                           ,EJECT

```

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↑↑↑↑

A1572	740040	K002	HLT	/NO FLAG. WAITED 5 SEC
W2573	602560		JMP CKFLG	
02574	102112	OKS2	JMS SCOPE	/CHECK FOR SCOPE MODE
02575	603100		JMP KTS3	/EXIT
W2576	602560		JMP CKFLG	/SCOPE

/

/

/TEST 3. CLEAR FLAG WITH KRB

/

03100		.LOC 3100		
W3100	102054	KTS3	JMS CRLF	/CR,LF
W3101	760263		LAW 263	
A3102	101175		JMS TLSSF	/PRINT 3
W3103	102054		JMS CRLF	
A3104	204212		LAC PRSKEY	
A3105	040012		DAC 12	
A3106	102274		JMS PHDR	/PRINT PRESS A KEY
A3107	102054		JMS CRLF	
W3110	703302	CLRFL	CAF	/I/O POWER CLEAR
A3111	700301		KSF	
A3112	603111		JMP .-1	/WILL LOOP UNTIL FLAG SETS
W3113	700312		KRR	/READ, CLEAR FLAG
A3114	700301		KSF	/SHOULD NOT SKIP
W3115	603121		JMP OKS3	/SUCCESS PATH
A3116	102112		JMS SCOPE	
W3117	740040	K003	HLT	/ERROR. KRB DID NOT CLEAR FLAG
A3120	603116		JMP CLRFL	
A3121	102112	OKS3	JMS SCOPE	/CHECK FOR SCOPE MODE
A3122	603150		JMP KTS4	/EXIT
A3123	603110		JMP CLRFL	
			.EJECT	

/TEST 4. CLEAR BUFFER TO ONES WITH CAF

/

03150		.LOC 3150	
03150	102054	JMS CRLF	/CR,LF
03151	760264	LAW 264	
03152	101175	JMS TLSSF	/PRINT 4
03153	102054	JMS CRLF	
03154	703302	CAF	/I/O POWER CLEAR
03155	700312	KRB	
03156	544040	SAD TH77	/MUST = 377
03157	741000	SKP	
03160	603174	JMP ERR4	
03161	204212	LAC PRSKEY	
03162	040012	DAC 12	
03163	102274	JMS PHDR	/PRINT PRESS A KEY
03164	102054	JMS CRLF	
03165	700301	KSF	
03166	603165	JMP .-1	/WAIT FOR INPUT
03167	700312	KRB	/READ, CLEAR FLAG
03170	703302	CAF	/I/O POWER CLEAR
03171	700312	KRB	
03172	544040	SAD TH77	/MUST = 377
03173	603203	JMP OKS4	/SUCCESS PATH
03174	041012	DAC TEMP	
03175	102112	JMS SCOPE	/CHECK FOR SCOPE MODE
03176	741000	SKP	
03177	603165	JMP CLRBF	/SCOPE
03200	201012	LAC TEMP	
03201	740040	HLT	/AC = CONTENTS OF BUFFER
03202	603165	JMP CLRBF	
03203	102112	JMS SCOPE	/CHECK FOR SCOPE MODE
03204	741000	SKP	
03205	603165	JMP CLRBF	
03206	750004	LAS	
03207	742010	RTL	
03210	741100	SPA	/CHECK FOR LOOP ON TEST 2-4
03211	602550	JMP KTS2	/LOOP
03212	102054	JMS CRLF	
03213	760317	LAW 317	
03214	101175	JMS TLSSF	
03215	760313	LAW 313	
03216	101175	JMS TLSSF	
03217	102054	JMS CRLF	
03220	740040	PRHLT2 HLT	/END OF LOGIC TESTS /PRESS CONTINUE FOR /INSTRUCTIONS FOR TEST5 /OR SET ADDRESS SWITCHES TO 3300 /AND PRESS START /TO OMIT INSTRUCTIONS

.EJECT

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↑↑↑↑

03221	102054	JMS CRLF	/CR,LF
03222	204222	LAC HDR5	
03223	040012	DAC 12	
03224	102274	JMS PHIR	/PRINT HEADER AND INSTRUCTIONS
03225	102054	JMS CRLF	
03226	603300	JMP KTS5	
	/		
	/		
	/		
/TEST 5. KEYBOARD CHARACTER INPUT TEST			
/ACS 0 UP = HALT TEST			
	/		
03300		.LOC 3300	
03300	102054	KTS5	JMS CRLF /CR,LF
03301	777752		LAW -26
03302	041016		DAC LINE
03303	750004		LAS
03304	741100		SPA
03305	102074		JMS HLTST /CHECK FOR HALT
03306	141012		DZM TEMP /INITIALIZE TIMER
03307	700301	WAIT	KSF
03310	741000		SKP
03311	603317		JMP CHEKIN /GOT AN INPUT
03312	441012		ISZ TEMP
03313	603307		JMP WAIT
03314	102054		JMS CRLF /TIMFD OUT - WAITED 2 SECS
03315	103341		JMS NOTHN /PRINT NO INPUT
03316	603300		JMP KTS5
	/		
03317	204502	CHEKIN	LAC (-35230
03320	041013		DAC TEMP+1
03321	700312		KRR
03322	040772		DAC NEWRD
03323	700312	CHECLK	KRB
03324	540772		SAD NEWRD
			/READ BUFFER
03325	603330		/SAVE DATA
03326	102054		/READ AGAIN
03327	603354		/STALL AND CHECK THAT
03328	441013		/TTI CLOCK STOPS
03329	603323		
03330	200772	JMP .+3	
03331	101175	JMS CRLF	
03332	760240	JMP RDCLK	/TTI CLOCK DIDN'T STOP
03333	441013	ISZ TEMP+1	
03334	603303	JMP CHECKL	
03335	101175	LAC NEWRD	/PRINT CHARACTER REC'D
03336	441016	JMS TLSSF	/SPACE
03337	603300	LAW 240	
03340	603300	JMS TLSSF	
		JMS TLSSF	/CHECK FOR 63 CHARACTERS
		JMP KTS5+3	/GO TO TOP OF LOOP+3
		JMP KTS5	/TOP OF LOOP
		,EJECT	

††††

/PRINT NO INPUT
/
03341 0000000
03342 204256
03343 040012
03344 750004
03345 740100
03346 603351
03347 102074
03350 603300
03351 102274
03352 102054
03353 623341
NOTHN 0
LAC NOINP
DAC 12
LAS
SMA /CHECK FOR HALT
JMP ,+3
JMS HLTST /HALT
JMP KTS5
JMS PHDR /PRINT NO INPUT
JMS CRLF
JMP* NOTHN
/
/PRINT CONTENTS OF BUFFER CONT'D SHIFTING
/AFTER FIRST READ
/
03354 204264 RDCLK LAC NOSTOP
03355 040012 DAC 12 /PRINT INFORMATION
03356 102274 JMS PHDR
03357 102054 JMS CRLF
03360 703302 CAF
03361 603300 JMP KTS5
/
.EJECT

↑↑↑↑

/TFST 6. KEYBOARD INTERRUPT TEST
 /TYPE ANY LENGTH MESSAGE. END MESSAGE
 /WITH CTRL D. MESSAGE WILL BE PRINTED FOR
 /VERIFICATION
 /ACS 0 UP = HALT TEST
 /

03374		.LOC 3374	
03374	102054	JMS CRLF	
03375	204320	LAC HDK6	
03376	040012	DAC 12	
03377	102274	JMS PHDR	
03400	1N2V54	KTS6 JMS CRLF	
03401	770000	LAW -1V0000	
03402	041012	DAC TEMP	
03403	204447	LAC INRUF	
03404	040011	DAC 11	
03405	160011	DZM* 11	/CLEAR INPUT BUFFER
03406	441V12	ISZ TEMP	
03407	603405	JMP .-?	
03410	204447	LAC INRUF	
03411	040011	DAC 11	
03412	102V54	JMS CRLF	
03413	204503	LAC (ISZ 0	
03414	040001	DAC 1	
03415	204504	LAC (JMP* 0	
03416	040002	DAC 2	
03417	777772	LAW -6	
03420	041016	DAC LINE	
03421	204212	PHRASE LAC PRSKFY	
03422	040011	DAC 11	
03423	703302	CAF	
03424	103434	JMS TTYOUT	
03425	750204	LAS	
03426	741100	SPA	/CHECK FOR HALT TEST
03427	102974	JMS HLTST	/HALT
03430	441V16	ISZ LINE	/CHECK FOR : LINE
03431	603421	JMP PHRASE	
03432	1N2V54	JMS CRLF	/CR,LF
03433	603417	JMP PHRASE-2	
/			
03434	000000	TTYOUT 0	/ROUTINE FOR PRESS A KEY
03435	220011	LAC* 11	
03436	041012	DAC TEMP	
03437	741200	SNA	
03440	623434	JMP* TTYOUT	
03441	103446	JMS TLFS	
03442	201012	LAC TEMP	
03443	102065	JMS ROTAT9	
03444	103446	JMS TLFS	
03445	603435	JMP TTYOUT+1	
		.EJECT	

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****

03446 000000 TLFS 0
03447 700042 ION
03450 700406 TLS
03451 603451 JMP .
03452 700314 IORS
03453 742010 RTL; RAL
03454 740010
03455 741100 SPA /CHECK KEYBOARD BIT
03456 603470 JMP KBRD
03457 740010 RAL
03460 740100 SMA
03461 603466 JMP ILLGAL /SPURIOUS INTERRUPT
03462 750004 LAS
03463 741100 SPA
03464 102074 JMS HLTST
03465 623446 JMP* TLFS /KEEP OUTPUTTING

03466 740040 ILLGAL HLT
03467 623446 JMP* TLFS /CONTINUE

03470 102054 KBRD JMS CRLF
03471 204447 LAC INRUF
03472 040011 DAC 11
03473 770000 LAW -10000
03474 041012 DAC TEMP
03475 160011 DZM* 11
03476 441012 ISZ TEMP
03477 603475 JMP .-2
03500 204447 LAC INRUF
03501 040011 DAC 11

03502 700312 NXTIN KRB
03503 543535 SAD K204
03504 603536 JMP NOMOR /REC'D EOT
03505 041013 DAC TEMP+1
03506 201012 LAC TEMP
03507 740200 SZA
03510 603516 JMP WATE+3
03511 241013 XOR TEMP+1
03512 041012 DAC TEMP
.EJECT

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↑↑↑↑

W3513	700301	WATE	KSF	/WAIT FOR INPUT
W3514	603513		JMP .-1	
W3515	603502		JMP NXTIN	
W3516	201013		LAC TEMP+1	
W3517	742010		RTL; RTL; PTL	
W3520	742010			
W3521	742010			
W3522	742010		RTL; RAL	
W3523	740010			
W3524	241012		XOR TEMP	
W3525	141012		DAC TEMP	/STORE 2 INPUTS
W3526	200011		LAC 11	
W3527	544505		SAD (17777)	
W3530	603550		JMP RUFUL	/FULL BUFFER - STOP ONPUT
W3531	201012		LAC TEMP	
W3532	060011		DAC* 11	
W3533	141012		DZM TEMP	
W3534	603513		JMP WATE	
W3535	0000204	K204	204	
		/		
W3536	201012	NOMOR	LAC TEMP	
W3537	741200		SNA	
W3540	603544		JMP .+4	
W3541	204506		LAC (215000)	
W3542	241012		XOR TEMP	
W3543	060011		DAC* 11	
W3544	204447		LAC INRUF	
W3545	040011		DAC 11	
W3546	103434		JMS TTYOUT	/START PRINTING INFO
W3547	603401		JMP KTS6+1	/START OVER
		/		
W3550	102054	RUFUL	JMS CRLF	
W3551	204434		LAC FULL	
W3552	040012		DAC 12	/PRINT FULL INPUT RUFFER
W3553	102274		JMS PHDR	
W3554	102054		JMS CRLF	
W3555	703302		CAF	
W3556	603544		JMP RUFUL-4	/DUMP BUFFER
			.EJECT	

```

/PRINT CONTENTS OF BUFFER AFTER EACH SHIFT
/ACS1 UP = SIMULATE CHARACTER IN ACS 10-17
/
03700 .LOC 3700
/
/HEADER ROUTINE
03700 102054 JMS CRLF /CR,LF
03701 760240 LAW 240
03702 101175 JMS TLSSF /SPACE
03703 760311 LAW 311
03704 101175 JMS TLSSF /I FOR INITIAL CONTENTS
03705 760260 LAW 260
03706 041013 DAC TEMP+1
03707 777773 PRNBR LAW -5
03710 041012 DAC TEMP
03711 760240 LAW 240
03712 101175 JMS TLSSF
03713 441012 ISZ TEMP /5 SPACES
03714 603712 JMP .-2
03715 201013 LAC TEMP+1
03716 101175 JMS TLSSF /PRINT NUMBER
03717 544507 SAD (LAW 271 /HAS #9 BEEN PRINTED
03720 603723 JMP .+3 /DONE WITH HEADER
03721 441013 ISZ TEMP+1
03722 603707 JMP PRNBR /5 SPACES, ANOTHER NUMBER
03723 102054 JMS CRLF /CR,LF

/
RSTRT LAS
03724 750004 RAL
03725 740010 SPA /CHECK FOR SIMULATION
03726 741100 JMP SIMUL /SIMULATE CHAR IN ACS 10-17
03727 604062 LAW -12
03730 777766 DAC TEMP+1 /STATE COUNTER
03731 041013 LAC CONBUF
03732 204045 DAC 11 /SETUP INPUT POINTER
03733 040011 DAC 11
03734 777776 LAW -2
03735 041012 DAC TEMP /5 SEC COUNT
03736 141014 DZM TEMP+2
03737 703302 CAF /I/O POWER CLEAR
03740 700301 KSF
03741 741000 SKP
.EJECT

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+++
W3742 740040 ERR7 HLT /FLAG SHOULD BE CLEAR. PRESS
W3743 700312 KRR /CONTINUE TO IGNORE
W3744 060011 DAC* 11 /READ BUFFER
W3745 044041 DAC LAST /INITIAL CONTENTS
W3746 700312 RDRFR KRR /READ
W3747 544041 SAD LAST /SEE IF SHIFT OCCURRED
W3750 603761 JMP TIMOUT /NO
W3751 060011 DAC* 11 /STORE DATA READ
W3752 044041 DAC LAST /SAVF NEW DATA
W3753 141014 DZM TEMP+2 /RE-INITIALIZE 5 SEC COUNT
W3754 777776 LAW -2
W3755 041012 DAC TEMP
W3756 441013 TSZ TEMP+1 /AS SKIP = CLOCK NOT STOPPING
W3757 603746 JMP RDRFR
W3760 603765 JMP TIMOUT+4
W3761 441014 ISZ TEMP+2 /5 SEC. COUNT
W3762 603746 JMP RDRFR
W3763 441012 ISZ TEMP
W3764 603746 JMP RDRFR
W3765 204041 LAC LAST /LAST DATA READ AT END OF 5 SEC
W3766 260011 DAC* 11
W3767 201013 LAC TEMP+1
W3770 741200 SNA
W3771 603774 JMP .+3
W3772 441013 ISZ TEMP+1
W3773 603765 JMP TIMOUT+4
W3774 102054 JMS CRLF /CR,LF
.,EJECT

/DUMP CONTENTS OF BUFFER

03775	204045	LAC CONBUF	
03776	040011	DAC 11	/SETUP POINTER
03777	777765	LAW -13	
04000	041013	DAC TEMP+1	/STATE COUNTER
04001	220011	LAC* 11	
04002	041012	DAC TEMP	
04003	504042	AND K300	
04004	742020	RTR; RTR;	RTR
04005	742020		
04006	742020		
04007	341264	TAD N260	
04010	101175	JMS TLSSF	/PRINT ONE OCTAL
04011	201012	LAC TEMP	
04012	504043	AND K70	
04013	742020	RTR; RAR	
04014	740020		
04015	341264	TAD N260	
04016	101175	JMS TLSSF	/PRINT 2ND
04017	201012	LAC TEMP	
04020	504044	AND K7	
04021	341264	TAD N260	
04022	101175	JMS TLSSF	/PRINT 3RD
04023	441013	ISZ TEMP+1	/CHECK FOR 11 STATES
04024	741000	SKP	
04025	604035	JMP DONE	
04026	777775	LAW -3	
04027	041012	DAC TEMP	/3 SPACES
04030	760240	LAW 240	
04031	171110	JMS TLSSF	
04032	441012	ISZ TEMP	
04033	604031	JMP ,+2	
04034	604031	JMP CONVRT	
<hr/>			
04035	1A2854	DONE	JMS CRUF
04036	246040	PRHLT3	HLT
04037	603724		JMP RSTART
04040	000377	TH77	377
04041	000000	LAST	0
04042	000300	K300	300
04043	000070	K70	70
04044	000007	K7	7
,EJECT			

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+++

04045 004045
04046 000000
04047 000000
04050 000000
04051 000000
04052 000000
04053 000000
04054 000000
04055 000000
04056 000000
04057 000000
04060 000000
04061 000000

CONBUF .
0; 0; 0; 0;
0; 0; 0; 0;

/
/
/SIMULATE BUFFER SHIFT
/

04062 750004
04063 504040
04064 041012
04065 101175
04066 102054
04067 201012
04070 744010
04071 742010
04072 742010
04073 742010
04074 742010
04075 244040
04076 041012
04077 204045
04100 040011
04101 777766
04102 041013
04103 201012
04104 504040
04105 060011
04106 201012
04107 740020
04110 041012
04111 441013
04112 604103
04113 740010
04114 060011
04115 603775

SIMUL LAS
AND TH77
DAC TEMP
JMS TLSSF /PRINT CHAR
JMS CRLF /CR,LF
LAC TEMP
RCL; RTL; RTL
RTL; RTL
XOR TH77
DAC TEMP /SAVE CHAR
LAC CONBUF
DAC 11 /SETUP POINTER
LAW -1P
DAC TEMP+1 /STATE COUNTER
LAC TEMP /INITIAL CONTENTS
AND TH77
DAC* 11
LAC TEMP
RAR
DAC TEMP
ISZ TEMP+1
JMP STORE
RAL
DAC* 11 /STATE 9
JMP CONVRT-4 /DUMP BUFFER CONTENTS
.EJECT

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/SCOPE MODE LOOP FOR KEYBOARD
/
04200 .LOC 4200
/
04200 703302 KBSCOP CAF /I/O POWER CLEAR
04201 700301 KSF
04202 604201 JMP .-1
04203 750004 LAS /CHECK FOR HALT
04204 741100 SPA
04205 741000 SKP
04206 604200 JMP KBSCOP
04207 700312 KRB
04210 102074 JMS HLTST
04211 604200 JMP KBSCOP /CONTINUE
/
/
/CONSTANTS FOR TEXTS, TABLES, HEADERS
/PRESS A KEY
PRSKY .
04212 004212 322320; 323305; 240323
04213 322320
04214 323305
04215 240323
04216 240301 240301; 305313; 240331
04217 305313
04220 240331
04221 000000 000000
.EJECT

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††††

V4222	004222	HDR5	.	305324;	324323;	265240;	240256
V4223	305324						
V4224	324323						
V4225	265240						
V4226	240256						
V4227	303240			303240;	301310;	301322;	324303
V4230	301310						
V4231	301322						
V4232	324303						
V4233	322305			322305;	311240;	320316;	324325
V4234	311240						
V4235	320316						
V4236	324325						
V4237	324240			324240;	323305;	256324;	212215
V4240	323305						
V4241	256324						
V4242	212215						
V4243	303301			303301;	240323;	240260;	320325
V4244	240323						
V4245	240260						
V4246	320325						
V4247	275240			275240;	310240;	314301;	240324
V4250	310240						
V4251	314301						
V4252	240324						
V4253	305324			305324;	324323;	000000	
V4254	324323						
V4255	000000						

/NO INPUT
/
NOINP .
V4256 004256
V4257 317316
V4260 311240
V4261 320316
V4262 324325
V4263 000000
.EJECT

/CONTENTS OF BUFFER CONT'D SHIFTING

/

04264	004264			
04265	317303	NOSTOP	, 317303; 324316; 316305;	323324
04266	324316			
04267	316305			
04270	323324			
04271	317240		317240; 240306;	325302; 306306
04272	240306			
04273	325302			
04274	306306			
04275	322305		322305; 303240;	316317; 247324
04276	303240			
04277	316317			
04300	247324			
04301	240304		240304; 310323;	306311; 311324
04302	310323			
04303	306311			
04304	311324			
04305	307316		307316; 301240;	324306; 322305
04306	301240			
04307	324306			
04310	322305			
04311	306240		306240; 322311;	324323; 322240
04312	322311			
04313	324323			
04314	322240			
04315	301305		301305; 256304;	000000
04316	256304			
04317	000000			

/

/

/TEST 6 HEADER AND INSTRUCTIONS

/

04320	004320	HDR6	, 305324; 324323;	266240; 313240
04321	305324			
04322	324323			
04323	266240			
04324	313240			
04325	331305		331305; 317302;	322301; 240304
04326	317302			
04327	322301			
04330	240304			
04331	316311		316311; 305324;	322322; 320325
04332	305324			
04333	322322			
04334	320325			
04335	240324		240324; 305324;	324323; 212215
04336	305324			
04337	324323			
04340	212215			
04341	331324		331324; 305320;	301240; 331316
04342	305320			
04343	301240			

W4344	331316			
A4345	314240	314240;	316305;	324307;
V4346	316305			240310
W4347	324307			
A4350	240310			
W4351	305315	305315;	323323;	307301;
W4352	323323			256305
V4353	307301			
A4354	256305			
A4355	212215	212215;	316305;	240304;
A4356	316305			305315
A4357	240304			
V4360	305315			
W4361	323323	323323;	307301;	240305;
W4362	307301			311327
A4363	240305			
V4364	311327			
W4365	310324	310324;	303240;	322324;
W4366	303240			240314
W4367	322324			
V4370	240314			
W4371	256304	256304;	212215;	305315;
V4372	212215			323323
W4373	305315			
V4374	323323			
W4375	307301	307301;	240305;	311327;
W4376	240305			314314
V4377	311327			
W4400	314314			
A4401	302240	302240;	240305;	322320;
V4402	240305			316311
V4403	322320			
V4404	316311			
A4405	305324	305324;	240304;	317306;
V4406	240304			240322
V4407	317306			
V4410	240322			
V4411	305326	305326;	311322;	311306;
V4412	311322			301303
V4413	311306			
V4414	301303			
V4415	311324	311324;	316317;	215256;
V4416	316317			301212
V4417	215256			
V4420	301212			
V4421	323303	323303;	260240;	325240;
V4422	260240			240320
V4423	325240			
V4424	240320			
V4425	240275	240275;	310240;	314301;
V4426	310240			240324
V4427	314301			
V4430	240324			
V4431	305324	305324;	324323;	0000000
V4432	324323			

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04433 000000

.EJECT

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††††

/FULL INPUT BUFFER

/

A4434	004434	FULL	.	325306;	314314;	311240;	320316
A4435	325306						
A4436	314314						
A4437	311240						
A4440	320316						
A4441	324325			324325;	302240;	306325;	305306
A4442	302240						
A4443	306325						
A4444	305306						
A4445	240322			240322;	000000		
A4446	000000						

A4447 007777

/INRUF 7777

/

/CHECK FOR 1 US OR 1.2 US CYCLE TIME

/

A4450	702704	PARCK	FWP	/FORCE WRONG PARITY
A4451	204400	CRPE	LAC 4000	
A4452	204451		LAC .-1	
A4453	702701		SPE	/SKIP IF PARITY
A4454	604464		JMP PRHLT4	
A4455	702702		CPE	/CLEAR PARITY ERROR
A4456	204465		LAC LK4K	/RESTORE CRPE
A4457	044451		DAC CRPE	

/

.EJECT

††††

/ADJUST OUTPUT CLOCK TIME CONSTANTS

/

04460	204510	LAC (JMP ALTIM
04461	041701	DAC TMEAGN

/

/ADJUST 150 MS STALL

/

04462	777754	LAW -24
04463	042101	DAC STL150+1
04464	740040	PRHLT4 HLT

/

702704	FWP=702704
702701	SPE=702701
702702	CPE=702702

/

04465	204000	LK4K LAC 4000
-------	--------	---------------

/

.END PARCK

04467	020000	*LIT
04470	000400	*LIT
04471	212215	*LIT
04472	242241	*LIT
04473	000272	*LIT
04474	317240	*LIT
04475	337247	*LIT
04476	160160	*LIT
04477	333247	*LIT
04500	030030	*LIT
04501	077777	*LIT
04502	742550	*LIT
04503	440000	*LIT
04504	620000	*LIT
04505	017777	*LIT
04506	215000	*LIT
04507	760271	*LIT
04510	602031	*LIT

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ACIN	02520
ALTIM	02031
ANOTHR	01136
ANTHRO	01140
BDCLK	03354
BUFUI	03550
CHECLK	03323
CHEKTN	03317
CKFLG	02560
CKHLT	01422
CKLINE	00737
CLRBF	03165
CLRFL	03110
CONTLINE	01017
CONRUF	04045
CONVRT	04001
CPF	702702
CRLF	02054
CRPE	04451
DONCON	01735
DONE	04035
DOTS6	02120
ERR3	00314
ERR4	03174
ERR7	03742
E01	00121
E02	00204
E03	00315
E04	00410
E04A	00425
E05	00521
E05A	00535
E05B	00537
E05C	00551
FFFD	01247
FLGERR	00120
FULL	04434
FWR	702704
GETNXT	00714
GODEC	01720
GRUP	01015
HDR5	04222
HDR6	04320
HDR7A	02170
HDR7B	02215
HTST	02074
ILLGAL	03466
ILLINT	00525
IN-HUF	04447
INCR	01235
JMP5	00523
JMP6	00524
JMSS	00770
KBRD	03470
KBSCOP	04200

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KE~1	02521
KE~1A	02516
KE~2	02572
KE~3	03117
KE~4	032W1
KTS1	02500
KTS2	02550
KTS3	03100
KTS4	03150
KTS5	03300
KTS6	03400
K2~4	03535
K3~0	04042
K7	04044
K7^	04043
LAST	04041
LINE	01016
LK4K	04465
LNFED	01217
LOCA3	00002
LOOPLF	01257
LOOPSP	01352
LOOPX	00623
LOOP7	00761
M01L35	01540
MSFC	02024
NEWRD	00772
NOINP	04256
NOMOR	03536
NOSTOP	04264
NOTHN	03341
NOT3G	00764
NOT3I	00755
NXTIN	03502
1200	01264
JKS1	02523
JKS2	02574
JKS3	03121
JKS4	03203
JK2	00206
JK4	00412
JK4A	00427
OK5	00553
UNE10	01361
OUTMS	01751
OUTPRT	01765
PARCK	04450
PCW	02014
PHDR	02274
PHRASE	03421
PRHLT1	02075
PRHLT2	03220
PRHLT3	04036
PRHLT4	04464
PRINT2	00724

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PR+RR	03707
PR+TWC	01574
PR+T33	01515
PR+T35	01544
PRSKFY	04212
PRSPFC	00624
PRTMS	01773
PR7B	02236
PR7C	02241
PR7D	02244
PR7E	02247
PR7F	02252
PR7G	02255
PR7H	02260
PR7I	02263
PR7J	02266
PR7K	02271
PUNCT	02163
PW+TFN	02015
RD+FR	03746
RESTRT	01321
RJMP1	01035
RJMP2	01053
RJMP3	01071
RJMP4	01107
RJMP5	01125
ROTAT9	02065
RSTRT	03724
SCUPF	02112
SIMUI	04062
SP+CF	01347
SPACNT	01327
SPAC69	01371
SPAOH	01300
SPF	7027V1
STL15H	02100
STURF	04143
SURAGN	01726
TA+LF	02004
TE+P	01012
TH77	04040
TIME	01707
TIMOUT	03761
TLES	03446
TLSSF	01175
TMFAGN	01701
TPINT	00514
TSTFIG	00541
TST6	00600
TST7R	02143
TST7C	02146
TST7D	02151
TST7F	02154
TST7F	02157
TS7B	01020

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TS7C	01036
TS7D	01054
TS7E	01072
TS7F	01110
TS7G	01126
TS7H	01202
TS7I	01265
TS7J	01362
TS7K	01500
TTST1	00100
TTST2	00200
TTST3	00300
TTST4	00400
TTST5	00500
TTST7	00700
TTYOUT	03434
WAIT	03307
WATE	03513
.SPACE	04466

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LOCAB	00002
TTST1	00100
FLGERR	00120
E01	00121
TTST2	00200
E02	00204
JK2	00206
TTST3	00300
ERR3	00314
E03	00315
TTST4	00400
E04	00410
JK4	00412
E04A	00425
JK4A	00427
TTST5	00500
TPINT	00514
E05	00521
JMP5	00523
JMP6	00524
ILLINT	00525
E05A	00535
E05B	00537
TSTFIG	00541
E05C	00551
JK5	00553
TST6	00600
LOOPX	00623
PRSPFC	00624
TTST7	00700
GETNXT	00714
PRINT2	00724
OKLINE	00737
NOT3L	00755
LOOP7	00761
NOT3G	00764
UMSS	00770
VERRD	00772
TEMP	01012
GRDUP	01015
LINE	01016
CNTLNE	01017
TS7R	01020
RJMP1	01035
TS7C	01036
RJMP2	01053
TS7D	01054
RJMP3	01071
TS7E	01072
RJMP4	01107
TS7F	01110
RJMP5	01125
TS7G	01126
ANOTHR	01136
ANTHRO	01140

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TLSSF	01175
TS7H	01202
LNFED	01217
INCR	01235
FEED	01247
LOOPIF	01257
N260	01264
TS7I	01265
SPAOH	01300
RESTRT	01321
SPACNT	01327
SPACF	01347
LOOPSP	01352
ONE10	01361
TS7J	01362
SPAC69	01371
CKHLT	01422
TS7K	01500
PRNT33	01515
MODL35	01540
PRNT35	01544
PRNTWC	01570
TMEAGN	01701
TIME	01707
GODEC	01720
SURAGN	01726
UONCON	01735
JUTMS	01751
JUTPRT	01765
PRTMS	01773
TABLE	02004
PCw	02014
PWRTFN	02015
1SEC	02024
ALTIM	02031
CRLF	02054
ROTAT9	02065
HLTST	02074
PRHLT1	02075
STL150	02100
SCOPE	02112
DOTS6	02120
TST7R	02143
TST7C	02146
TST7D	02151
TST7F	02154
TST7F	02157
PUNCT	02163
HDR7A	02170
HDR7R	02215
PR7B	02236
PR7C	02241
PR7D	02244
PR7E	02247
PR7F	02252

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PR7G	02255
PR7H	02260
PR7I	02263
PR7J	02266
PR7K	02271
PHDR	02274
KTS1	02500
KE11A	02516
ACIN	02520
KE11	02521
IKS1	02523
KTS2	02550
CKFLG	02560
KE12	02572
IKS2	02574
KTS3	03100
CLRF1	03110
KE13	03117
IKS3	03121
KTS4	03150
CLRBF	03165
ERH4	03174
KE14	03201
IKS4	03203
PRHLT2	03220
KTS5	03300
WAIT	03307
CHEKTN	03317
CHECLK	03323
10THN	03341
RDCLK	03354
KTS6	03400
PHASE	03421
ITYOUT	03434
TLFS	03446
ILLGAL	03466
RHD	03470
VXTIN	03502
WATE	03513
X2A4	03535
NOFOR	03536
BUFL1	03550
PR1BR	03707
RSTRT	03724
ERH7	03742
RDHFR	03746
TIMOUT	03761
CONVRT	04001
JOSE	04035
PRHLT3	04036
TH77	04040
LAST	04041
X310	04042
X71	04043
X7	04044

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CONBUF	04045
SIMUI	04062
STURF	04143
KRSCOP	04200
PRSKFY	04212
MDR5	04222
VOINP	04256
10STOP	04264
MDR6	04320
FULL	04434
INBUF	04447
PARCK	04450
CRPE	04451
PRHLT4	04464
LK4K	04465
SPACE	04466
SPF	702701
CPF	702702
FWP	702704

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ACIN	42824	JMSS	44774	PR-ELT2	43224
ALTIM	42681	KBRD	43474	PR-ELT3	44046
ANTHR	41136	ABSCOP	44294	PR-ELT4	44444
ANTHRO	41167	KE41	42521	PRINT2	42724
ADCLK	43354	KE41A	42516	PR-APP	43747
AUFL	43584	KE42	42572	PR-JTW2	41574
BHECLK	43328	KE43	43117	PR-T33	41515
BHEKTN	43357	KE44	43211	PR-T35	41544
BKFLG	42560	KT51	42564	MRSKF7	44212
BKLT	41420	KT52	42569	MRSPR7	4624
BLKLINE	40737	KT53	43104	MREMS	41723
BLKRF	43125	KT54	43154	PR7B	42216
BLKFI	43110	KT55	43344	PR7C	42243
BLKLMF	41617	KT56	43444	PR7D	42244
BLKBLF	44075	N24	43535	PR7E	42247
BLKVRT	44001	N30	44042	PR7F	42249
BLK	42712	N7	44044	PR7G	42249
BLKF	42714	N7	44143	PR7H	42249
BLKPE	44411	LAST	44441	PR7I	42245
BLKCON	41755	L11E	41116	PR7J	42245
BLKF	44048	LK4K	44465	PR7K	42271
BLKSA	42127	LNEEFL	41217	PU-CIT	42163
BLK3	41314	LOCAR	40012	PWXTF4	42015
BLK4	41314	LOUPIE	41257	SDPFR	43746
BLK7	40742	LOIPSP	41342	SE-STRF	41321
BLK1	44121	LOUPX	41613	KJ-PI1	41435
BLK2	4424	LO-PP7	40741	KJ-PI2	41435
BLK3	42315	LO-135	41543	KJ-PI3	41471
BLK4	40414	LO-SP	42524	KJ-PI4	41117
BLK5A	400414	LO-SPB	41774	KJ-PA4	41117
BLK5	41521	LO-TRP	46286	KO-AT12	42045
BLK6A	40644	LO-TR8	43154	PS-STR	42714
BLK6B	40644	LO-TR8	43154	SD-OF1	42112
BLK6C	40644	LO-TR8	43154	SD-TH1	44048
BLKD	44047	LO-TR8	43154	SP-AC1	41347
BLKFR	4111	LO-TR8	43155	SP-AC2	41327
BLKL	44424	LO-TR8	43155	SP-AC79	41321
BLK7	42474	LO-TR8	43155	SP-CH1	4131
BLKFNXT	407214	IK-1	42815	SPR	42471
BLKFC	40121	IK-2	42821	ST-154	42116
BLKUP	411205	IK-3	42821	ST-361	42116
BLKS	44211	IK-4	42823	SUR-AD	41726
BLK6	41411	IK-	42826	TALE	42114
BLK7A	40431	IK-	43143	TE4P	41012
BLK7B	40431	IK-A	43143	TH77	44327
BLKST	40211	IK-	43144	TI-46	41747
BLKGAL	40741	IK-10	43144	TI-104	43741
BLKLNT	40741	IK-1S	43144	TEPS	44436
BLKUF	406412	IK-TRP	43144	TESS	41125
BLKCR	412212	IK-TR8	43144	TMFAV	41711
BLK5	4111	IK-TR8	43144	TPINT	42514
BLK6	40641	IK-TR8	43145	TSFET	426531
BLK8	40641	IK-TR8	43145	TS16	42613
		IK-17	43145	TS12	42445

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IST7C	02146	LOCA3	000002	IS7G	01126
TST7D	02151	TTST1	00100	ANOTHR	01136
TST7F	02154	FLGERR	00120	ANTHRO	01140
TST7F	02157	E01	00121	FLSSF	01175
TS7B	01020	TTST2	00200	TS7H	01202
TS7C	01036	E02	00204	LNFED	01217
TS7D	01054	JK2	00206	INCR	01235
TS7E	01072	TTST3	00300	FEFD	01247
TS7F	01110	ERR3	00314	LOUPIE	01257
TS7G	01126	E03	00315	4260	01264
TS7H	01202	TTST4	00400	TS7I	01265
TS7I	01265	E04	00410	SP4OH	01306
TS7J	01362	JK4	00412	RESTRT	01321
TS7K	01500	E04A	00425	SPACNT	01327
TTST1	00100	JK4A	00427	SPACF	01347
TTST2	00200	TTST5	00500	LOOPSP	01352
TTST3	00300	TPINT	00514	JNF10	01361
TTST4	00400	E05	00521	TS7J	01362
TTST5	00500	JMP5	00523	SPACK9	01371
TTST7	00700	JMP6	00524	CKHLT	01422
TTYOUT	03434	ILLINT	00525	TS7K	01500
WAIT	03307	E05A	00535	PR4T33	01513
NATE	03513	E05B	00537	400L35	01540
		TSFIG	00541	PR4T35	01544
		E05C	00551	PR4TWC	01574
		JK5	00553	TIMEAGN	01701
		TTST6	00600	TIME	01707
		LOOPX	00623	S0NEC	01720
		PRSPFC	00624	SUBAGN	01726
		TTST7	00700	S0NCON	01735
		GETNXT	00714	SUTMS	01751
		PRINT2	00724	SUTPRT	01765
		CKLINE	00737	SUTMS	01773
		40T31	00755	TABLE	02004
		LOOP7	00761	TCW	02014
		40T3G	00764	PWRTEW	02015
		JMSS	00770	ISFC	02024
		ENRD	00772	ALTIM	02031
		TEMP	01012	CRIF	02054
		GROUP	01015	ROTATW	02065
		LINF	01016	ALTEST	02074
		CNTLNE	01017	PRHLT1	02075
		TS7B	01020	STL154	02104
		RJMP1	01035	SC4PF	02112
		TS7C	01036	40TSA	02124
		RJMP2	01053	IST7R	02143
		TS7D	01054	IST7C	02146
		RJMP3	01071	IST7D	02151
		TS7E	01072	IST7F	02154
		RJMP4	01107	IST7F	02157
		TS7F	01110	PUNCT	02153
		J	01120	40R7A	02174
		RJMP5	01125	40R7B	02215
				PR7R	02236

MAINDEC-9A-D2BA-LA

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PR7C	02241	TH77	04041
PR7D	02244	LAST	04041
PR7E	02247	340	04042
PR7F	02252	374	04043
PR7G	02255	37	04044
PR7H	02258	CONBUF	04045
PR7I	02263	SIMUL	04046
PR7J	02266	STURE	04143
PR7K	02271	KBSCCP	04247
PR7R	02274	PRSKFY	04212
TS1	02514	DR5	04222
SE1A	02516	NOTNP	04246
SCIN	02517	POSTOP	04244
SE11	02521	DR6	04324
IKS1	02523	FULL	04444
TS2	02528	INHUF	04447
SKFLG	02530	PARCK	04454
SE12	02532	CRPE	04451
IKS2	02534	PRHLT4	04464
TS3	03101	LK4K	04465
LRFI	03112	SPF	702741
SE13	03117	CPF	702742
IKS3	03121	CWP	702744
TS4	03157		
CLXRF	03165		
ERX4	03174		
SE14	03241		
IKS4	03243		
PRHLT2	03226		
AT55	03314		
WALT	03317		
CHFK1	03317		
CHFC1	03323		
DLHM	03341		
DLCLK	03354		
AT56	03414		
THKASE	03421		
TYTOUT	03434		
TLFS	03446		
ILLGAL	03446		
NHDN	03474		
IXFIN	03542		
NATE	03513		
NS14	03545		
NO10R	03545		
SUFL	03554		
PR4RR	03727		
LSRT	03724		
CR47	03742		
RD4FR	03744		
TIMOUT	03741		
CONVRT	04001		
JOVE	04035		
PRHLT3	04036		

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