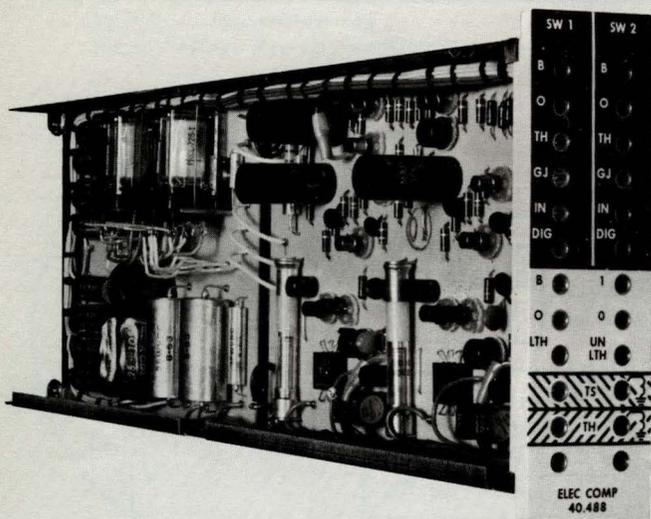


an accessory for PACE[®] TR-48 Analog Computers

ELECTRONIC COMPARATOR, model 40.488



FEATURES

- 5 microsecond typical switching time
- 1 millivolt sensitivity
- Plug-in, solid state, modular design
- Analog signal or external digital control
- Special "latch" "unlatch" inputs, simplifying complex operations
- Provides "track" and "hold" modes for repetitive and real time operation

The Electronic Comparator, Model 40.488 is a solid state signal comparator intended for use in the TR-48 Analog Computer. It is a single unit, performing the function of a form C relay, a mechanical relay comparator module and a repetitive operation display module. Control is by analog signals applied to its inputs, or by digital signals applied directly to the inputs of its two electronic switches. With appropriate patching and the addition of two external capacitors, two independent channels of track and storage (hold) may be provided.

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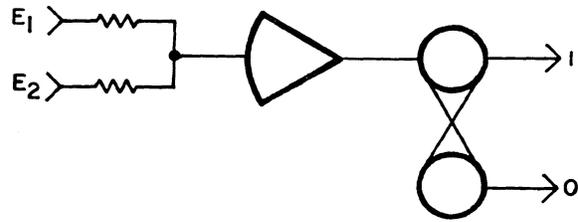
(EAI RESERVES THE RIGHT TO REVISE ITS PRODUCT SPECIFICATIONS IN ACCORDANCE WITH ITS CONTINUING PROGRAM OF PRODUCT DEVELOPMENT.)

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ELECTRONIC ASSOCIATES, INC. *West Long Branch, New Jersey*

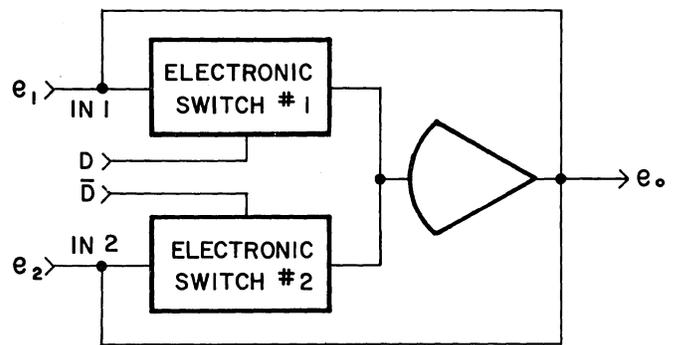
COMPARATOR

In the basic comparator, analog input control signals are applied to the two input terminals of a high gain d-c summing amplifier — whose output signal in turn is used to set a flip-flop. With the sum of the amplifier input signals positive, the flip-flop is set with its 1 output at a binary One and its 0 output at a binary Zero. For a negative summation of the amplifier input signals, the flip-flop state is reversed.



ELECTRONIC SWITCH

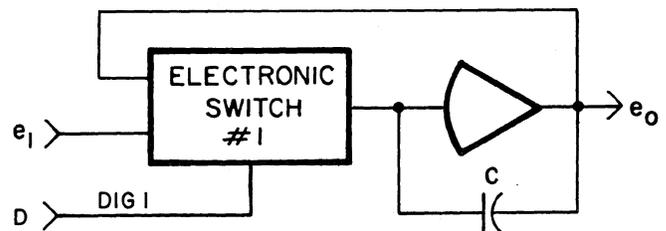
The flip-flop outputs or external digital signal levels may be used to control the two electronic switches in the unit — as applied to the DIG terminals on the computer patch panel. Complementary control is obtained by applying complementary digital signal levels at the D and \bar{D} inputs of the electronic switches. Signals to be switched, e_1 and e_2 , are applied to the IN terminals of each electronic switch and the output e_0 is obtained at the terminal of an associated dc amplifier. With the level of binary One at input D and the level of binary Zero at input \bar{D} , switch #1 is closed, switch #2 is open and the D-C amplifier output is $-e_1$. With the complementary control levels reversed the output is $-e_2$.

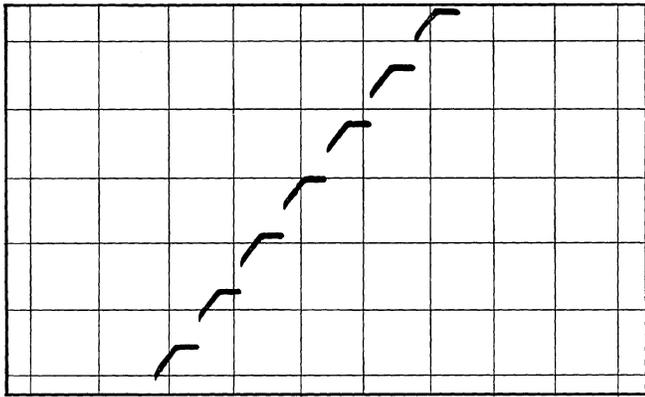


TRACK AND HOLD

The track and hold mode is an extension of the electronic switch section. A relay is energized by the bottle plug connection in the +H area, which connects 2K input and feedback resistors in the loop instead of the 10K resistors. A connection from +H to the amplifier output 0 provides the connection for the storage capacitor. When a +S bottle plug is installed, the circuit is prepared for repetitive operation.

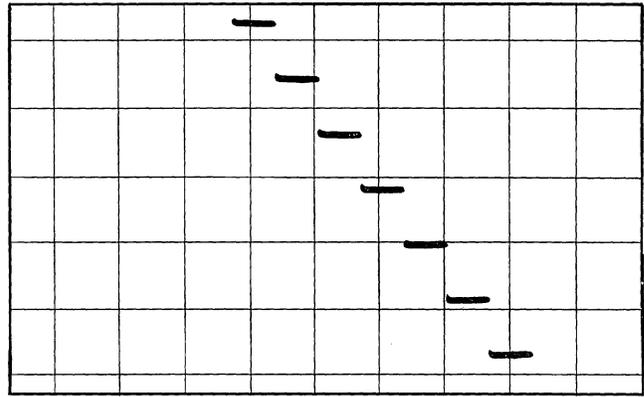
When D is a binary ONE the amplifier is in TRACK; similarly, when D is a binary ZERO the amplifier is in HOLD.





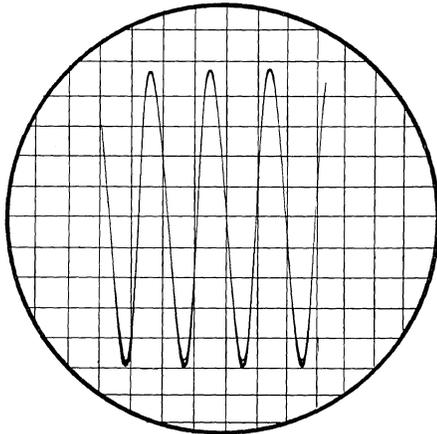
Output of 40.488 Used as Track. Hold Unit Sampling a Ramp at a 500 CPS Rate.

1/2V/CM -- Sensitivity (X)
 1V/CM -- Sensitivity (Y)
 Sweep Voltage Rep Op Sampling Rate -- 500 CPS



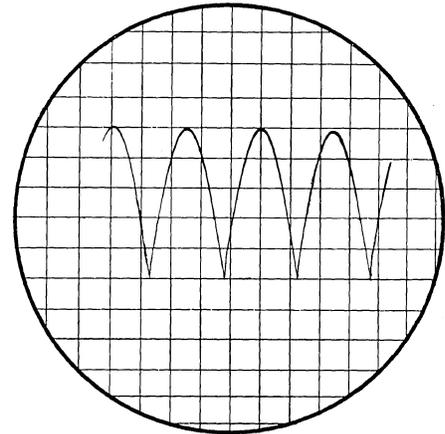
Output of Second Track-Hold Channel Which is Sampling Output of the First Track-Hold Channel (1) Controlled By Complementary Logic.

1/2V/CM -- Sensitivity (X)
 1V/CM -- Sensitivity (Y)
 Sweep Voltage Rep Op Sampling Rate -- 500 CPS



(Left) Output of 40.488 as Electronic Comparator Switching Between Same 1 KC Sine-wave

2V/CM -- Sensitivity
 .5 MS/CM -- Sweep 1 KC Sine-wave



(Right) Output of 40.488 Switching Between 1 KC Sine-wave and a 1 KC Cosine-wave

2V/CM -- Sensitivity
 .2 MS/CM -- Sweep 1 KC Sine-wave

SPECIFICATIONS

a. Power Supply Requirements

+15 VDC	± 16 MA, Nominal
-15 VDC	± 12 MA, Nominal
+10 VDC (Reference)	± 2.0 MA
-20 VDC	Nominal, ± 80 MA

b. Basic Comparator

Switching Sensitivity	± 1.0 Millivolt, min.
Propagation Time (1), (2)	5.0 Microseconds, typ.
Analog Input Voltage Range	-10 Volts to +10 Volts
Latch Input Impedance	100 K Ohms, min.
Digital Output Levels:	
Binary One	5.0 VDC, Nominal
Binary Zero	0.2 VDC, Nominal

c. Electronic Switch

Signal Input Impedance	10 K Ohms
Propagation Time (2)	1.0 Microsecond, typ.
DC Offset Voltage	± 500 Microvolts
Digital Control Input Levels:	
Binary One	5.0 VDC, Nominal
Binary Zero	0.2 VDC, Nominal
Control Input Impedance	100 K Ohms, min.

d. Track and Hold

Digital Control Input Levels:

Binary One	5.0 VDC, Nominal
Binary Zero	0.0 VDC, Nominal
Control Input Impedance	100 K Ohms, min.
Signal Input Impedance	2K Ohms
Drift In Hold Mode:	

Real Time Operation	2.0 Millivolts/second
Fast Repetitive Operation	100 Microvolts/ Millisecond

Phase Shift-Sinusoidal Input:

Real Time Operation	0.06° typ., (20 VPP, 10 cps)
Fast Repetitive Operation	0.03° typ., (20 VPP, 1 kcps)

Gain Error - Sinusoidal Input:

Real Time Operation	0.02% typ., (10 VPP, 10 cps)
Fast Repetitive Operation	0.1% typ., (10 VPP, 1 kcps)

NOTES:

- (1) Basic comparator propagation time is dependent upon the slope of the algebraic sum of the input signals. The propagation time is 1 Microsecond typical for an input signal sum having a rise time of 5 Microseconds.
- (2) The propagation time of the comparator and electronic switch in cascade is 5 Microseconds typical even though the propagation time of each is 5.0 and 1.0 microseconds respectively.

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