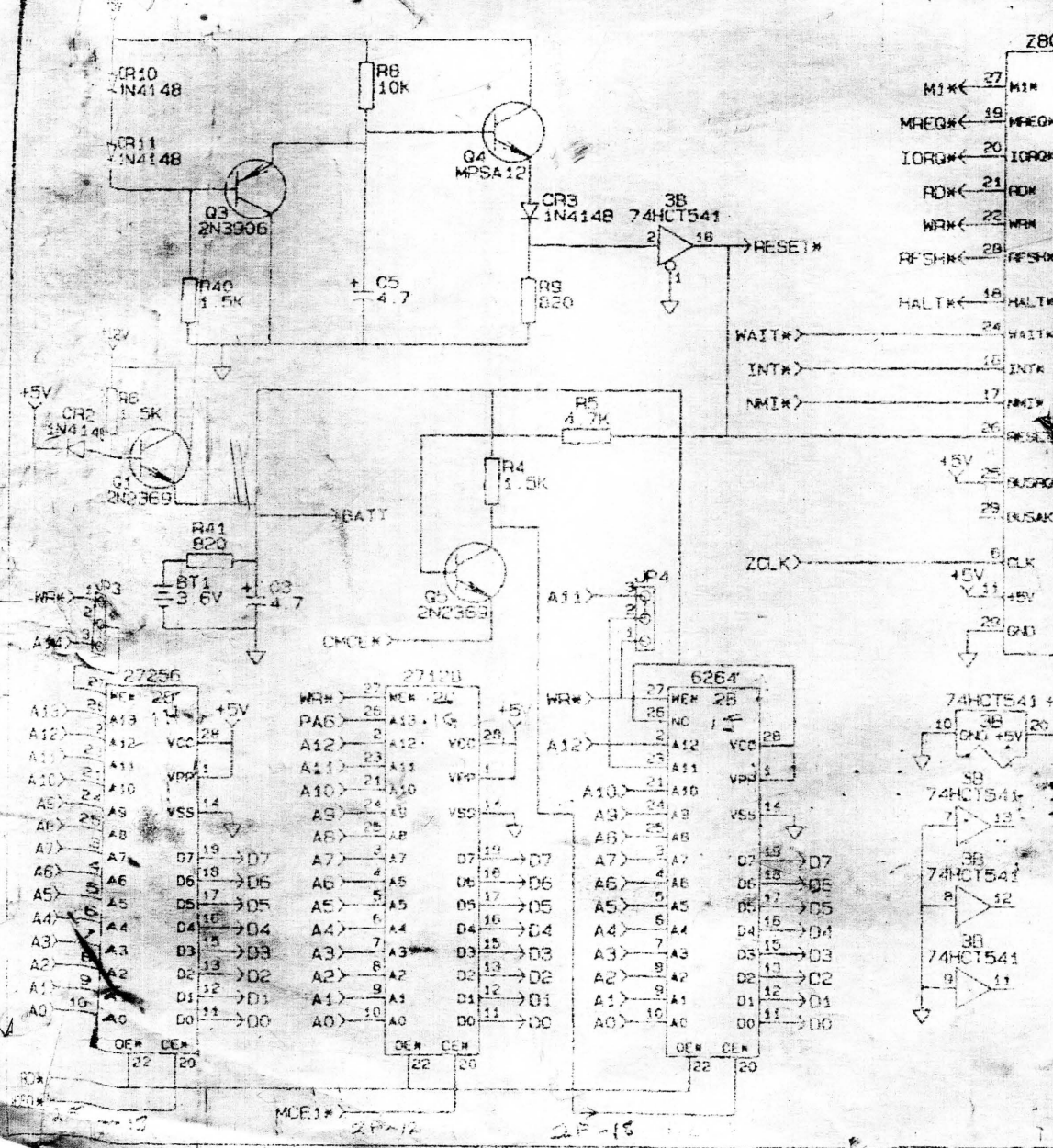




Z80A-CPU

M1*	37	M1M	A15	5	→ A15
MREQ*	19	MREQM	A14	4	→ A14
IORQ*	20	IORQM	A13	3	→ A13
RD*	21	RD	A12	2	→ A12
WR*	22	WR	A11	1	→ A11
RFSH*	28	RFSH	A10	40	→ A10
HALT*	18	HALT	A9	39	→ A9
			A8	38	→ A8
			A7	37	→ A7
			A6	36	→ A6
			A5	35	→ A5
			A4	34	→ A4
			A3	33	→ A3
			A2	32	→ A2
			A1	31	→ A1
			A0	30	→ A0
			D7	13	→ D7
			D6	10	→ D6
			D5	9	→ D5
			D4	8	→ D4
			D3	12	→ D3
			D2	11	→ D2
			D1	15	→ D1
			D0	14	→ D0

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A15	14	A4	D7	9	→ INT*
A14	13	A3	D6	8	→ NMIX*
A13	12	A2	D5	7	→ SMCEM
A12	11	A1	D4	6	→ GCMCEM
SBA13	10	A0	D3	5	→ CMCEM
MREQ*	15	0M	D2	4	→ MCE1M
			D1	3	→ MCE0M
			D0	2	

74S138

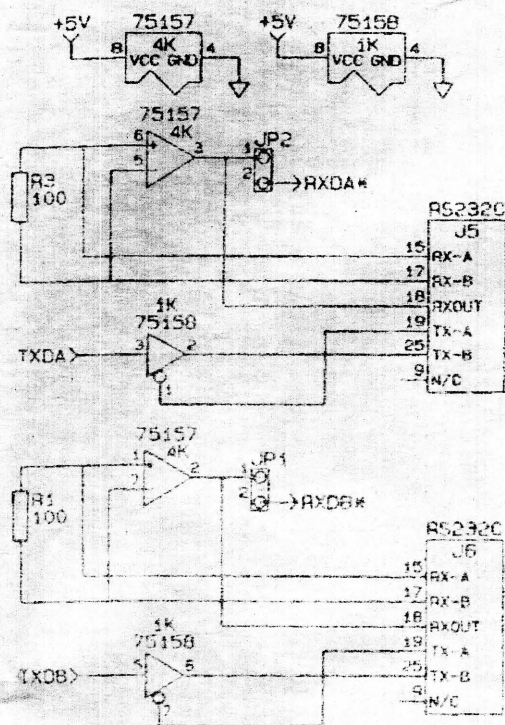
M1*	8	03	Y7	7	→ CTCEM 7X
IORQ*	5	02	Y6	9	→ SIOEM 6X
A7	4	01	Y5	10	→ CRTEM 5X
			Y4	11	→ KEYRDM 4X
			Y3	12	→ PICEM 3X
			Y2	13	→ DACWRM 2X
			Y1	14	→ CTLWRM 1X
			Y0	15	→ KEYSRM 0X

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D5	14	05	15	→ SBA13
D4	13	04	12	→ ATBEN
D3	11	03	10	→ PROREV
D2	10	02	7	→ PRINT
D1	9	01	5	→ S132
D0	8	00	2	→ KRESET*

Point Data Control
A 60 61
B 62 63
C 64 65

APPROVALS		DATE	FALCO DATA PRODUCTS	
PREP	PKI	NOV 27, 85	SIZE	DWG NO
			B	230165-003
			1 SHEET 2 OF 10	



OPTIONAL RS422 INTERFACE

