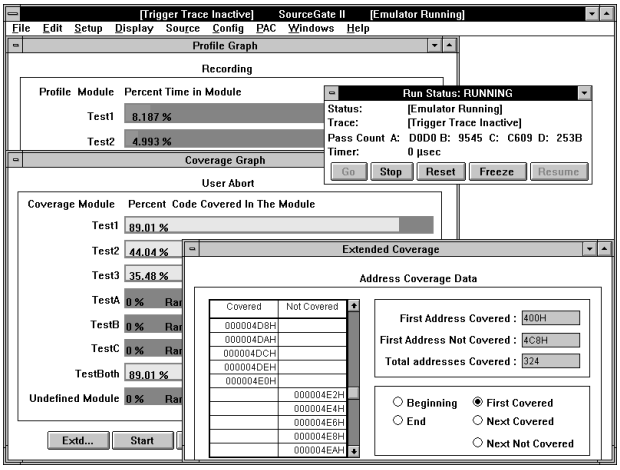
HMI Unleashes 683XX Emulation Power...

with the HMI-200-CPU32 and CPU32+! Combined with SourceGate II, HMI's acclaimed source-level debugger, the HMI-200-CPU32 and CPU32+ provide the industry's most powerful debugging tool for the 683XX family of devices. Of course, you would expect nothing less from the company that provides the best development tools in the industry. That company is HMI!

The HMI-200-CPU32 and CPU32+ emulators are high performance development systems which combine the control of an in-circuit emulator with the power of a logic analyzer to provide a complete debugging environment for hardware and software.

- Real-time emulation up to 33MHz
- 256K of emulation overlay memory std. (1 or 2MB optional). Shadow RAM for real-time data monitoring.
- Single-step, 4 hardware, and 4 address breakpoints
- Four Events consisting of bit patterns or address ranges, data values, status bits, external trace leads, and pass counters
- Hardware breakpoints or trace trigger points based upon sequences of defined events; e.g. A THEN B THEN C THEN D
- Two 16K x 104 bit trace buffers
- Trace qualification to allow selective tracing. Freeze trace allows viewing of trace buffers and changing of events during emulation
- High-speed host communications via 115.2KB RS-232 serial port. Parallel port for faster code downloads.
- **PERFORMANCE ANALYSIS CARD:**
  - Real-time hardware implementation
  - Analysis of up to 8 test modules
  - Minimum, maximum, and average execution time displays
  - Histograms and code coverage displays
  - Trace data time stamping



PRODUCT INFORMATION

HMI-200 Series in-circuit emulators represent the best in emulation technology and provide the most accurate and complete microprocessor development systems available.

Primary communication with the host computer is provided using a high speed serial port or an optional Ethernet port. A parallel port is provided for high speed code downloads.

The overlay RAM provided with each emulator may be mapped in small 2KB blocks throughout the memory address space of the target system. Shadow RAM blocks may be mapped to allow real-time monitoring of critical memory variables.

Breakpoint, trigger point, and interval timing functions are activated by referencing a set of user defined events (A, B, C, and D). Each event can be based upon address ranges, data patterns, control signals (BERR', R/W', etc.) or the status of external trace bit lines. These events may then be logically combined (e.g. "IF A OR B THEN C") to initiate emulator actions.

Trace buffers capture bus activity parameters including addresses, data, control signals, and external trace bits. A Freeze Trace command may be issued to capture a snap shot of a trace buffer which can then be viewed without stopping processor execution.

A real-time hardware implemented Performance Analysis system is offered for all HMI-200 Series emulators. This premium system graphically displays accurate execution timing data for detailed analysis of system performance. Trace time stamping and code coverage profiling are also provided.

Source-Level Debugger Support

SourceGate II, HMI's acclaimed source-level debugger software, is supplied with all HMI-200 Series emulators and provides full native GUI environment support for Windows 3.1/NT/95, SUN/SPARC, and HP platforms.

Processor Support

The HMI-200-CPU32 provides support for the 68330, 68331, 68332, 68F333, and 68340. The HMI-200-CPU32+ provides support for the 68349 and 68360.

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MOTOROLA EMBEDDED PLATINUM DEVELOPER