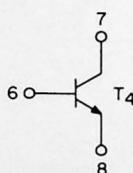
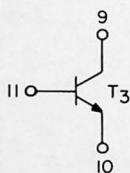
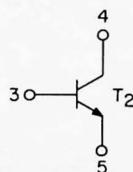
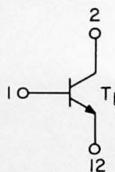


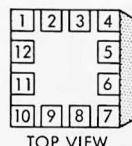
Functional Description

The Four Transistor, FTX-2A, module consists of four single transistors with the individual base, emitter and collector leads terminated at specific pins. The individual transistors offer the circuit designer uniformity of circuit packaging as well as flexibility in application with other SLT modules.

Schematic



Terminal Configuration



Mechanical
Chamfer
Right Side

TOP VIEW

Maximum Ratings

$I_E = 50$ Milliamps

PTX-2A Test Conditions

TESTS	TEST CONDITIONS	T O C	LIMITS		
			MIN	MAX	UNITS
BV_{CEO}	$I_C = 5\text{mA}$, $I_B = 0$	25	8		V
BV_{CBO}	$I_C = 10\ \mu\text{A}$	25	12		V
BV_{EBO}	$I_E = 10\ \mu\text{A}$	25	2.5		V
I_{CEX}	$V_{CE} = 5\text{V}$, $V_{BE} = .35\text{V}$	75		20	μA
I_{BEX}	$V_{CE} = 9\text{V}$, $V_{BE} = -3\text{V}$	75		10	μA
H_{FE}	$I_E = 0.1\text{mA}$, $V_{CB} = 0\text{V}$	25	10		
H_{FE}	$I_E = 1.0\text{mA}$, $V_{CB} = 0\text{V}$	25	25		
H_{FE}	$I_E = 10.0\text{mA}$, $V_{CB} = 0\text{V}$	25	30		
H_{FE}	$I_E = 50.0\text{mA}$, $V_{CB} = 0\text{V}$	25	25		
τ_s	See Figure 1	25		40	ns
/GAIN/	$f = 100\ \text{mhz}$, $I_E = 10\text{mA}$, $V_{CB} = 3.0\text{V}$	25	1.5		
C_{ib}	$V_{EB} = 0\text{V}$, $f = 1 \pm .5\ \text{mhz}$	25		6.5	pf
C_{ob}	$V_{CB} = 0\text{V}$, $f = 1 \pm .5\ \text{mhz}$	25		6.5	pf
V_{CE}	$I_C = 1.0\text{mA}$, $I_B = .05\text{mA}$	25		.30	V
V_{CE}	$I_C = 10.0\text{mA}$, $I_B = .5\text{mA}$	25		.30	V
V_{CE}	$I_C = 50.0\text{mA}$, $I_B = 2.5\text{mA}$	25		.50	V
V_{BE}	$I_C = 1.0\text{mA}$, $I_B = .05\text{mA}$	25	.60	.75	V
V_{BE}	$I_C = 10.0\text{mA}$, $I_B = .5\text{mA}$	25	.70	.85	V
V_{BE}	$I_C = 50.0\text{mA}$, $I_B = 2.5\text{mA}$	25	.80	1.10	V
h_{rb}	$I_C = 1.0\text{mA}$, $V_{CB} = +1\text{V}$, $f = 10\ \text{mhz}$	25		.05	

τ_s Test Circuit

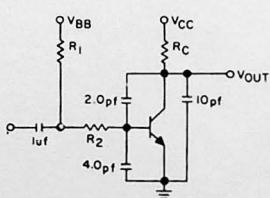


FIGURE 1

