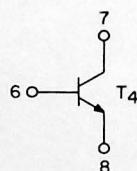
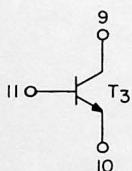
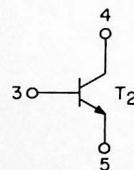
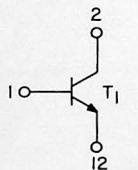


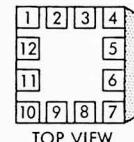
Functional Description

The Four Transistor, FTX-1C, module consists of four single transistors with the individual base, emitter and collector leads terminated at specific pins. The four transistor module offers the circuit designer uniformity of circuit packaging as well as flexibility in application with other SLT modules.

Schematic



Terminal Configuration



Maximum Ratings

$I_E = 50$ millamps

FTX-1C Test Conditions

INDIVIDUAL DEVICE PARAMETER TESTS					
TESTS	TEST CONDITIONS	T °C	LIMITS		
			MIN	MAX	UNITS
I_{CEX}	$V_{CE} = +13V, V_{BE} = +0.35V$	75		20	μA
I_{BEX}	$V_{CE} = 13V, V_{BE} = -2.5V$	75		10	μA
BV_{CEO}	$I_C = 5mA$	25	10		V
BV_{EBO}	$I_E = 10\mu A$	25	2.5		V
BV_{CBO}	$I_C = 10\mu A$	25	15		V
h_{FE1}	$I_E = .1mA, V_{CB} = 0.0V$	25	10		-
h_{FE2}	$I_E = 1mA, V_{CB} = 0.0V$	25	25		-
h_{FE3}	$I_E = 10mA, V_{CB} = 0.0V$	25	30		-
h_{FE4}	$I_E = 50mA, V_{CB} = 0.0V$	25	25		-
V_{CE1} (sat)	$I_C = 1mA, I_B = 0.05mA$	25		0.22	V
V_{CE2} (sat)	$I_C = 10mA, I_B = 0.5mA$	25		0.24	V
V_{CE3} (sat)	$I_C = 22mA, I_B = 1.2mA$	25		0.32	V
V_{CE4} (sat)	$I_C = 50mA, I_B = 2.5mA$	25		0.40	V
V_{BE1} (sat)	$I_C = 1mA, I_B = 0.05mA$	25	.60	0.75	V
V_{BE2} (sat)	$I_C = 10mA, I_B = 0.5mA$	25	.70	0.85	V
V_{BE3} (sat)	$I_C = 50mA, I_B = 2.5mA$	25	.85	1.1	V
V_{BE} (ON)	$I_C = 1.0mA, V_{CB} = +1.0V$	75	.50		V
C_{ib}	OV BIAS f = 1 ± 0.5 mhz	25		6.0	pF
C_{ob}	OV BIAS f = 1 ± 0.5 mhz	25		6.0	pF
/GAIN/	$I_E = 10mA, V_{CB} = 3V, f = 100$ mhz $R_L = 50\Omega$	25	1.5		-
t_{s1}	$I_C = 10mA, t_{BON} = .33ns$ See Fig 1	25		250	ns
h_{ie}	$I_C = 5.0mA, V_{CE} = +5V, f = 1khz$	25	0.10	2.0	KΩ
h_{fe2}	$I_C = 1.0mA, V_{CE} = +5V, f = 1khz$	25	20	180	-
h_{fe3}	$I_C = 5.0mA, V_{CE} = +5V, f = 1khz$	25	30	250	-
h_{rb}	$I_E = 5.0mA, V_{CB} = +5V, f = 1khz$	25	0	1.6×10^{-3}	-
h_{ob}	$I_E = 5.0mA, V_{CB} = +5V, f = 1khz$	25	0	2.5	μmho

t_s Test Circuit

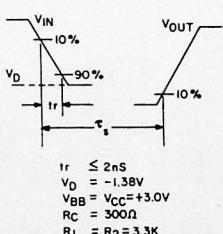
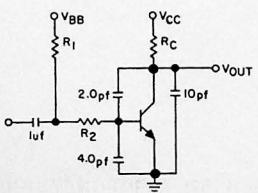


FIGURE 1