



MOTOROLA

MCM68366

Advance Information

8192 x 8-BIT READ ONLY MEMORY

The MCM68366 is a mask-programmable byte-organized memory designed for use in bus-organized systems. It is fabricated with N-channel silicon-gate technology. For ease of use, the device operates from a single power supply, has compatibility with TTL and DTL, and needs no clocks or refreshing because of static operation.

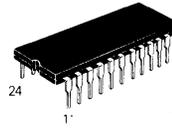
The memory is compatible with the M6800 Microcomputer Family, providing read only storage in byte increments. The active level of the Output Enable input and the memory content is defined by the user. The Output Enable input deselects the output.

- Fully Static Operation
- Fast Data Valid Time for High Speed Microprocessors
- Low Power Dissipation — 225 mW Active (Typical)
- Single $\pm 10\%$ 5-Volt Power Supply
- High Output Drive Capability (2 TTL Loads)
- Three-State Data Output for OR-Ties
- Mask Programmable Output Enable
- TTL Compatible
- Maximum Access Time — 120 ns from Output Enable
250 ns from Address — MCM68366-25
350 ns from Address — MCM68366-35
- Pin Compatible with 8K and 32K — Mask-Programmable ROMs
- Pin Compatible with MCM68766 64K EPROM

MOS

(N-CHANNEL, SILICON-GATE)

8192 x 8-BIT READ ONLY MEMORY

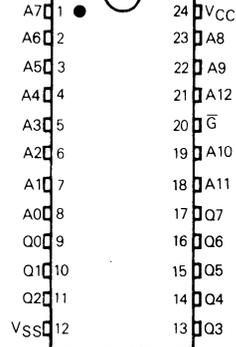


C SUFFIX
FRIT-SEAL
CERAMIC PACKAGE
CASE 623



P SUFFIX
PLASTIC PACKAGE
CASE 709

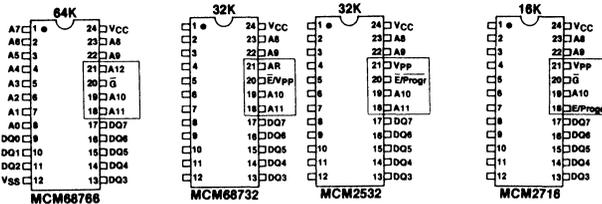
PIN ASSIGNMENT



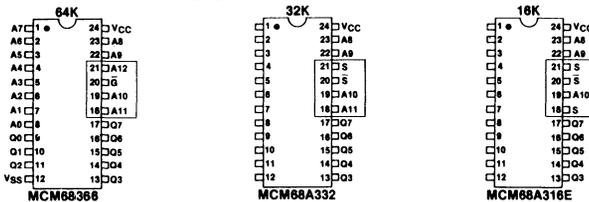
PIN NAMES

- A0-A12 Address
- G Output Enable
- Q0-Q7 Data Output
- VCC +5 V Power Supply
- VSS Ground

MOTOROLA'S PIN-COMPATIBLE EPROM FAMILY



MOTOROLA'S PIN-COMPATIBLE ROM FAMILY



INDUSTRY STANDARD PINOUTS

This device contains circuitry to protect the inputs against damage due to high static voltages or electric fields; however, it is advised that normal precautions be taken to avoid application of any voltage higher than maximum rated voltages to this high impedance circuit.