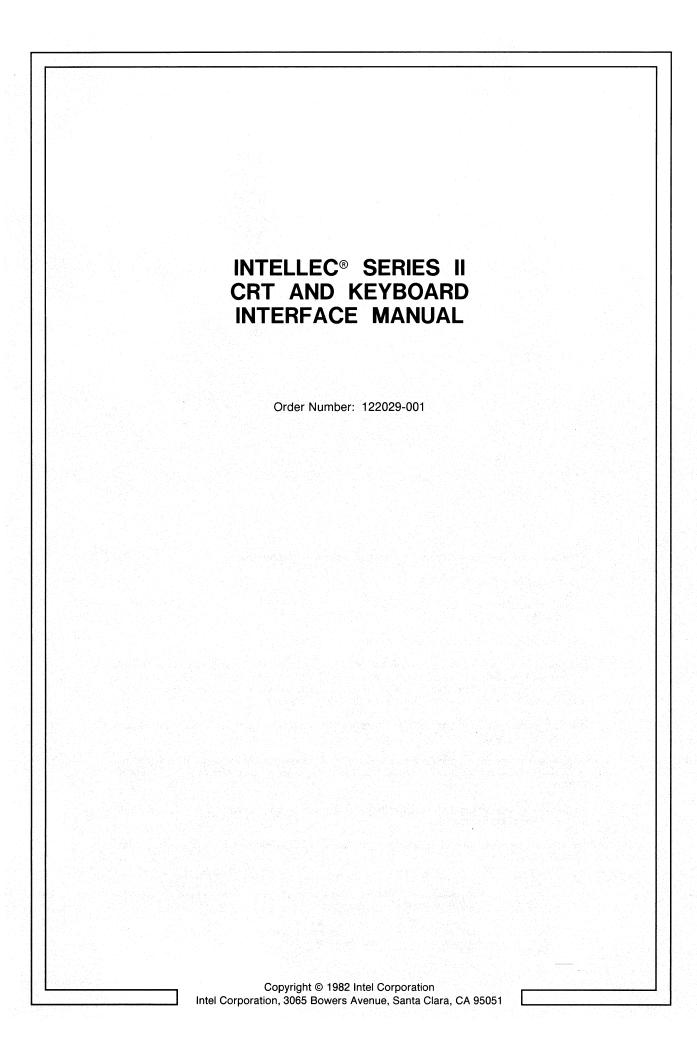
INTELLEC® SERIES II CRT AND KEYBOARD INTERFACE MANUAL

Order Number: 122029-001





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PREFACE



This manual describes the CRT and keyboard interface for the enhanced Series II development system.

It is divided into four chapters and two appendixes.

Chapter 1, "Introduction," briefly describes the capabilities of the enhanced Series II CRT and keyboard interface.

Chapter 2, "Preprogrammed Keys," lists the preprogrammed keys and their associated keywords.

Chapter 3, "Console Output Functions," describes the console output function codes with examples.

Chapter 4, "Block Movement of Data to the CRT," describes the IOC commands (with examples) that allow block movement of data to the CRT.

Appendix A, "Keyboard Codes Generated," lists the hexadecimal values of the Intel keyboard characters.

Appendix B, "ASCII Character Set," lists the ASCII codes with their hexadecimal values.

Related Publications

Model 511 IOC Firmware Enhancement Kit Installation Instructions, order number 122014

Intellec Series II Microcomputer Development System Hardware Interface Manual, order number 9800555

Intellec Series II Microcomputer Development System Hardware Reference Manual, order number 9800556

Component Data Catalog, order number 210298

Notational Conventions

UPPERCASE Indicates that characters shown in uppercase must be entered

in the order shown.

italic Indicates variable information.

[] Indicate optional arguments or parameters.



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CHAPTER 1 INTRODUCTION

The Series II CRT and keyboard are controlled by an Input/Output Controller Board, which is referred to as the IOC throughout this manual. All models of the Series II development systems provide cursor movement, clear screen and clear line functions, and display of the ASCII character set.

But the enhanced Series II provides much more:

- Automatic keystroke repeat function
- Preprogrammed keystroke sequences
- New console output functions, including direct cursor addressing
- Block movement of data to the CRT

These functions are provided by new IOC and keyboard firmware. (The cursor now appears as a solid, non-blinking block.)

If you have purchased the new firmware as an update package (iMDX 511), you will receive:

- Two single-density diskettes
- Two double-density diskettes
- Four 2716 PROMs (IOC CRT firmware)
- One 8741A PROM (IOC keyboard firmware)
- Two key caps (labeled FUNC)

Firmware installation is described in the *Model 511 IOC Firmware Enhancement Kit Installation Instructions*, order number 122014.

If you have purchased a new development system, the PROMS and key cap have already been integrated into your system.



CHAPTER 2 PREPROGRAMMED KEYS

The enhanced Series II IOC firmware provides numerous preprogrammed keys. When these keys and the special function key, FUNC, are pressed simultaneously, preprogrammed character sequences are transmitted as keyboard input. (The preprogrammed keys are listed below.)

For example, if you type FUNC A, the keyboard will respond as if you typed ALTER followed by a blank. Upper and lowercase are significant when typing preprogrammed keys. That is, if you type FUNC a, the keyboard will transmit alter in lowercase letters.

The HELP command, executed by typing FUNC H, displays all of the preprogrammed keys and their associated keywords on screen. When you invoke the HELP menu, the screen is cleared of current text. When you type any character, the HELP menu disappears and previous text is restored to the screen.

Keys

Quote marks are used to show significant spaces; they are not transmitted. (Remember, you must press FUNC simultaneously with the key you need.)

```
Α
         "ALTER"
C
         "COPY"
D
         "DIR"
E
         "CREDIT"
    _
I
         "ATTRIB"
    =
         "JOB"
J
K
         "DELETE"
L
         ":LP: "
M
         "LOGON"
         "ASSIGN"
N
O
         "LOGOFF"
P
         ":SP: "
R
         "RUN"
S
         "SUBMIT"
T
         " TO "
U
         "ACCESS"
X
         "EXPORT"
n
              = :Fn: (where <math>n = (0,1,...,9))
SHIFT + n
              = /JOBn < cr > (where n = (0,1,...,9))
Η
              = HELP — displays all of the function keys and their
                           preprogrammed keywords
```

Keywords

```
U
ACCESS
ALTER
               Α
ASSIGN
               N
ATTRIB
               I
COPY
               \mathbf{C}
CREDIT
               Ε
DELETE
               K
DIR
               D
EXPORT
               n \text{ (where } n = (0,1,...,9))
:Fn:
HELP
               Η
JOB
               SHIFT + n (where n = (0,1,...,9))
/JOBn
LOGON
LOGOFF
               0
:LP:
               L
RUN
               R
               P
:SP:
               S
SUBMIT
               T
TO
```

Repeat Function

To send multiple characters, simply hold down the key that you wish to repeat and it will repeat automatically. (The key will repeat after it is held down for 0.5 seconds and will continue to repeat at a rate of 15 characters per second until it is released.)



CHAPTER 3 CONSOLE OUTPUT FUNCTIONS

New Console Output Codes

The IOC firmware modifications provide several new console output functions.

The console output functions are obtained by sending the following codes to console output.

ESC R

Clear text from current cursor position to the end of the line.

ESC S

Clear text from current cursor position to the end of the screen.

ESC T

Clear entire screen (cursor remains in current position).

ESC Y

Allows direct cursor addressing.

This command requires coordinates for the row number and column number. (Row number is the row address + 20H; column number is the column address + 20H.)

The command has the following form:

ESC Y rc

where

r and c

are variables for row number and column number. If a value given for ror c is too large (greater than 38H for row; greater than 6FH for column), the current coordinate is used.

Example 1

1BH 59H 20H 20H

This command moves the cursor to the upper left corner of the screen. (20H, 20H indicates the upper left corner of the screen.)

ESC W

ESC W is a generalized insert and delete line function.

The command has the following form:

ESC W id

where

i and d

are line numbers taken from table 3-1.

The command inserts a blank line at line *i* on the screen and deletes the line at line *d*. All intervening lines scroll in the direction of the deleted line. The cursor moves to the start of the current line.

Line numbers can be presented in three ways: relative to the top of the screen, relative to the bottom of the screen, and relative to the current cursor position. Given that the CRT has exactly 25 lines, the relative-to-bottom settings are redundant. They are provided so that your cursor control sequence will be compatible with possible future terminals with other than 25 lines.

Example 1

1BH 57H 60H 3FH

This command inserts a blank line at the current cursor position.

Example 2

1BH 57H 3FH 60H

This command deletes the line at the current cursor position.

Table 3-1 lists the possible values for i and d.

Table 3-1. Line Coordinates for ESC W

Coordinate	Line
00H 01H 02H	Top line One below top Second below top
17H 18H	23rd from top 24th from top
27H	24th from bottom
3EH 3FH	One from bottom Bottom line
47H	24th line above current cursor position
5FH 60H 61H	Line above current cursor position Line at current cursor position Line below current cursor position
78Н	24th line below current cursor,position

ESC X

Allows you to set the following flags: USE\$8\$BITS\$FLAG and PAUSE\$FLAG.

The command has the following form:

ESC X ns

where

n is the flag number.

s is the setting (1 = true; 0 = false).

USE\$8\$BITS\$FLAG

Eight-bit codes are used to take advantage of the field attribute features provided by the 8275 CRT controller chip. (See the *Component Data Catalog* for information on the 8275 chip.)

The values in table 3-2, when sent to the CRT, are called "attribute bytes." Wherever they are placed on the CRT screen, a blank will appear in that position. Each succeeding position, until the next attribute byte appears, will have the attribute given.

Attributes can be combined by ORing together the appropriate attribute bytes; e.g., 92H causes blinking characters and reverse video. If blinking and reverse video are enabled simultaneously, only the characters will blink.

USE\$8\$BITS\$FLAG is set false to accommodate programs that send the eighth bit as a parity bit.

The following two ESC sequences are used to set USE\$8\$BITS\$FLAG:

1. 1BH 58H 00H 01H

All 8 bits of bytes received for console output are used.

2. 1BH 58H 00H 00H

The top bit is masked away (default setting).

Table 3-2. Attribute Bytes

Attribute Byte	Attribute	
80H	None	
82H	Blinking characters	
90H	Reverse video	
A0H	Underline	

PAUSE\$FLAG

The following two ESC sequences are used to set PAUSE\$FLAG:

1. 1BH 58H 01H 01H

Enter PAUSE mode.

- The IOC pauses (approximately five seconds) every time it is about to scroll the screen (when 20 lines have been output without any keyboard input).
- CNTL S stops output until something other than CNTL S is typed.
- If CNTL Q is typed, or if five seconds elapse, the screen will continue scrolling.
- 1BH 58H 01H 00H

No PAUSE mode (default setting).

Existing Console Output Codes

This chapter documents the console output functions available with the previous IOC firmware as well as the new firmware. Codes with the same function are grouped together; i.e., ESC A and up arrow are different codes, but they perform the same function.

ESC A up arrow

Move cursor up. If the cursor is at the top of the screen, the screen wraps and the cursor appears at the bottom of the screen.

ESC B down arrow CNTL J

Move cursor down. If the cursor is at the bottom of the screen, the screen scrolls.

ESC C right arrow

Move cursor right. If the cursor is at the end of the line, it moves to the beginning of the next line. If the cursor is at the end of the screen, the screen scrolls.

ESC D left arrow

Move cursor left. If the cursor is at the start of the line, the cursor moves to the end of the previous line. If the cursor is at the start of the screen, the cursor moves to the end of the screen.

ESC E

Clear entire screen; cursor moves to the beginning of the screen.

ESC H HOME

Move the cursor to the top left corner of the screen.

ESC J

Clear text from the beginning of the current line (the line on which the cursor is positioned) to the end of the screen.

ESC K

Clear entire line.

CNTL G

Ring bell.

RETURN

Move the cursor to the start of the following line.

RUBOUT

Do nothing.

All other control keys are ignored.

All other keys display the ASCII character.

Table 3-3 lists the standard CRT output controls, their hexadecimal values, and functions.

Table 3-3. Standard CRT Output Codes

Code	Hexadecimal	Function
Code	Value	A CONTRACTOR OF THE CONTRACTOR
ESC A up arrow	1B 41 1E	Move cursor up. Move cursor up.
ESC B down arrow CNTL J	1B 42 1C 0A	Move cursor down. Move cursor down. Move cursor down.
ESC C right arrow	1B 43 14	Move cursor right. Move cursor right.
ESC D left arrow	1B 44 1F	Move cursor left. Move cursor left.
ESC H HOME	1B 48 1D	Move cursor to beginning of screen. Move cursor to beginning of screen.
RETURN	0D	Move cursor to start of line.
ESC Y *	1B 59 <i>r c</i>	Move cursor to row r, column c.
ESC K	1B 4B	Clear line.
ESC R *	1B 52	Clear text from current cursor position to end of line.
ESC J	1B 4A	Clear text from current line to end of screen.
ESC S *	1B 53	Clear text from current cursor position to end of screen.
ESC T *	1B 54	Clear screen; cursor remains in current cursor position.
ESC E	1B 45	Clear screen; cursor moves to beginning of screen.
ESC W *	1B 57 <i>i d</i>	Insert a line at line i, delete a line at line d.
ESC X *	1B 58 <i>n s</i>	Set flags: USE\$8\$BITS\$ FLAG and PAUSE\$FLAG; $n = \text{flag}$ number; $s = \text{setting}$.
CNTL G	07	Ring bell.
RUBOUT	7F	

^{*} Indicates new console output codes.



CHAPTER 4 BLOCK MOVEMENT OF DATA TO THE CRT

To allow fast block movement of data to the CRT, several new IOC commands have been implemented as the Block Write function.

The Block Write function is invoked from application programs running on an enhanced Series II. It cannot be invoked through the keyboard.

The Block Write function is initiated by sending a 0FH, 2FH, 4FH, or 6FH command to the command port of the IOC.

The commands 0FH or 4FH require the line number and column number of the position on the screen where the data will be output to the data port following a 0FH or 4FH command. (0,0 is the top left corner of the screen.)

The commands 2FH or 6FH output data at the current cursor position. Table 4-1 summarizes the differences between the commands.

Data is transmitted as follows:

- 1. The Block Write command (0FH, 2FH, 4FH, or 6FH) is given to the IOC command port.
- 2. If the command is 0FH or 4FH, the row and column number are output to the IOC data port (00H 00H = upper left corner). If the values for row number and column number are too large (greater than 18H for row number; greater than 4FH for column number), the current coordinates are used.
- 3. A stream of console output data bytes is output to the IOC data port. If bytes are deposited to the end of the screen, the cursor wraps around to the top of the screen. Bytes are interpreted as follows:
 - Bytes less than 80H are deposited directly in screen memory, except for carriage return and line feed, which have their usual functions.
 - Bytes between 80H and 0FDH are interpreted as follows:

 If the command is 0FH or 2FH, byte *n* is treated as (*n*-80H) blanks.

 If the command is 4FH or 6FH, the byte is an attribute byte to be deposited directly in screen memory. (See Chapter 3, section "USE\$8\$BITS\$FLAG" for a description of attribute bytes.)
 - Byte 0FEH is dropped, but the next byte is placed in screen memory no matter what it is.
 - Byte 0FFH terminates the data and the command unless literalized as described above.

Figure 4-1 is a sample assembly language program that illustrates how to send commands and data to the IOC.

This program uses the following interface procedures to access the IOC commands:

- PCIOC transfers command byte to the IOC.
- PDIOC transfers each following data byte to the IOC.
- GDIOC retrieves data bytes from the IOC.

If there is a possibility of an interrupt procedure (or another processor) accessing the IOC, the interrupts should be disabled with a DI instruction at the beginning of the procedure and enabled with an EI instruction at the end of the procedure. Or, the interrupts can be masked at the beginning of the procedure and unmasked at the end.

Interrupts in this category include pressing Interrupt 0 on the Series II console, since the Series II monitor performs console output.

Similarly, the Series III RUN program communicates with the IOC and therefore precludes 8086 application programs from directly accessing the IOC.

Table 4-1. Block Write Commands

Command	Requires Coordinates	Action for High Bytes
0F	Yes	(n−80H) blanks
2F	No	(n-80H) blanks
4F	Yes	Attribute character
6F	No	Attribute character

n =value of byte

```
ISIS-II 8080/8085 MACRO ASSEMBLER, V4.1
                                                         FASTCO
                                                                    PAGE
 LOC OBJ
                      LINE
                                    SOURCE STATEMENT
                          1 NAME FASTCO
                          2 CSEG
                          4 ; FASTCO fills the CRT screen with the 2000 bytes of memory starting at BC.
                         5; All the bytes are assumed to be less than 80H.
                          7 FASTCO:
                            PUSH B
                                                 ; save the pointer ; IOC block move to CRT command
 0000 C5
                             MVI C, ØFH
 0001 0E0F
 0003 CD2400
                              CALL PCIOC
                                                  ; put out the command byte
                  c
                              LXI D, Ø CALL PDEIOC
                                                  ; coordinates for beginning of screen
 0446 110000
                        11
 0000 11000
0000 110007
                  С
                                                  ; put them out
                        12
                        13
                              LXI D,80*25
                                                    number of bytes being output
 000F E1
                              POP H
                                                  ; HL points to memory to be moved
                        14
                           FCLOOP:
                                                 ; count exhausted?
 0010 7A
                        16
                              MOV A,D
 0011 B3
                              ORA E
                        17
                              JZ FCEXIT
  0012 CA1E00
                                                  ; if so then all data is sent
 0015 4E
                        19
                              MOV C,M
                                                  ; put out one data byte
 0016 23
                              INX H
                        20
 0017 CD3400
                  C
                              CALL PDIOC
 001A 1B
                        22
                              DCX D
                                                 ; count down ; loop for next data byte
 031B C31000
                  С
                              JMP FCLOOP
                        25 FCEXIT:
 001E 0EFF
                              MVI C, ØFFH
                        26
                                                 ; termination byte
 0020 CD3400
0023 C9
                                                 ; send it to IOC; block movement to CRT is complete
                  C
                        27
                              CALL PDIOC
                        28
                              RET
                        31; PCIOC sends the C register as a command byte to the IOC.
                        32
                        33 PCIOC:
 0024 DBC1
                             IN ØC1H
                                                 ; check status of IOC
                                                 ; bottom three bits of status byte are examined ; they must be zero for IOC to be ready to receive ; IOC command byte
 0026 E607
                        35
                              ANI 111B
JNZ PCIOC
 0028 C22400
                 C
                        36
 002B 79
                              MOV A,C
 002C D3C1
                        38
                              OUT ØC1H
                                                 ; port Cl is the IOC command output port
 992E C9
                        39
                              RET
                        40
                        41; PDEIOC sends data bytes E then D to the IOC.
42; PDIOC sends the C register as a data byte to the IOC.
                        44 PDEIOC:
45 MOV C
                                                 ; E is the first data byte: parameter to PDIOC; send it; D is the second data byte
 002F 4B
                              MOV C,E
 0030 CD3400
0033 4A
                              CALL PDIOC
MOV C,D
                 C
                        46
                        47
                        48 PDIOC:
                              IN ØC1H
 0034 DBC1
                        49
                                                 ; check status of IOC
                                                 ; bottom three bits of status byte are examined ; they must be zero for IOC to be ready to receive
                              ANI 111B
 0036 E607
                  С
 0038 C23400
                        51
                              JNZ PDIOC
                                                 ; get byte to be output
 003B 79
003C D3C0
                        52
                              MOV A.C
                        53
                             OUT ØCØH
                                                 ; port CØ is the IOC data output port
 003E C9
                        55
                        56; GDIOC fetches an IOC data byte into the A register.
                        57
                        58 GDIOC:
                        59
 003F DBC1
                              IN ØC1H
                                                 ; check status of IOC
 0041 E607
0043 3D
                                                 ; bottom three bits of status byte are examined ; they must be 001 for IOC to be ready to send
                              ANI 111B
                              DCR A
                        61
 0044 C23F00
0047 DBC0
                              JNZ GDIOC
IN ØCØH
                  c
                                                 ; port CØ is the input port for IOC data
                        63
 0049 C9
                              RET
                        65
                        66 END
PUBLIC SYMBOLS
EXTERNAL SYMBOLS
USER SYMBOLS
                                        FCLOOP C 0010
                                                             GDIOC C 003F
                                                                                  PCIOC C 0024
                                                                                                      PDEIOC C 002F
                                                                                                                           PDIOC C 0034
                    FCEXIT C 001E
ASSEMBLY COMPLETE,
                       NO ERRORS
```

Figure 4-1. Sample Program: FASTCO



APPENDIX A KEYBOARD CODES GENERATED

This appendix lists all of the Intel keyboard characters and their hexadecimal values.

Key	Unshift TPWR Lock	Unshift TPWR Unlock	Shift	CNTL
A B C D E F G H J K L M N O P Q R S T U V W X Y Z O 1 2 3 4 \$ % & ' () = { } `	61 62 63 64 65 66 66 66 66 66 67 71 72 73 74 75 77 77 78 79 70 31 32 33 33 33 33 34 55 50 50 50 20 21 51 51 51 51 51 51 51 51 51 51 51 51 51	41 42 43 44 45 46 47 48 48 44 48 48 49 48 49 48 49 49 49 49 49 49 49 49 49 49 49 49 49	41 42 43 44 46 48 44 44 45 51 52 53 54 55 55 57 57 22 22 22 22 22 23 77 77 65 28 33 33 60 14 15 11 11 11 11 11 11 11 11 11 11 11 11	01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0F 10 11 12 13 14 15 16 17 18 19 1A

FUNC Key

When the FUNC key is pressed simultaneously with any other key (or key combination), the code returned depends on whether the IOC is in local mode or on-line mode.

If the IOC is in local mode, 80H is added to the code that would have been returned if FUNC had not been pressed. The 80H bit is output to the console only if the USE\$8\$BITS\$FLAG is set to true. (See Chapter 3, section "USE\$8\$BITS\$FLAG.")

If the IOC is in on-line mode, the preprogrammed sequence is returned as described in Chapter 2.

To put the IOC in local mode, type:

CNTL SHIFT L

(Hold down the CNTL key and the SHIFT key, then type L.)

To put the IOC in on-line mode, type:

CNTL SHIFT D



APPENDIX B ASCII CHARACTER SET

	1	<u> </u>	<u>,</u>
ASCII Character	HEX	ASCII Character	HEX
NUH STX EONOK EENO	00 01 02 03 04 05 06 07 08 09 0A 0B 0C 0D 0E 10 11 12 13 14 15 16 17 18 19 1A 1B 1D 1E 1F 20 21 22 23 24 25 26 27 28 29 20 21 31 32 33 34 35 36 37 38 38 39 30 30 31 31 32 33 34 36 37 38 38 38 38 38 38 38 38 38 38 38 38 38	@ABCDEFGH-JKLMNOPQRSTUVWXYN[/](), abcdefgh-jk-Enopgrstuvwxynf-}	40 41 42 43 44 45 46 47 48 49 44 44 45 51 52 53 55 55 55 55 55 55 55 56 61 62 63 64 65 66 67 68 66 67 77 77 77 78 77 77 77 77 78 77 77 77 77

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