

HA-2510/2512/2515

High Slew Rate Operational Amplifiers

FEATURES		DESCRIPTION						
HIGH SLEW RATE FAST SETTLING WIDE POWER BANDWIDTH HIGH GAIN BANDWIDTH HIGH INPUT IMPEDANCE LOW OFFSET CURRENT INTERNALLY COMPENSATED APPLICATIONS DATA ACQUISITION SYSTEMS R.F. AMPLIFIERS VIDEO AMPLIFIERS SIGNAL GENERATORS PULSE AMPLIFICATION	60V/μs 250ns 1,000kHz 12MHz 100MΩ 10nA	The HA-2510/2512/2515 are a series of high performance operational amplifiers which set the standards for maximum slew rate, highest accuracy and widest bandwidth for internally compensated monolithic devices. In addition to excellent dynamic characteristics, these dielectrically isolated amplifiers also offer low offset current and high input impedance. The ±60V/ μs slew rate and 250ns (0.1%) settling time of these amplifiers is ideally suited for high speed D/A, A/D, and pulse amplification designs. HA-2510/2512/2515's superior 12MHz gain bandwidth and 1000kHz power bandwidth is extremely useful in R.F. and video applications. For accurate signal conditioning these amplifiers also provide 10nA offset current, coupled with 100MΩ input impedance, and offset trim capability. The HA-2510/2512 are available in metal can (TO-99) and 14-pin flat packages. HA-2510 and HA-2512 are specified from -55°C to +125°C. HA-2515 is specified over the 0°C to +75°C range, and is available in the TO-99 package.						
PINOUT		SCHEMATIC						
TO-99 TOP VIEW BANDWIDTH CONTROL OFFSET ADJ OFFSET ADJ OFFSET ADJ	Package Code 2A	OFFEET OF 12K OF 18B NS						
TO-86 TOP VIEW	Package Code 9V	11.3% ON						

(HA-2510/2512 Only)

CAUTION: These devices are sensitive to electrostatic discharge. Users should follow IC Handling Procedures specified on pg. 1-4.

CASE

ABSOLUTE MAXIMUM RATINGS

Voltage Between V⁺ and V⁻ Terminals

Differential Input Voltage

40.0V ±15.0V **Peak Output Current** Internal Power Dissipation 50mA 300mW

Operating Temperature Range

HA-2510/HA-2512 HA-2515

 $^{-55^{0}C} \! \leq \! \mathsf{T}_{\mathsf{A}} \! \leq \! +125^{0}C \\ 0^{0}C \! \leq \! \mathsf{T}_{\mathsf{A}} \! \leq \! +75^{0}C$

Storage Temperature Range

 $-65^{\circ}C \le T_{A} \le +150^{\circ}C$

ELECTRICAL CHARACTERISTICS

V+ = +15V D.C., V- = 15V D.C.

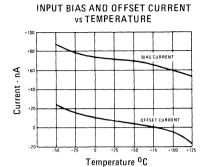
		ı			,			ı			1
		HA-2510 -55 ⁰ C to +125 ⁰ C			HA-2512 -55 ⁰ C to +125 ⁰ C			HA-2515 0°C to +75°C			
		LIMITS			LIMITS			LIMITS			
PARAMETER	TEMP.	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.	UNITS
INPUT CHARACTERISTICS * Offset Voltage	+25°C		4	8		5	10		5	10	mV
,	Full			11			14			14	mV
Offset Voltage Average Drift	Full		20			25			30		μV/ ⁰ C
* Bias Current	+25 ⁰ C Full		100	200 400		125	250 500		125	250 500	nA nA
* Offset Current	+25°C Full		10	25 50		20	50 100		20	50 100	nA nA
Input Resistance	+25°C	50	100		40.	100		40	100		мΩ
Common Mode Range	Full	±10.0			±10.0			±10.0			v
TRANSFER CHARACTERISTICS											
* Large Signal Voltage Gain (Note 1,4)	+25 ⁰ C Full	10K 7.5K	15K		7.5K 5K	15K		7.5K 5K	15K		V/V V/V
* Common Mode Rejection Ratio (Note 2)	Full	80	90		74	90		74	90		dB
Gain Bandwidth Product (Note 3)	+25°C		12			12			12		MHz
OUTPUT CHARACTERISTICS											
Output Voltage Swing (Note 1)	Full	±10.0	±12.0		±10.0	±12.0		±10.0	<u>+</u> 12.0		٧
* Output Current (Note 4)	+25 ⁰ C	<u>+</u> 10	<u>+</u> 20		<u>+</u> 10	<u>+</u> 20		<u>±</u> 10	<u>+</u> 20		mA
Full Power Bandwidth (Note 4)	+25°C	750	1000		600	1000		600	1000		kHz
TRANSIENT RESPONSE											
Rise Time (Notes 1, 5, 6 & 8)	+25°C		25	50		25	50		25	50	ns
Overshoot (Notes 1, 5, 7 & 8)	+25°C		25	40		25	50		25	50	%
* Slew Rate (Notes 1, 4, 5 & 8)	+25 ⁰ C	<u>+</u> 50	<u>+</u> 65		<u>+</u> 40	±60		<u>+</u> 40	<u>+</u> 60		V/ μ s
Settling Time (Notes 1, 4, 5 & 8)	+25 ⁰ C		0.25			0.25			0.25		μs
POWER SUPPLY CHARACTERISTICS											
* Supply Current	+25 ⁰ C		4	6		4	6		4	6	mA
* Power Supply Rejection Ratio (Note 9)	Full	80	90		74	90		74	90		dB

NOTES: 1. $R_L = 2K$ 2. $V_{CM} = \pm 5.0V$ 3. $A_V > 10$ 4. $V_O = \pm 10.0V$ 5. $C_L = 50pF$ 6. $V_O = \pm 400mV$

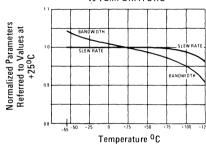
7. V_O = ±600mV
 8. See transient response test circuits and waveforms page four.
 9. \(\Delta \) V = ±5.0V

^{*100%} Tested For DASH 8

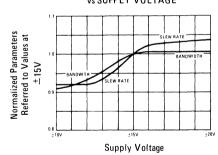
V+ = 15VDC, V- = 15VDC, $T_A = 25^{\circ}$ C UNLESS OTHERWISE STATED.



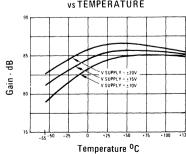




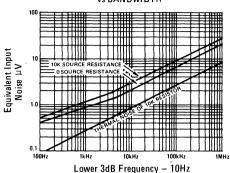
NORMALIZED AC PARAMETERS vs SUPPLY VOLTAGE



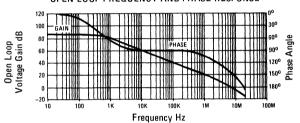
OPEN LOOP VOLTAGE GAIN vs TEMPERATURE



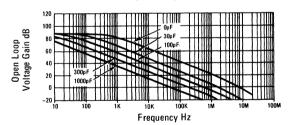
EQUIVALENT INPUT NOISE vs BANDWIDTH



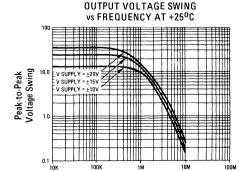
OPEN LOOP FREQUENCY AND PHASE RESPONSE



OPEN-LOOP FREQUENCY RESPONSE FOR VARIOUS VALUES OF CAPACITORS FROM BANDWIDTH CONTROL PIN TO GROUND

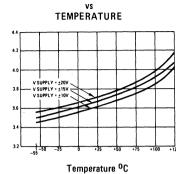


NOTE: External compensation components are not required for stability, but may be added to reduce bandwidth if desired.

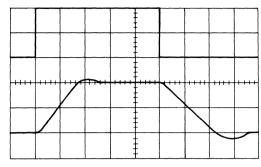


Frequency Hz

POWER SUPPLY CURRENT



VOLTAGE FOLLOWER PULSE RESPONSE



 $R_1 = 2K \Omega$, $C_1 = 50pF$

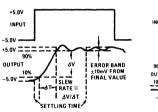
Upper Trace: Input Lower Trace: Output Vertical = 5V/Div.

Horizontal = 100n/Div.

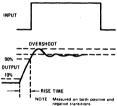
 $T_A = +25^{\circ}C$, $V_S = \pm 15.0V$

SLEW RATE AND SETTLING TIME

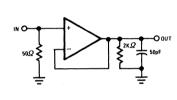
Current – mA



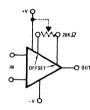
TRANSIENT RESPONSE



SLEW RATE AND TRANSIENT RESPONSE



SUGGESTED OFFSET ZERO ADJUST HOOK-UP



DEFINITIONS

INPUT OFFSET VOLTAGE - That voltage which must be applied between the input terminals through two equal resistances to force the output voltage to zero.

INPUT OFFSET CURRENT - The difference in the currents into the two input terminals when the output is at zero voltage.

INPUT BIAS CURRENT - The average of the currents flowing into the input terminals when the output is at zero voltage.

INPUT COMMON MODE VOLTAGE - The average referred to ground of the voltages at the two input terminals.

COMMON MODE RANGE - The range of voltages which is exceeded at either input terminal will cause the amplifier to cease operating.

COMMON MODE REJECTION RATIO - The ratio of a specified range of input common mode voltage to the peak-to-peak change in input offset voltage over this range.

OUTPUT VOLTAGE SWING - The peak symmetrical output voltage swing, referred to ground, that can be obtained without clipping.

INPUT RESISTANCE - The ratio of the change in input voltage to the change in input current.

OUTPUT RESISTANCE - The ratio of the change in output voltage to the change in output current.

VOLTAGE GAIN - The ratio of the change in output voltage to the change in input voltage producing it.

UNITY GAIN BANDWIDTH - The frequency at which the voltage gain of the amplifier is unity.