pb 250 Cinch interpreter

PBC 1006

pb Packard Bell Computer

A BUBSIDIARY OF PACKARD BELL ELECTRONICS

1905 ARMACOST AVENUE . LOS ANGELES 25, CALIFORNIA . GRANITE 8-4247

NOTICE

This document involves confidential PROPRIETARY information of Packard Bell Computer Corporation and all design, manufacturing, reproductions, use, and sale rights regarding the same are expressly reserved. It is submitted under a confidential relationship for a specified purpose, and the recipient, by accepting this document assumes custody and control and agrees that (a) this document will not be copied or reproduced in whole or in part, nor its contents revealed in any manner or to any person except to meet the purpose for which it was delivered, and (b) any special features peculiar to this design will not be incorporated in other projects.

When this document is not further required for the specific purposes for which it was submitted, the recipient agrees to return it.

PREFACE

The CINCH Manual is provided for information pertaining to coding and does not intend to cover the subject in its entirety. Certain additions, deletions and amendments will be incorporated in later editions.

PB 250 CINCH INTERPRETER

ERRATA SHEET

Page		
2-3	paragraph C, line 5	change to read:" space if not present." add footnote:" The space is not necessary but may be included for format purposes."
2-4	paragraph D, lime 8	add:"A carriage return following the termination character will clear the word following the alphanumeric data."
4-1	paragraph A, line 18	change to read: "another relevant code letter is read. A code letter must follow the termination character for alphanumeric data."
	line 19	delete sentence:"Tabs, in any position, are ignored."
	following line 22	add new indented paragraph:"When the operator makes an error, either in preparing a tape or entering information from the keyboard, the lower case key will reset the datum or command being typed. Commands have two sections, and the lower case key will only reset to the start of the section currently being entered. These sections are as follows:(a) everything up to and including the index register designation, and (b) the address. Data has only one section. After typing the lower case key, merely retype the current section."

\mathbf{Pa}	g	e
---------------	---	---

4-2	lines 1 through 3	delete lines 1 through 3; insert:" When a code letter appears on a tape or is typed, the next character to be read must follow immediately. There may be no spaces, tabs, carriage returns, etc., between the code letter and the first data character."
	line 10	change to read:" able tape. nnn may not be
	line 12	change to read: " location nnn. nnn may not be 0000."
	line 25	change to read: " Note the L, C, D, and E codes."
4-6	line 4	add new paragraph: "After the Enter Trace Mode (ETM) command is performed, all subsequent commands (with or without a trace tag) will be traced if the BREAKPOINT switch is down. This condition prevails until a Leave Trace Mode (LTM) command is per- formed. Blocks of instructions, or a whole program, may thus be traced without recourse to the trace tag."
5-1	line 3	change to read:" will be ready for one of ten external operations. See Appendix B for a further description of External Operations."
5-2	line 14	add:" The address may not be 0000."

Page 5-2	line 26	change to r	ead: "location. The address	;
5-3	line 15	-	ead: " lator will be typed in at. The D should not be type	ed."
			g, control is then returned to or another external operation	
		" Listable	tapes	
5-3	at foot of page	following . graph:	" end of tape." add new p	para-
" Type Index	Register Y	register (A or carriage limit of the typed. Cor	A-G) followed by a space, talk return. The current base a specified index register will atrol is returned to the keybo external operation."	b, and I be
6 - 2	at foot of page	-	" continues sequentially. ay not be 0000."	11
6-4	at foot of page	following .	" in sequence. " add:	
		all	ENTER TRACE MODE ne BREAKPOINT switch is do commands are traced until a M operation is encountered, LEAVE TRACE MODE see ETM"	

į.	Page		
	6-5	line 5	following " (See paragraph IV.A)" add: " M may not be 0000."
		line 8	following "(See paragraph IV. A)" add: " M may not be 0000."
1. 1	6 - 6	line 7	following " is truncated. " add: " Note that M is not an address, but an actual value."
	7-3	paragraph C, line 11	change to read: " line number 1." add footnote: " for CINCH/I use 1010."
	A-7	step 7	change to read: '' 7. Type: \$0100 C/R to set up program counter.
			Type: G to transfer control to the start of the program."
	B-4	paragraph l-4, line l	delete: " This operation may be used when a program has been debugged."
		line 6	change to read: " to be punched out and Δ represents a space or carriage return."
	B-5	paragraph 1-5, line 4	change to read: " $M0000_{\Delta}$ "
)		paragraph 1-6, line l	change title to read: "SET PROGRAM COUNTER"
	B-6	paragraph 1-8, line 1	change title to read: "TYPE PROGRAM COUNTER"
·.		paragraph 1-9, line 6	following" will be executed. " Leave two blank lines.
			4

Page

B-6

add new paragraph 1-10:

" 1-10. TYPE INDEX REGISTER

To type the current value of the base and limit of an index register, the operator must type

YIt

where I is the desired index register (A-G) and t is a space, tab, or carriage return. The increment is not typed."

C-l Table C-l, line 5

change to read: "location nnnn. nnnn may not be 0000."

line 8

change to read: " Set Program Counter to nnnn.

nnnn may not be 0000."

at foot of table

delete underscore, add new Type and Operation:

Y Ic Specifying index register; c may be space, tab, or carriage return.