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INSTALLATION PROCEDURE

PM-11D MEMORY MANAGEMENT UNIT

1.0 SCOPE

This document describes the procedure for installation of the PM-llD memory management unit manufactured by Plessey Peripheral Systems, Irvine, California 92714.

2.0 UNPACKING AND INSPECTION

Visually inspect the shipping container for any signs of damage. Open the shipping container, remove the packing material, and inspect the contants for damage. If the unit has been damaged in shipment, notify the transportation company immediately. Save the shipping container as evidence of damage for their inspection.

3.0 PREPARATION

Before starting to install the PM-llD be sure that the following equipment is available:

- Soldering iron (25-35 watt)
- Solder
- Diagonal cutters
- Medium, slot-type screwdriver
- Solder removal tool
- Alcohol
- Acid brush

4.0 PARTS

The following parts are supplied with the PM-llD:

- PM-11D memory management unit
- PM-llA stack limit module (optional)
- Capacitor 680pF +5%, 100V MICA
- Capacitor 560pF +5%, 100V MICA

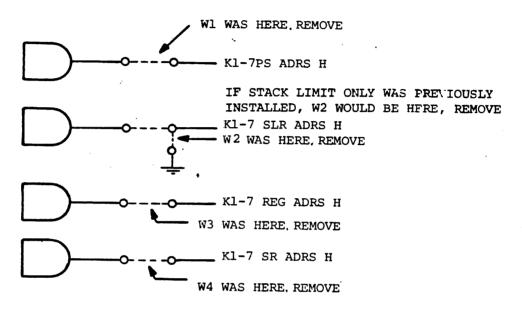
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FORM 000021

5.0 PROCESSOR PREPARATION

Refer to the PDP-11/35 or 11/40 system engineering drawings during installation.

Install the PM-llA stack limit module according to the instructions in the technical manual (MA 700405) provided with the unit.



Remove the M7231 module from the processor. Alter the jumpers as described.

Remove jumpers W1, W2, W3, and W4 as illustrated. Refer to data paths K1-7.

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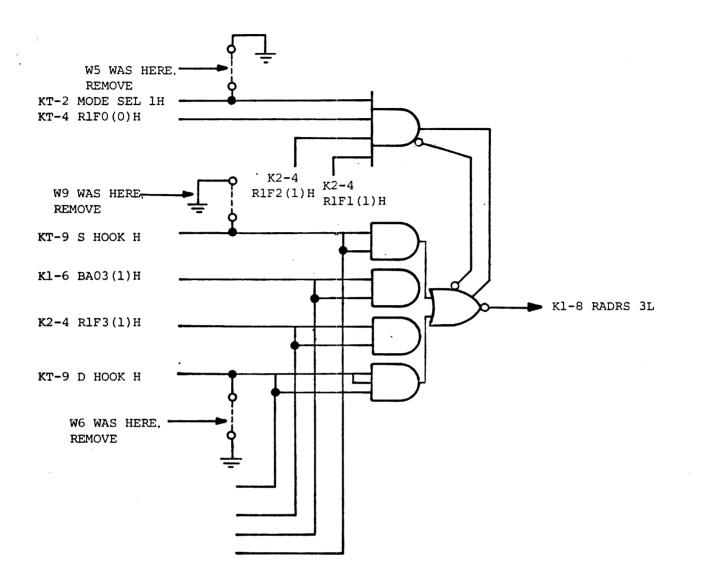
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Remove jumpers W5, W6, and W9 as illustrated. Refer to data paths K1-8.

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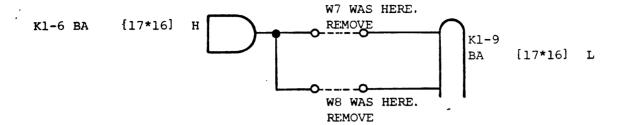
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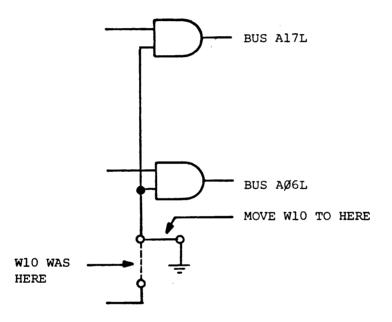
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Remove jumpers W7 and W8. Refer to data paths K1-9.



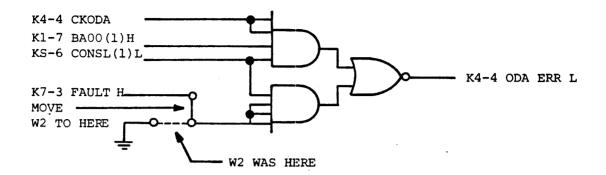
Move jumper W10 as shown. Refer to data paths K1-6.

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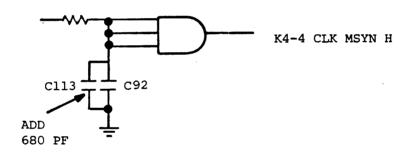
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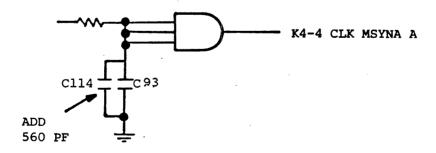
Remove the M7234 card from the processor.

Move jumper W2 as shown. Refer to timing BUS DATA CNTL, K4-4.



Install the 680pF capacitor in location Cll3 as illustrated.

NOTE: On early revision boards this capacitor was located at Cl07. Check your prints.



Install the 560pF capacitor in location Cl14 as illustrated.

NOTE: On early revision boards this capacitor was located at Cl06. Check your prints.

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Clean solder joints with alcohol.

Install M7231 and M7234 modules back into the CPU.

Install the PM-11D board into slot.

6.0 CHECKOUT

When installation is complete, it is recommended that the unit be checked out using the following diagnostics:

ZP103-RB KTllD document and papertape kit

MDEC-11-DBKTA-C-D-PB Basic logic test document, binary

papertape

MDEC-11-DBKTB-A-D-PB Access keys test document, binary

papertape

MDEC-11-DBKTC-A-D-PB MTPI/MFPI with memory management

test document, binary papertape

MDEC-11-DBKTD-A-D-PB Processor states test document, binary

papertape

MDEC-11-DCKBF-A1-D-A2-PB Stack limit test document, binary

papertape

MDEC-11-DBKTG-C-D-PB Exerciser document, binary papertape

MDEC-11-DBKTF-B-D-PB Memory management abort tests document,

binary papertape.

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