SMART-T INTERFACE
OPTION, PRIAM 8" DRIVE
UNPACKING AND INSTALLATION CHECKLIST

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								DESIG	IN .	SPC	72	⁶ 83	SCAL	E -				7
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SMART-T INTERFACE

OPTION, PRIAM 8" DRIVE

UNPACKING AND INSTALLATION CHECKLIST

This checklist is provided to aid you in unpacking and installing the PRIAM SMART-T Interface on a DISKOS 3450 or 7050 8" Winchester disc drive. Please follow these instructions carefully and check off the operations in the order they appear on the sheet. It is important for protection of your warranty that these instructions be followed exactly.

UNPACKING AND HANDLING

<u> </u> 1.	Inspect the exterior of the shipping container for obvious signs of shipping damage. If there are indications that the container is damaged, NOTIFY THE CARRIER AT ONCE.							
<u> </u>	If no damage Adapter, docu	If no damage is apparent, open the shipping container and remove ANSI Adapter, documentation, mounting hardware, and cables.						
<u> </u> 3.	Check materia	ls received in option package.	They	should	include:			
	PART NUMBER	DESCRIPTION	UNIT MEAS	OF	300105 QTY-XX			
	101081	TIE, CABLE SELF LOCKING	EA		3			
	101322	SUPPORT, 3/4" PCBA	EA		6			
	330420-XX	ASM, CABLE FLAT 50 COND 2.0 II			1			
	101 35 1	CONN, HOUSING W/LOCKING RAMP	EA		1			
	101352	CONN, TERMINAL	EA		2			
	Notify PRIAM	of any discrepancies.						
		INSTALLING THE SMART-T INTERF	ACE					
<u> </u> 1.	Disconnect po	ower to 8" drive.						
<u> </u> 2.	Lock spindle, using lever located below Jl of RW/DIGITAL (top) Printed Circuit Board Assembly (PCBA).							
	NOTE: The fo	NOTE: The following explanation will reference Figure 3.						
<u> </u>	Remove 6 mour	ting screws that hold the RW/D	IGITAL	, PCBA t	o frame.			
4.	Jl on the RW	end of the 50 pin flat cable P/ DIGITAL PCBA. Insure that Pinnects to Pin 1 of flat cable.	N 3304	20-XX, the con	to connector Jl	on		
<u> </u>	Secure RW/DIO	Secure RW/DIGITAL PCBA to the frame using six 3/4" PCBA supports, P/N						

1 6.	Insure that PRIAM terminator, P/N 200153 is installed in J4 on	the
	RW/DICITAL PCBA. If drive is last in string of only drive the	ntify
	that Pin 1 of terminator is inserted into Pin 1 of J4.	

T 7.	Set the s	ector switches	on the	RW/DIGITAL	PCBA	at	location	11K	as
11	desired.	Refer to Figur	re l.						

LOGICAL SIZE (Bytes)	PHYSICAL SIZE (Bytes)	SECTOR PER TRACK	SWITCH SETTING (Location 5H)
128	181	74	7, 4, 2 ON; 8, 6, 5, 3, 1 OFF
256	311	43	6, 4, 2, 1 ON; 8, 7, 5, 3 OFF
512	582	23	5, 3, 2, 1 ON; 8, 7, 6, 4 OFF
1024	1117	12	4, 3 ON; 8, 7, 6, 5, 2, 1 OFF

8. Set drive select switches on the RW/DIGITAL PCBA at location 6K for the required physical address. Refer to Figure 1.

Switch 6K-1 ON Switch 6K-2, 3, 4 OFF

This selects a drive address of one.

9. Set the clock selection switch on the RW/DIGITAL PCBA at location 6K for closed loop write clock operation by setting:

Switch 6K-7 ON. (See Figure 1).

10. Set write ENABLE switch on the RW/DIGITAL PCBA at location 6K to ENABLE writing by setting:

Switch 6K-8 ON. (See Figure 1).

11. Install jumper to DISABLE writing in the skip defect area by setting:
W1 jumpered
W2 not jumpered

NOTE: When W1 is jumpered the jumper will be parallel to I.C. alignment. (See Figure 1).

12.	Mount the SMART-T onto 3/4" PCBA supports. Press firmly until the supports come through mounting holes and lock. Orientation of the SMART-T PCBA should be as shown in Figure 3, with J1 of the SMART-T directly in line with J1 of the RW/DIGITAL PCBA.					
<u> </u> 13.	Connect loose end of 50 pin flat cable from Jl of the RW/DIGITAL PCBA to Jl of SMART-T PCBA and insure that Pin 1 of Jl connects to Pin 1 of flat cable.					
14.	Construct a power cable using the connector housing, P/N 101351, two (2) terminals, P/N 101352, and two (2) 18 AWG wires, one (1) RED and one (1) BLACK (the wire is user supplied). Refer to Figure 4 for the correct positions of the RED and BLACK wire in the connector. The RED wire must go to the +5 volt terminal of the user supplied power source, and the BLACK wire must go to the ground terminal.					
<u> </u> 15.	Route and secure the power cable using tie wraps as shown in Figure 3.					
16.	Connect the 2 pin power connector to J3 of SMART-T PCBA. Refer to Figure 2. IMPORTANT: Insure that lip of connector is facing up, locks with J3 receptacle, and the RED, +5 volt wire is toward the outside edge of the PCBA. INCORRECT ORIENTATION WILL RESULT IN DAMAGE TO PCBA.					
<u> </u> 17.	Connect Host (40 Pin) flat cable to J2 of SMART-T Interface.					
18.	Set the SMART-T controls at location 7B as desired. These switches are set to define the power up of reset default configuration of the SMART-T. These functions may also be specified under software control.					
<u> </u> 19.	Unlock spindle.					
	SMART-T SWITCHES/CONTROLS					
SWITCH	FUNCTIONS					
1-5	These switches should be off.					
6	ON - Enables an interrupt when the power up/initiali- zation sequence has completed.					
7	ON - Enables an interrupt when a command has completed.					
8	ON - Enables an interrupt when a host bus parity error has been detected.					

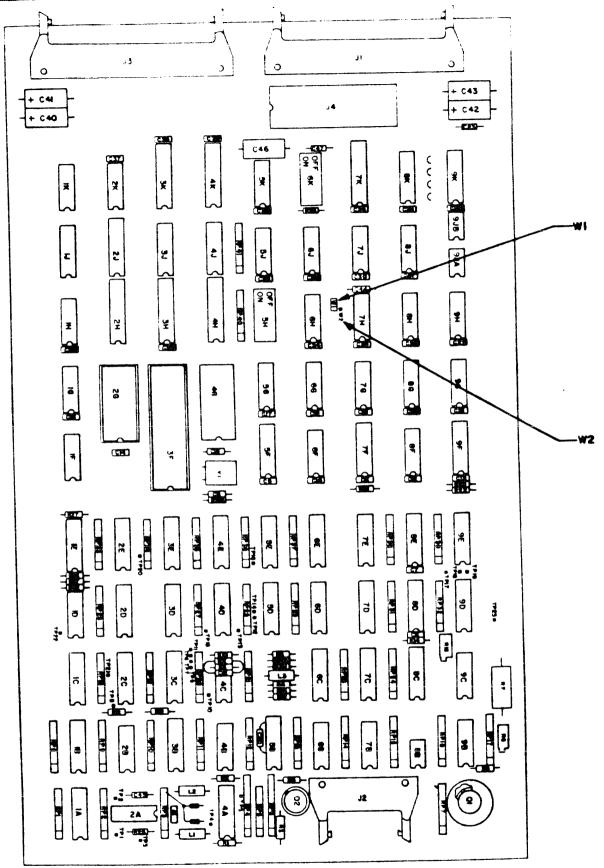


FIGURE 1 - R/W & DIGITAL PCBA JUMPER LOCATIONS

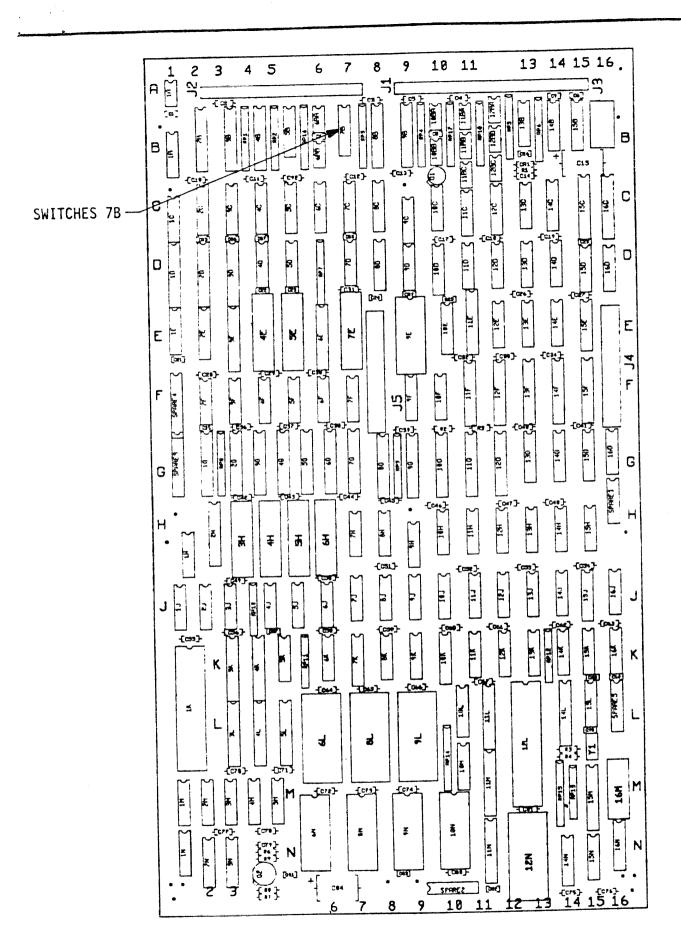


FIGURE 2 - SMART-T

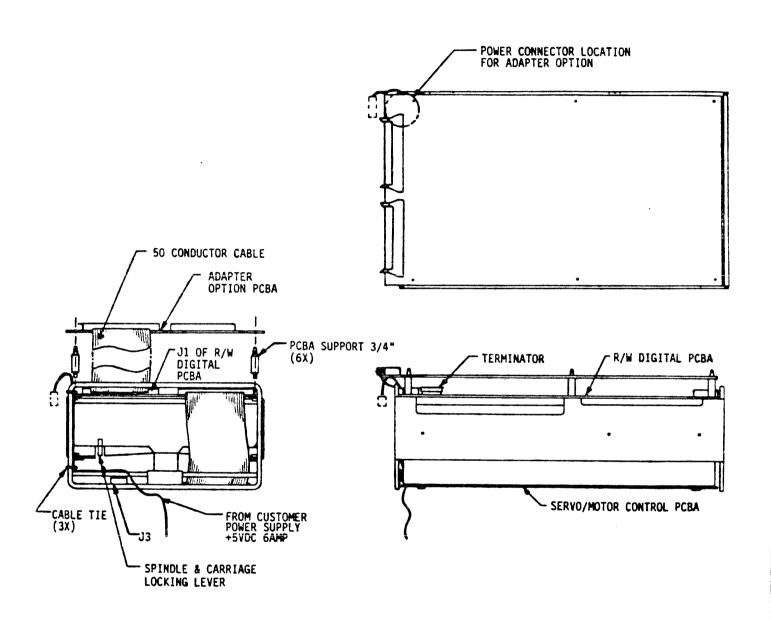


FIGURE 3 - SMART-T INSTALLATION

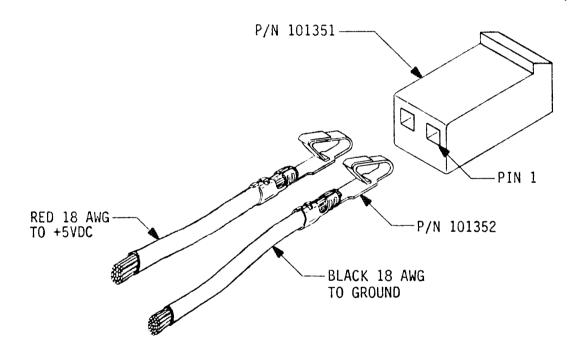


FIGURE 4 - SMART-T POWER CABLE