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QUALITY SOFTWARE

700 PROGRAMMING SYSTEMS

MP FLOATING LOAD

DATE: February 1968
ID CODE: BPV
DRAWING: 391075
LABEL: FLD
AUTHOR: STVL
SOURCE: SYM I Assembly Language
OBJECT: Relocatable

PURPOSE

To load into the software registers MNT1, MNT2, MNT3, a mid-precision floating point number set in three consecutive words of memory.

USAGE

Calling Sequence

```
L-1    SMB    FLD
L      JSX    FLD
L+1   D      FARG
L+2   return
```

The routine will return to L+2 with the contents of MNT2 in the hardware accumulator.

Argument Description

FARG is the first of three consecutive words of memory containing a floating point number. The first will be an exponent; the remaining two will be a normalized two-word mantissa.

Storage Requirements

Four words of common storage: RET1, MNT1, MNT2, MNT3.

METHOD

Indexed loads and direct stores constitute the entire logic.

RESTRICTIONS

Entries

FLD

Other Routines

None.

External Constants

None.

Space Used

10 words

Timing

19 cycles.

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700 PROGRAMMING SYSTEMS

MP FLOATING LOAD

APPENDIX A

ASSEMBLY LISTING

of

MP FLOATING LOAD

Drawing No.

391075 (Revision B)

ID Code

BPV

MATH MP FLOATING LOAD DN391075 E'

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? MP FLOATING LOAD DN391075 E'
3 BLK MATH
4 LIBR FLD
5 STX RET1
6 LDX * 0
7 LDW * 0
8 STW MNT0
9 LDW * 2
10 STW MNT3
11 LDW * 1
12 STW MNT2
13 LDX RET1
14 JMP * 1
15 *****
16 NTWY FLD
17 END
0 009 0 *****9

X=REF

LIB 0 000 0 FLU
EXT 0003 MNT0 0 005 0
EXT 0007 MNT2 0 007 0
EXT 0005 MNT3 0 005 0
EXT 0008 RET1 0 000 0 0 008 0

NO ERRORS

CARDS SYMBOLS LTR STACK
17 5 625 0 2

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PASS 2

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PASS 2

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700 PROGRAMMING SYSTEMS

MP FLOATING STORE

QUALITY SOFTWARE

DATE: February 1968
ID CODE: BPW
DRAWING: 391077 (Rev B)
LABEL: FST
AUTHOR: STVL
SOURCE: SYM I Assembly Language
OBJECT: Relocatable

PURPOSE

To store into three consecutive words of memory a mid precision floating point number set in the software registers MNT1, MNT2, MNT3.

USAGE

Calling Sequence

L-1 SMB FST
L JSX FST
L+1 D FARG
L+2 return

The routine will return to L+2 with the contents of MNT2 in the hardware accumulator.

Argument description

FARG is the first of three consecutive words of memory. The first will be an exponent; the remaining two will be a Double Precision Normalized word.

Storage Requirements

Four words of common storage: RET1, MNT1, MNT2, MNT3.

METHOD

Direct loads and indexed stores constitute the entire logic.

RESTRICTIONS

Entries

FST

Other Routines

None.

External Constants

None.

Space Used

10 words.

Timing

19 cycles.

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700 PROGRAMMING SYSTEMS

MP FLOATING STORE

APPENDIX A

ASSEMBLY LISTING

of

MP FLOATING STORE

Drawing No.

391077 (Revision B)

ID Code

BPW

MATH MP FLOATING STORE DN391077 B!

09/27/68

PAGE 2

2 IMP FLOATING STORE DN391077 B!
3 BLK MATH
4 LIBR FST
5 STX RET1
6 LDX * 0
7 LDW MNT0
8 STW * 0
9 LDW MNT3
10 STW * 2
11 LDW MNT2
12 STW * 1
13 LDX RET1
14 JMP * 1
15 *****
16 NTHY FST
17 END
0 009 0*****9

X=REF

LIB 0 000 0 FST
EXT 0002 MNT0 0 002 0
EXT 0006 MNT2 0 006 0
EXT 0004 MNT3 0 004 0
EXT 0008 RET1 0 000 0 0 008 0

NO ERRORS

CARDS SYMBOLS LIBR STACK
17 5 625 0 2

BPW 0004
BPW 0005
BPW 0006
BPW 0007
BPW 0008
BPW 0009
BPW 0010
BPW 0011
BPW 0012
BPW 0013
BPW 0014
BPW 0015
BPW 0016
BPW 0017
BPW 0018
BPW 0019

PASS 2

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700 PROGRAMMING SYSTEMS

CONVERT FIXED POINT TO MP FLOAT

DATE: December 1968
ID CODE: BRF
DRAWING: 391094 (Rev C)
LABEL: FLT, FLT2
AUTHOR: JACQ
SOURCE: SYM I Assembly Language
OBJECT: Relocatable in Block "MATH"

PURPOSE

This subroutine converts data given in the single or double precision fixed point formats to a normalized mid precision floating point number. The result is stored in the software register MNT1, MNT2, MNT3.

USAGE

Scale Factor

The binary point in a fixed point number is normally considered to be between bits 0 and 1, i.e., immediately in front of the first magnitude bit. The use of an algebraic integer N as a scale factor permits to shift the point N bit positions from its normal location, either to the right if the factor is positive, or to the left if it is negative. That is equivalent to multiplying the fixed point number by 2^N prior to conversion, but without altering the number.

Calling Sequences

FLT refers to a single precision fixed point number. FLT2 refers to a double precision fixed point number. The calling sequence is as follows:

L-1	SMB	FLT (or FLT2)
L	JSX	FLT (or FLT2)
L+1	DATA	ARG
L+2	DATA	N + X'8000'
L+3	Return	

Where ARG is the address of the fixed point number, and 2^N is the scale.

To convert from an unscaled integer, the scale factor N = 15 (FLT) or N = 30 (FLT2) need not be given:

L-1 SMB FLT (or FLT2)
 JSX FLT (or FLT2)
 DATA ARG + X'8000'

Using the standard subroutine calls available with SYM I and SYM II is more convenient:

SYM I

SMB FLT (or FLT2)
JSX FLT (or FLT2), ARG, N

or:

SMB FLT (or FLT2)
JSX FLT (or FLT2), ARG

SYM II

FLT (or FLT2) ARG, N

or:

FLT (or FLT2) ARG

METHOD

The argument is normalized, i. e., shifted so that its most significant bit is brought into bit 1 of the higher word of the mantissa. In the process the exponent is decremented by 1 for each bit shifted, from an initial value equal to the scale factor plus the bias of the exponent. Exponent and mantissa are stored in the software registers MNT1, MNT2, MNT3.

A zero argument yields three zero words.

A negative argument with a magnitude equal to an integer power of two is given the unnormalized mantissa: X'C000', 0.

ACCURACY

Not applicable.

RESTRICTIONS

Scale Factor N

-129 < N < 128

Entries

FLT, FLT2

Loading

This subroutine locally references storage words and constants located in the "MATH POOL" module, and must therefore be loaded in the same 2k block as the pool.

Other Routines

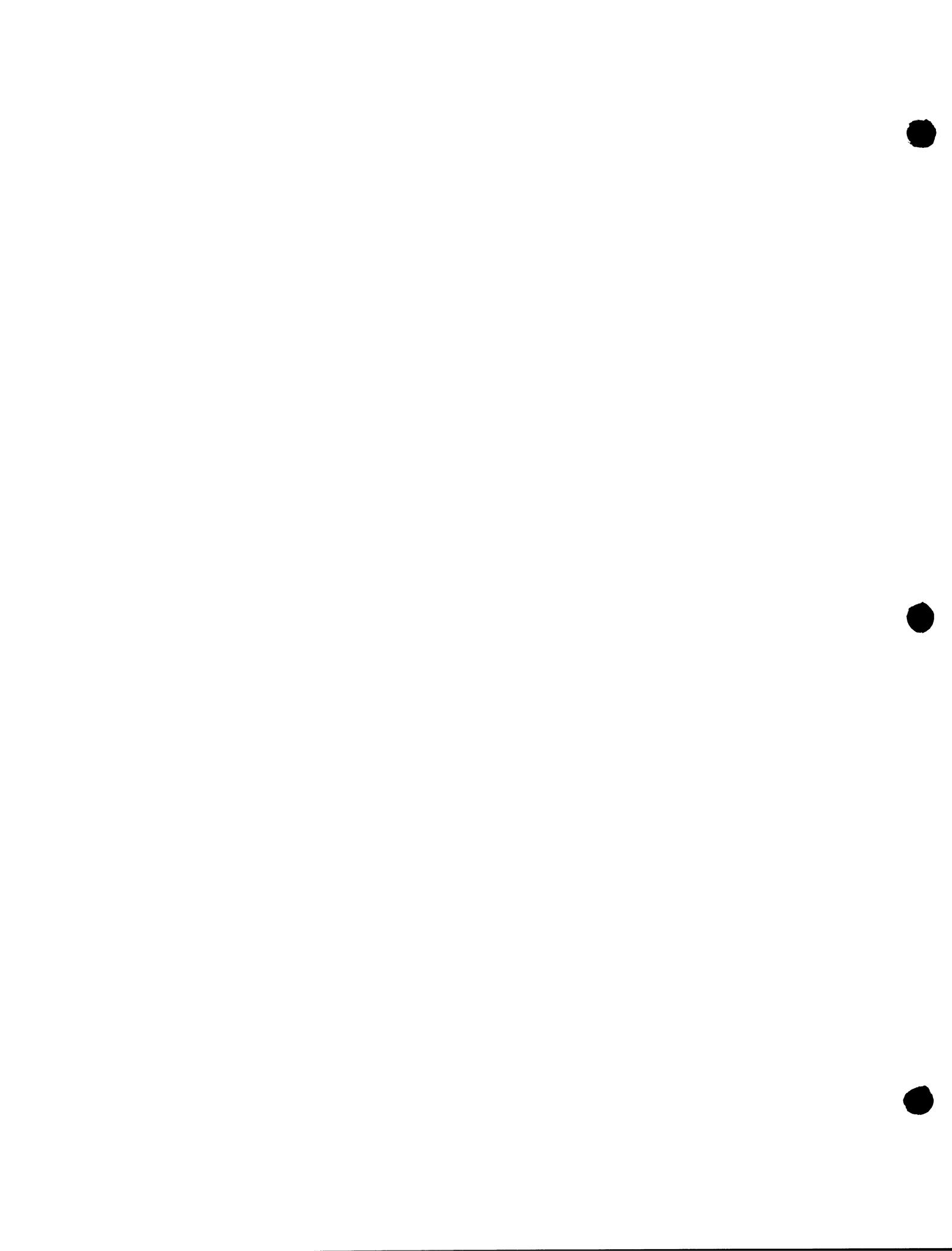
FNRM

Space Used

29 words

Timing (in machine cycles)

	<u>FLT</u>	<u>FLT2</u>
Minimum	41	42
Maximum	161	180
Average	116	127



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700 PROGRAMMING SYSTEMS

CONVERT FIXED POINT TO MP FLOAT

APPENDIX A

ASSEMBLY LISTING

of

CONVERT FIXED POINT TO MP FLOAT

Drawing No.

391094

ID Code

BRF

```

1 * CONVERT FIX PT TO MP FLOAT DN391094 C
2     BLK MATH
3     LIBR FLT,FLT2
4 ****
5     EQU S
6     RET1
7     LDX #0
8     LDW #0
9     STW MAT2
10    CLR MNT3
11    STW L13
12    JMP BOTH
13    EQU S
14    FLT2
15    STX RET1
16    LDX #0
17    LDW #0
18    STW MAT2
19    LDW #1
20    STW MNT3
21    LDW 0158
22    BOTH
23    XP NDSC
24    JMP S
25    SCAL
26    L1B 128
27    LDX RET1
28    ADD #1
29    AND D275
30    NDSC
31    STW MAT1
32    JSX FNRN
33    LDX RET1
34    LDW #0
35    SAP
36    JMP #1
37    JMP #2
38    0158
39    NTRY FLT,FLT2
40    END

RF: C0000
RF: C0010
RF: C0020
RF: C0030
RF: 00040
RF: 00050
RF: 00060
RF: 00070
RF: 00080
RF: 00090
RF: 10000
RF: 10010
RF: 10020
RF: 10030
RF: 10040
RF: 10050
RF: 10060
RF: 1D070
RF: 10080
RF: 10090
RF: 20000
RF: 20010
RF: 20020
RF: 20030
RF: 20040
RF: 20050
RF: 20060
RF: 20070
RF: 20080
RF: 20090
RF: 30000
RF: 30010
RF: 30020
RF: 30030
RF: 30040
RF: 30050
RF: 30060
RF: 30070
RF: 30080
RF: 30090

```

```

0014 D255
0016 FNRR
0015 MNJ1
0018 MNK2
0008 MNF3
0000 MNF3
0017 RET1

```

NO ERRORS

CONVERT FIX #1 TO MP FLOAT 20191094 C

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BC TH	000F	D158	001C	D255	0014	FLT	0000
FLT2	000E	FNRM	0016	MATH	0000	MNT1	0015
MNT2	000E	MNT2	000D	NOSC	0015	RETI	0017
SCAL	0011						



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700 PROGRAMMING SYSTEMS**CONVERT MP FLOAT TO DP FIX**

DATE: December 1968
ID CODE: BYY
DRAWING: 392338
LABEL: FIX2
AUTHOR: JACQ
SOURCE: SYM I Assembly Language
OBJECT: Relocatable in Block "MATH"

PURPOSE

This subroutine converts data from the mid precision floating point format to the double precision fixed point format. The latter consists in two consecutive words in two's complement form, where in the first word bit 0 is reserved to the sign and bits 1 - 15 are the most significant half, while in the second word bit 0 is always zero and bits 1 - 15 are the least significant half.

The binary point is normally located between bits 0 and 1 of the most significant word. However, the use of a binary scale, extrinsic to the format, permits to consider the point to the left or right of its normal location.

A fixed point number can be considered as an integer by shifting the binary point to the right of the least significant bit.

USAGE**Scale Factor**

The use of an algebraic integer N as a scale factor permits to shift the binary point, in the fixed point number, N bit positions from its normal location, either to the right if the factor is positive, or to the left if it is negative. That is equivalent to scaling the floating point number up or down by 2^N prior to the conversion, but without upsetting the given number.

Calling Sequences

The floating point number to convert is set in the software registers MNT1, MNT2, MNT3, before calling the subroutine FIX_2 as follows:

```
L-1    SMB      FIX2
L     JSX      FIX2
L+1   DATA     STORE
L+2   DATA     N + X'8000'
L+3   Return
```

Where STORE is the address of the converted number and 2^N is the scale.

To convert to an unscaled integer, $N = 30$ need not be given:

```
L-1    SMB      FIX2
L     JSX      FIX2
L+1   DATA     STORE + X'8000'
L+2   Return
```

Those sequences are coded more conveniently with the standard subroutine calls available in SYM I and SYM II:

SYM I

```
SMB      FIX2
JSX      FIX2,STORE,N
```

or:

```
SMB      FIX2
JSX      FIX2,STORE
```

SYM II

```
FIX2      STORE,N
```

or:

```
FIX2      STORE
```

The floating point number set in MNT1, MNT2, MNT3, is not upset by the subroutine FIX2.

METHOD

The mantissa X_m of the floating point number, $X = X_m \cdot 2^{x_e}$, is shifted to the right by a number of bit positions S , if S is positive. The shift count S is the lesser of $N - x_e$ or 30. A negative mantissa is converted to one's complement prior to the shift. The shifted mantissa is restored to two's complement and stored as the result.

A negative shift count S shows an overflow condition. The result is in this case replaced with $\pm(1-2^{-30})$.

ERROR CONDITIONS

An overflow conditions occurs, and the result is then set to $\pm(1-2^{-30})$, when the exponent of the floating point number is greater than the scale factor.

ACCURACY

Not applicable.

RESTRICTIONS

Scale Factor N

$-129 < N < 128$

Entry

FIX2

Loading

This subroutine locally references storage words and constants located in the "MATH POOL" module, and must therefore be loaded in the same 2k block as the pool.

Other Routines

FAD

Space Used

38 words

Timing (in machine cycles)

Minimum	33
Maximum	105
Average	98



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700 PROGRAMMING SYSTEMS

CONVERT MP FLOAT TO DP FIX

APPENDIX A

ASSEMBLY LISTING

of

CONVERT MP FLOAT TO DP FIX

Drawing No.

392338

ID Code

BYY

```

1 *CONVERT MP FLOAT TO DP FIX DN392338 A
2      BLK MATH
3      LIBR FIX2
4 *****
5 *NEXT 2 CARDS TO AGREE WITH FAD
6 FADS EQU X'13'
7 FADX EQU X'3A'
8 *****
9 FIX2 EQU '$
10      SIX RETI
11      LDW * 0
12      STW TMP4
13      LLB 30
14      SAM
15      LDW * 1
16      ADD D128
17      AND D255
18      SUB MN11
19      LDX * 0
20      SAP
21      JMP OVER
22      STW TMP1
23      LDW MN12
24      STW * 0
25      LDW MN13
26      STW * 1
27      LDX FADC
28      LDW BACK
29      STW * FAD
30      LDW TMP1
31      JMP * FA05
32      BACK JMP EXIT
33      OVER EQU $
34      LDW MN12
35      SAZ ORI M15R
36      SAP
37      ORE M15R
38      STW * 0
39      SAP
40      SLC 1
41      STW * 1
42      EXIT EQU $
43      LDW RETI
44      LDW * 0
45      SAP
46      JMP * 1
47      JMP * 2
48      FAD
49      D FAD
50      NTRY FIX2
51      END
YY 00000
YY 00010
YY 00020
YY 00030
YY 00040
YY 00050
YY 00060
YY 00070
YY 00080
YY 00090
YY 00100
YY 00110
YY 00120
YY 00130
YY 00140
YY 00150
YY 00160
YY 00170
YY 00180
YY 00190
YY 00200
YY 00210
YY 00220
YY 00230
YY 00240
YY 00250
YY 00260
YY 00270
YY 00280
YY 00290
YY 00300
YY 00310
YY 00320
YY 00330
YY 00340
YY 00350
YY 00360
YY 00370
YY 00380
YY 00390
YY 00400
YY 00410
YY 00420
YY 00430
YY 00440
YY 00450
YY 00460
YY 00470
YY 00480
YY 00490
YY 00500
0006 0122
0007 0255
0025 FAD

```

CONVERT MP FLOAT TO DP FIX D0392338 A

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0018 M15F
0019 M11
0017 M1 T2
0016 M1 T3
0020 RET11
0014 TMP1
0002 TMP4

NO ERRORS

CONVERT MP FLOAT TO OP FIX 3192338 A 12/19/68 PAGE 4

BACK	001E	0128	0006	0255	0007	EXIT	0020
FAD	0C25	FADC	0025	FADS	0013	FADX	001A
FIX2	0C0C	M15R	001B	MATH	0000	MNT1	0008
INT2	0017	MNT3	000F	OVER	0017	RETI	0020
IMP1	0014	IMP4	0002				

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700 PROGRAMMING SYSTEMS

CONVERT MP FLOAT TO SP FIX

DATE: December 1968
ID CODE: BYZ
DRAWING: 392339
LABEL: FIX
AUTHOR: JACQ
SOURCE: SYM I Assembly Language
OBJECT: Relocatable in Block "MATH"

PURPOSE

This subroutine converts data from the mid precision floating point format to the single precision fixed point format. A single precision fixed point number consists in one word in two's complement form, where bit 0 and bits 1 - 15 are reserved respectively to sign and magnitude.

The binary point is normally located between bits 0 and 1. However, the use of a binary scale, extrinsic to the format, permits to consider the point at any position to the left or right of its normal location.

A fixed point number can be considered as an integer by shifting the binary point to the right of the least magnitude bit.

USAGE

Scale Factor

The use of an algebraic integer N as a scale factor permits to shift the binary point, in the fixed point number, N bit positions from its normal location, either to the right if the factor is positive, or to the left if it is negative. That is equivalent to scaling the floating point number up or down by 2^N prior to the conversion, but without upsetting the given number.

Calling Sequences

The floating point number to convert is set in the software registers, MNT1, MNT2, MNT3, before calling the subroutine FIX as follows:

```
L-1    SMB      FIX
L     JSX      FIX
L+1   DATA     STORE
L+2   DATA     N + X'8000'
L+3   Return
```

Where STORE is the address of the converted number and 2N is the scale.

To convert to an unscaled integer, N = 15 need not be given:

```
L-1    SMB      FIX
L     JSX      FIX
L+1   DATA     STORE + X'8000'
L+2   Return
```

Those sequences are coded more conveniently with the standard subroutine calls available in SYM I and SYM II:

SYM I

```
SMB      FIX
JSX      FIX, STORE, N
```

or:

```
SMB      FIX
JSX      FIX, STORE
```

SYM II

```
FIX      STORE, N
```

or

```
FIX      STORE
```

The floating point number set in MNT1, MNT2, MNT3, is not upset by the subroutine FIX.

METHOD

The mantissa Xm of the floating point number, $X = Xm \cdot 2^{xe}$, is shifted to the right by a number of bit positions S, if S is positive. The shift count S is the lesser of N-Xe or 15. A negative mantissa is converted to one's complement prior to the shift. The most significant half of the shifted mantissa is restored to two's complement and stored as the result.

A negative shift count S shows an overflow condition. The result is in this case replaced with $\pm(1 - 2^{-15})$.

ERROR CONDITIONS

An overflow condition occurs, and the result is then set to $\pm(1 - 2^{-15})$, when the exponent of the floating point number is greater than the scale factor.

ACCURACY

Not applicable.

RESTRICTIONSScale Factor N

~~-129 < N < 128~~

Entry

FIX

Loading

This subroutine locally references storage words and constants located in the "MATH POOL" module, and must therefore be loaded in the same 2k block as the pool.

Other Routines

None

Space Used

39 words

Timing (in machine cycles)

Minimum	29
Maximum	51
Average	45



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QUALITY SOFTWARE

700 PROGRAMMING SYSTEMS

CONVERT MP FLOAT TO SP FIX

APPENDIX A

ASSEMBLY LISTING

of

CONVERT MP FLOAT TO SP FIX

Drawing No.

392339

ID Code

BYZ

```

1 * CONVERT MP FLOAT TO SP FIX DN392339 A
2      BLK MATH
3      LIBR FIX
4 *****
5 FIX   EQU S
6      STX RETI
7      LDW * 0
8      LIB 15
9      SAM
10     LDW * 1
11     ADD DL23
12     AND D255
13     SUB MNT1
14     SAP
15     JMP OVER
16     CAX
17     OVS
18     LIB 15
19     AND S-1
20     STB SHFT1
21     LDW MNT2
22     LDX MNT3
23     SAP
24     DXS 1
25     JMP S+2
26     SUB D1
27     SHFT SRA 0
28     SAP
29     ADD D1
30     EQU S
31     LDX RETI
32     LDW * 0
33     STW * 0
34     CXA
35     RETI
36     SAP
37     JMP * 1
38     JMP * 2
39     OVER EQU S
40     LDW MNT2
41     SAZ
42     ORI M15R
43     SAP
44     LDW NMIX
45     JMP STOR
46     NMIX D -12767
47     NTRY FIX
48     END

0017 01
0005 D128
0006 0255
0022 M15R
0007 MNT1

```

```

YZ CO000
YZ CO010
YZ CO020
YZ CO030
YZ CO040
YZ CO050
YZ CO060
YZ CO070
YZ CO080
YZ CO090
YZ CO100
YZ CO110
YZ CO120
YZ CO130
YZ CO140
YZ CO150
YZ CO160
YZ CO170
YZ CO180
YZ CO190
YZ CO200
YZ CO210
YZ CO220
YZ CO230
YZ CO240
YZ CO250
YZ CO260
YZ CO270
YZ CO280
YZ CO290
YZ CO300
YZ CO310
YZ CO320
YZ CO330
YZ CO340
YZ CO350
YZ CO360
YZ CO410
YZ CO420
YZ CO430
YZ CO440
YZ CO450
YZ CO460
YZ CO470

```

CONVERT MP FLOAT TO SP FIX 00392339 A 12/19/68 PAGE 3

0020 MANT2
0010 MANT3
0010 RET1

NO ERRORS

CONVERT MP FLGAT TO SP FIX DM392339 A 12/19/68 PAGE 4

.01	00.17	01.28	00.05	02.55	0006	FIX	0000
M15R	00.22	MATH	00.00	MNT1	0007	MNT2	0020
MAT3	001C	NMAX	0026	OVER	0020	RETI	001C
SHFT	0C15	STOR	0018				

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QUALITY SOFTWARE

700 PROGRAMMING SYSTEMS

MP FLOATING ADD, SUBTRACT, NORM.

DATE: March 1968

ID CODE: BRD

DRAWING: 391090 (Rev D)

LABEL: FAD, FSB, FNRM

AUTHOR: FACQ

SOURCE: SYM I Assembly Language

OBJECT: Relocatable

PURPOSE

FAD, FSB: To add or subtract two mid-precision floating point numbers and leave the normalized mid-precision sum or difference in the software registers MNT1, MNT2, MNT3.

FNRM: To normalize a floating point number set in the software registers MNT1, MNT2, MNT3.

USAGE

Calling Sequence

FAD, FSB:

L-1 SMB FAD (FSB)
L JSX FAD (FSB)
L+1 DATA FARG
L+2 Return

FNRM

L-1 SMB FNRM
L JSX FNRM
L+1 Return

Argument Description

All three routines use the Floating Point Register as one argument. FAD and FSB need a search argument, FARG.

Storage Requirements

External storage in RET3, TMP2, TMP3, TMP4, OVFL, and the Floating Register MNT1, MNT2, MNT3.

METHOD

The mantissa of the lesser argument is shifted to the right, by a number of bits equal to the difference of the two exponents, before algebraic summation or subtraction with the mantissa of the other argument. The exponent of the greater argument is given to the sum or difference. It is incremented or decremented upon normalization of the resulting mantissa on bit 1 as the first significant bit.

ERROR CONDITIONS

An overflow or an underflow gives an exponent off by +256 and causes the flag word "OVFL" to be set to non-zero. No error message is given.

ACCURACY

30 bits

The shifted arguments is not rounded off before summation.

RESTRICTIONSEntries

FAD, FSB, FNRM

Other Routines

DAD, DSUB,

External Constants

B0, B1, D0, D1, D255, M8R

Space Used

149 words

<u>Timing (in cycles)</u>	<u>Average</u>	<u>Minimum</u>	<u>Maximum</u>
FAD	120	107	208
FSB	120	107	208
FNRM	50	18	119

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QUALITY SOFTWARE

700 PROGRAMMING SYSTEMS

MP FLOATING ADD, SUBTRACT, NORM.

APPENDIX A

ASSEMBLY LISTING

of

MP FLOATING ADD, SUBTRACT, NORM.

Drawing No.

391090 (Revision C)

ID Code

BRD

```

2      * IMP FLONATING ADD, SUBTRACT, NORM. DN391090 C,
3      *
4      *      BLK      MATH
5      LIBR      FAD,FSB,FNRM
6      * FLONATING MODE = ADD, SUBTRACT, NORMALIZE
7      *****
8      R      *****
9      * CONSTANTS
10     FADC      JSX      DAD
11     FSBC      JSX      DSUR
12     *****
13     * FLONATING NORMALIZE - ENTRY
14     FNRM      EQU      $      DECREMENT RETURN ADDRESS
15     DXS      1      TO PERMIT EXIT WITH JMP *1
16     JMP      $+1      SAVE RETURN
17     STX      RET3
18     LDX      MNT2
19     JMP      NRM
20     *****
21     FSBD      EQU      $      DECODE DSUB
22     LDW      FSBC      TO CALL DSUB
23     JMP      BOTH
24     *      FAD      EQU      $      TO CALL DAD
25     LDW      FADC      TO CALL DAD
26     *      FAD      EQU      $      SET CALL TO DOUBLE PRECISION
27     BDTH      STW      CALL
28     BDTH      STX      RET5
29     BDTH      LDX      *      SAVE RETURN
30     BDTH      LDX      *      GET SECOND TERM
31     BDTH      LDX      *      SEE MANTISSA
32     BDTH      STW      TMP1
33     BDTH      STW      TMP2
34     BDTH      STW      MNTD
35     BDTH      STW      MNTD
36     BDTH      SUB      *      SEE WHICH TERM TO SHIFT
37     BDTH      SFT      *      FROM DIFFERENCE OF EXPONENTS
38     BDTH      SFT      *      CHECK SHIFT COUNT
39     BDTH      CAL1      2ND TERM TO SHIFT
40     BDTH      SHFT      KEEP 1ST EXP, FOR SUM
41     BDTH      LDX      *      1ST TERM TO SHIFT
42     BDTH      STX      MNTC
43     BDTH      LDX      *      USE 2ND EXP, FOR SUM
44     BDTH      CMP      P.A.
45     BDTH      STX      TMP4
46     BDTH      CAX      SAVE ADDR, OF TERM TO SHIFT
47     BDTH      DXS      16
48     BDTH      JMP      SHF1
49     BDTH      LLB      X'20'
50     BDTH      JMP      SHF2
51     BDTH      LLB      X'2F'
52     BDTH      DXS      15
53     BDTH      JMP      SHF3
54     BDTH      IXS      X'30'.

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BRD 0005
BRD 0006
BRD 0007
BRD 0008
BRD 0009
BRD 0010
BRD 0011
BRD 0012
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BRD 0057

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PASS 2

MATH	X.P	FLOTTING ADD, SUBTRACT, NORM, BN391090 C.	09/27/68	PAGE 4
		SAC	IS SECOND WORD ZERO	PASS 2
0 057 0	0800	108	NRM3	
0 058 0	102F	109	JMP	BRD 0111
0 029 0	0A11	110	JMP	BRD 0112
0 054 0	0A11	111	SET2	BRD 0113
0 058 0	0810	112	SLL 1	BRD 0114
0 05C 0	10/R	113	SAP	BRD 0115
0 05D 0	9018	114	JMP	BRD 0116
0 05E 0	1068	115	LDX	BRD 0117
0 05F 0	0810	116	JMP	BRD 0118
0 060 0	0110	117	SAP	BRD 0119
0 061 0	902D	118	CMP	BRD 0120
0 062 0	050F	119	LDX	BRD 0121
0 063 0	1067	120	DXS 15	BRD 0122
0 064 0	27FF	121	JMP	BRD 0123
0 065 0	107B	122	JSX	BRD 0124
0 066 0	3026	123	JMP	BRD 0125
0 067 0	6061	124	STX	BRD 0126
0 068 0	0A11	125	SLL 1	BRD 0127
0 069 0	0810	126	SAP	BRD 0128
0 06A 0	106E	127	JMP	BRD 0129
0 06B 0	0501	128	NRMX	BRD 0130
0 06C 0	1068	129	DXS 1	BRD 0131
0 06D 0	1064	130	JMP	BRD 0132
0 06E 0	0630	131	NRMS	BRD 0133
0 06F 0	A067	132	UNDR	BRD 0134
0 070 0	606F	133	LLB X'30'	BRD 0135
0 071 0	B070	134	ADU	BRD 0136
0 072 0	30ED	135	STX	BRD 0137
0 073 0	9055	136	MNT0	BRD 0138
0 074 0	0A31	137	STB NRMS+1	BRD 0139
0 075 0	8066	138	LDX MNT3	BRD 0140
0 076 0	0930	139	SLL D 1	BRD 0141
0 077 0	7075	140	MNT2	BRD 0142
0 078 0	0140	141	SLA D 0	BRD 0143
0 079 0	0A01	142	LDW MNT2	BRD 0144
0 0/A 0	7073	143	STW MNT3	BRD 0145
0 0/B 0	900B	144	RET3	BRD 0146
0 07C 0	1801	145	JMP * 1	BRD 0147
		146	***** OVERFLOW IN SUM OF MANTISSAE - SHIFT 1 RIGHT	BRD 0148
		147	***** BACK TO CALLER	BRD 0149
			***** CARRY	BRD 0150
*	*	0 0/D 0	90/A 9 0 07A	BRD 0151
*	*	0 0/E 0	0810 148	BRD 0152
*	*	0 0/F 0	1064 149	BRD 0153
*	*	0 0/0 0	0401 150	BRD 0154
*	*	0 0/0 0	0401 151	BRD 0155
*	*	0 0/1 0	A04B 152	
*	*	0 0/2 0	0400 153	
*	*	0 0/3 0	9034 154	
*	*	0 0/4 0	0084 155	
*	*	0 0/5 0	0921 156	
*	*	0 0/6 0	0022 157	
*	*	0 0/7 0	052 158	
*	*	0 0/8 0	7077 159	
*	*	0 0/9 0	0140 160	
*	*	0 0/A 0	0810 0810	

GET MOST SIGNIFICANT BIT
OUT OF SIGN, RESTORE SIGN

STW MNT2
CXA
SAP

KEEP TWO'S COMPLEMENT
CARRY

UNDERFLOW - GIVE ZERO

SHIFT HIGHER WORD

DONE

YES

NO - DECREMENT EXPONENT

KEEP SHIFTING

UNDERFLOW

MANTISSA TO SHIFT

**COUNT SET ABOVE

MANTISSA TO SHIFT

MATH MP FLOATING ADD, SUBTRACT, NORM, DN391090 C,

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PASS 2

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*    0 089 0 B7FF B 0 7FF      161      SUB     B1
*    0 08A 0 707D 7 0 07D      162      STW     MNT3
*          * INCREMENT EXPONENT BY ONE
*    0 08B 0 8071 8 0 071      163      FIXE    LDW     MNT0
*    0 08C 0 A081 A 0 081      164      ADD     D1
*    0 08D 0 708B 7 0 08B      165      SETZ   STW     MNT0
*    0 08E 0 0600 06 00       166      LLB     0
*    0 08F 0 0800 08 00       167      SAZ     CHECK OVERFLOW
*    0 090 0 27FF 2 0 7FF      168      JSX     M,OV
*    0 091 0 1078 1 0 078      169      JMP     EXIT
*          * NEGATIVE 2**30 - SHIFT 1 TO THE RIGHT
*    0 092 0 0901 090 1       170      N230   SRA     1
*    0 093 0 7086 7 0 086      171      STW     MNT2
*    0 094 0 1088 1 0 088      172      FIXE
*          *****NTRY FAD,FSB,FNRM
*    0 094 0*****148          173      ****
*          *****END
*    0 094 0*****148          174      ****
*          *****END
*    0 094 0*****148          175      ****
*          *****END
*    0 094 0*****148          176      ****
*          *****END
*    0 094 0*****148          177      ****

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X-REF

0 00A 0	BOTH	0 008 0	
EXT 0085	80	0 052 0	0 085 0
EXT 0089	B1	0 089 0	
0 043 0	CALL	0 00A 0	
0 044 0	CAL1	0 015 0	
EXT 0000	DAU	0 000 0	
EXT 0001	DSUR	0 001 0	
EXT 0083	DU	0 054 0	0 083 0
EXT 008C	D1	0 02F 0	0 038 0
0 078 0	EXIT	0 05C 0	0 048 0
LIB 0 009 0	FAU	0 009 0	0 091 0
0 000 0	FAUC	0 009 0	
0 088 0	FIXE	0 094 0	
0 07D 0	FIX0	0 046 0	
0 084 0	FIXP	0 07F 0	0 084 0
LIB 0 002 0	FNRM	0 002 0	
LIB 0 007 0	FSB	0 007 0	
0 001 0	FSBC	0 007 0	
EXT 0090	M,OV	0 090 0	
EXT 0064	M,E	0 064 0	
EXT 008D	MNT0	0 011 0	
EXT 0093	MNT2	0 005 0	0 061 0
EXT 008A	MNT3	0 049 0	0 066 0
0 047 0	NRM	0 006 0	0 067 0
0 068 0	NRM	0 068 0	0 070 0
0 076 0	NRS	0 072 0	0 071 0
0 06E 0	NRX	0 06A 0	0 093 0
0 04F 0	NRM1	0 048 0	
0 05A 0	NRM2	0 050 0	
0 05F 0	NRM3	0 058 0	0 086 0
0 067 0	NRM4	0 063 0	0 08A 0
0 068 0	NRM5	0 06C 0	

MATH MP FLOATING ADD, SUBTRACT, NORM, DN391090 C,

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0	092	0	N230	U	04E	0
0	066	0	P.A	0	019	0
EXT	0078	0	RET3	0	004	0
	008D	0	SETZ	0	059	0
	0017	0	SFI A	U	014	0
	003E	0	SFTN	0	034	0
	003D	0	SFTX	0	037	0
	0033	0	SFT1	0	026	0
	0034	0	SFT2	0	028	0
	001B	0	SHFT	0	016	0
	0021	0	SHF1	0	01E	0
	0024	0	SHF2	0	020	0
	0025	0	SHF3	0	023	0
EXT	0044	0	TMP1	0	00E	0
	0010	0	TMP2	0	010	0
EXT	0040	0	TMP3	0	030	0
EXT	003E	0	TMP4	0	018	0
	0064	0	UNDR	0	06D	0

NO ERRORS

CARDS	SYMBOLS	LITR	STACK
177	50	614	0 6