# REAL TIME CLOCK

**OWNER'S MANUAL** 

SciTronics Inc.

# RTC-100 Real-Time Clock USER'S MANUAL

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#### -- Note --

All clocks have an on-board, fully charged lithium battery. Serious damage to the battery may result if the circuit board is placed on a conducting surface.

#### INTRODUCTION

The SciTronics Inc. S-100 Real Time Clock offers computer enthusiasts an extremely accurate time piece for their system. It employs the latest microprocessor clock chip available and is crystal-controlled to assure .002% accuracy. A lithium battery provides clock power in the event that system power is removed. The RTC-100 may be used to generate interrupts while operating in the background, providing time resolutions as fine as every second. The clock provides year, month, date, day of week, hour, minute, and second data. The RTC-100 is made to be fully compatible with the SciTronics Remote Controller for real-time control of a-c operated appliances. Interactive software packages written in BASIC provide the user with easy-to-use programs to set and read the RTC-100. There are two programs required to use your clock, RTCSET, to set the clock, and RTCREAD to read and display the clock data. In addition to Basic versions of each of these programs, an assembly language program is supplied as an example of how to use the clock in a non-Basic interrupt-driven environment.

Before the clock is installed in your system, you will have to set the base port address and, if you plan to use program interrupt, the interrupt restart address.

# SELECTING THE BASE PORT ADDRESS

The S-100 clock uses four port addresses; one base address, and three additional consecutive addresses. These four ports are used to read and write various data registers within the Peripheral Interface Adaptor chip. In order to select your base address, you must set or reset the six port-select switches located on the circuit board. These switches are part of the 8-position dip switch, PORT, shown in Figure 1. The general form of the base port address is:

AØ Al A2 A3 A4 A5 A6 A7, where A2 through A7 are user defined address bits. Putting any switch into the "ON" ("OFF") position causes that address bit to be valid when high (low).

As an example, to set the base address to 144 decimal (220 octal/90 hexadecimal), the PORT switches should be set as follows:

Note that address bits Al and AØ do not get set because they are used internally to select among the clock functions. It is important that you set the switches with reference to the markings on the circuit board and the diagram of Figure 1, below. Do not use the switch markings as a guide to the identification of the address bits as they may vary from one production run to another. The only valid base addresses are those which are multiples of 4, such as: Ø, 4, 8, 12, 16, 2Ø, . . . . . 244, 248, 252. Table I is a listing of valid port addresses in binary and decimal. Since the Basic clock software will ask you for your base port address in decimal, choose one that does not conflict with any I/O ports already assigned in your system and note it here.

Base Port Address = 
$$\frac{96}{60^{+}}$$
 (decimal)

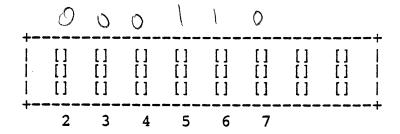


Figure 1. - Base Port Address Switch

Table I. Valid Base Port Addresses

Decimal	Binary	Decimal	Binary	Decimal	Binary
ØØ	000000	88	Ø11Ø1Ø	172	110101
Ø 4	100000	92	111010	176	· · · · · · <del>- ·</del>
Ø8	010000	96	-000110	180	001101
12	110000	100	100110	184	101101
16	001000	104	010110		011101
20	101000	108	110110	188	111101
24	011000	112		192	000011
28	111000		001110	196	100011
32		116	101110	200	010011
36	000100	120	011110	204	110011
	100100	124	111110	208	001011
40	010100	128	000001	212	101011
44	110100	132	100001	216	011011
48	001100	136	010001	220	111011
52	101100	140	110001	224	000111
56	011100	144	001001	228	100111
60	111100	148	101001	232	010111
64	000010	152	011001	236	110111
68	100010	156	111001	240	
72	010010	160	000101		001111
76	110010	164		244	101111
8Ø	001010		100101	248	011111
84	101010	168	010101	252	111111
04	TATATA				

Note: the binary digits are listed in the same order as they would be set into the PORT switch with the S-100 connector towards you.

#### READING THE CLOCK

Your Scitronics Real-Time clock keeps time continuously; powered by your system when it is on, and by its onboard lithium battery when your system is off. You may read the clock at any time when your system is running; an example of a Basic program to read and display the clock data is given in the Appendix. This program, RTCREAD, reads and displays the clock data on a continuous basis. The program elements, and, in particular, the clock read subroutine, may be incorporated as part of your own software.

# SEQUENCE TO ENABLE CLOCK READ no interrupts active:

port # = dec = hex = 76 543 2 10 - binary bit order

port 1 = 240 = F0H = 11 110 0 00 - to PIA 'A' ctrl port CA2 = low = hold high = start of stop clock pulse

150us delay

port 1 = 244 = F4H = 11 110 1 00 - to PIA 'A' ctrl port sets CA2 low & port direction CA2 = low = hold high = clock stopped

port 3 = 248 = F#H = 11 111 000 - to PIA 'B' ctrl port sets CB2 high CB2 = high = start of read pulse

## SEQUENCE TO READ CLOCK (repeat as needed):

digit address out to PIA port 'A'

6us delay

read PIA port 'A'

clear lower four bits

rotate upper four bits into lower four bits

save result

do next digit

AND 3 to month tens digit - eliminate leap year indicator AND 3 to hours tens digit - eliminate AM/PM indicator

# SEQUENCE TO RETURN CLOCK TO RUN MODE:

port 3 = 240 = F0H = 11 110 0 00 - to PIA 'B' ctrl port sets CB2 low

CB2 = low = read pulse off

port 1 = 248 = F8H = 11 11 0 00 - to PIA 'A' ctrl port sets CA2 high CA2 = high = hold low = end of clock stop pulse = clock run mode active

port p = 15= 0FH port 3 = 248 = FBH Sups 34

#### SETTING THE CLOCK

Once you have installed your clock and are able to read it, you will want to set it to correspond to your local time zone. A Basic program, RTCSET, is provided for this purpose. You must tell the program your base port address (in decimal), and the current date and time. You will be asked to set the clock to the next full minute because the clock automatically resets its seconds to zero when it is set. You will be asked for the last two digits of the year, the one or two digits of the month, the date, the day of the week (Sunday = 0, etc.) and the time on a 24-hour basis. The clock is capable of handling Leap Year, although that feature is not included in the program as released.

# SEOUENCE TO ENABLE CLOCK SETTING no interrupts active:

port # = dec = hex = 76543210 - binary bit order

port Ø = 255 = FFH = 1111 1111 - to PIA 'A' data input register port 2 = 255 = FFH = 1111 1111 - to PIA 'B' data input register data to preset both PIA ports to output

150us delay

Continued from previous page.

SEQUENCE TO SET CLOCK (repeat as needed):

port 2 = 0 = 00H = 0000 0000 - to PIA 'B' data input register turns write pulse on

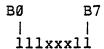
do next digit

SEQUENCE TO RETURN CLOCK TO RUN MODE:

port 1 = 248 = F8H = 11 111 0 00 - to PIA 'A' ctrl port sets CA2 high end of stop pulse CA2 = high = hold low = run

#### INTERRUPT MODE OPERATION

As an alternative to reading the clock upon demand, you may set up your system to read the clock on an interrupt basis. If you read the clock under interrupt, the process of reading and displaying the data are separate functions. The clock is read every second in response to an interrupt request generated by the clock. At the conclusion of the instruction cycle in progress when the interrupt request was generated, the processor (if interrupt was enabled) initiates the machine interrupt cycle. This interrupt cycle resembles an ordinary instruction fetch cycle, except that the INTA (interrupt acknowledge) status bit is set high. The clock will respond during T3 of M1 by "jamming" an instruction onto the processor's data bus. This instruction, the RST (RESTART) instruction is of the form:



where xxx is the binary representation of the interrupt number selected for your clock. Once the processor receives the RST, control is transferred to the instruction located at the address eight times the decimal equivalent of the binary interrupt number xxx.

For example, suppose the processor receives RST 2 upon interrupt; that is, the clock places 11010111 on the data buss. Then control will be transferred to  $8 \times 2 = 16$ .

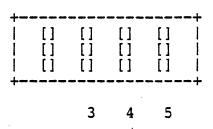


Figure 2. - Interrupt Address Switch

The three bits xxx allow control to be transferred to any one of eight fixed locations; namely, Ø, 8, 16, 24, 32, 40, 48, and 56. To give the user maximum flexibility in selecting any one of these eight locations, a 4-position dip switch, INT, is provided as shown in Figure 2. Please note the markings on the diagram. Also note that the switch is negative logic, that is, ON for Ø and OFF for 1. It is recommended that you avoid having all switches on (RST Ø) or all switches off (RST 7) as, frequently, these addresses are used by other devices. Table II lists legal interrupt codes and their decimal equivalents.

Note your INT code here = \_\_\_\_\_ (decimal)

Table II. - Valid Interrupt Addresses

Interrupt Cod	Switch Setting	Decimal Equivalent
BØ xxx B7               111 000 11   111 100 11   111 010 11   111 101 11   111 011 11   111 011 11   111 111 11	ON ON ON OFF ON OFF OFF ON OFF OFF OFF O	Ø 8 16 24 32 4Ø 48 56

# SOFTWARE DESCRIPTION

The two Basic programs, RTCREAD and RTCSET, have been described previously in brief. This section will discuss the format of the clock data as read by RTCREAD and as used by RTCSET to set the clock. This format is also used by the Scitronics a-c controller software to schedule and control a-c appliances and lights. The clock data block consists of a 13-byte area, each byte of which contains a single clock digit in its least significant 4 bits. The clock digits are in the order: year tens, year units, month tens (Leap Year flag is included with this digit), month units, day-of-week digit, hour tens (24-hour flag is included with this digit), hour units, minute tens, minute units, second tens, and second units. The read subroutine in RTCREAD places the clock board in read mode with Digit Hold on (so the digits will not change while you read them) and fills the clock data block.

- days turs, days wat

The actual clock read operation requires transmitting a digit code to the clock (0=seconds units, 1= seconds tens, etc.), and then reading the clock port. At the conclusion of the read cycle, the clock is released from Hold. RTCREAD does not enable clock interrupts because it does not operate under interrupt, but reads the clock continuously. Because it cycles through the read and display portions of the program continuously, and because the speed of the various Basic programs vary considerably, the program may display the time more or less often than once per second on your system.

RTCSET uses a similar clock data block to set the clock; however, the format of the data actually used for setting the clock is somewhat different. The first part of RTCSET asks the user for the clock base port address and the clock setting information. Next the information is formatted with the data in the high order 4 bits and the digit code in the low order four bits. The digit code is the same as that used in reading the clock, that is Ø=seconds units, l=second tens, etc. The clock is set by the sequence of port operations shown in the program which transmit the data-digit/code-digit bytes to the clock and then place the clock back into normal mode. Once again, the clock interrupt is not enabled in the Basic programs.

The Appendix also contains a listing of a program demonstrating interrupt-driven use of the RTCl00. This program is divided into two major sections: first, a section which is user-dependent; and second, the clock routines to set, read, start, and stop the clock. These routines, SETTER, INTR, STARTS, and STOPS should form part of any interrupt-driven software.

SETTER expects to find the clock data in the low four bits of the bytes in the clock area. It shifts each to the high four bits, inserts the proper code digit and sends the digits to the clock. SETTER must be run with interrupt off to avoid having INTR affect the data being set up; also, it does not enable interrupt mode after the clock is set. It is important to realize that the clock chip keeps time even when interrupt is off.

INTR is the interrupt service routine which is invoked automatically each second to read the clock data into the data array, CLOCK. The data is placed as individual digits into the array; the Leap Year and 24-hour flags are stripped off prior to storage. As shown in the first part of the demonstration program, the clock data may be printed from the data array by adding ASCII zero to each digit and sending them to an ASCII printer. INTR restores interrupt mode when it has finished reading the clock data.

Once the clock has been set, it will keep time and may be read; however, it will not be enabled to interrupt back ground programs until the program STARTS is run. This program enables the seconds 'tick' from the clock chip through the PIA chip to cause a program interrupt request. You must have set the INT switch on your RTCl00 board to a suitable address and that address must be used for the program constant, RSTART. STARTS will set up the interrupt vector at RSTART to point to the interrupt service routine, INTR, before enabling interrupt.

You must have an interrupt vector at RSTART and the service routine INTR active in memory whenever the clock is generating interrupts. If not, your computer will get 'lost' and crash whatever program you are running. The program, STOPS, is provided to disable the clock interrupts so you may use your computer in the normal manner. STOPS disables the clock interrupts in two ways; first, it disables ALL interrupts by the command, DI, and second, it programs the PIA chip to not pass on the clock 'ticks'. If you are running other interrupt-driven devices besides the RTCl00, you may wish to remove the DI command from the beginning of STOPS. Once again, STOPS stops the interrupts, not the time-keeping!

# SOFTWARE

#### APPENDIX

#### I. RTCREAD.BAS

```
10
        REM **READ SCITRONICS REAL-TIME CLOCK**
20
        REM **COPYRIGHT 1980 - SCITRONICS, INC. **
30
        DIM TIME(13), W$(7)
40
        DATA SUN, MON, TUE, WED, THU, FRI, SAT
5Ø
        FOR X=1 TO 7 : READ W$(X) : NEXT X
6 Ø
        PRINT
7Ø
        PRINT "PROGRAM TO DISPLAY CLOCK DATA"
        PRINT "SCITRONICS, INC., BETHLEHEM, PA"
80
9 Ø
        PRINT
100
        REM THIS IS THE NON INTERRUPT-DRIVEN VERSION
110
        PRINT "ENTER CLOCK BASE PORT ADDRESS"
        PRINT "IN DECIMAL INTEGER FORM - 0,4,8,12,16,20,ETC"
120
130
        PRINT "MAXIMUM VALUE = 252"
140
        INPUT P
15Ø
        REM CHECK FOR PROPER VALUE AND RANGE
160
        IF P > = 256 THEN 60
170
        M = INT(P/4)
180
        IF (4*M-P) <> \emptyset THEN 6\emptyset
190
        Pl=P+1
200
        P2=P+2
21Ø
        P3=P+3
22Ø
        REM DISPLAY LOOP STARTS HERE
230
        GOSUB 1000
240
        Y=TIME(1)*10+TIME(2)
250
        MO=TIME(3)*10+TIME(4)
260
        D=TIME(5)*10+TIME(6)
27Ø
        W=TIME(7)+1
280
        H=TIME(8)*10+TIME(9)
290
        MI=TIME(10)*10+TIME(11)
300
        S=TIME(12)*10+TIME(13)
        PRINT Y; ": "; MO; ": "; D; " ";
310
        PRINT W$(W);". ";
320
        PRINT H;":";MI;":";S;" "
330
340
        PRINT
35Ø
        GOTO 230
360
        REM
```

# Continued from previous page.

```
1000
         REM *************
1010
        REM THIS READS THE CLOCK DATA *
1020
        REM ******************
1030
         OUT P1,240 - \not\vdash \phi
        OUT P3,252 FC
1040
                      ifc V
1050
1060
        OUT P1,244 - F4
1070
        FOR X=1 TO 13
1080
        OUT P,13-X
1090
        TIME(X) = INP(P) : NEXT X
        REM PUT CLOCK BACK INTO RUNNING MODE
1100
1110
        OUT P1,248 - F8 -
1120
        OUT P,15 ~ ♥ F ✓
        OUT P3,248 -F8 V
1130
1140
        OUT P1,252 -FC ~
1150
        OUT P.15 - OF -
        FOR X=1 TO 13
1160
        TIME(X) = INT(TIME(X)/16) : NEXT X
1170
        REM THERE ARE SPECIAL MARKS ON HOURS AND MONTH
1180
1190
        REM STRIP THEM OFF
        TIME (3) = TIME (3) AND 3
1200
        TIME(8) = TIME(8) AND 3
1210
122Ø
        RETURN
1230
        REM **LATEST REVISION - 801107**
```

# II. RTCSET.BAS

```
REM PROGRAM TO SET SCITRONICS CLOCK - 801030
 10
          REM **COPYRIGHT 1980 - SCITRONICS, INC**
 20
 3Ø
          DIM TIME (13)
          PRINT "PROGRAM TO SET THE SCITRONICS CLOCK"
 40
 5Ø
         PRINT "SCITRONICS, INC., BETHLEHEM, PA 18015"
 6Ø
          PRINT
 7Ø
         PRINT "ENTER CLOCK BASE PORT ADDRESS"
         PRINT "IN DECIMAL INTEGER FORM - 0,4,8,12,16,20,ETC"
 80
 90
         PRINT "MAXIMUM VALUE = 252"
 100
         INPUT P
 110
         REM CHECK FOR PROPER VALUE AND RANGE
 120
         IF P>=256 THEN 60
130
         M = INT(P/4)
140
         IF(4*M-P)<>0 THEN 60
15Ø
         Pl=P+1
160
         P2=P+2
17Ø
         P3=P+3 .
180
         REM GET THE TIME DATA
190
         PRINT
         PRINT "ENTER DATE IN THE FORM: YR, MO, DA "
200
         PRINT "ONLY LAST 2 DIGITS FOR YR!"
210
22Ø
         INPUT YR, MO, DA
230
         PRINT
240
         PRINT "ENTER DAY-OF-THE-WEEK - Ø=SUNDAY "
25Ø
      INPUT W
26Ø
         PRINT
         PRINT "ENTER TIME ON 24HR CLOCK IN THE"
27Ø
280
         PRINT "FORM: HR, MIN"
290
         PRINT "CLOCK ALWAYS STARTS ON 00 SECONDS, SO"
300
         PRINT "BE SURE TO ENTER THE NEXT MINUTE."
31Ø
         INPUT HR, MIN .
32Ø
         REM PREPARE THE DATA FOR ENTRY
33Ø
         TIME(1) = 16 * INT(YR/10) + 12
340
         TIME(2) = 16*(YR-10*INT(YR/10))+11
35Ø
         TIME(3) = 16 * INT(MO/10) + 10
360
        TIME(4) = 16*(MO-10*INT(MO/10)) + 9
37Ø
         TIME(5) = 16 * INT(DA/10) + 8
380
        TIME(6) = 16*(DA-10*INT(DA/10)) + 7
                                        -24h pow
39Ø
        TIME(7) = 16*W+6
400
        TIME(8) = 16 * INT(HR/10) + 5 + 128
        TIME(9) = 16*(HR-10*INT(HR/10))+4
410
        TIME(1\emptyset) = 16 \times INT(MIN/1\emptyset) + 3
420
        TIME(11) = 16*(MIN-10*INT(MIN/10))+2
430
440
        TIME(12) = 1
450
        TIME(13) = \emptyset
```

#### Continued from previous page.

```
460
        REM DATA IS READY, ASK FOR
        REM START SIGNAL FROM USER
470
480
        PRINT
490
        PRINT "ENTER 1 AND CARRIAGE RETURN TO SET CLOCK"
500
        PRINT "PUSH CR ON THE ZERO SECOND"
51Ø
        INPUT J
        REM THIS IS THE SETTING SEQUENCE
520
        OUT P3,244 F9
53Ø
                   FF
    (2) OUT P2,255
54Ø
                   FØ
        OUT P3,240
55Ø
                   FF
        OUT P2,255
56Ø
        OUT P1,240 FØ
57Ø
     DOT P,255
                    FF
58Ø
                    FY
     ● OUT P3,244
59Ø
     9 OUT P1.244
600
        FOR X=1 TO 13
610
62Ø
        OUT P, TIME(X)
        OUT P2,0 V
63Ø 02
        OUT P2,1 ~
64Ø
65Ø
        NEXT X
      O OUT P1,248 F8
66Ø
        OUT P3,240
670
        PRINT "THE CLOCK IS SET"
68Ø
69Ø
        END
```

#### III. RTC.ASM

```
SciTronics S-100 Real-Time Clock
       Date
                     801024
                     801202
       Revision
       Programmer -
                     A. I. Larky
· ***************************
    COPYRIGHT 1980 - SCITRONICS, INC
*************
              100H
       ORG
BDOS
       EOU
              5H
                     ; DISK SYSTEM ETP
                     ; READ CONSOLE DEVICE
CONIN
       EOU
              1 H
              9 H
PRINT
       EQU
                     ; PRINT LINE BUFFER
CR
       EQU
              13
       EQU
LF
              10
· *************
           TEST PROGRAM
  THE CODE WHICH FOLLOWS IS AN EXAMPLE *
  OF THE INITIALIZATION AND DISPLAY OF *
  CLOCK INFORMATION.
  IT SHOULD BE CUSTOMIZED FOR YOUR OWN *
  APPLICATION. THE CODE WHICH FOLLOWS *
  THE DIVIDING LINES IS COMMON TO ALL
  CLOCK PROGRAMS.
********
              SP, STACK
       LXI
              MSG
       CALL
       DW
              ID
       CALL
              STOPS
                    ;STOP THE CLOCK
       CALL
              SETUP
                     ; SET UP DATE AND TIME
       CALL
              STARTS ; START THE CLOCK
       CALL
              DISPLA
AGAIN:
       JMP
              AGAIN
ID
              '(801202) - SCITRONICS REAL-TIME CLOCK'
       DB
       DB
              CR, LF, '$'
```

```
****************
    THE DISPLAY ROUTINES BUILD AN OUTPUT STRING
    AT "LINE" WHICH CAN BE PRINTED TO SHOW DATE
    AND TIME.
              THE USER SHOULD INSERT HIS OWN
                                                 *
    PRINT ROUTINE IN PLACE OF THE BDOS CALL IN
                                                 *
    THE ROUTINE "MSG".
 **************
DISPLA: LXI
                H,OLD
        MOV
                A, M
                        ; EXAMINE FLAG
        ANA
                Α
        JZ
                DISPLA
                        ;SAME OLD DATA
        DI
                        ;DON'T INTERRUPT WHILE
                        ;WE'RE MAKING THE LINE IMAGE
        MVI
                M,Ø
                        ;CLEAR THE FLAG
        LXI
                B, 6
        LXI
                D, LINE
                        ;OUTPUT BUFFER
        LXI
                H, CLOCK
        CALL
                DISP8
                        ;DISPLAY DATE
        INX
                Η
                        ;SKIP DAY OF THE WEEK
        CALL
                BLANK
        IVM
                C,6
        CALL
                DISP8
                        ;SET UP TO DISPLAY TIME
  IF YOU HAVE A Z-80 CPU YOU MUST SELECT MODE 0
  INTERRUPTS BY PUTTING
        DB
                ØEDH
  AND
        DB
                46H
  IN PLACE OF THE NOP'S BELOW.
        NOP
        NOP
ï
        EI
                        ; ITS OK TO TURN INTERRUPT BACK ON
        CALL
                CRLF
        CALL
                MSG
                        ; PRINT THE LINE IMAGE
        DW
                LINE
        RET
DISP8:
       CALL
               DISP9
        CALL
               DISP9
                       ; PLANT 2ND DIGIT
        MVI
               A, 1:1
       CALL
               DISP9A
                       ; PLANT COLON
       JNZ
               DISP8
       RET
```

```
DISP9:
        VOM
                 A, M
        INX
                 H
        ANI
                 ØFH
                 ıøı
        ADI
        DCR
                 C
DISP9A: STAX
                 D
         INX
                 D
        RET
                 A, 1 1
BLANK:
        IVM
                 DISP9A
        JMP
CRLF:
        MVI
                 A,13
                 DISP9A
        CALL
        MVI
                 A,10
        CALL
                 DISP9A
        IVM
                 A, '$'
        JMP
                 DISP9A
SETUP:
        CALL
                 MSG
        DW
                 DOYOU
        CALL
                 READ
                          ; GET ANSWER
        CPI
                 IYI
        STA
                 OLD
                          ;UNKNOWN TIME
       RNZ
                          ; NO
        CALL
                 MSG
        DW
                 ASKFOR
                          ; PROMPT FOR DATA
        CALL
                 NUMIN
                          GET DATE AND TIME
        JMP
                 SETTER /
                          ;SET THE CLOCK
NUMIN:
        LXI
                 D, CLOCK
        IVM
                 B,4
        CALL
                 NUMCON
                          ;GET DATE INFO
        DCX
                 D
                          ONLY ONE DIGIT TO DAY-OF-WEEK
        LDAX
                 D
                          ;GET IT
        DCX
                 D
                          ; BACKUP
        STAX
                 D
                          ; PLANT IT
        INX
                 D
        IVM
                 B,2
        JMP
                 NUMCON ; GET TIME INFO (HR. & MIN. ONLY)
```

```
NUMCON: MVI
                 L,Ø
                         ; CLEAR THE NUMBER
 NUMC2:
         CALL
                 READ
                         GET ONE DIGIT
                 1 1
         CPI
         JZ
                 NUMC4
                         ; BLANK IS AN ENDING
         CPI
                 1,1
         JZ
                 NUMC4
                        ;SO IS COMMA
         CPI
                 1.1
        JZ
                 NUMC4
                        ;SO IS PERIOD
         SUI
                'Ø'
                         ; NOW WE ASSUME A DIGIT
        DAD
                 H
                         ;BEGIN THE *16
        DAD
                H
        DAD
                H
        DAD
                Η
        ADD
                L
                         ; INSERT NEW DIGIT
        MOV
                L,A
        JMP
                NUMC2
                        ;GET MORE DIGITS
   THE CODE ABOVE ENSURES THAT WE TREAT ONE-DIGIT
    AND TWO-DIGIT ENTRIES ALIKE.
NUMC4:
        VOM
               A,L
        RRC
                         ; MOVE HIGH STUFF LOW
        RRC
        RRC
        RRC
        ANI
                ØFH
      STAX
                D
        INX
                D
        IVM
                A,ØFH
        ANA
                L
        STAX
                D
                        ; PLANT A DIGIT
        INX
                D
        DCR
                В
                        ; MORE PAIRS ?
        JNZ
                NUMCON ; YES
        RET
;
  THE USER SHOULD INSERT HIS OWN OUTPUT
  ROUTINE CALL IN PLACE OF THE CALL TO BDOS
          $ IS THE END-OF-PRINT-LINE FLAG
  FOR BDOS, NOT A PRINTABLE CHARACTER.
```

Continued from previous page.

```
MSG:
        XTHL
        MOV
                E,M
        INX
                H
        MOV
                D, M
        INX
                Η
        XTHL
        IVM
                C, PRINT
        JMP
                BDOS
   THE USER SHOULD INSERT HIS OWN CHARACTER INPUT
   ROUTINE IN PLACE OF THE CALL TO BDOS.
READ:
        PUSH
                В
        PUSH
                D
        PUSH
                H
        IVM
                C, CONIN
        CALL
               BDOS
        POP
               H
        POP
               D
        POP
               В
        RET
ASKFOR: DB
                  ENTER DATE AND TIME', CR, LF
        DB
                   (YR, MON, DAY, DATE, HR, MIN.) '
        DB
                CR, LF, ' IN THE FORM: 80,10,25,6,18,12.
               CR, LF, '
        DB
                       (SUNDAY = DAY \emptyset)', CR, LF, '$'
DOYOU:
       DB
               CR, LF, ' SET NEW TIME ? - $'
LINE:
       DS
               25
OLD:
       DS -
               1
                       ;OLD CHECKSUM KEPT HERE
***********
        END OF DISPLAY EXAMPLE
THE ROUTINES WHICH FOLLOW SHOULD BE
  PART OF ANY CLOCK SOFTWARE.
                              WHILE
  THE READING OF THE CLOCK IS DONE VIA *
  PRIORITY INTERRUPT, THE USER MAY READ*
  THE CLOCK AT ANY TIME BY CALLING THE *
  ROUTINE "INTR". IF THE CLOCK IS NOT *
  TO BE READ UNDER INTERRUPT, REMOVE
  THE 'EI' INSTRUCTION FROM THE END OF *
  THE "INTR" AND "STARTS" ROUTINES.
************************************
```

```
PORT
         EOU
                 24
                         ; BASE PORT ADDRESS
   THE VALUE FOR 'PORT' MUST MATCH THAT WHICH YOU HAVE
; SELECTED BY THE SWITCHES ON YOUR CARD.
                                            THE ONE SHOWN
; ABOVE CORRESPONDS TO SWITCH SETTINGS: 0110000
PORT1
         EQU
                 PORT+1
PORT2
        EQU
                 PORT+2
PORT3
        EQU
                 PORT+3
   THE VALUE FOR 'RSTART' MUST MATCH THAT WHICH YOU HAVE
; SELECTED BY THE SWITCHES ON YOUR CARD.
                                            THE ONE SHOWN
; BELOW CORRESPONDS TO SWITCH SETTINGS: ON OFF OFF
RSTART
        EQU
                 48
                         ; RESTART ADDRESS
   THIS IS THE INTERRUPT SERVICE ROUTINE WHICH IS
  INVOKED BY THE CLOCK, ONCE PER SECOND.
INTR:
        PUSH
                 PSW
                         ; SAVE ALL REGISTERS
        PUSH
                 В
        PUSH
                 D
        PUSH
                 H
        MVI
                 A,ØFØH
                        ;SEQUENCE TO ENABLE THE CLOCK
        OUT
                 PORTL
                        ; FOR READING
        MVI
                A, ØFH
        OUT
                PORT
        MVI
                A,Ø7FH
LOOP1:
        INR
                Α
        JNZ
                LOOP1
        MVI
                A, ØFCH
        OUT
                PORT3
        IVM
                A,ØF4H
        OUT
                PORT1
        LXI
                B, NDATA
        LXI
                H, CLOCK+NDATA-1
```

# Continued from previous page.

```
LOOP2:
         VOM
                  A,B
         OUT
                  PORT
                          ; CODE DIGIT TO SELECT CLOCK ELEMENT
         NOP
                          ;WE NEED A BIT OF DELAY HERE TOO
         NOP
         NOP
         NOP
         NOP
         NOP
         IN
                 PORT
                          ; READ CLOCK ELEMENT
         ANI
                 ØFØH
                          ;DROP CODE DIGIT
         RRC
                          ; MOVE DATA TO LOW NIBBLE
         RRC
         RRC
         RRC
        MOV
                 M,A
                          ;SAVE DATA DIGIT
        DCX
                 H
        INR
                 В
        DCR
                 C
        JNZ
                 LOOP2
   NOW WIPE OUT THE LEAP YEAR AND 24HR MARKS
        LXI
                 D, HOUR-CLOCK+1
        DAD
                 D
                          ;OFFSET TO HOUR TENS
        VOM
                 A,M
        ANI
                 3
        VOM
                 M,A
       DCX
                 H
                         ;BACK UP TO MONTH
        DCX
                 H
        DCX
                 H
        MOV
                 A,M
        ANI
                 3
                         ; WIPE OUT LEAP YEAR MARK
        VOM
                 M, A
INTR4:
                        ; BEGIN SEQUENCE TO RETURN 178
        MVI
                 A,ØF8H
        OUT
                 PORT1
                         ; CLOCK TO INTERRUPT MODE
        MVI
                A, ØFH
                         15
        OUT
                PORT ·
        IN
                PORT2
                         ; CLEAR MINUTES INTERRUPT
```

```
; NOTE ** IF YOU WANT TO INTERRUPT EVERY MINUTE, CHANGE
; THE TWO INSTRUCTIONS BELOW TO THE VALUES IN PARENTHESES.
                 A,ØF8H
                                         ****
                         ; (MVI A, ØF9H)
        MVI
        OUT
                         ; DISABLE (ENABLE) MINUTES INTERRUPT
                 PORT3
                 A, ØFDH ; (MVI A, ØFCH) *****
        MVI
                       ; ENABLE (DISABLE) SECONDS INTERRUPT
        OUT
                 PORTI
        MVI
                 A,ØFH
        OUT
                 PORT
        STA
                         ;SET DISPLAY FLAG
                 OLD
                         ; RESTORE ALL REGISTERS
        POP
                 H
                 D
        POP
        POP
                 В
                 PSW
        POP
; IF YOU HAVE A Z-80 CPU, SELECT INTERRUPT MODE 0 BY
; PUTTING
        DB
                 ØEDH
; AND .
        DB
                 46H
 IN PLACE OF THE NOP'S WHICH FOLLOW
        NOP
        NOP
;
        ΕI
                         ; RE-ENABLE INTERRUPT MODE
        RET
                         ;TURN OFF INTERRUPT
SETTER: DI
                                   your how
        MVI
                 A, ØF4H
                         244
        CUT
                 PORT3
                          255 /
        IVM
                 A, ØFFH
        TUO
                 PORT2
                          240
        IVM
                 A, ØFØH
        OUT
                 PORT3
                         258 V
        IVM
                 A, ØFFH
        OUT
                 PORT2
        IVM
                 A,ØFØH
        OUT
                 PORT1
        IVM
                 A, ØFFH
                 PORT
        OUT
                         2440
        IVM
                 A, ØF4H
        OUT
                 PORT3
        IVM
                 A, ØF4H
        OUT
                 PORT1
```

```
* ****************
   THE CLOCK IS PROGRAMMED BY OUTPUTTING THE
; DESIRED DIGITS, ONE AT A TIME.
                                EACH DIGIT
; MUST BE IN THE UPPER HALF OF A BYTE WITH
; A DIGIT IN THE LOWER BYTE TO IDENTIFY WHICH
; CLOCK ELEMENT IS TO BE SET. Ø=SECONDS, 1=TENS*
; OF SECONDS, 2=MINUTES, 3=TENS OF MINUTES, 4= *
; HOURS, 5=TENS OF HOURS, 6=DAY-OF-THE-WEEK, 7= *
; DATE, 8=TENS OF DATE, 9=MONTH, 10=TENS OF MON-*
; TH, 11=UNITS OF YEAR, 12=TENS OF YEAR.
  THE LOOP BELOW SHIFTS THE INFORMATION DIGITS
; TO THE HIGH NIBBLE, AND ADDS THE CODE DIGITS.
 NOTE: THE SECONDS MUST BE ACCESSED, EVEN
; THOUGH THEY CAN'T BE SET, IN ORDER TO RESET
; THEM. A MARKER IS SET ON TENS OF HOURS
; FOR 24-HR. TIME.
**************
       LXI
               H, CLOCK ; SET UP CONTROL DIGITS
       MVI
               C, NDATA
START2: MOV
               A,M
                       GET ONE DIGIT
       ADD
               Α
                       ; MOVE TO HIGH END
       ADD
               Α
                      ; *4
       ADD
               Α
                      ; *8
       ADD
               Α
                      ; *16
       ADD
               С
                      ; INSERT CODE DIGIT
       DCR
               Α
                      ; CORRECT IT
       VOM
               M, A
       INX
               Η
       DCR
               C
       JNZ
               START2
       LXI
              D, HOUR-CLOCK-NDATA
                                     ;OFFSET TO HOUR
       DAD
       MOV
              A, M
       ORI
              128
                      ;SET MILITARY TIME
       VOM
              M,A
```

```
; NOW THAT WE HAVE THE PROPER FORM OF DATA IN 'CLOCK',
; WE CAN DO THE ACTUAL CLOCK SETTING OPERATION.
                 H, CLOCK ; INITIAL DATA AREA
        LXI
        IVM
                 B, NDATA
LOOP4:
        VOM
                 A,M
                          ;SET UP CLOCK
        OUT
                 PORT
        MVI
                 A,Ø
        OUT
                 PORT2
                          ; CREATE A STROBE PULSE
        NOP
                          ;WE NEED SOME DELAY
        NOP
        NOP
        NOP
        NOP
        NOP
        MVI
                 A,1
        OUT
                 PORT2
                          ; END OF STROBE
        INX
                 Η
        DCR
                 В
        JNZ
                 L00P4
                           Izestal chark
        MVI
                 A,ØF8H
        OUT
                 PORT1
        MVI
                 A, ØFØH
        OUT
                 PORT3
        RET
STARTS: DI
        PUSH
                 PSW
                          ; DUMMY PUSHES TO ENABLE US
        PUSH
                 В
                          ; TO USE THE TAIL END OF
        PUSH
                 D
                          ; THE "INTR" ROUTINE TO
                          ; ENABLE INTERRUPTS
                 Η
        PUSH
   SET UP THE INTERRUPT VECTOR
        LXI
                 H, RSTART
        MVI
                 M, JMP
        INX
                 Η
        IVM
                 M, INTR AND ØFFH
        INX
                 Η
        IVM
                 M, INTR SHR 8
        JMP
                 INTR4
                          ; NOW DO THE ENABLING
                            not like Bonic or doo (3)
STOPS:
        DI
        MVI
                 A,Ø
        OUT
                 PORT1
        OUT
                 PORT3
        RET
```

Continued from previous page.

```
;CLOCK DATA AREA
CLOCK:
                                            (80)
                 8CH, ØBH ; YEAR DIGITS
        DB
                 ØAH,99H; MONTH
        DB
                                            (09)
        DB
                 18H,87H ;DATE
                                            (18)
                                            (2 = TUES.)
                 26H
        DB
                          ; DAY-OF-WEEK
HOUR
        EQU
                 $
                          ;OFFSET TO HOURS
                 Ø5H,94H; HOURS
                                            (09)
        DB
        DB
                 13H,2H; MINUTES
                                            (10)
                                            (ØØ)
        DB
                 1,0
                          ; SECONDS
NDATA
        EQU
                 $-CLOCK
;
        DS
                 50
STACK:
                 Ø
        DS
         END
```

# IV. TIME.BAS

```
lØ
     DEFINT A-Z
 20
     DIM TIME(13), W$(7), MO$(12)
 30
                'CLOCK PORT ØDØH change to your port address
 40
     Pl=P\emptyset+1
 5Ø
     P2=P\emptyset+2
 60
     P3=P\emptyset+3
 7Ø
     W$(Ø)
             ="Sunday "
 80
     W$(1)
             ="Monday "
 9 Ø
     W$(2)
              ="Tuesday "
 100 W$(3)
              ="Wednesday "
 110 W$(4)
              ="Thursday"
 120 W$(5)
              ="Friday "
 130 W$(6)
              ="Saturday "
 140
150 MO$ (1) =" January "
160 MOS (2) =" February "
170 MOS (3) =" March "
180 \text{ MO}$ (4) = "
                 April
190 MO$ (5) ="
                 May "
200 \text{ MO$} (6) = 
                 June "
210 \text{ MO$} (7) = "
                 July "
220 MO$ (8) ="
                 August "
230 \text{ MO$} (9) = "
                 September "
240 MOS(10) =" October "
250 MOS(11) =" November "
260 MO$(12) =" December "
27Ø '
280 PRINT CHR$(26) clears screen on TeleVideo 912/920 terminal
290 PRINT
300 PRINT "Enter <T> for time display ONLY"
310 PRINT "Enter <S> for time setting"
320 PRINT "Enter <E> for EXIT"
330 X1$=INKEY$
340 IF X1$< CHR$(32) THEN 330
350 IF X1$="T" THEN 1000
360 IF X1$="t" THEN 1000
370 IF X1$="E" THEN END
380 IF X1$="e" THEN END
390 IF X1$="S" THEN 420
400 IF X15="s" THEN 420
410 GOTO 330
```

```
420 PRINT CHR$(26)
430 PRINT
440 PRINT "Enter DATE in the form:" 450 PRINT " YY, MM, DD"
460 INPUT YR, MO, DA
470 PRINT
480 PRINT "Enter DAY-OF-THE-WEEK"
490 PRINT
500 PRINT "
              Ø = Sunday"
510 PRINT "
              1 = Monday"
520 PRINT "
              2 = Tuesday"
530 PRINT "
              3 = Wednesday"
540 PRINT "
              4 = Thursday"
550 PRINT "
              5 = Friday"
560 PRINT " 6 = Saturday"
570 PRINT
580 INPUT W
590 PRINT
600 PRINT "Enter TIME on 24HR clock in the form of:"
610 PRINT " HH, MM"
62Ø INPUT HR.MIN
630 'PREPARE THE DATA FOR ENTRY
640 TIME (1) =16*INT(YR/10)+12
650 TIME (2) =16*(YR-10*INT(YR/10))+11
660 TIME (3) = 16 \times INT(MO/10) + 10
670 TIME (4) = 16*(MO-10*INT(MO/10))+9
680 TIME (5) =16*INT(DA/10)+8
690 TIME (6) = 16*(DA-10*INT(DA/10))+7
700 \text{ TIME } (7) = 16 * W + 6
710 TIME (8) = 16 \times INT(HR/10) + 5 + 128
720 TIME (9) =16*(HR-10*INT(HR/10))+4
730 TIME(10) = 16 \times INT(MIN/10) + 3
740 TIME(11) = 16*(MIN-10*INT(MIN/10))+2
750 \text{ TIME}(12) = 1
760 \text{ TIME}(13) = 0
770 'DATA IS READY, ASK FOR
780 'START SIGNAL FROM USER
790 PRINT
800 PRINT "Press any key to set and start clock with zero seconds"
810 X$=INKEY$
820 IF X1$< CHR$(32) THEN 810
830 IF LEN(X$) = \emptyset THEN 810
```

```
840 'THIS IS THE SETTING SEQUENCE
 850 OUT P3,244
 860 OUT P2,255
870 OUT P3,240
 880 OUT P2,255
 890 OUT P1,240
 900 OUT P0,255
 910 OUT P3,244
 920 OUT P1,244
930 FOR X=1 TO 13
940 OUT PØ, TIME(X)
95Ø
        OUT P2,0
96Ø
        OUT P2,1
970 NEXT X
980 OUT P1,248
990 OUT P3,240
1000 ' READ CLOCK
1010 PRINT CHR$(26)
1020 '
1030 ' THIS READS THE CLOCK DATA
1040 '
1050 OUT P1,240
1060 OUT P0, 15
1070 OUT P1,244
1080 OUT P3,252
1090 FOR X=1 TO 13
1100
         OUT PØ,13-X
1110
         TIME(X) = INP(P\emptyset)
1120 NEXT X
1130 ' PUT CLOCK BACK INTO RUNNING MODE
1140 OUT P3,240
1150 OUT P1,248
1160 FOR X=1 TO 13
        TIME (X) = INT(TIME(X)/16)
117Ø
1180 NEXT X
1190 ' THERE ARE SPECIAL MARKS ON HOURS AND MONTH
1200 ' STRIP THEM OFF
1210 \text{ TIME}(3) = \text{TIME}(3) \text{ AND } 3
1220 TIME(8) = TIME(8) AND 3
1230 '
```

# Continued from previous page.

```
1240 \text{ Y} = CHR$(TIME (1)+48)+CHR$(TIME (2)+48)
1250 \text{ MO} = \text{TIME}(3)*10+\text{TIME}(4)
1260 IF TIME(5)>0 THEN D$=CHR$(TIME(5)+48)+CHR$(TIME(6)+48):GOTO 1280
1270 D$ = CHR$(TIME (6)+48)
1280 W
                 TIME (7)
1290 H$ = CHR$(TIME (8)+48)+CHR$(TIME (9)+48)
1300 MI$ = CHR$(TIME(10)+48)+CHR$(TIME(11)+48)
1310 S$ = CHR$(TIME(12)+48)+CHR$(TIME(13)+48)
1320 '
1330 X$=INKEYS
1340 IF X1$< CHR$(32) THEN 1330
1350 IF LEN(X$)<>0 THEN 280
1360 IF S1$=S$ THEN 1050 ELSE S1$=S$
137Ø '
1380 PRINT CHR$(30); Cursor home command
1390 PRINT "
1400 PRINT W$(W);
1410 PRINT HS; ": "; MIS; ": "; SS;
1420 PRINT MOS(MO); DS; " "; "19"; YS
1430 PRINT CHR$(30);
1440 GOTO 1050
```

# V. READ.ASM

# --- Note --Interrupts not used

;PGM by Harry Kaemmerer 810220

PROM CONOUT PORT	EQU EQU EQU	HXXXX HXXXX HXX	; ADD START OF PROM ADDRESS; ADD YOUR CONOUT CALL ADDRESS; ADD BASE PORT ADDRESS FOR CLOCK
PORT1 PORT2 PORT3	EQU EQU EQU	PORT+1 PORT+2 PORT+3	; ; ; ;
SECU SECT MINU MINT HRSU HRST DAYW DAYU DAYT MONU MONT YEAU YEAT	EQU EQU EQU EQU EQU EQU EQU EQU EQU EQU	SECU+1 SECT+1 MINU+1 MINT+1 HRSU+1 HRSU+1 DAYW+1 DAYW+1 DAYU+1 DAYT+1 MONU+1 MONT+1 YEAU+1	; SECONDS UNITS ; SECONDS TENS ; MINUTES UNITS ; MINUTES TENS ; HR'S UNITS ; HR'S TENS ; DAY OF THE WEEK ; DAY UNITS ; DAY TENS ; MONTH UNITS ; MONTH TENS ; YEAR UNITS ; YEAR TENS
ASCII	EQU ORG	30H PROM	;OFFSET TO MAKE CHAR. ASCII
TIME:	CALL CALL CALL RET	TSTART DAYØ TIMED	; DO CLOCK INIT SUB ; DISPLAY TIME ; RESET CLOCK BOARD ;

Continued from previous page.

;			·;
; ;	SUB ROU!	TINES START HERE	•
DAYØ	MVI CALL MOV DAD MOV LXI DAD	B, VH A, DAYW GETDIG1 C, A B C, M H, STR4	; ; ;POINTS TO LOOKUP TABLE FOR DAY ;CLEAR B&C REGISTERS ;DAY READ INSTRUCTION ;GET IT ;MOVE DAY OF WEEK POINTER IN C. ;ADD B&C TO H&L REGISTERS ;GET OFFSET ADDRESS ;GET DAY NAME ;POINT TO NAME ;SEND IT TO TERMINAL
HRS1	CALL ANI	A, HRST GETDIG1 3 SENDCH	; ;SELECT HR'S TENS DIGIT ;GET DIGIT ;ELIMINATE EXTRA BIT ;SEND IT TO TERMINAL
HRS2		A,HRSU GETDIG	; ;SELECT HR'S UNITS DIGIT ;SEND IT TO TERMINAL
HRS3	MVI CALL	A,':' SENDIT	; ;LOAD ":" SEPERATOR ;SEND IT TO TERMINAL
MINI	MVI CALL	A, MINT GETDIG	; SELECT MINUTES TENS DIGIT ; SEND IT TO TERMINAL
MIN2	MVI CALL	A, MINU GETDIG	; SELECT MINUTES UNITS DIGIT ; SEND IT TO TERMINAL
ENIM	MVI CALL	A,':' SENDIT	;LOAD ":" SEPERATOR ;SEND IT TO TERMINAL
SEC1	MVI CALL	A, SECT GETDIG	; ;SELECT SECONDS TENS DIGIT ;SEND IT TO TERMINAL
SEC2	MVI CALL	A, SECU GETDIG	;;SELECT SECONDS UNITS DIGIT;SEND IT TO TERMINAL;

# Continued from previous page.

MON1	MVI CALL ANI LXI CPI CZ	A, MONT GETDIG1 3 H, STR5 1 LAB1	;SELECT MONTH TENS DIGIT ;GET DIGIT ;ELIMINATE EXTRA BIT ;POINT TO OFFSET LOOKUP TABLE ;SEE IF IT IS A MONTH AFTER SEPT. ;IF IT IS A ONE THEN CALL
MON2	MVI CALL DCR LXI MOV DAD MOV LXI DAD CALL	C,A B	;SELECT MONTH UNITS DIGIT ;DECR. MONTH VALUE BY ONE JAN.=0 THEN ;CLEAR B&C REGISTERS ;PUT CLOCK DIGIT IN C REGISTER ;ADD B&C TO H&L REGISTERS ;PUT OFFSET NUMBER IN C REGISTER ;LOAD MONTH NAME POINTER ;ADD OFFSET TO H&L REGISTERS ;SEND MESSAGE
	ADI MOV	Ø DAY2 PSW ASCII C,A	; SELECT DAY TENS DIGIT ; ; TEST FOR ZERO ; JUMP TO DAY IF ZERO ; SAVE RESULT ON STACK ; MAKE IT ASCII ; PUT IN C REGISTER ; SEND IT ; RESTORE A REGISTER
DAY2	MVI CALL	A,DAYU GETDIG	;SELECT DAY UNITS DIGIT ;SEND IT TO TERMINAL
YEAØ	LXI CALL	H,STR7 MESAG	;LOAD FIRST TWO DIGITS OF YEAR ;SEND IT TO TERMINAL
YEAl	MVI CALL	A, YEAT GETDIG	SELECT YEAR TENS DIGIT SEND IT TO TERMINAL
YEA2	MVI CALL RET	A, YEAU GETDIG	;SELECT YEAR UNITS DIGIT ;SEND IT TO TERMINAL ;

# Continued from previous page.

TSTART	MVI OUT MVI OUT	A, ØFØH ~ PORT1 A, ØFH ~ PORT	;SEQUENCE TO ENABLE THE CLOCK ; ; ;
	MVI CALL	A,0F0H DELAY	; ;LOAD DELAY CONST. ;DO SOME DELAY
	MVI OUT MVI OUT RET	A, ØFCH / PORT3 A, ØF4H / PORT1	;
TIMED .	MVI OUT	A,ØF8H V	; RETURN CLOCK TO RUN MODE
	MVI OUT	A, ØFH	;
	MVI	PORT /	; ;
	OUT	PORT3	;
	MVI	A, ØFCH	<b>;</b>
-	MVI	PORT1	;
	OUT	A, ØFH PORT	<i>i</i>
***	RET		; ;
MESAG	MOV CPI JZ CALL INX JMP	A,M Ø MSS1 CONOUT H MESAG	; ;GET CHARACTER FROM MEMORY ;TEST FOR ZERO ;IF ZERO EXIT ;SEND CHARACTER ;MOVE POINTER TO NEXT CHARACTER ;DO IT AGAIN UNTILL DONE
MSS1	RET		; RETURN
GETDIG SENDCH	CALL PUSH ADI MOV CALL POP RET	PSW ASCII C,A CONOUT PSW	; READ CLOCK ; SAVE FLAGS AND ACC. ; MAKE IT ASCII ; PUT IN ACC ; SEND IT TO TERMINAL ; RESTORE ACC. AND FLAGS ; RETURN TO CALLER ;

Continued from previous page.

SENDIT	PUSH MOV CALL POP RET	PSW C,A CONOUT PSW	; SAVE FLAGS AND ACC. ; PUT IN ACC ; SEND IT TO TERMINAL ; RESTORE ACC. AND FLAGS ; RETURN TO CALLER
GETDIG1	OUT MVI CALL IN ANI RRC RRC RRC RRC RRC RRC	PORT A, ØFAH DELAY PORT ØFØH	; CODE DIGIT TO SELECT CLOCK ELEMENT ; DELAY FACTOR ; DELAY SOME TIME HERE ; READ CLOCK ELEMENT ; DROP CODE PART OF DIGIT ; MOVE DATA TO LOW NIBBLE ; ; NUMBER RETURNED IN ACC. ;
LAB1	LXI MVI DAD RET	D,Ø E,ØAH D	; CLEAR D&E REGISTERS ; IF THE RESULT WAS OCTOBER OR LATER ; ADD X10 OFFSET ; LOAD POINTER OFFSET AND RETURN
DELAY:	INR JNZ RET	A DELAY	; ;NOT DONE DELAY SOME MORE ;
STR3	DB DB	ØØH,Ø8H,1ØH,19H 24H,2EH,36H	; OFFSET LOOKUP TABLE ; FOR DAY OF THE WEEK
STR4	DB DB DB DB DB DB	'Sunday ',0 'Monday ',0 'Tuesday ',0 'Wednesday ',0 'Thursday ',0 'Friday ',0 'Saturday ',0	; ; ; ; ; ; ; ;

# Continued from previous page.

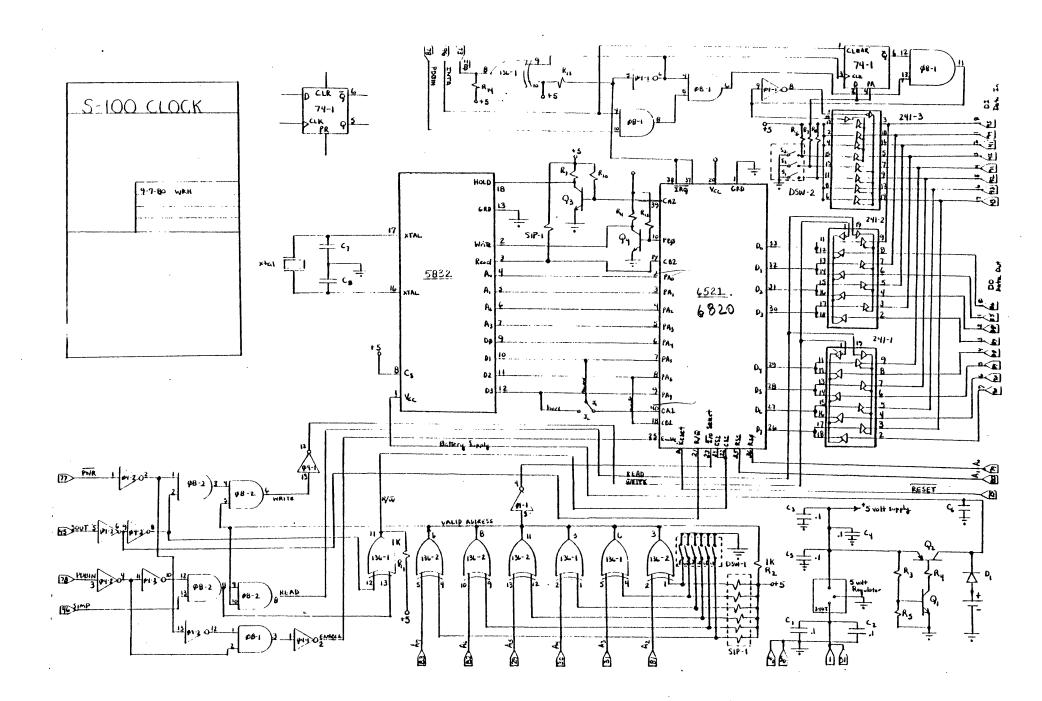
STR5	DB DB DB	00H,0AH,15H,1DH 25H,2BH,32H,39H 42H,4FH,59H,64H	;OFFSET LOOKUP TABLE ;FOR MONTH NAME ;
STR6	DB	January ',0 February ',0 March ',0 April ',0 May ',0 June ',0 July ',0 August ',0 September ',0 October ',0 November ',0 December ',0	; ; ; ; ; ; ; ; ; ;
STR7	DB END	' 19',0 PROM	; ;YEAR LEADIN MESS.

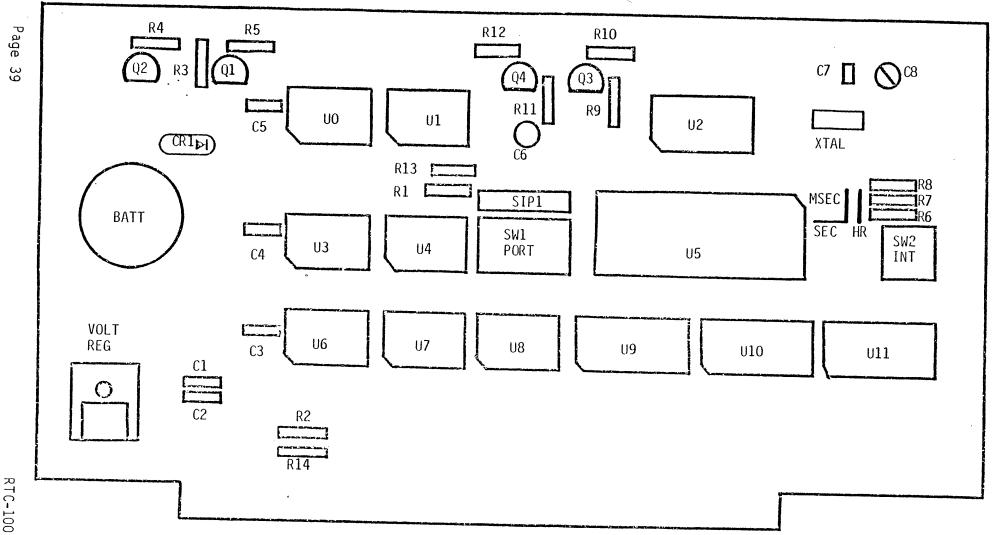
```
VI. NORTHSTAR SOFTWARE
```

```
10 REM**************************
20 REM****NORTH STAR BASIC****************
30 REM PROGRAM TO SET SCITRONICS CLOCK - 801003*
40 REM **COPYRIGHT 1980 - SCITRONICS, INC******
50 REM****CONV BY RICE COMMUNICATIONS INC******
60 !"PROGRAM TO SET THE SCITRONICS CLOCK"\DIMT(13)
70 !"SCITRONICS, INC., BETHLEHEM, PA 18015"\!
80 ! "ENTER CLOCK BASE PORT ADDRESS"
90 !"IN DECIMAL INTER FORM - 0,4,8,16,20,ETC"
100 ! "MAXIMUM VALUE = 252"\INPUT P
110 REM CHECK FOR PROPER VALUE AND RANGE
120 IFP>=256THEN80\M=INT(P/4)\IF(4*M-P)<>0THEN80\P1=P+1\P2=P+2\P3=P+3
130 REM GET THE TIME DATA
140 !\!"ENTER DATE IN THE FORM: YR, MO DA"\!"ONLY LAST TWO DIGITS FOR YR!"
150 INPUT Y, M, D\!\! "ENTER DAY-OF-THE-WEEK - 0=SUNDAY"\INPUTW\!
160 !"ENTER TIME ON 24HR CLOCK IN THE"\!"FORM: HR, MIN"
170 !"CLOCK ALWAYS STARTS ON 00 SECONDS, SO"
180 !"BE SURE TO ENTER THE NEXT MINUTE."\INPUTH, M1\REM PREPARE THE DATE
190 T(1) = 16 \times INT(Y/10) + 12 \setminus T(2) = 16 \times (Y-10 \times INT(Y/10)) + 11 \setminus T(3) = 16 \times INT(M/10) + 10
200 T(4)=16*(M-10*INT(M/10))+9\T(5)=16*INT(D/10)+8\T(6)=16*(D-10*INT(D/10))+7
210 T(7) = 16 *W + 6 \ T(8) = 16 *INT(H/10) + 5 + 128 \ T(9) = 16 * (H-10 *INT(H/10)) + 4
220 T(10) = 16 \times INT(M1/10) + 3 \times T(11) = 16 \times (M1-10 \times INT(M1/10)) + 2 \times T(12) = 1 \times T(13) = 0
230 REM DATA IS READY, ASK FOR START SIGNAL FROM USER
240 !\!"ENTER 1 AND CARRIAGE RETURN TO SET CLOCK"
250 ! "PUSH CR ON THE ZERO SECOND" \ INPUTJ \ REM THIS IS TO SET SEQUENCE
260 OUTP3,244\OUTP2,255\OUTP3,240\OUTP2,255\OUTP1,240\OUTP,255\OUTP3,244
270 OUTP1,244\FORX=1TO13\OUTP,T(X)\OUTP2,0\OUTP2,1\NEXTX\OUTP1,248\OUTP3,240
280 !"THE CLOCK IS SET"\CHAIN"RTCREAD"
```

```
10 REM **************************
20 REM THIS PROGRAM IS IN NORTH STAR BASIC*******
30 REM ****CONV BY RICE COMMUNICATIONS INC*******
40 REM ** READ SCITRONICS REAL-TIME CLOCK******
50 REM *COPYRIGHT 1980 - SCITRONICS, INC. *******
60 REM *****PRESS (ESC) TO STOP READ OUT******
70 DIM T(13), W$(21) \W$="SUNMONTUEWEDTHRFRISAT"
80 !"PROGRAM TO DISPLAY CLOCK DATA"
90 !"SCITRONICS, INC., BETHLEHEM, PA"\!
100 REM THIS IS THE NON INTERRUPT-DRIVEN VERSION
110 !"ENTER CLOCK BASE PORT ADDRESS"
120 !"IN DECIMAL INTER FORM - 0,4,8,12,16,20,ETC"
130 ! "MAXIMUM VALUE = 252"\INPUT P
140 REM CHECK FOR PROPER VALUE AND RANGE
150 IFP>=256THEN80\M=INT(P/4)\IF(4*M-P)<>0THEN80\P1=P+1\P2=P+2\P3=P+3
160 IFINP(2)=155THEN80\GOSUB200\REM DISPLAY LOOP STARTS HERE
170 Y=T(1)*10+T(2) M0=T(3)*10+T(4) D=T(5)*10+T(6) W=T(7)+1 H=T(8)*10+T(9)
180 M1=T(10)*10+T(11) \S=T(12)*10+T(13)
190 !Y,":",MO,":",D," ",\!W$(W*3-2,W*3),". ",\!H,":",M1,":",S," "\!\GOTO160
200 REM***************
210 REM THIS READS THE CLOCK DATE *
220 REM*****************
230 OUT P1,240\OUTP,15\OUTP3,252\OUTP1,244\FORX=1TO13\OUTP,13-X
240 T(X) = INP(P) \setminus NEXT X
250 REM********************
260 REM PUT CLOCK BACK INTO RUNNING MODE*******
270 REM**********************
280 OUT P1,248\OUTP,15\OUTP3,248\OUTP1,252\OUTP,15\FORX=1TO13
290 T(X)=INT(T(X)/16) \NEXT X\T(3)=T(3)-14T(3)/4+4\7(8)=T(8)-14T(T(8)/4)+4
```

# SCHEMATIC & PARTS LIST





RTC-100 PARTS LAYOUT: Refer to Parts List for parts description

PRODUCT: RTC-100

PAGE 1 OF 3

LAYOUT #	SCITRONICS P/N	DESCRIPTION
B 1	17031	3 V Lithium Battery
C 1	12024	.1mF @ 12V Ceramic Disc Capacitor
C 2	12024	.1mF @ 12V Ceramic Disc Capacitor
С 3	12024	.1mF @ 12V Ceramic Disc Capacitor
C 4	12024	.1mF @ 12V Ceramic Disc Capacitor
C 5	12024	.1mF @ 12V Ceramic Disc Capacitor
C 6	12014	4.7mF @ 10V Solid Tantallum Capacitor
C 7	12031	15pF Ceramic Disc Capacitor
C 8	12031	15pF Ceramic Disc Capacitor
С 9	12032	5-30pF Variable Ceramic Capacitor
CR 1	13011	IN6263 Diode
· D1	20079	8 Position DIP Switch
D 2	20084	4 Position DIP Switch
· DS O	17012	14 Pin DIP Socket
DS 1	17012	14 Pin DIP Socket
DS_2	17032	18 Pin DIP Socket
DS 3	17012	14 Pin DIP Socket
DS 4	17012	14 Pin DIP Socket
DS 5	17034	40 Pin DIP Socket
DS 6	17012	14 Pin DIP Socket
DS 7	17012	14 Pin DIP Socket
DS 8	17012	14 Pin DIP Socket
DS 9	17033	20 Pin DIP Socket
DS 10	17033	20 Pin NIP Socket
DS 11	17033	20 Pin DIP Socket

FRODUCT: RTC-100

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LAYOUT #	SCITRONICS P/N	DESCRIPTION
HS 1	17030	Heat Sink for Voltage Regulator
J 1	22010	Jumper (seconds)
Q 1	18001	2N4124 Transistor
Q 2	18003	2N4126 Transistor
Q 3	18006	MPSA13 Transistor
Q 4	18006	MPSA13 Transistor
VR 1	14041	+5 V Voltage Regulator
R 1	11013	1K 坛w Carbon Film Resistor
R 2	11013	1K 뉰w Carbon Film Resistor
R 3	11022	51K ┧w Carbon Film Resistor
R 4	11001	10K ¼w Carbon Film Resistor
R 5	11001	10K ¼w Carbon Film Resistor
R 6	11028	3.3K ¼w Carbon Film Resistor
R 7	11028	3.3K ¼w Carbon Film Resistor
R 8	11028	3.3K ¼w Carbon Film Resistor
R 9	11012	20K ¼w Carbon Film Resistor
R 10	11012	20K ¼w Carbon Film Resistor
R 11	11012	20K 뇌w Carbon Film Resistor
R 12	11012	20K 坛w Carbon Film Resistor
R 13	11028	3.3K ¼w Carbon Film Resistor
R 14	11028	3.3K ¼w Carbon Film Resistor
SR 1	11033	3.3K SIP Resistor Network
UО	14047	7474 Dual D Flip Flop
U 1	14046	7408 Quad 2-in AND Gate
U 2	14045	MSM5832 OKI Clock Chip

PAGE \_3\_ OF \_3\_

PRODUCT: RTC-100

LAYOUT #	SCITRONICS P/N	DESCRIPTION	
U 3	14046	7408 Quad 2-in AND Gate	
U 4	14030	74LSO4 Hex Inverter	
U 5	14048	6821 Peripheral Interface Adaptor	
U 6	14030	74LS04 Hex Inverter	
U 7	14049	74LS136 Quad Exclusive OR Gate (0.C.)	
U 8	14049	74LS136 Quad Exclusive OR Gate (0.C.)	
U 9	14050	74LS241 Octal Bus/Line Driver	
U 10	14050	74LS241 Octal Bus/Line Driver	
U 11	14050	74LS241 Octal Bus/Line Driver	
HW 1	21001	6-32 ¼" Screw	
HW 2	21030	6-32 Hex Nut	
PC 1	17012	S-100 Real Time Clock PC Board	
X 1	17035	Crystal (32.768KHz)	
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#### WARRANTY

All SciTronics Inc. computer boards are assembled and fully tested at the factory before shipment. Your Real Time Clock is warranted against defects in material and workmanship for a period of six (6) months from date of delivery. SciTronics will repair or replace products found to be defective during the warranty period, provided they are returned to SciTronics Inc. No other warranty is expressed or implied. We are not liable for improper use or inconsequential damages. We reserve the right to refuse to repair any product that in our opinion has been subjected to abnormal electrical or mechanical abuse. Products out of warranty are subject to a minimum service fee. Contact SciTronics before sending your unit in for repair. Please fill out the enclosed self-addressed warranty card to validate your warranty.

	(located on board)	
Serial #		
Date Purch	ased	
for your r	ecords:	