2A

MODIFICATION TO:

N-16044/000/01--THE AN/FSQ-32

A Description and Coding Manual for Experienced Programmers



APPROVED

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CURRENT MODIFICATION	
Modified Pages	Corrections*
8	In the third instruction, LDC - Load B Register Complement, delete the letters "LDC" and insert the letters "LBC."
26	Under NRA - Normalize Accumulator, in the sentence "The number of shifts required," delete the words "the right half of $c(x)$ " and insert the words "location x, bits 41-47, clearing S-40."
50	In the last line on the page, under the Description, delete "xby(4_7)" and insert "x."
51	In the second line under Description, delete "by(4_7)."

^{*} All these corrections may be handwritten.