N = :	17473/001/00
-------	--------------

AUTHOR / / / A play for the same of
nomen of the strength
1

J. A. Slaybaugh

DATE PAGE 1 OF 1 PAGE
1 February 1962

Author Delivered

NOTE as

an internal working paper

System Development Corporation/2500 Colorado Ave./Santa Monica, California 2/6

HALF-WORD MODE WITH AOR, SOR, AND ATR

97

SD

Byte activity is independent of mode. That is, mode affects the adders and accumulators, whereas byte activity affects transfers between the exchange and A registers. Therefore, if byte activity is not compatible with mode in a left or right operation, bytes from the unaffected half accumulator will be set into the memory word.

For Example:

AØR, left mode, bytes 0-5 active, displacement zero

Original memory word 00112233 44556677 Original accumulator 01234567 76543210

New memory word 00112234 76546677 New accumulator 00112234 76543210