



SYSTEMS CONCEPTS

520 THIRD STREET SAN FRANCISCO, CALIFORNIA 94107

DIGITAL SYNTHESIZER
PRELIMINARY
ENGINEERING DRAWINGS
VOLUME 1

Proprietary Information

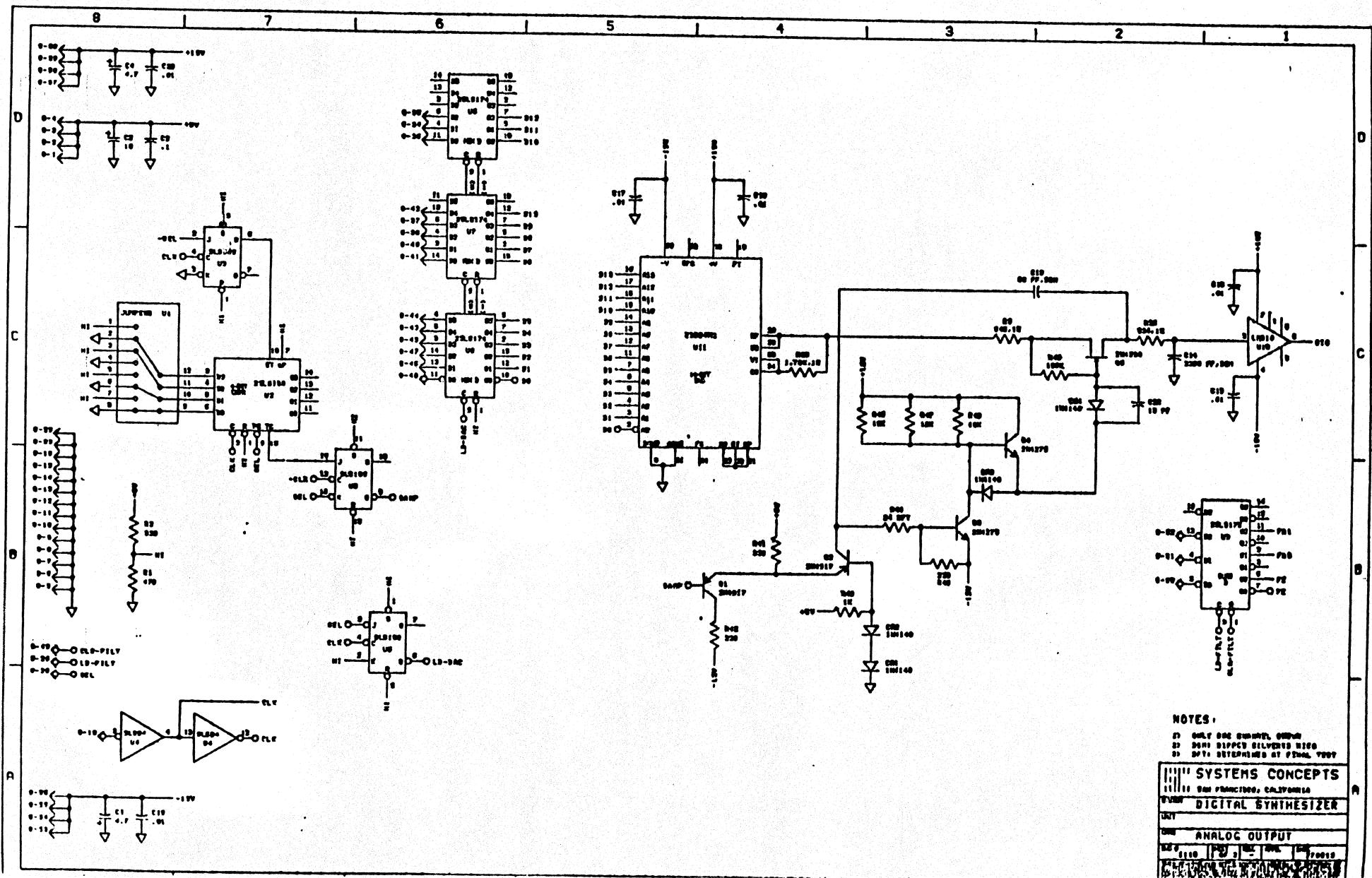
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TELEPHONE: 415-442 1500

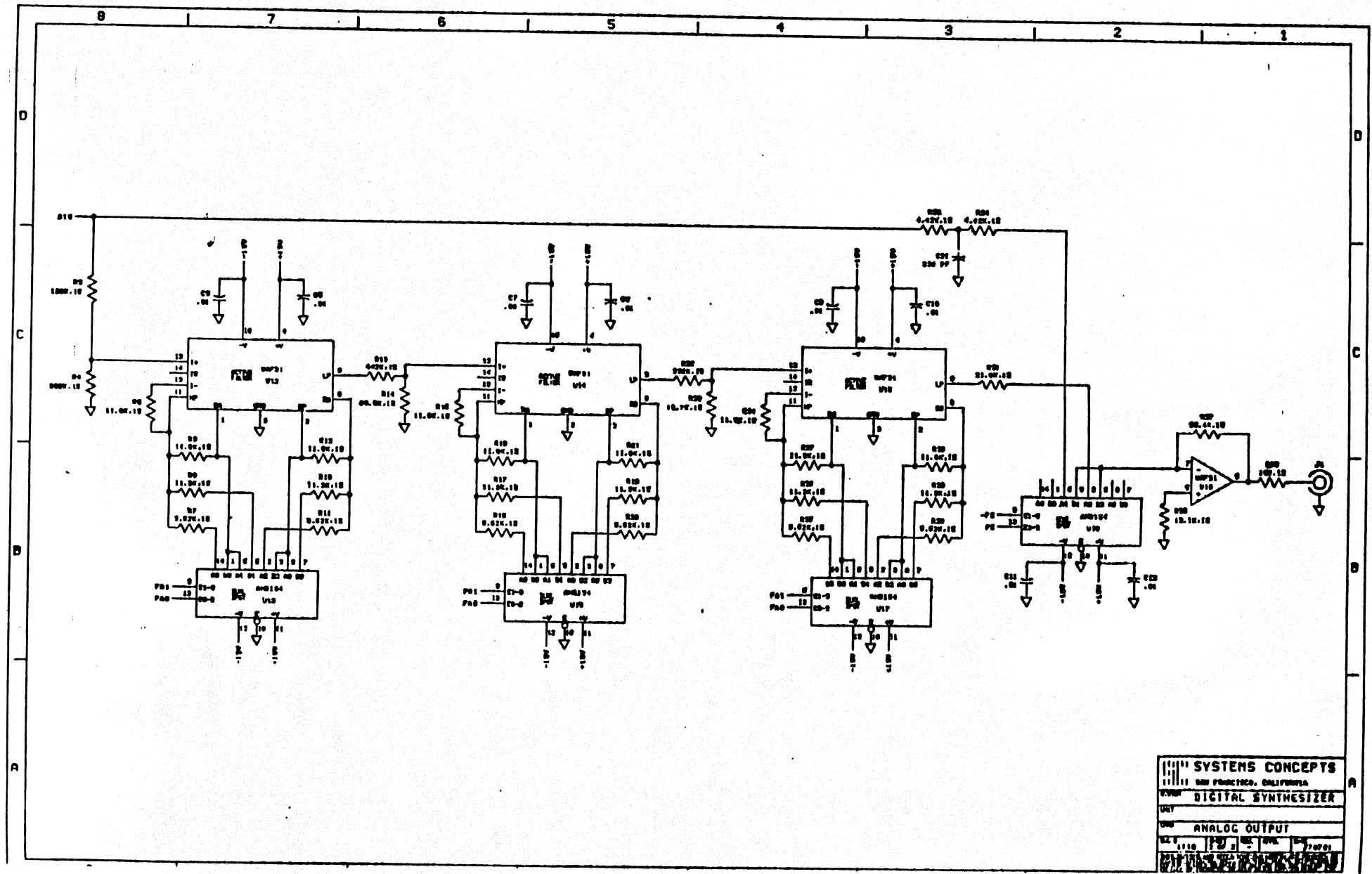
TWX: 910-372-6062

Exh. SC2

SFE; ANALOGI

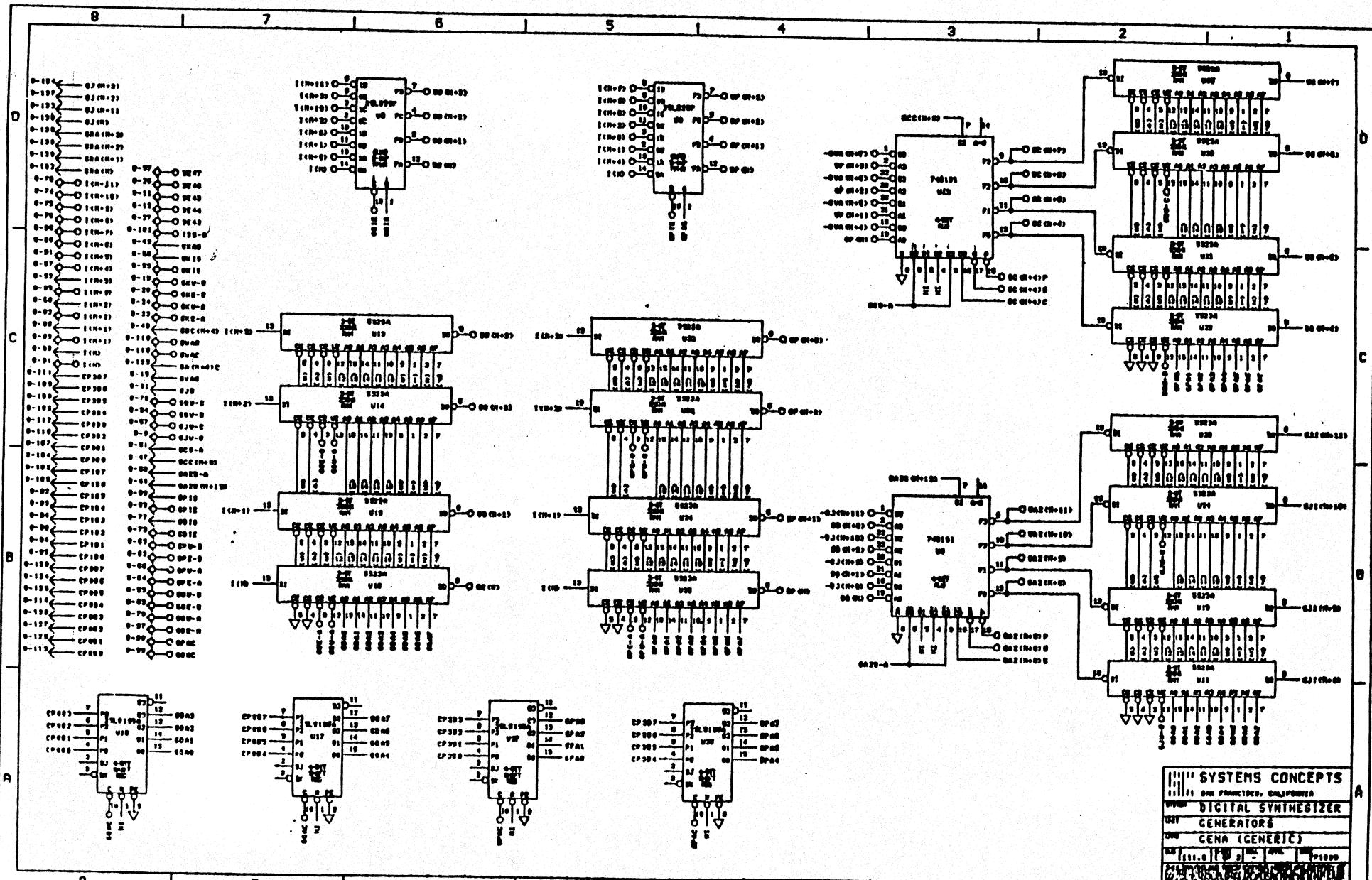


SFE; ANALOG 2



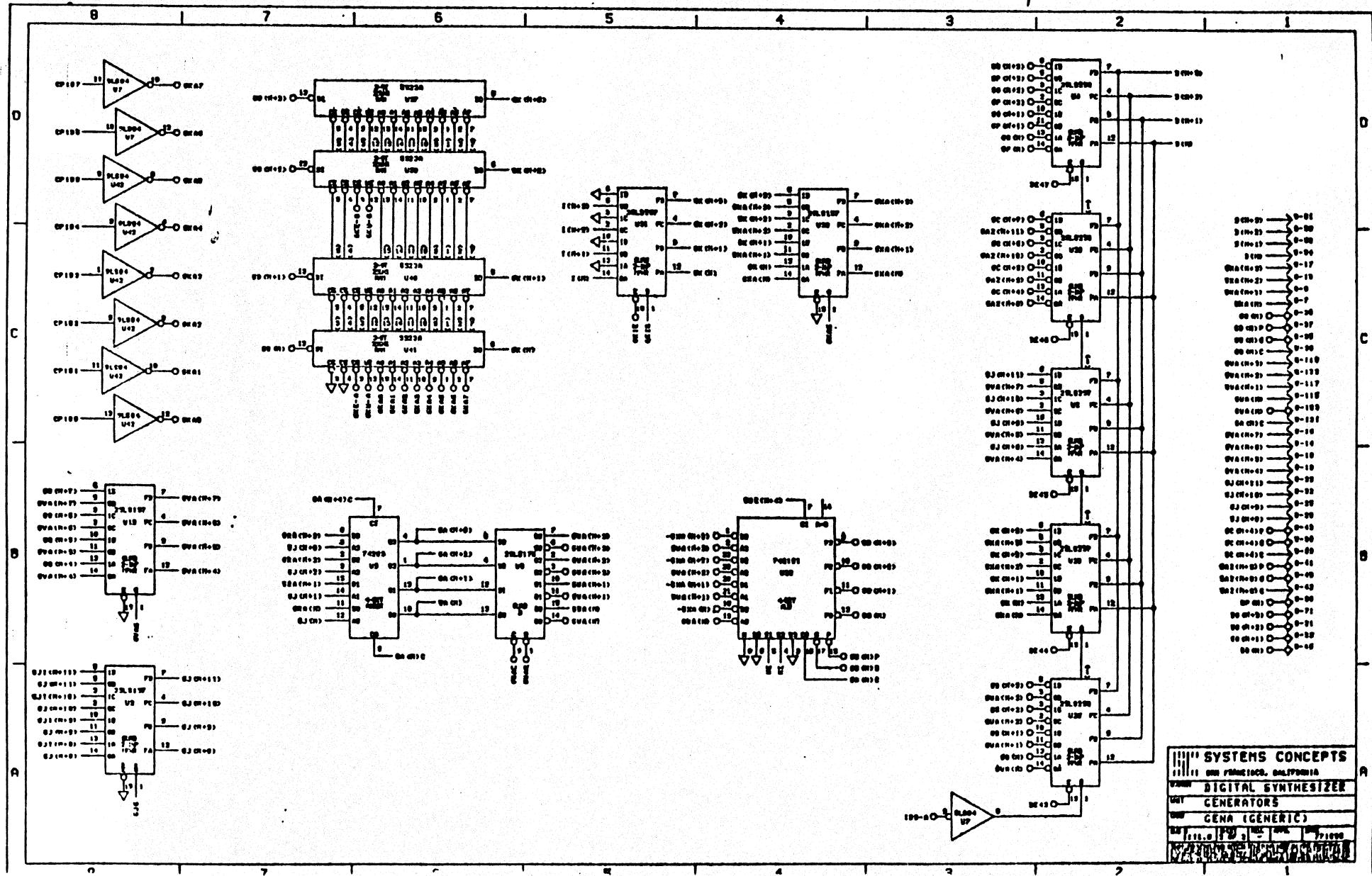
SWB; GENAI

$$PTF = 1$$



SWB; GENA2

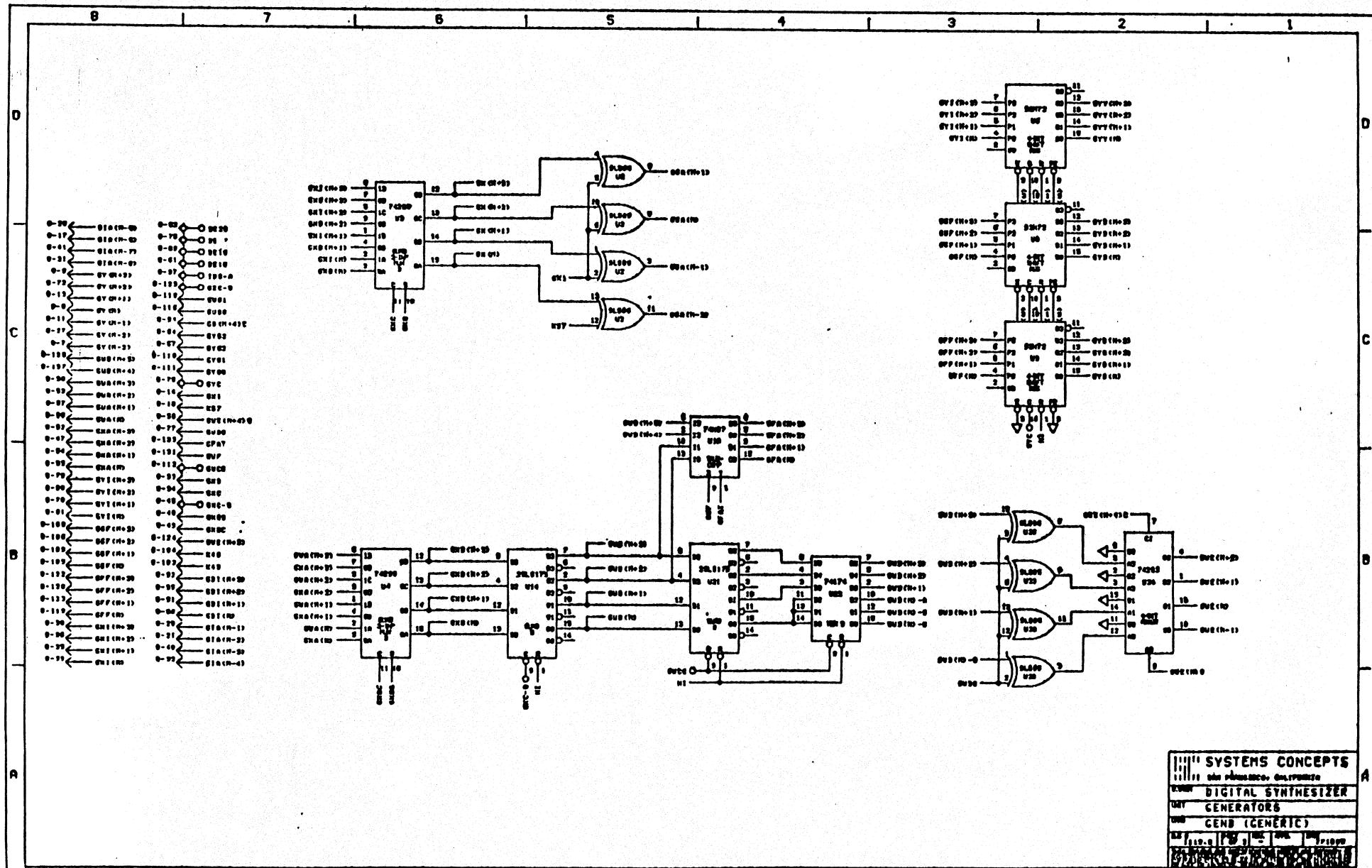
PTF=1



SYSTEMS CONCEPTS
11 SAN FRANCISCO, CALIFORNIA
DIGITAL SYNTHESIZER
GENERATORS
GENA (GENERIC)
1111.0 1111.1 1111.2 1111.3 1111.4

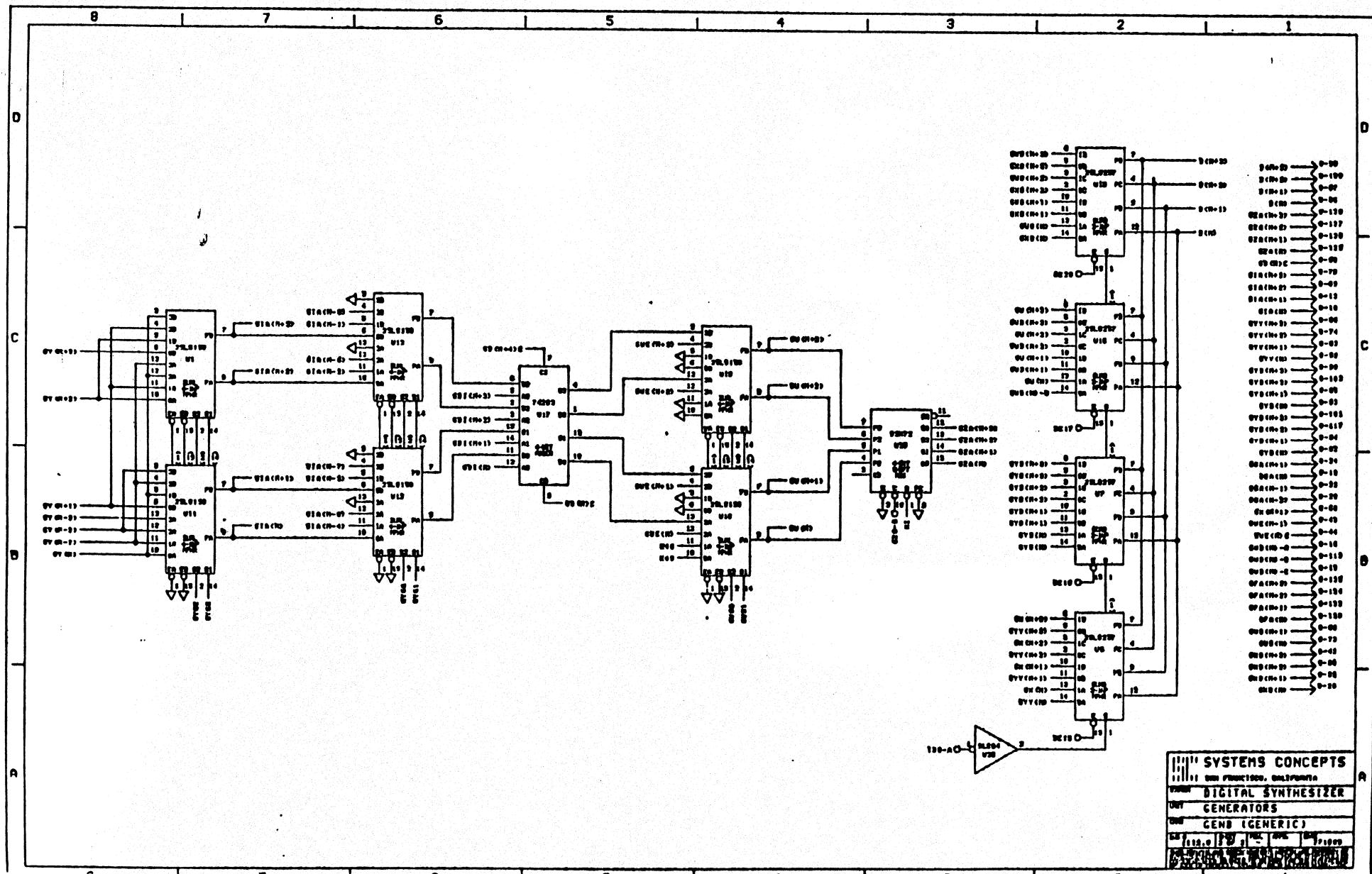
SWB; GEN(B)

$$PTF = 1$$



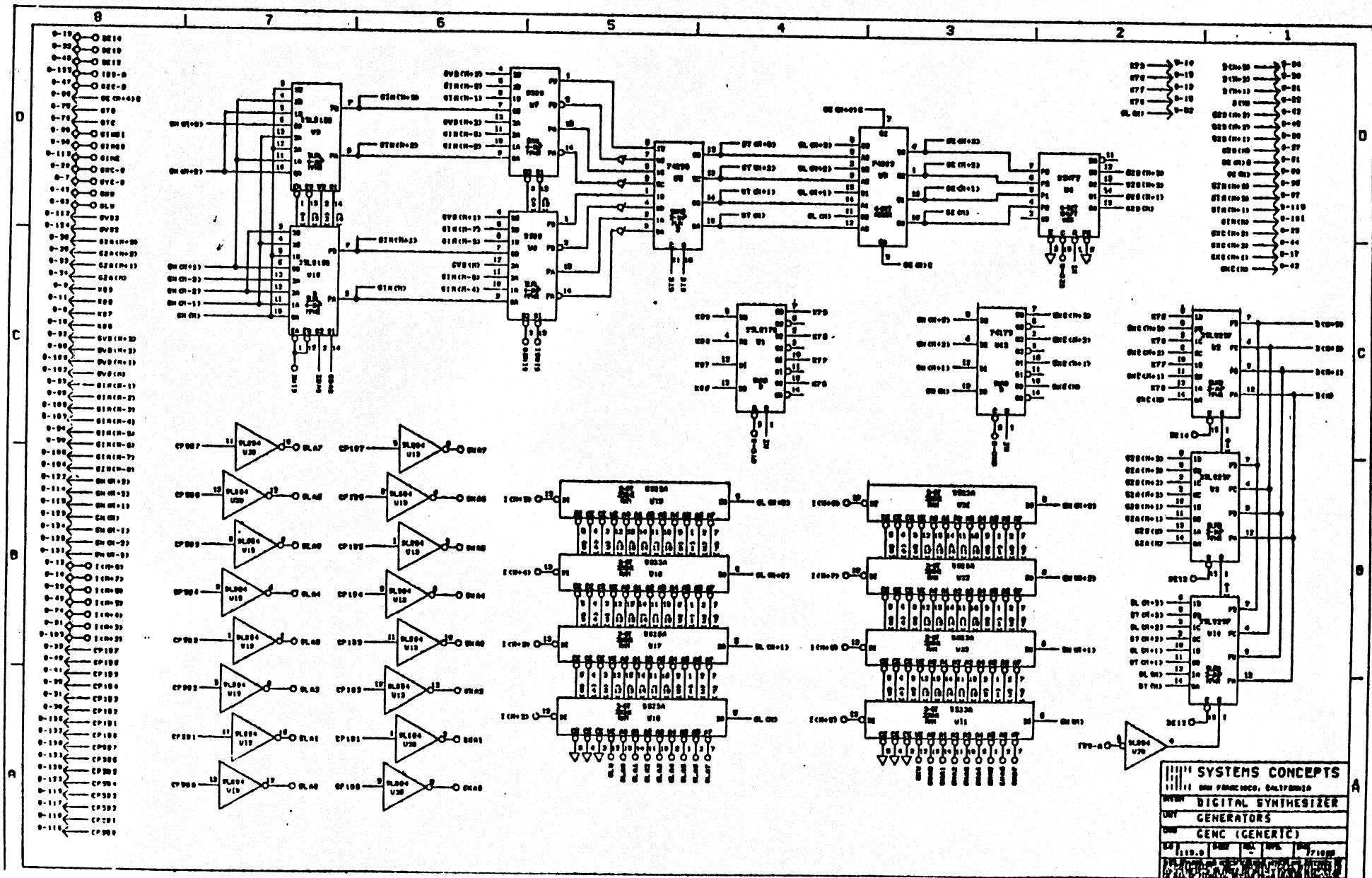
SWB; GENB2

PTF=2



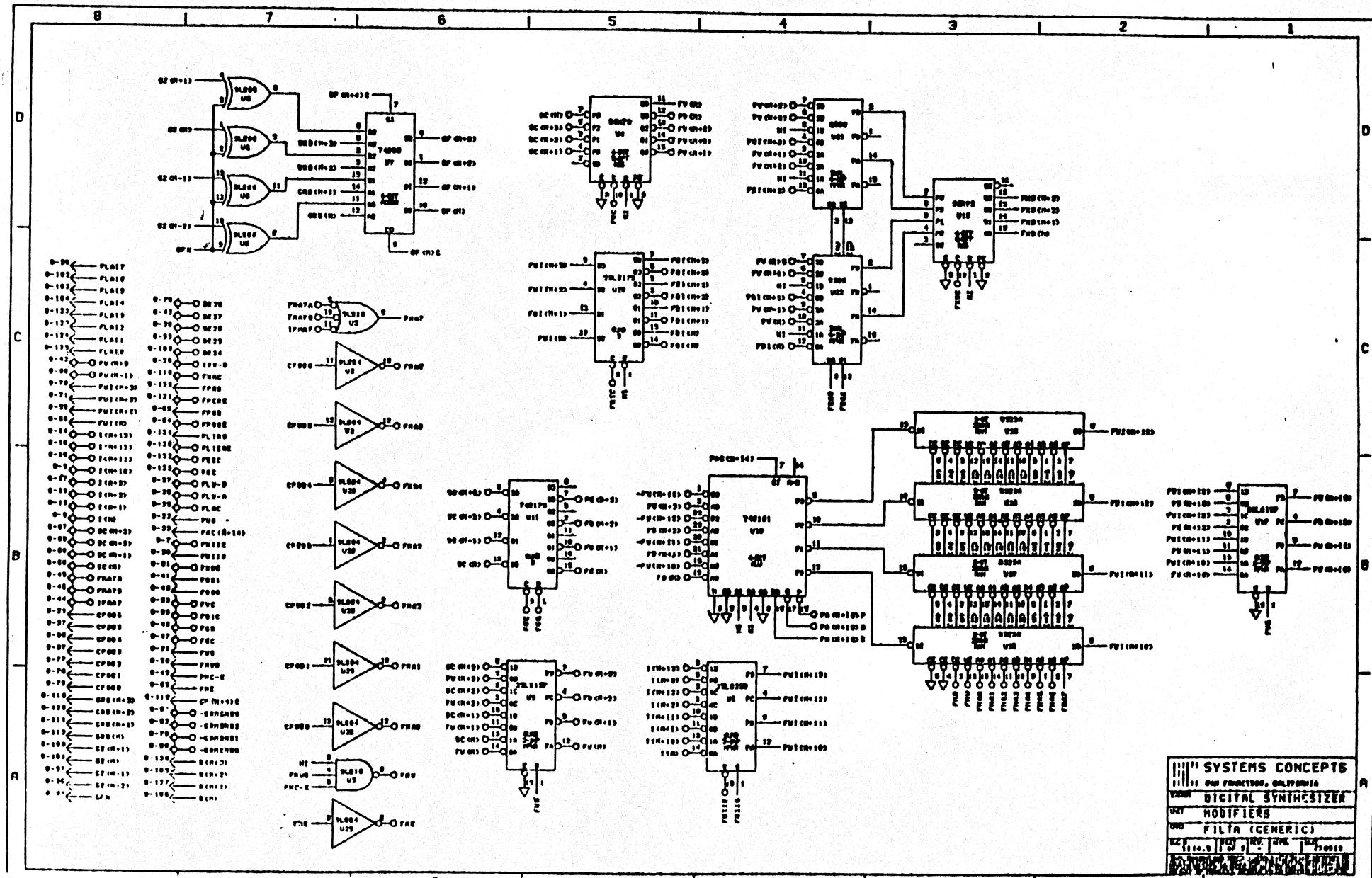
SFE; GENC

PTF=1



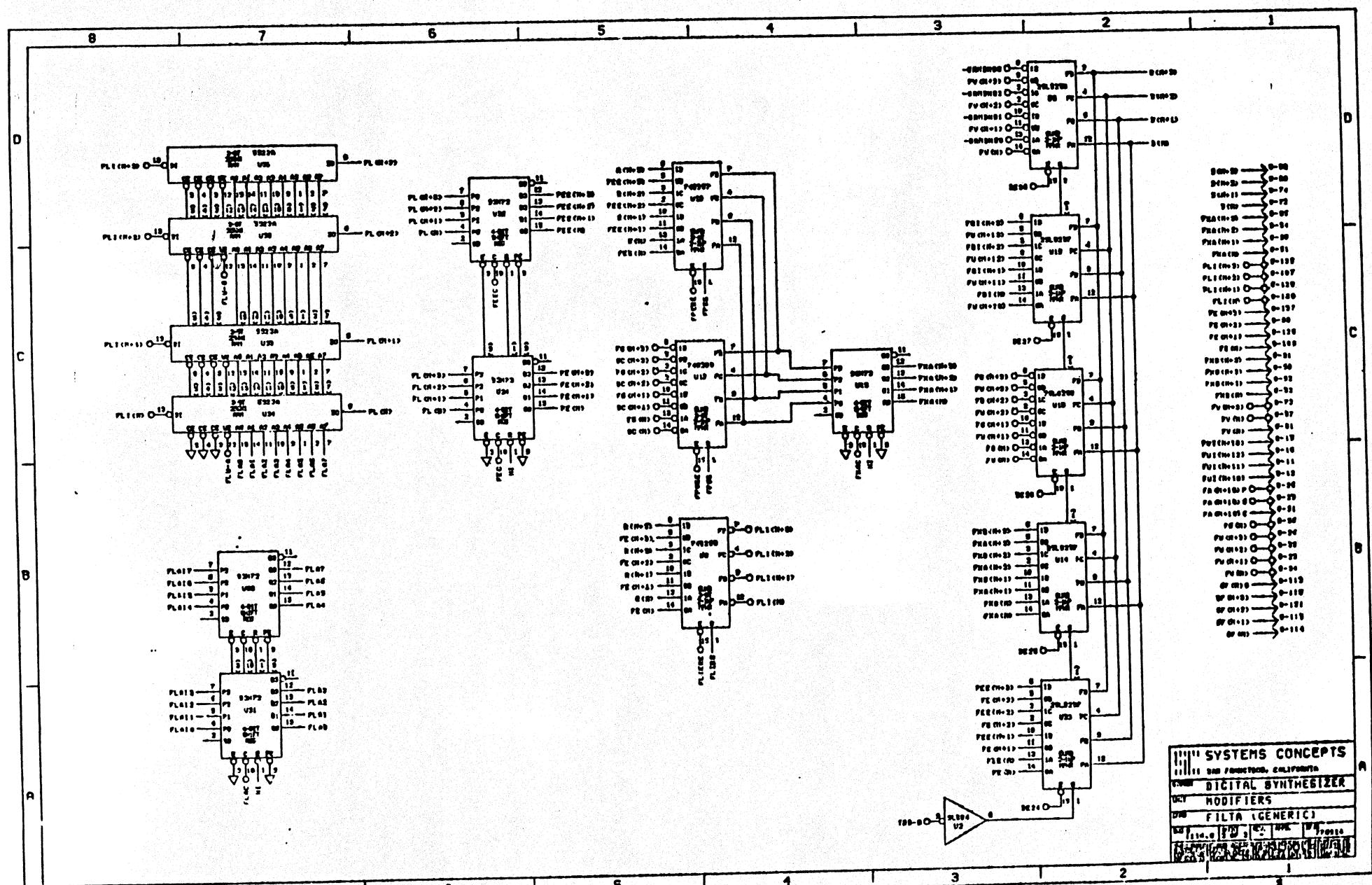
SFE; FILTA 1

$$PTF = 1$$



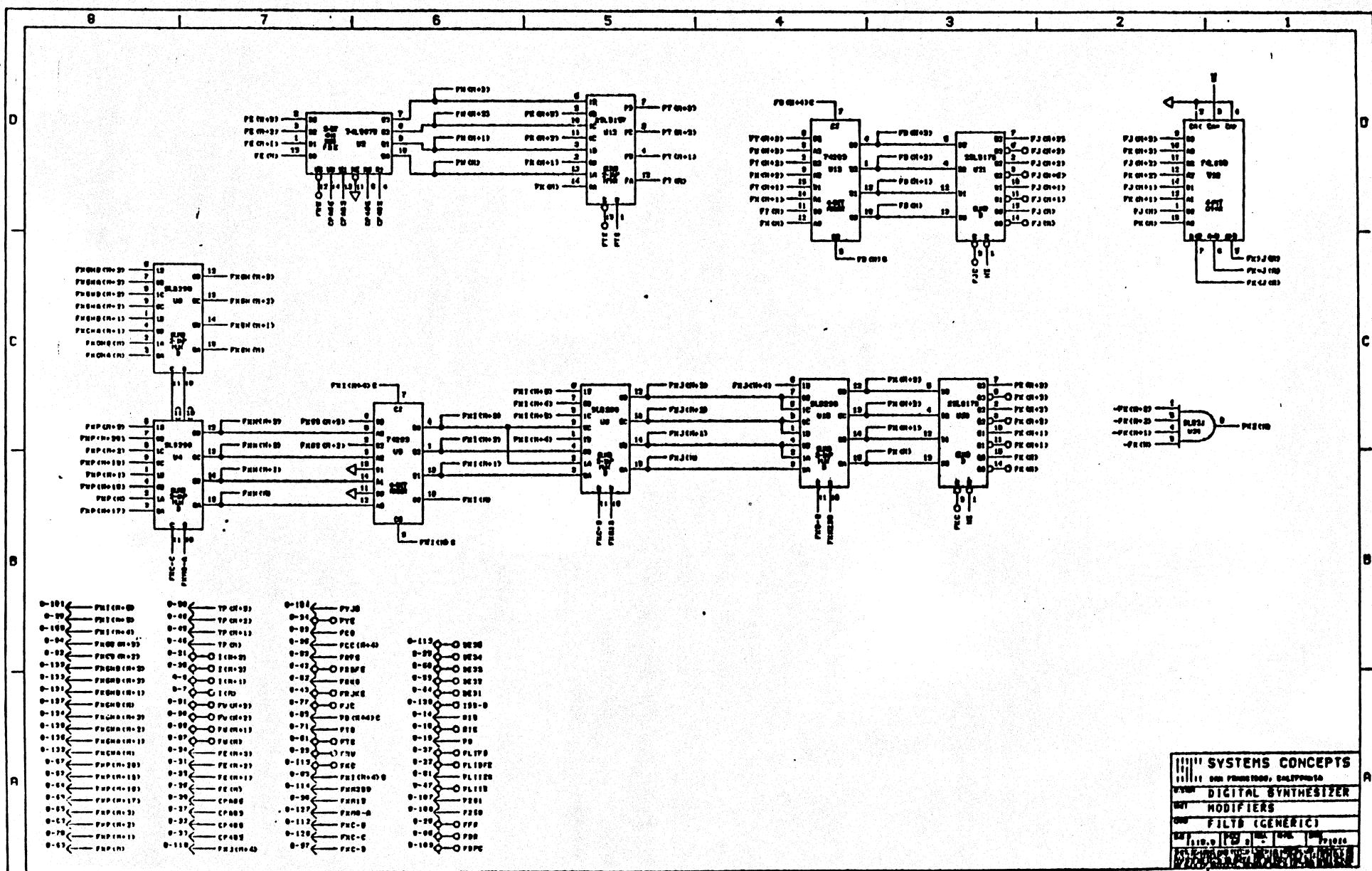
SFE; FILT A2

PTF=1



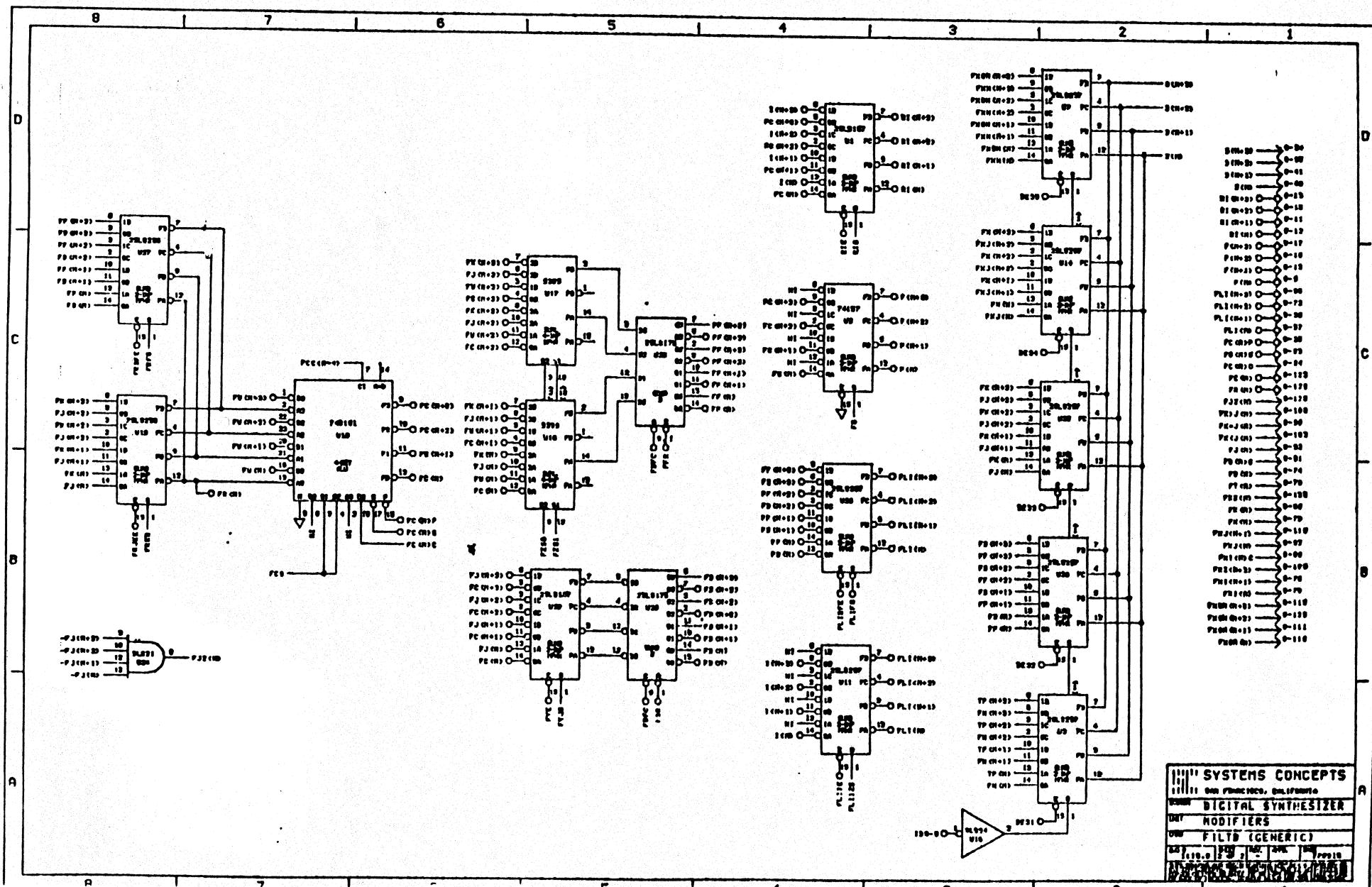
SFE; FILTB1

$$PTF=1$$



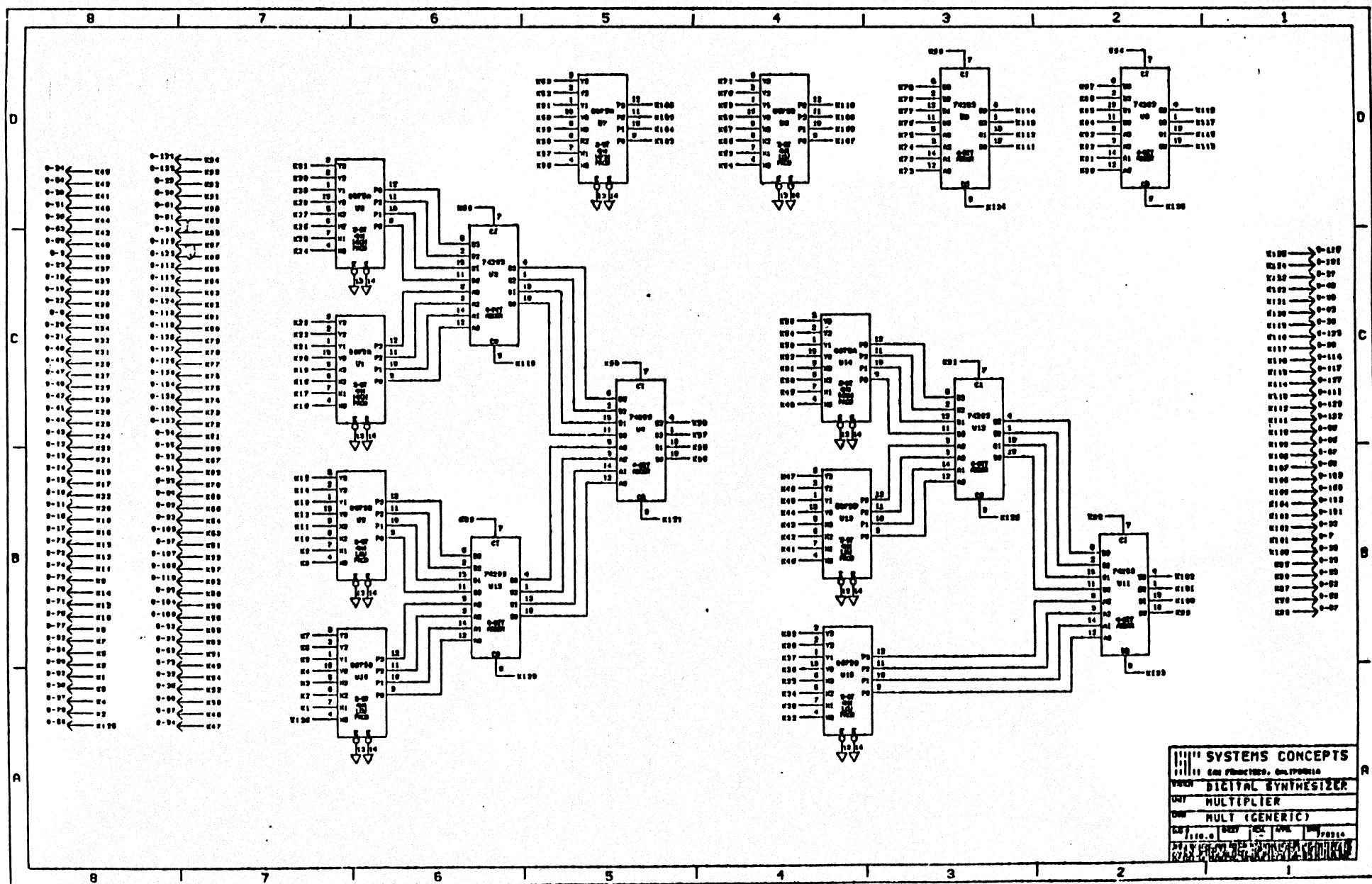
SFE; FILTER

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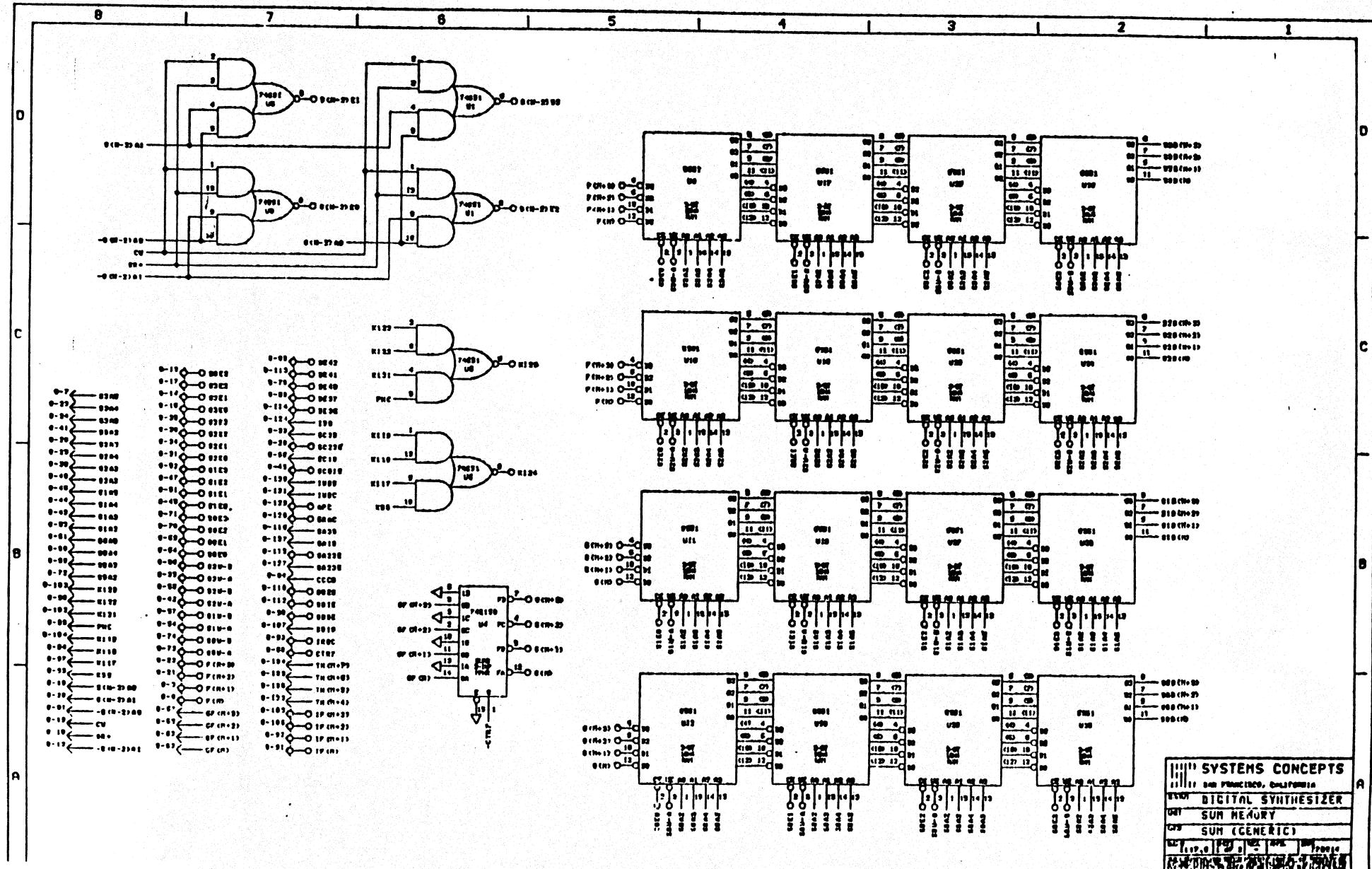
DM; MULT

$$PTF = 1$$



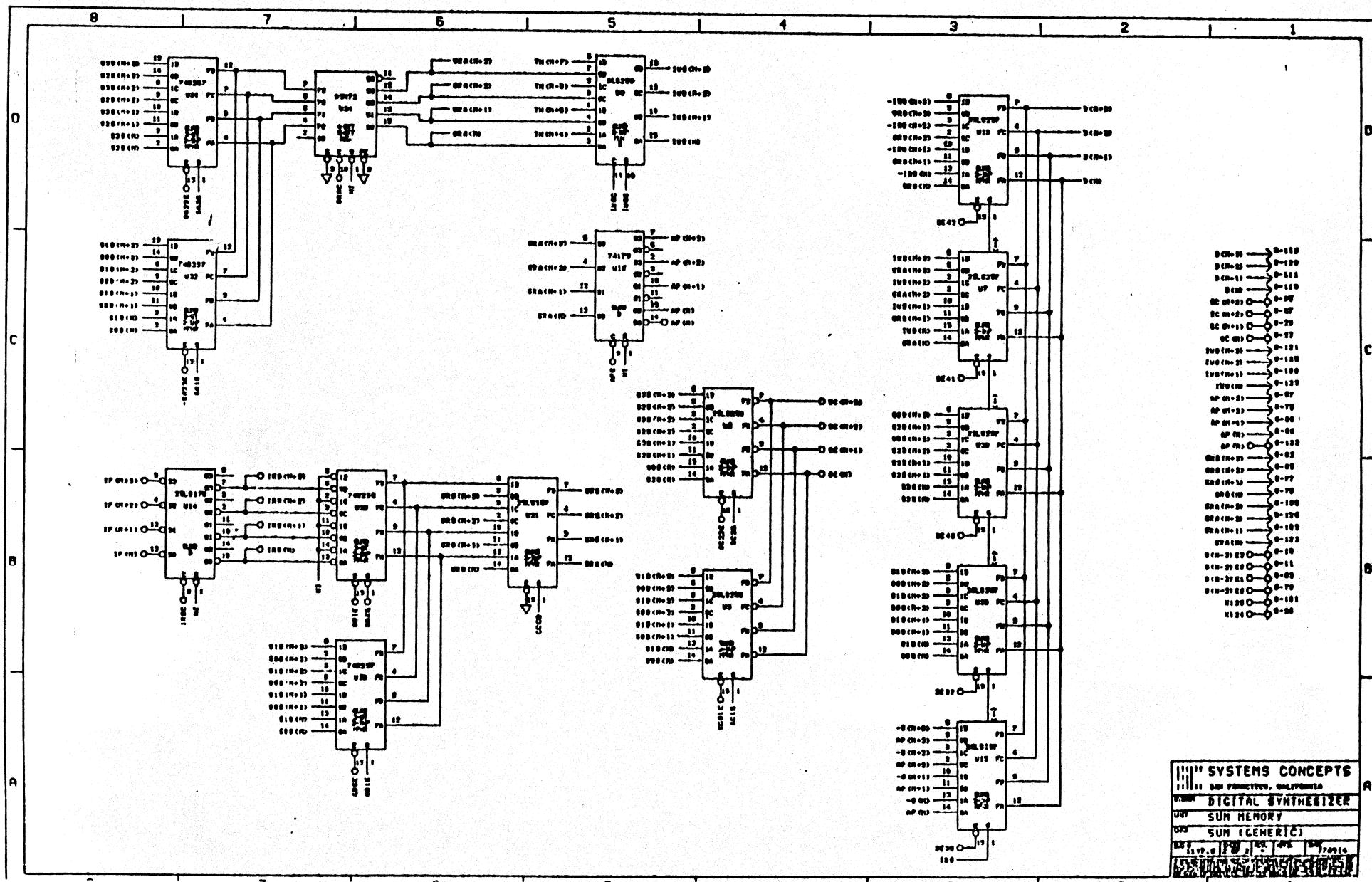
SFE; SUM 1

PTF = 1



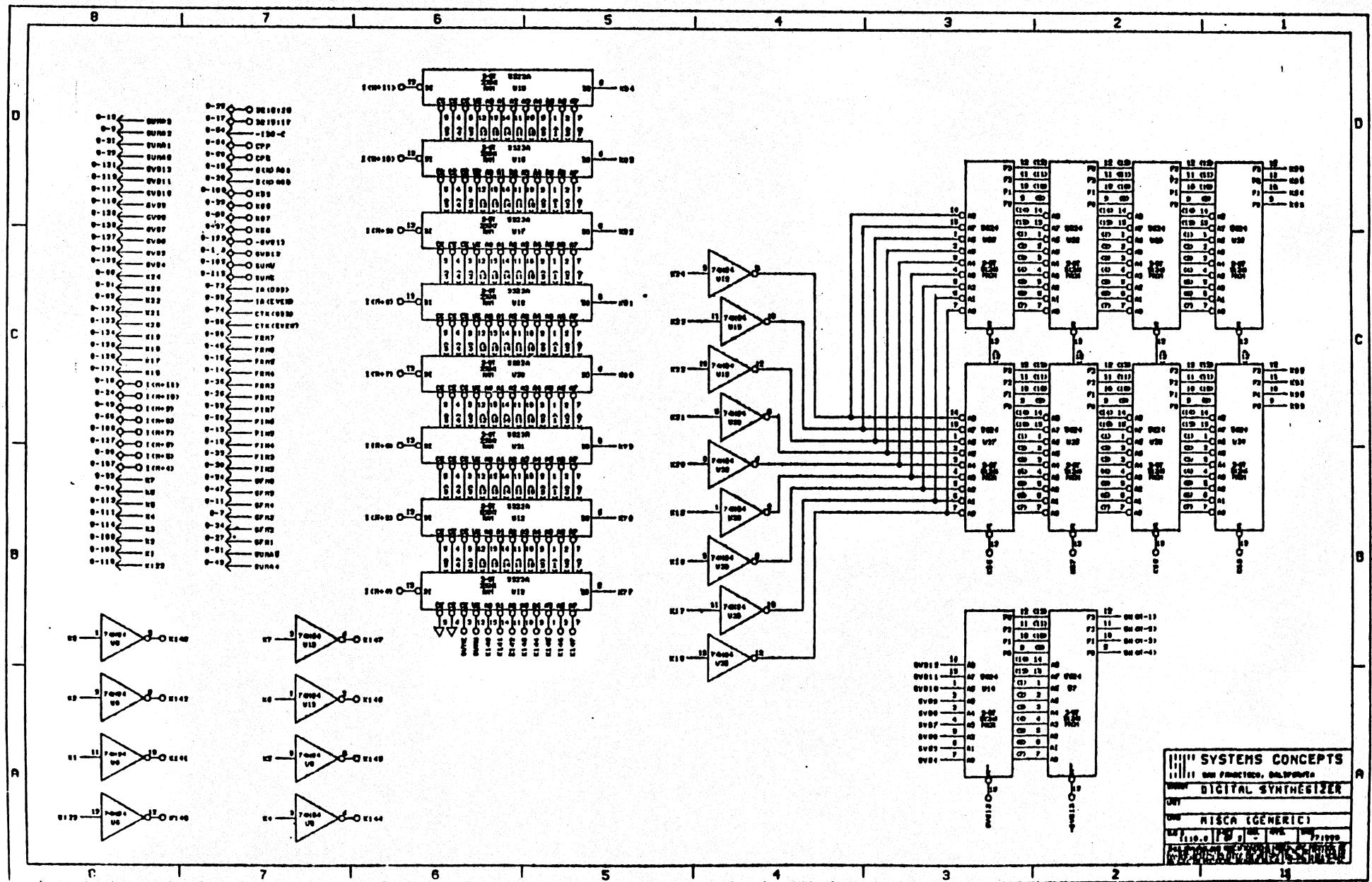
SFE; SUM2

$\text{PTF} = 1$



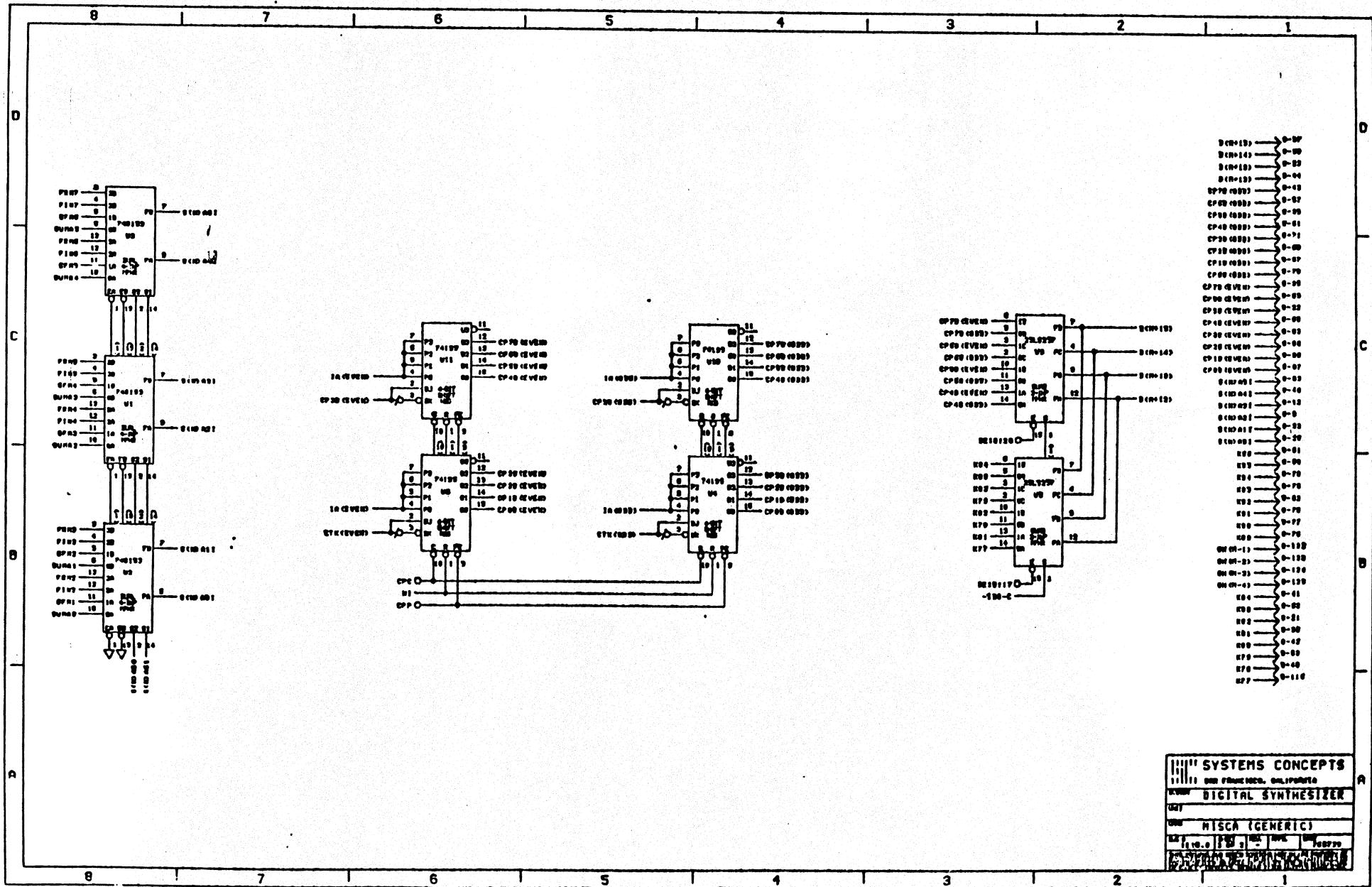
SRE' MISCAI

$$PTF=1$$



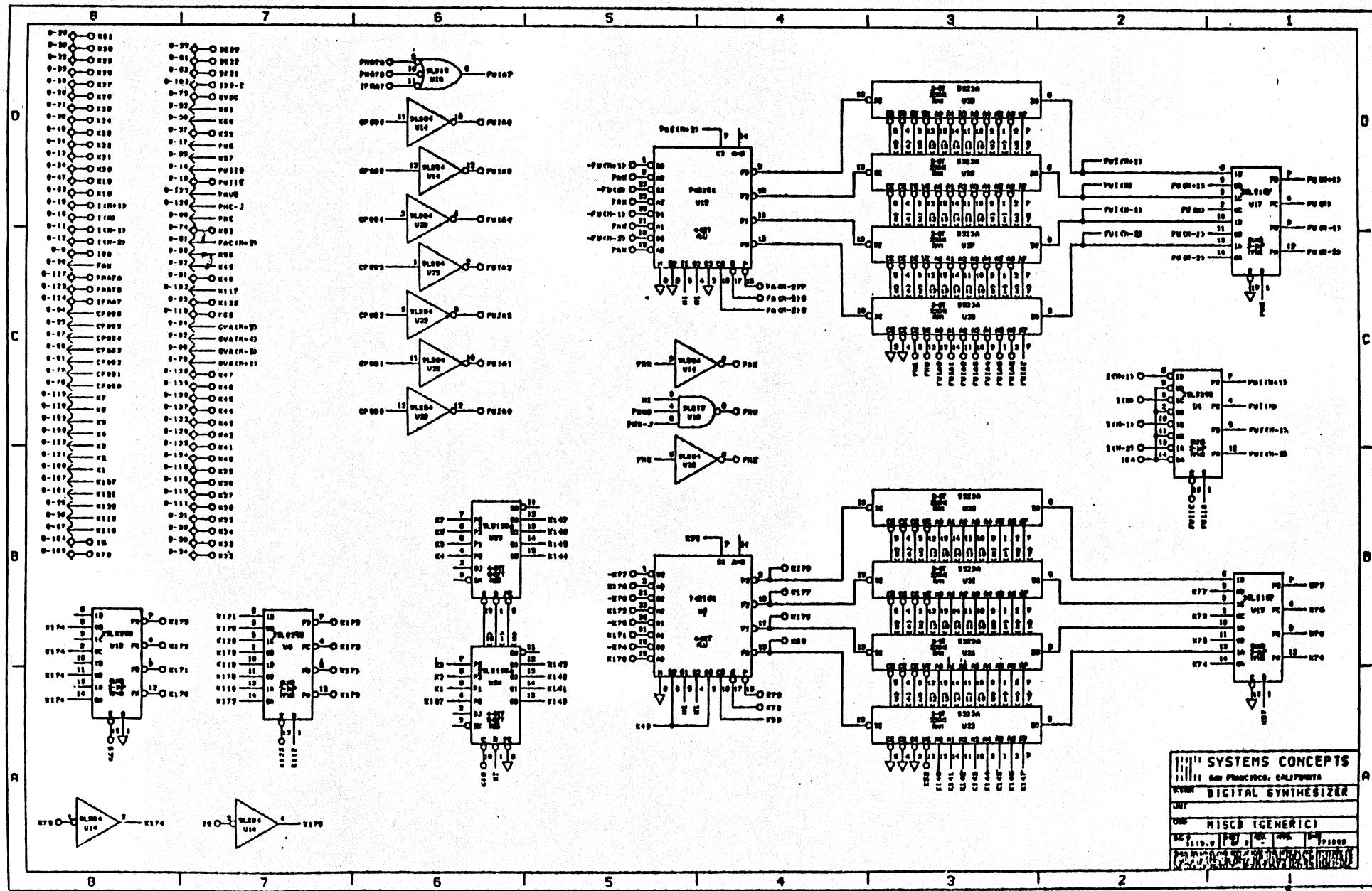
SRE; MISCA2

PTF=1



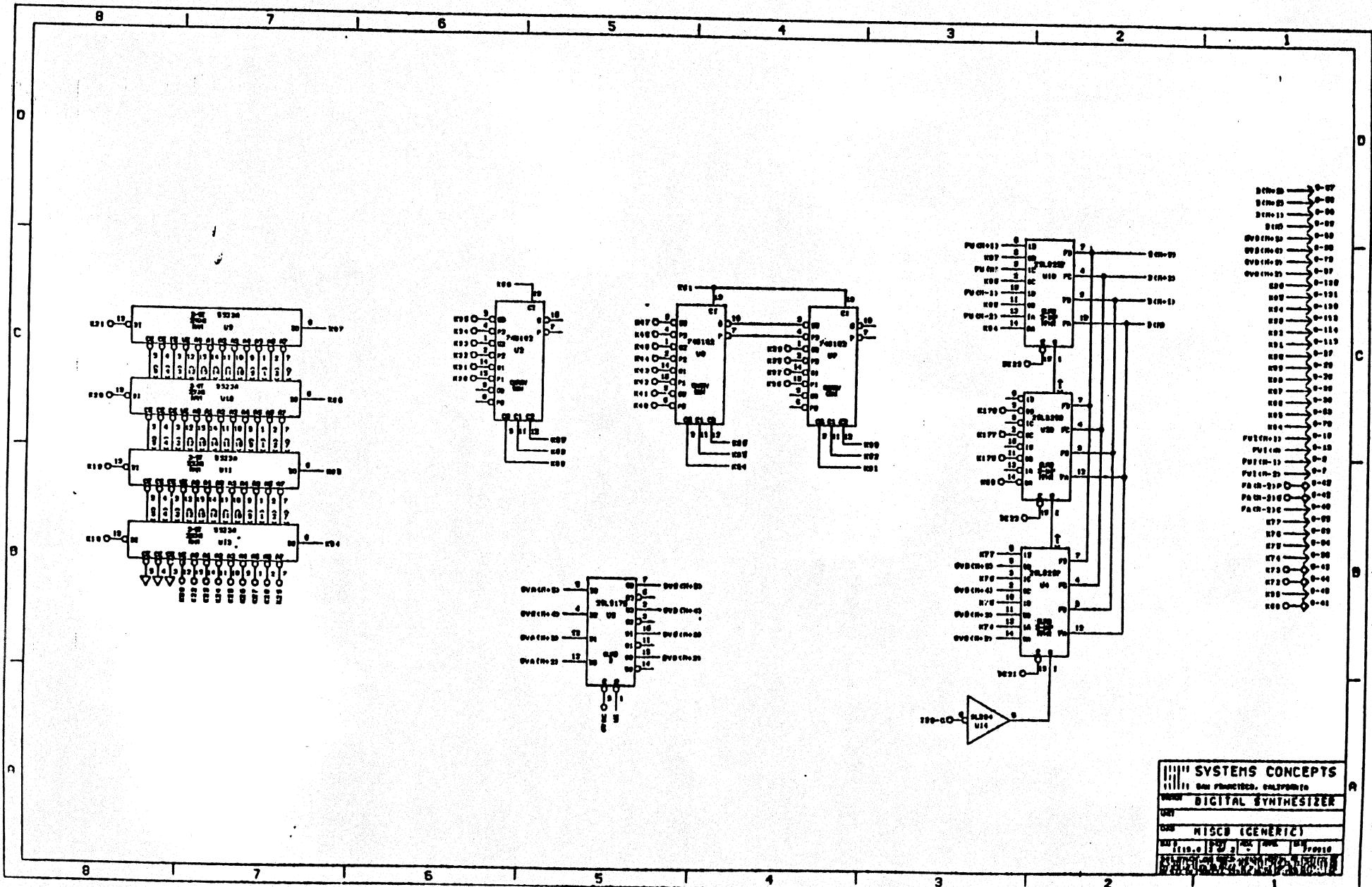
BG; M1 SCB1

PTF = 1

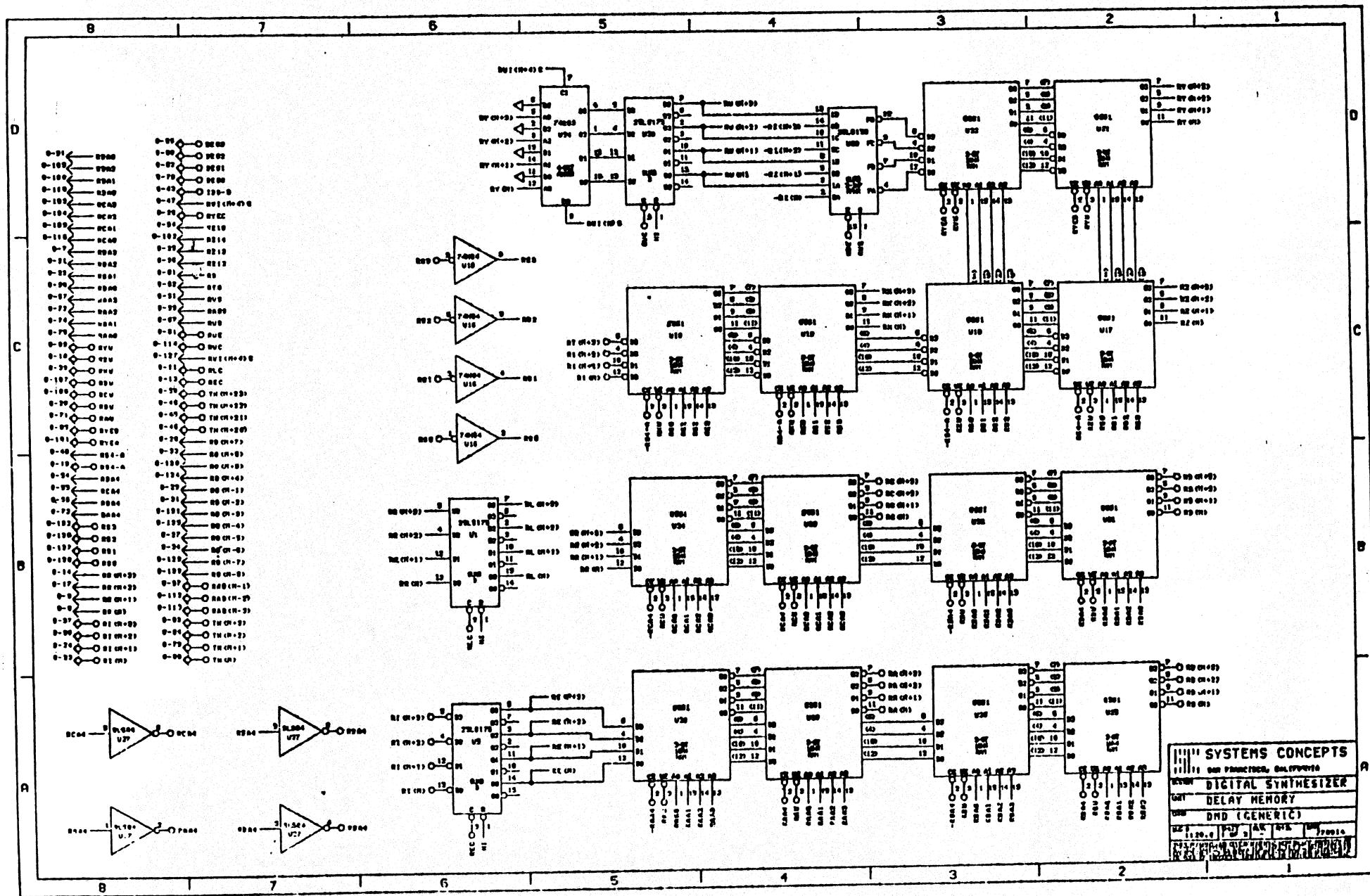


BG; MIS CB2

PTF=1

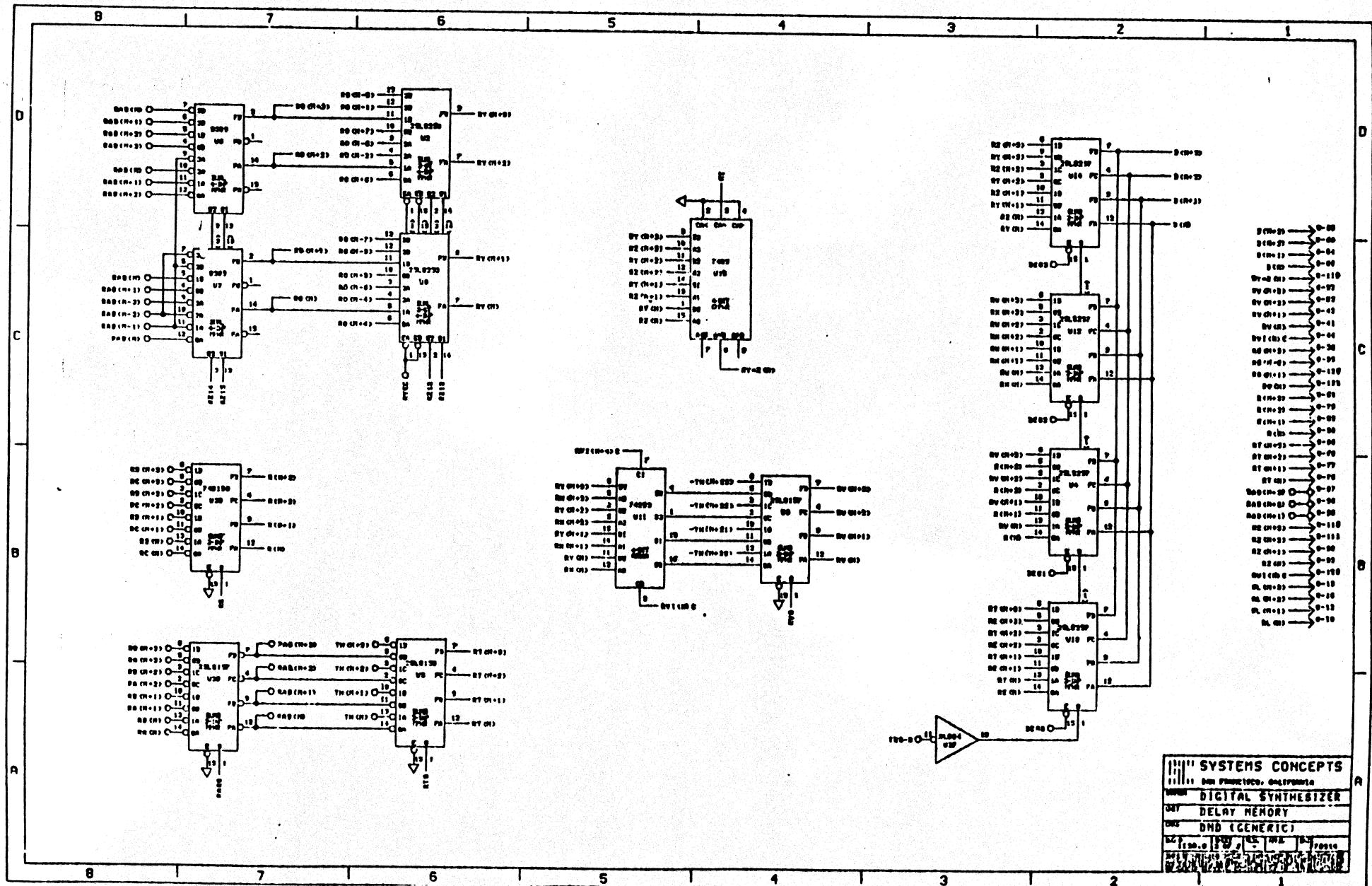


SWB: DMDI
PTF = 1



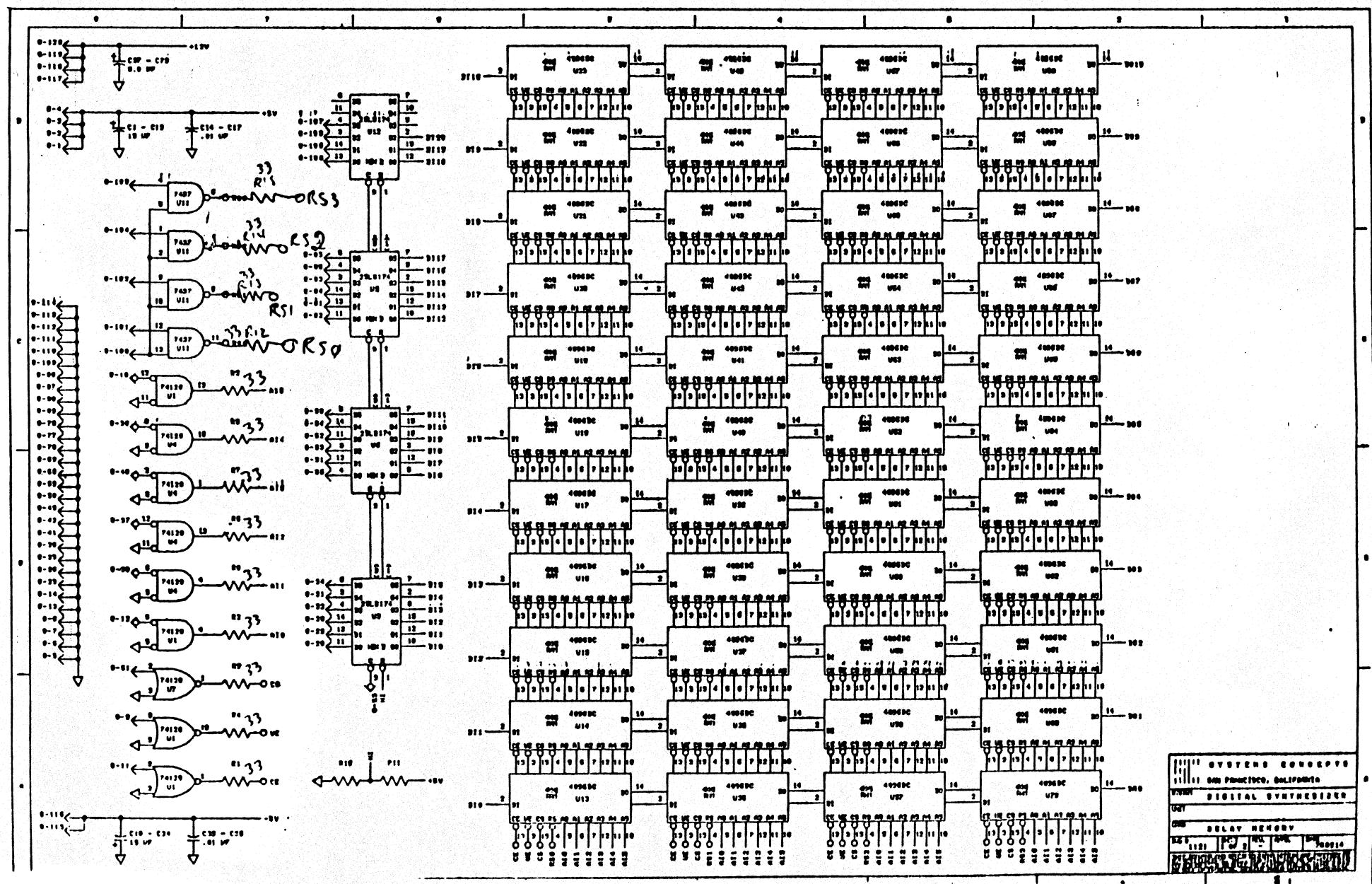
SWBS; DMD2

PTF = 1



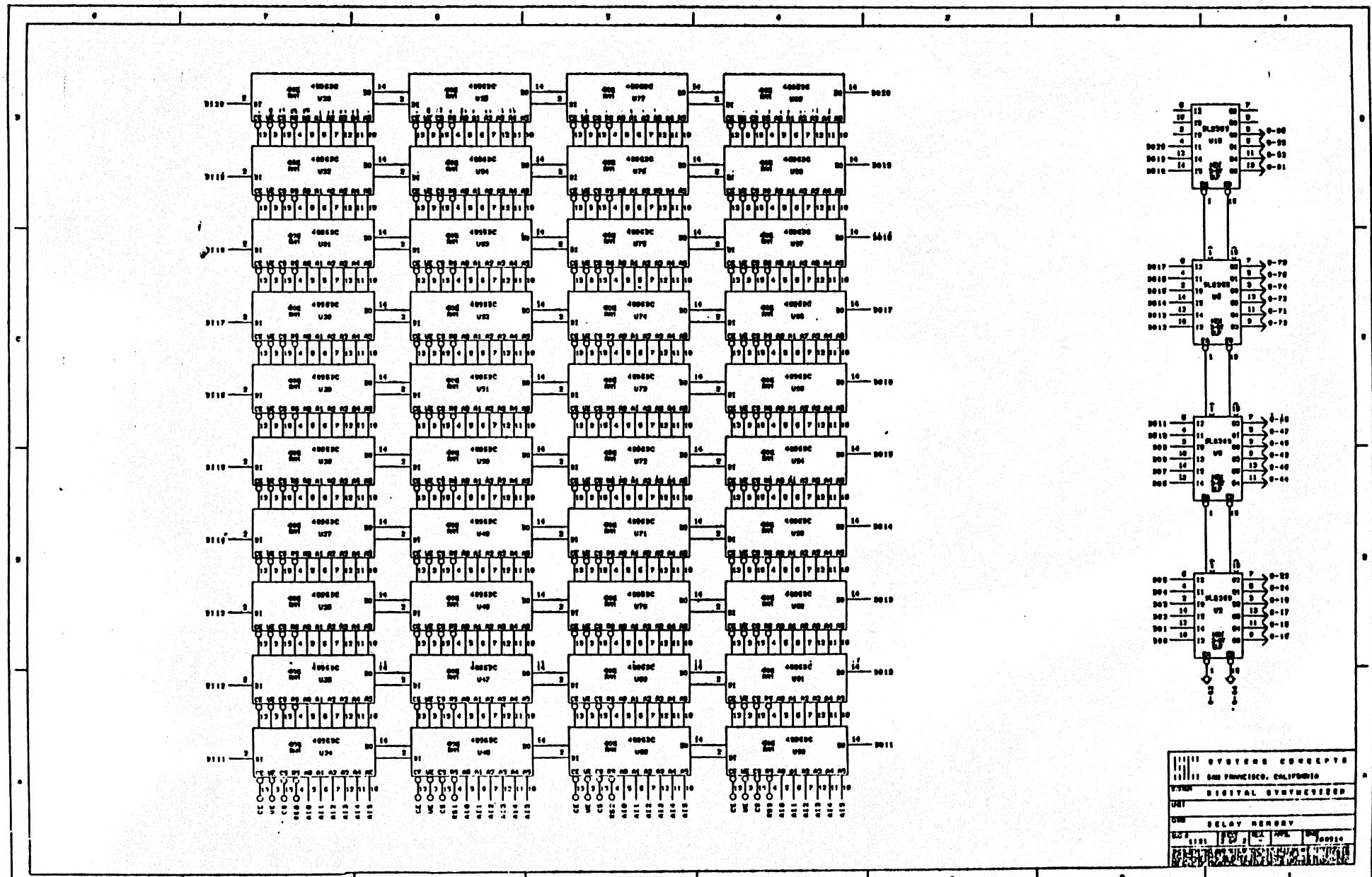
DXM; DEMRM 1

$$\text{BIT } 21 = \emptyset$$



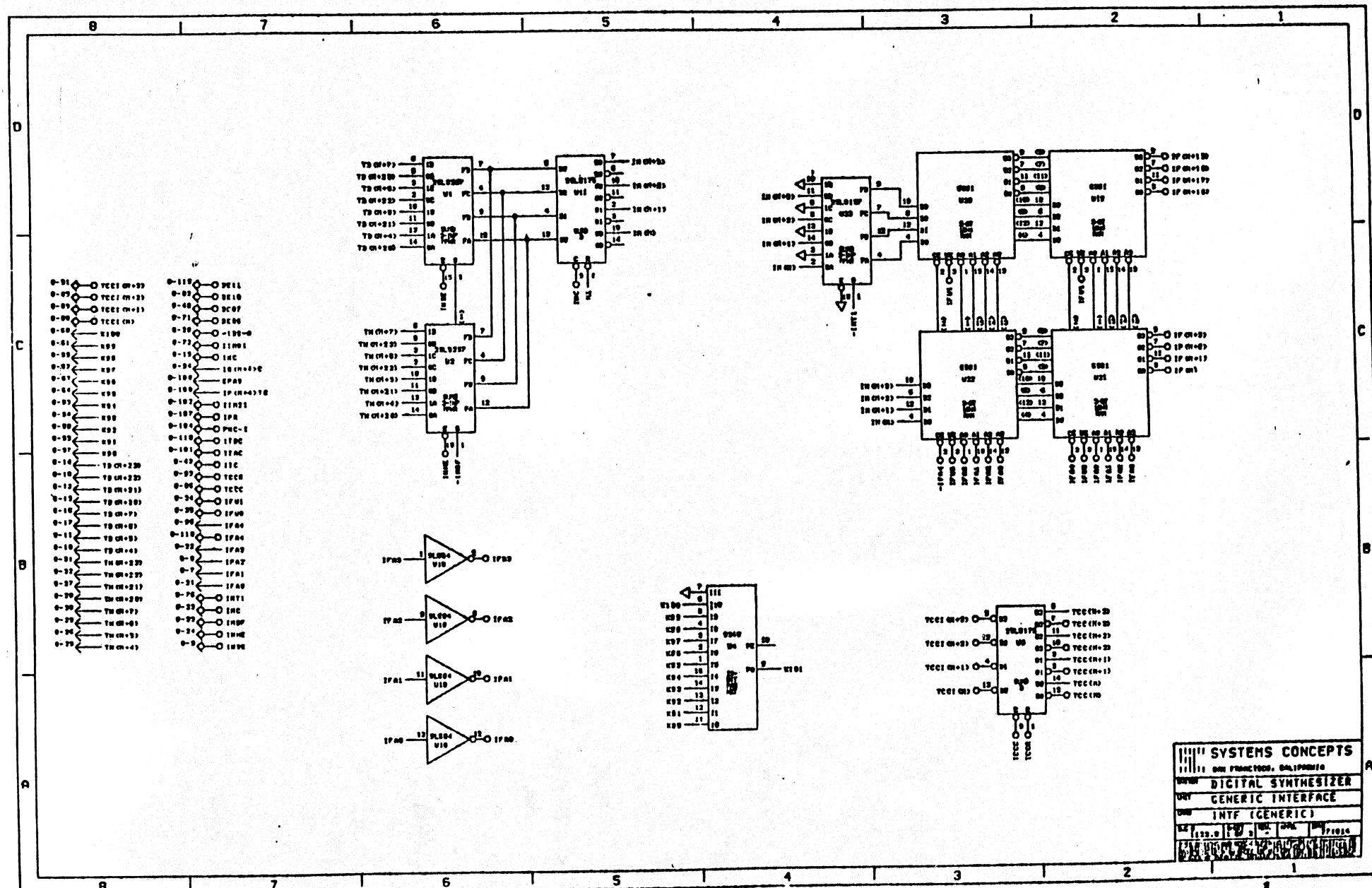
DXM; DEMEM 2

$$\text{BIT21} = \emptyset$$



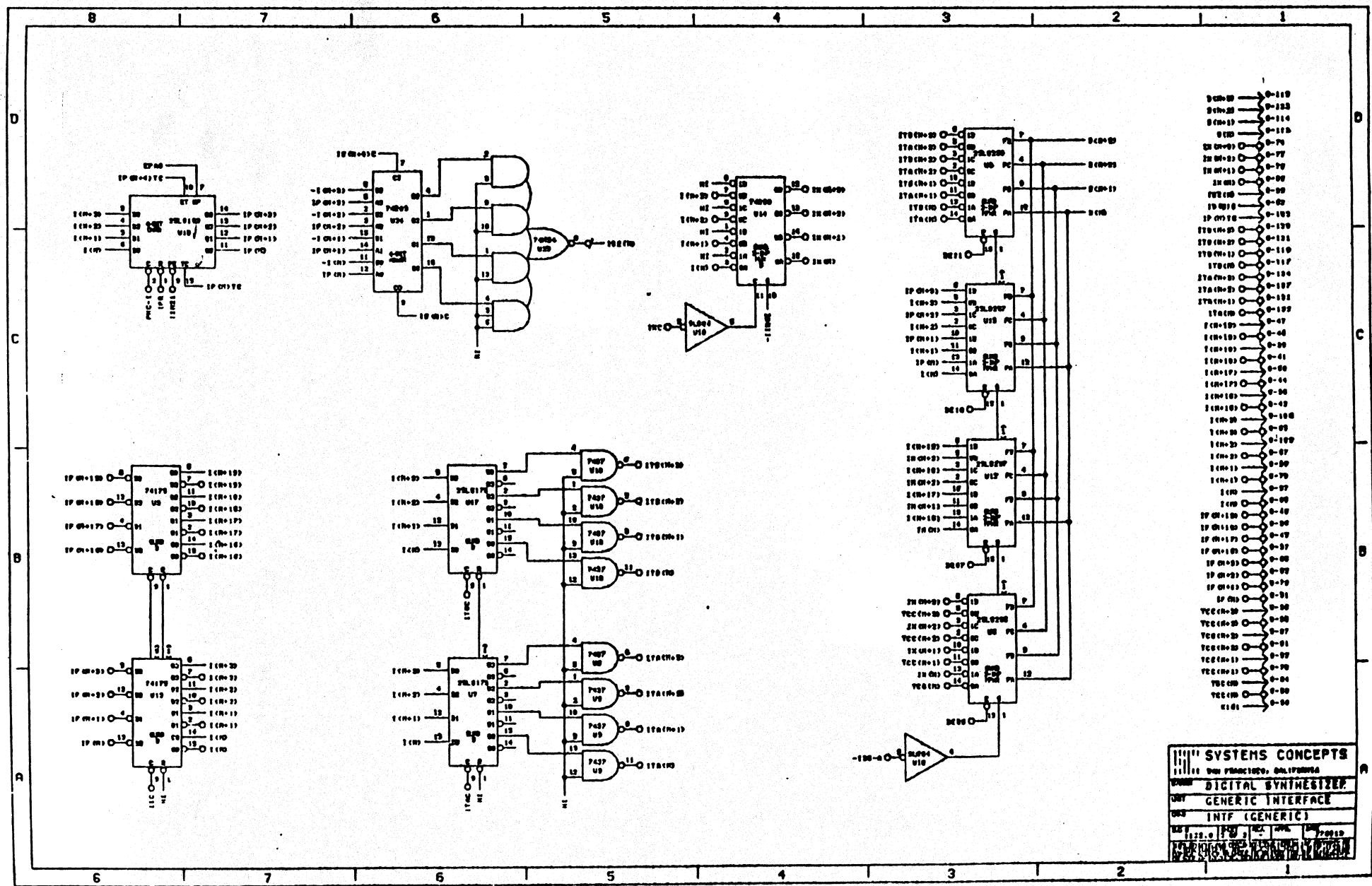
DM; INTF1

$$PTF = 1$$



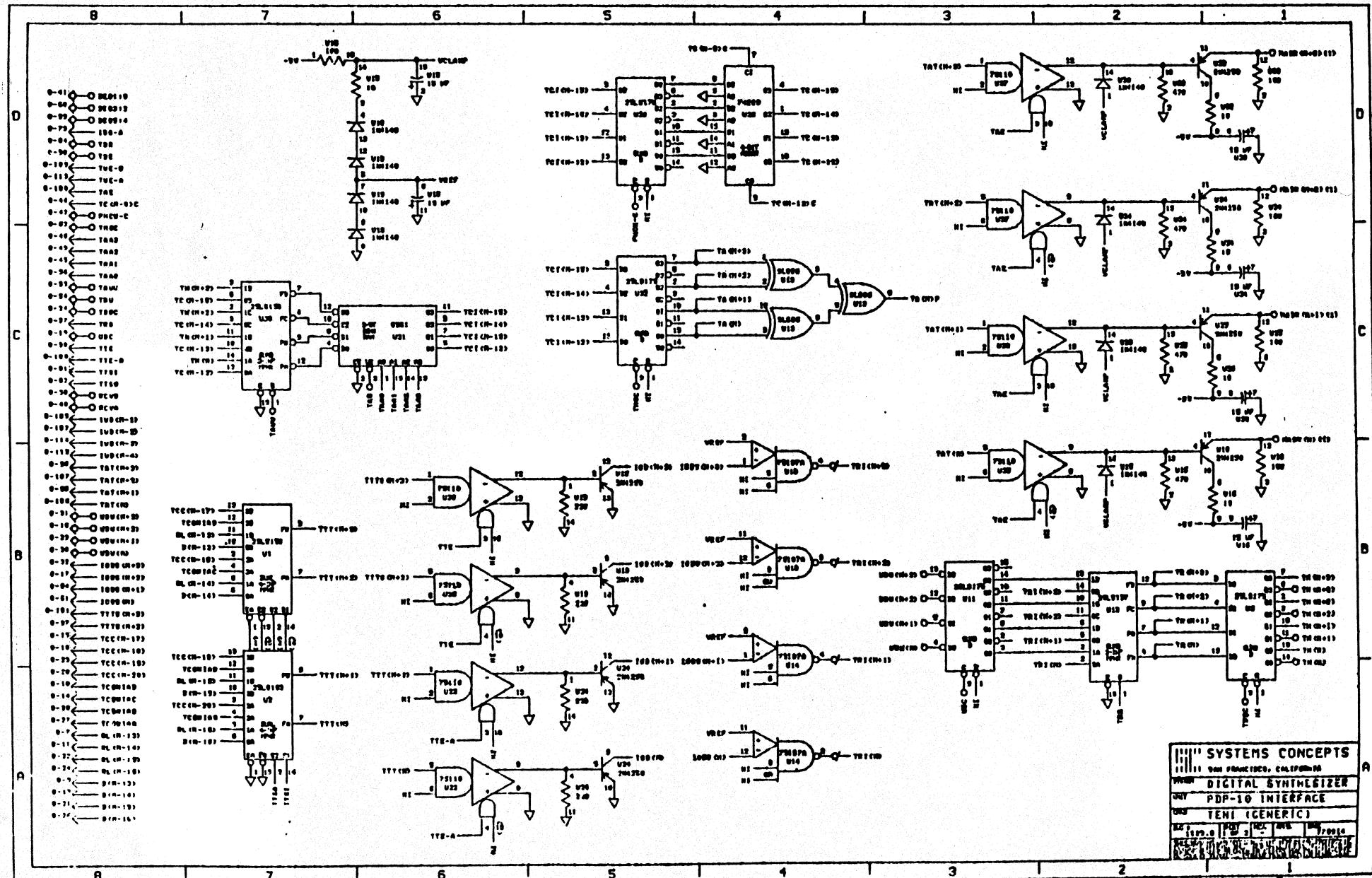
DM; INTF2

$$PTF = 1$$



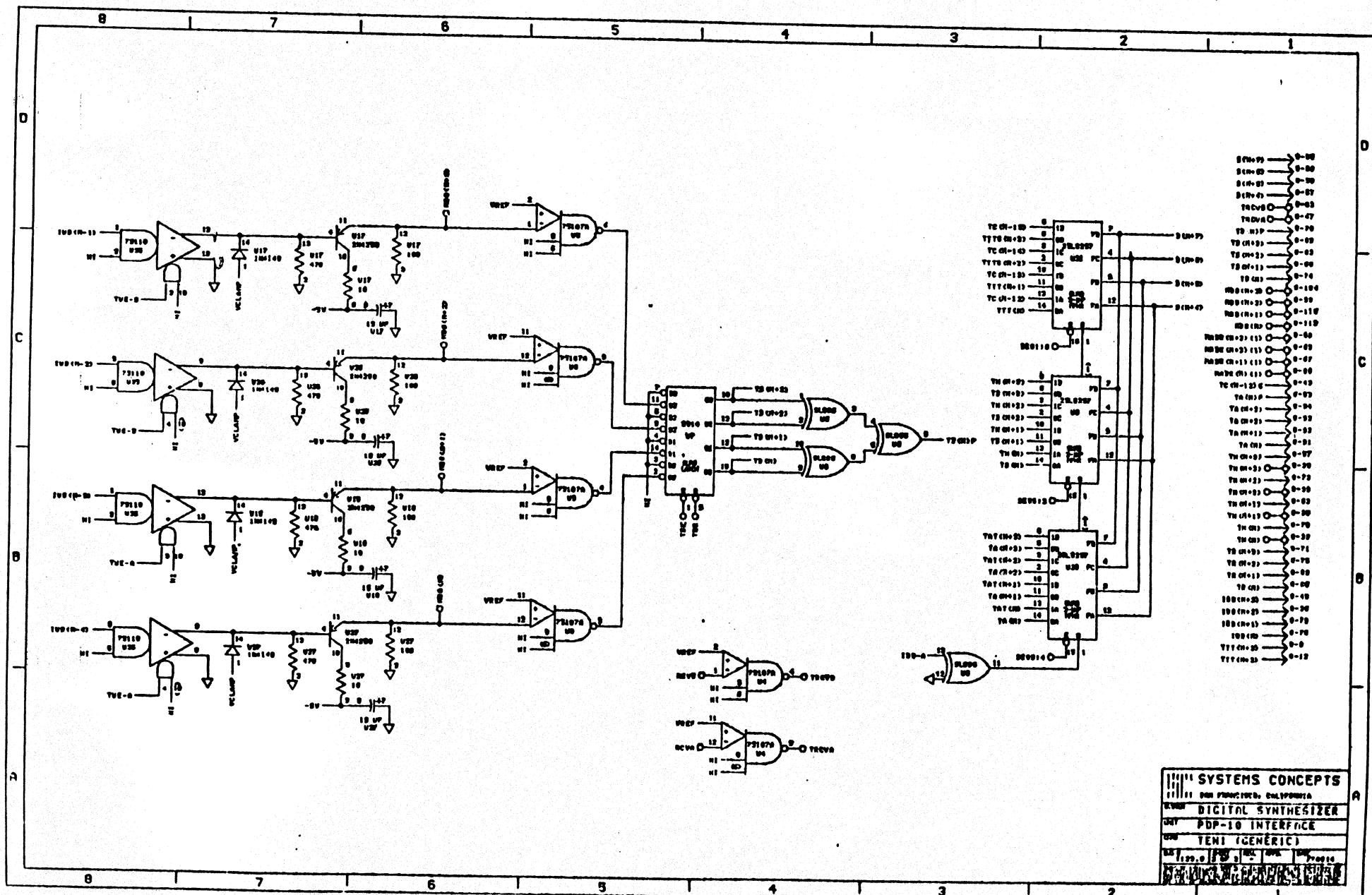
SWB, TENI

PTF = 1

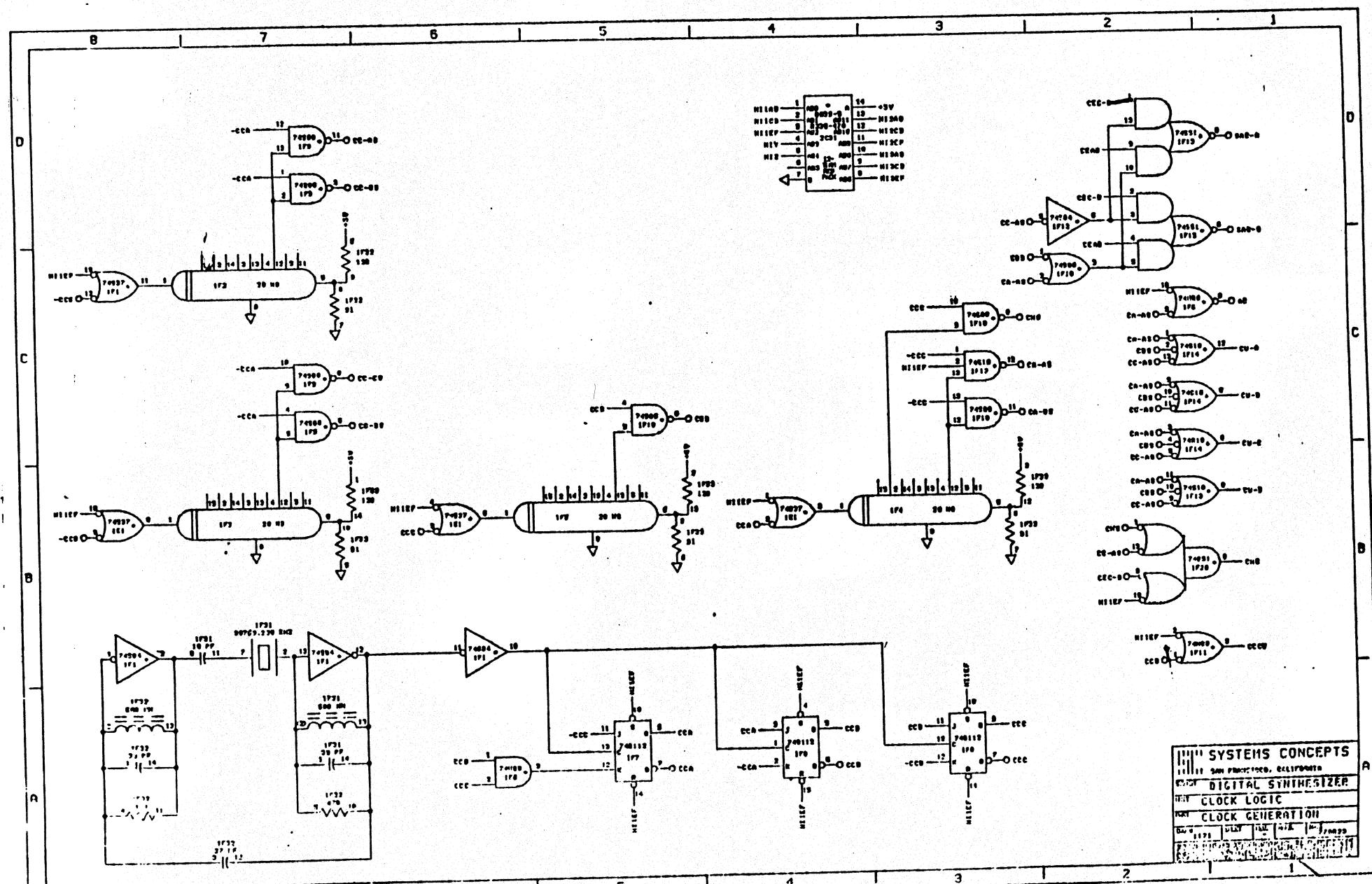


Subtentil

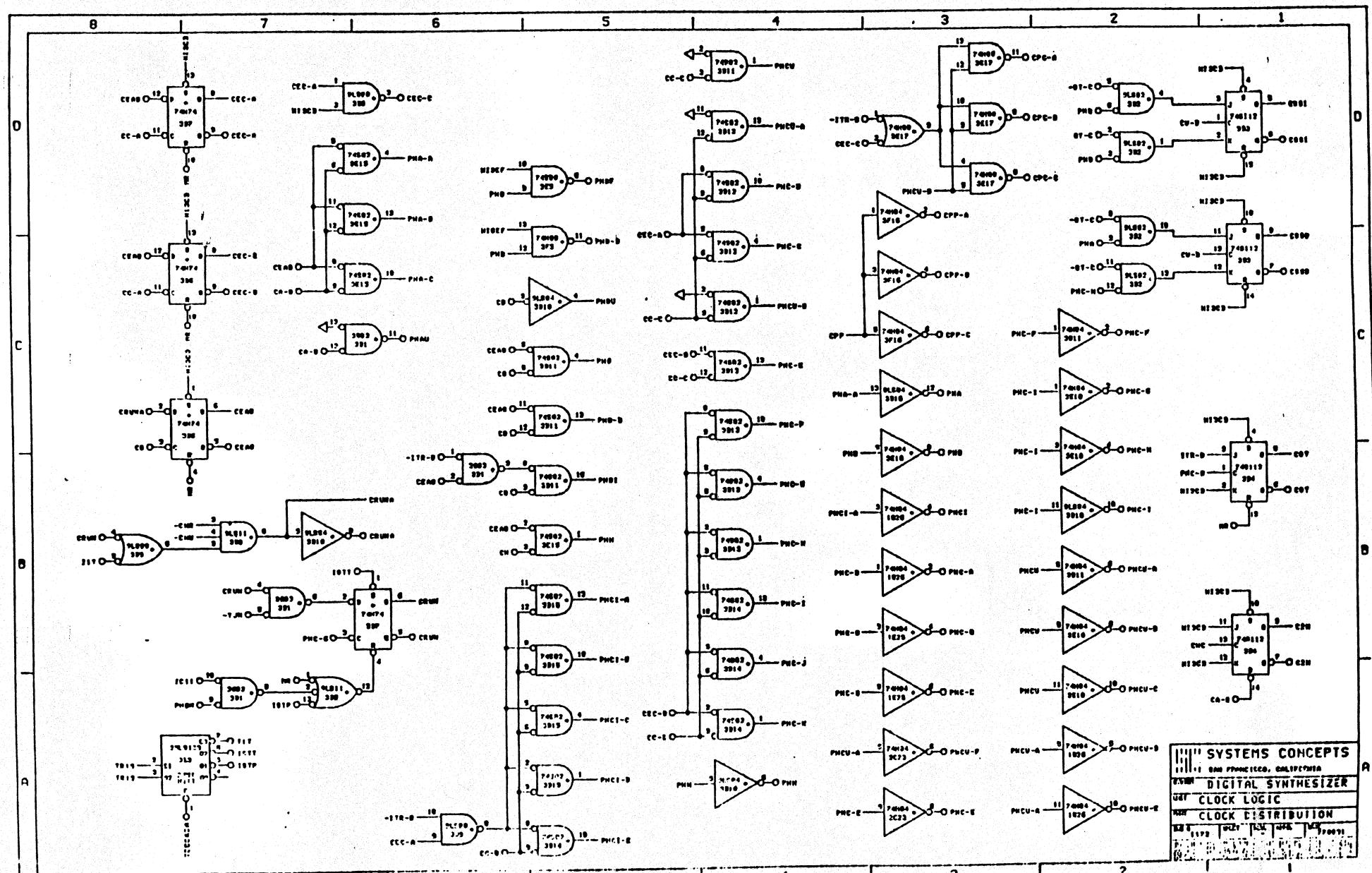
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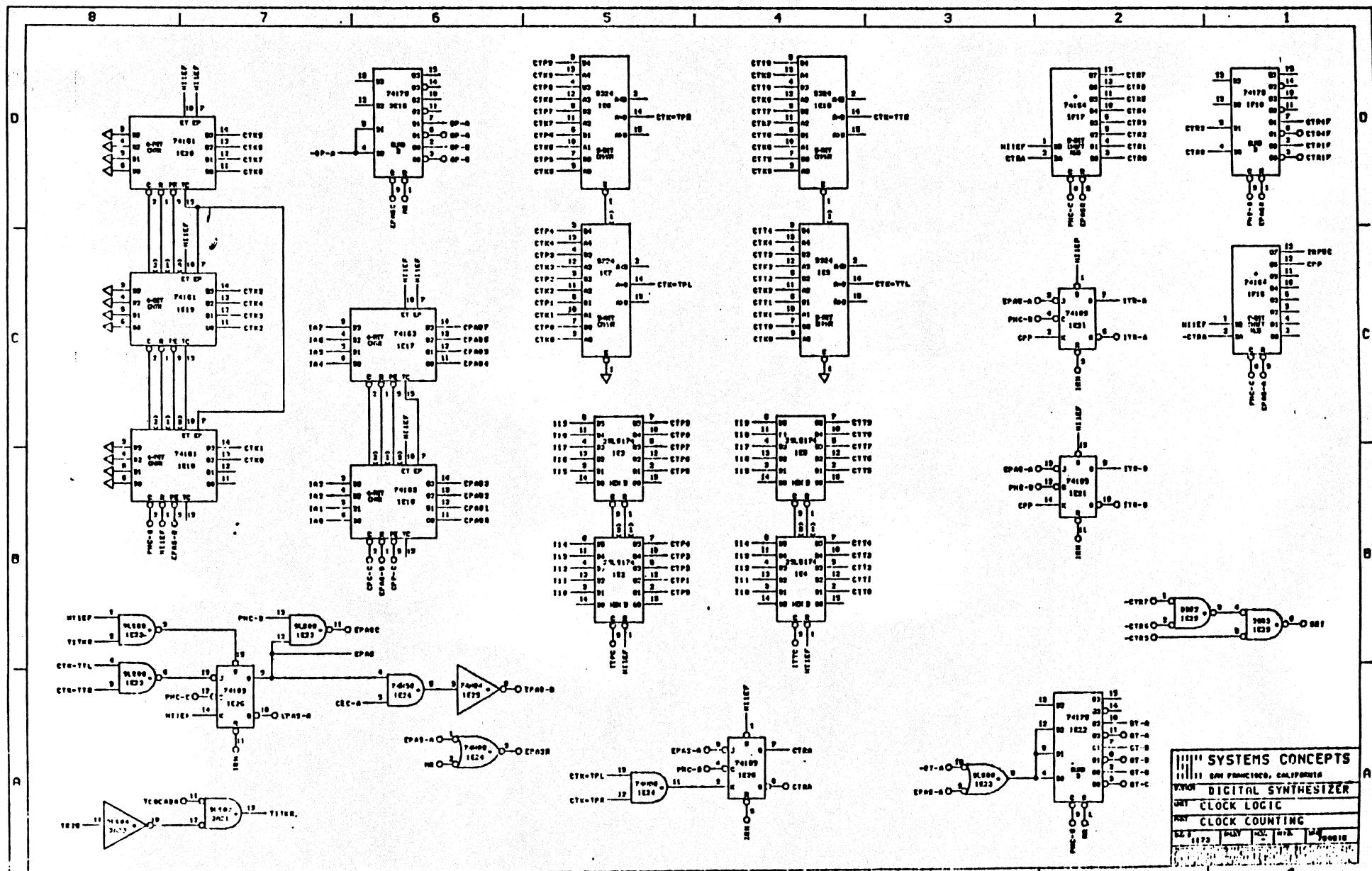
DXM; CLKGN



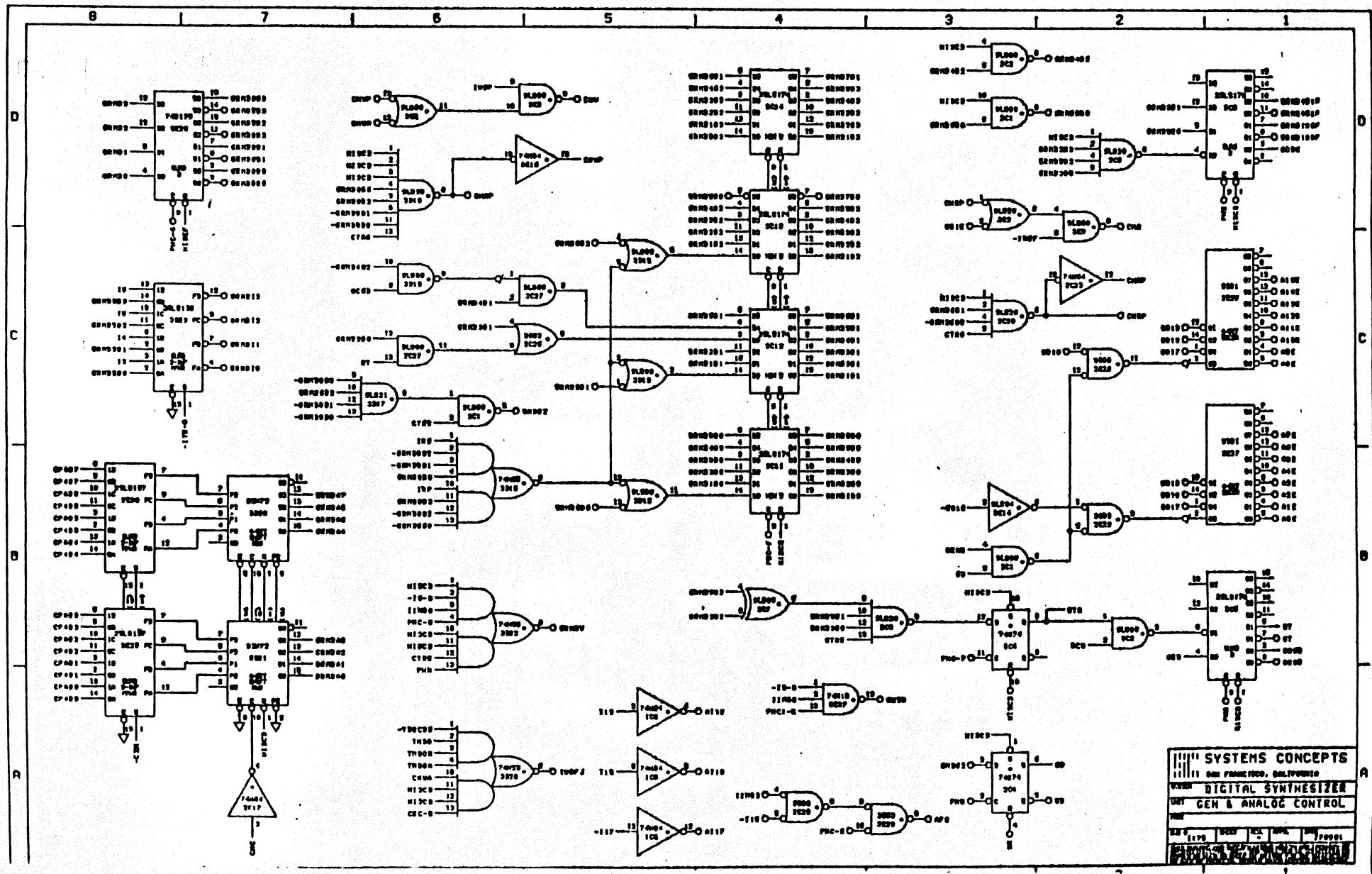
SRE; CLKDST



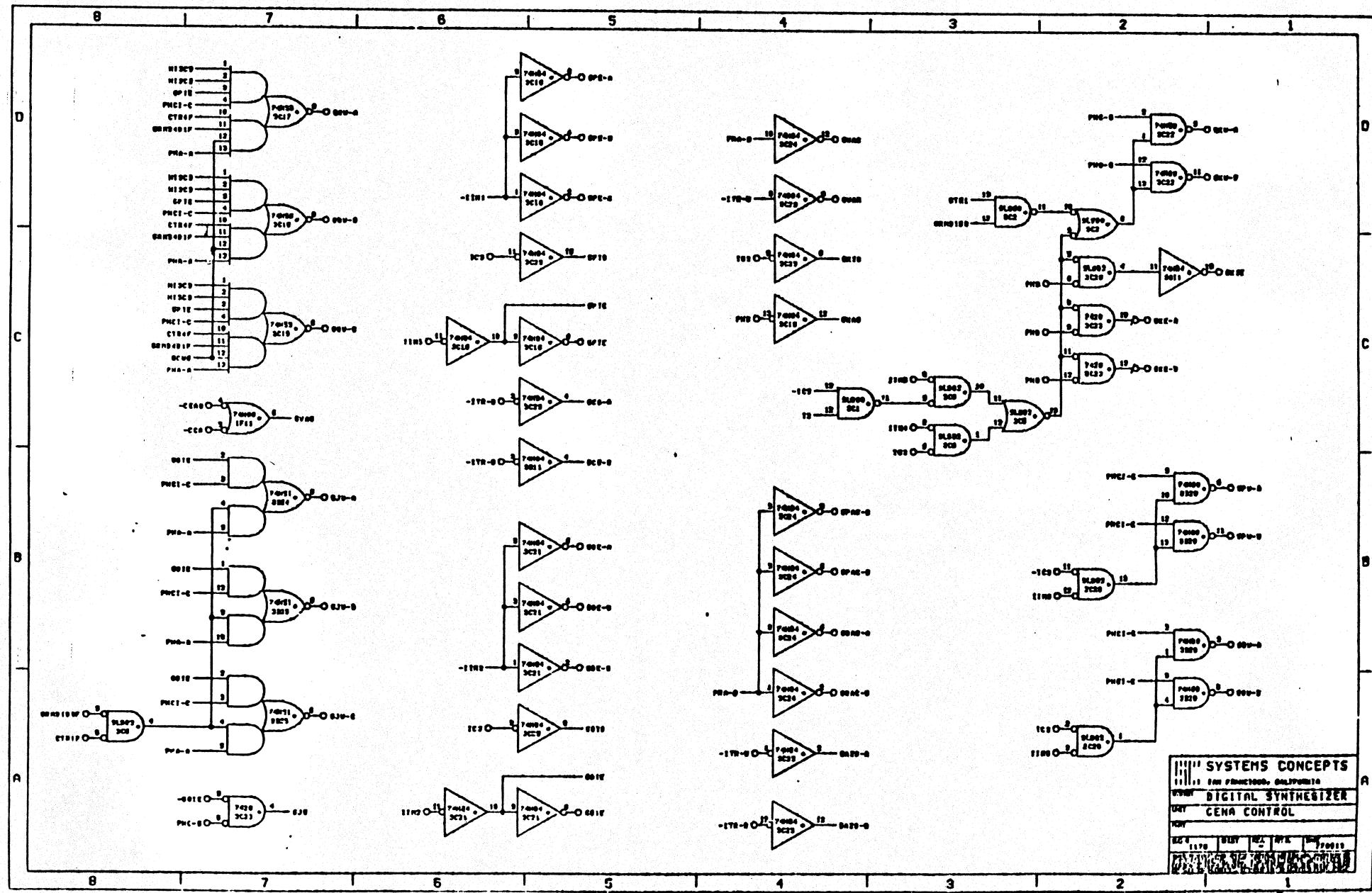
DXM; CLKCNT



DIXM, GENANC

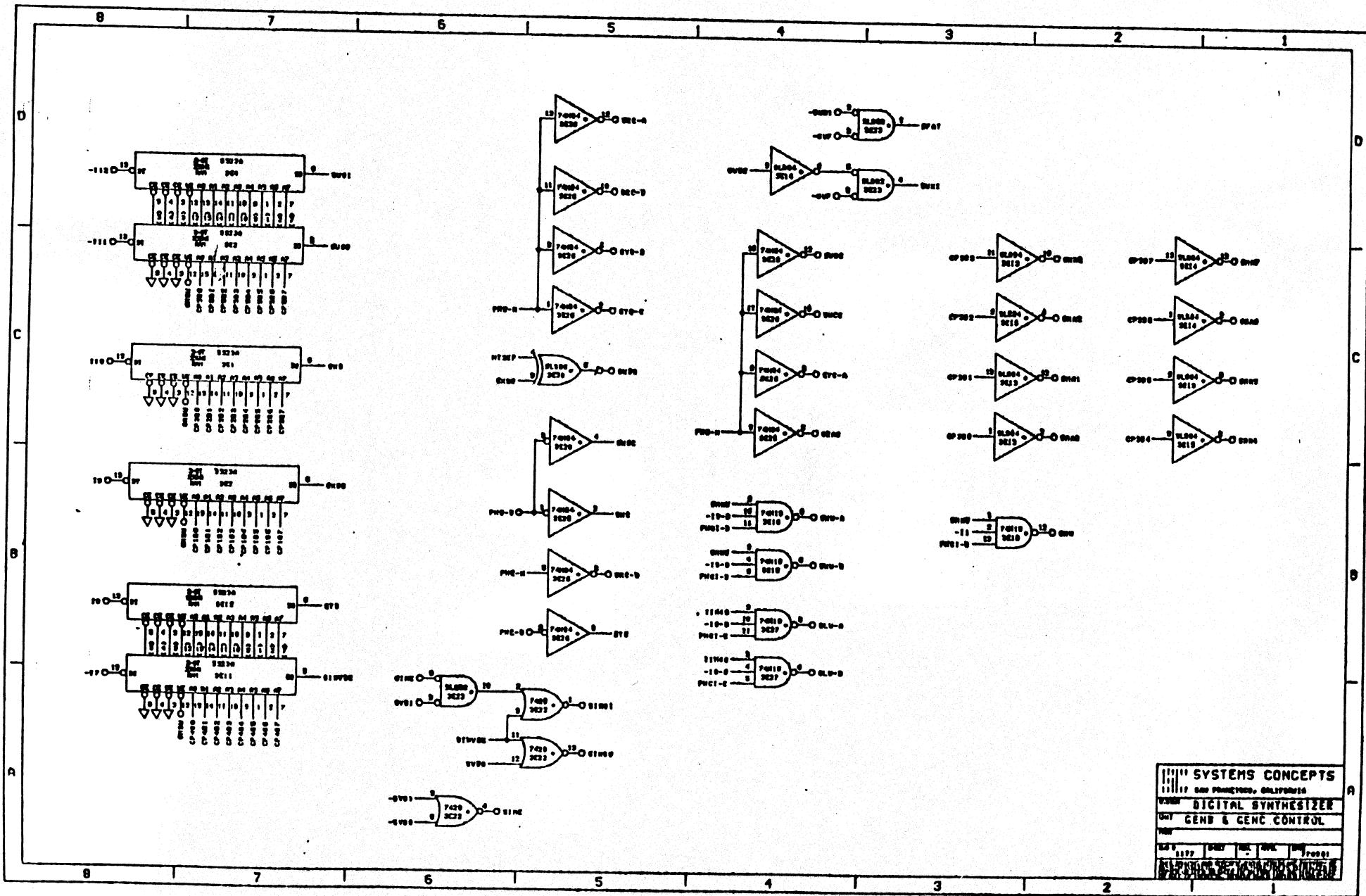


BG; GENAC

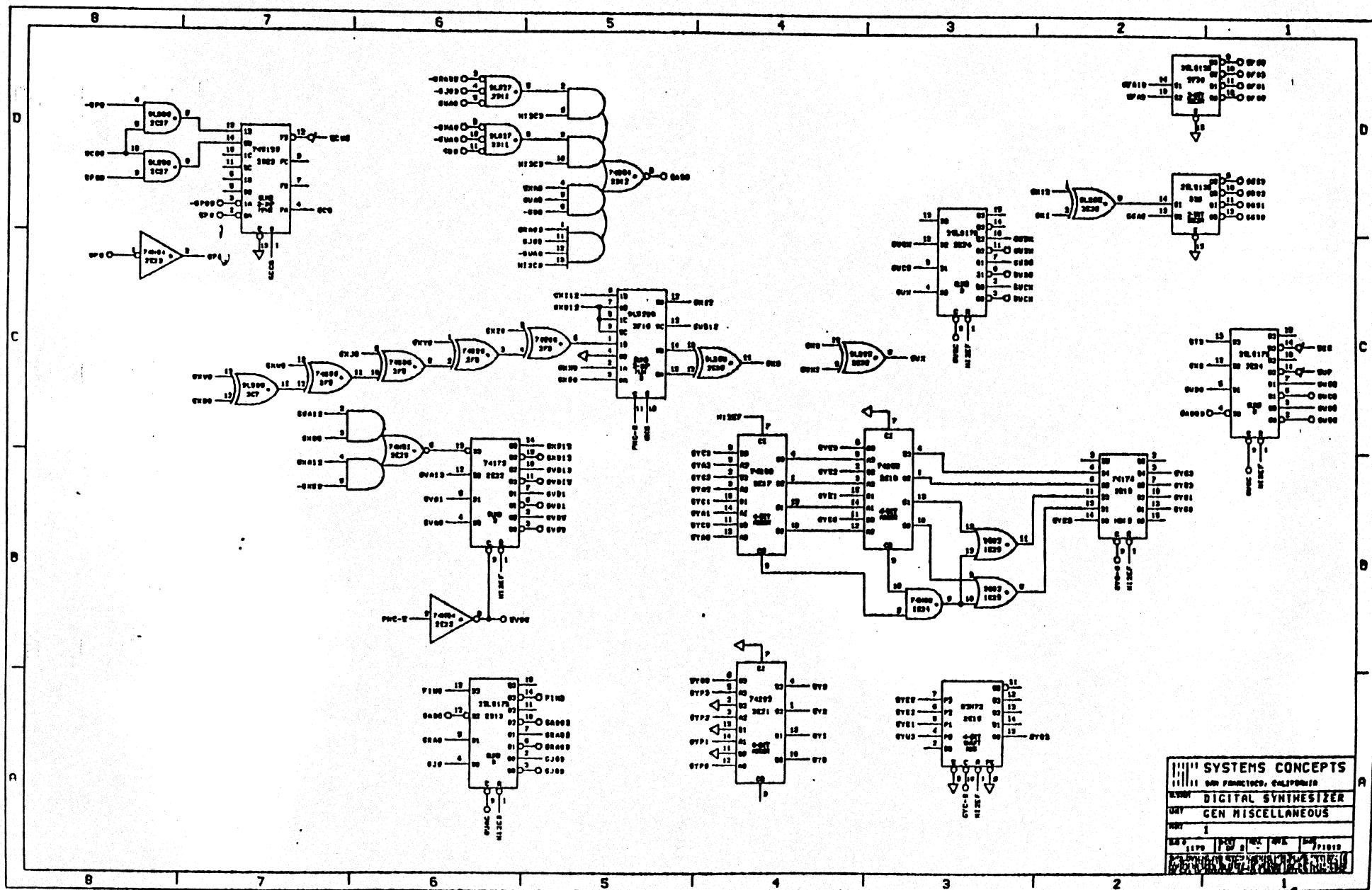


SRE ; GENBCC

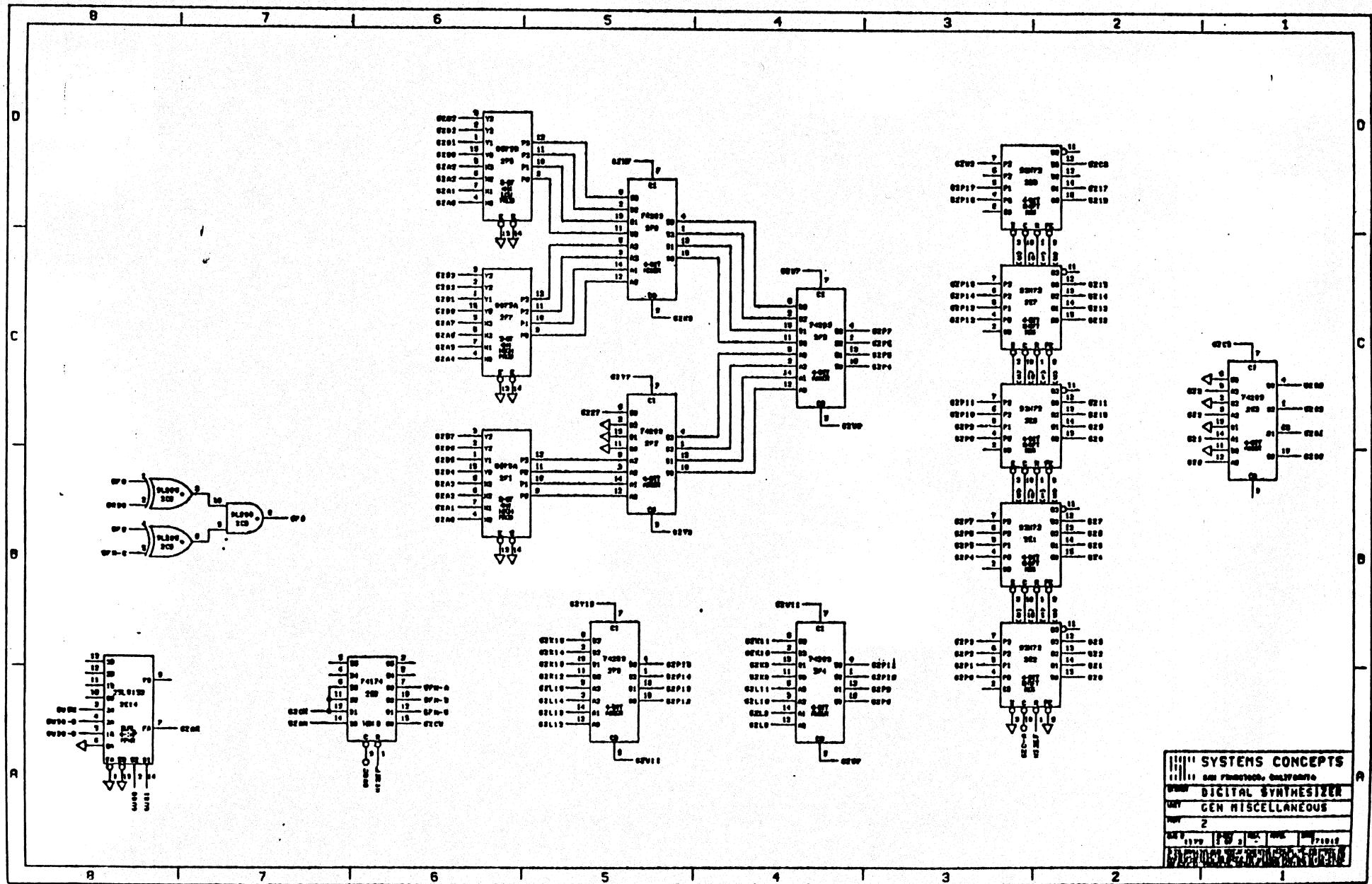
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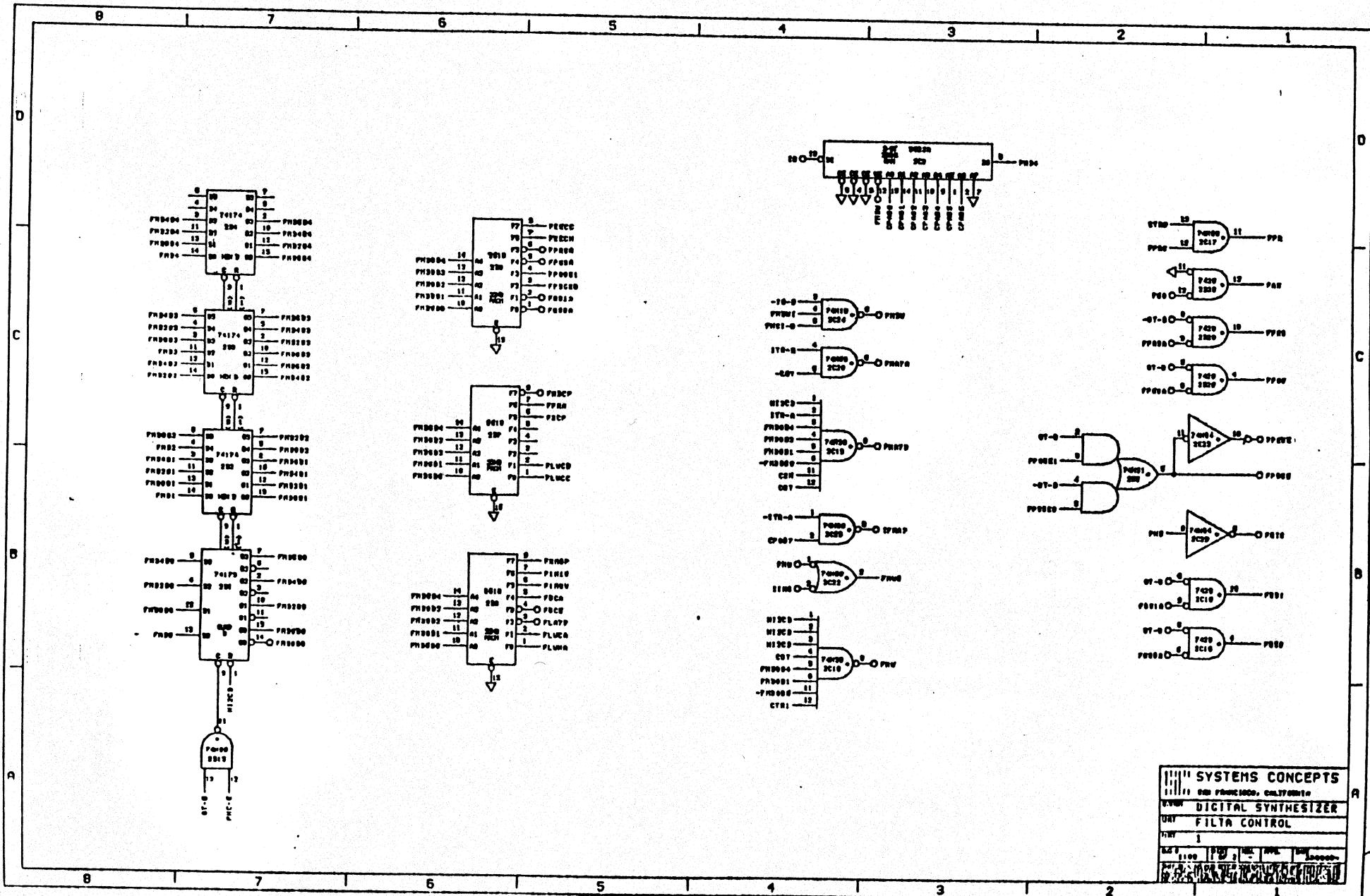
SWB: GENM |



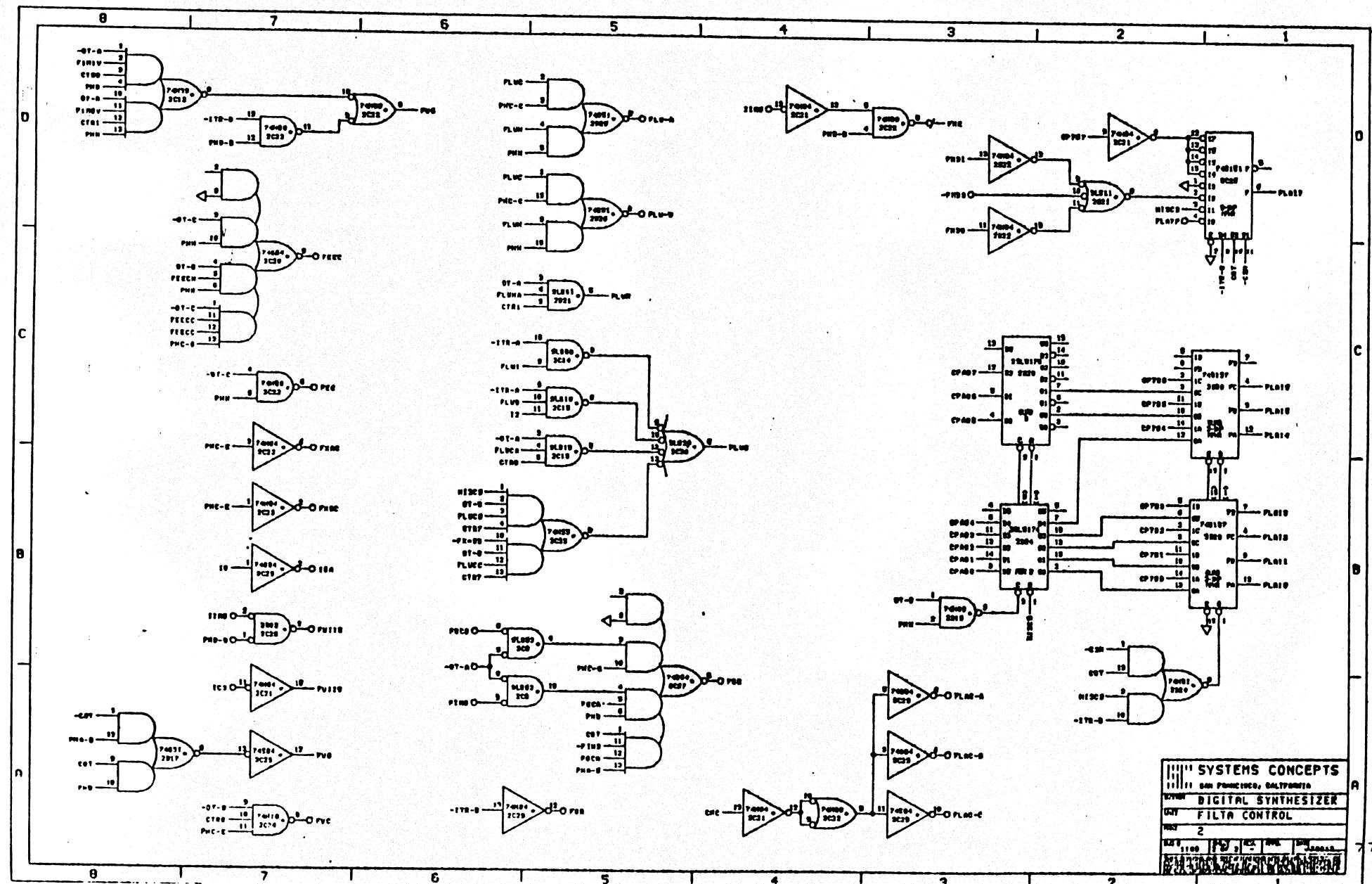
SWB; GENM2



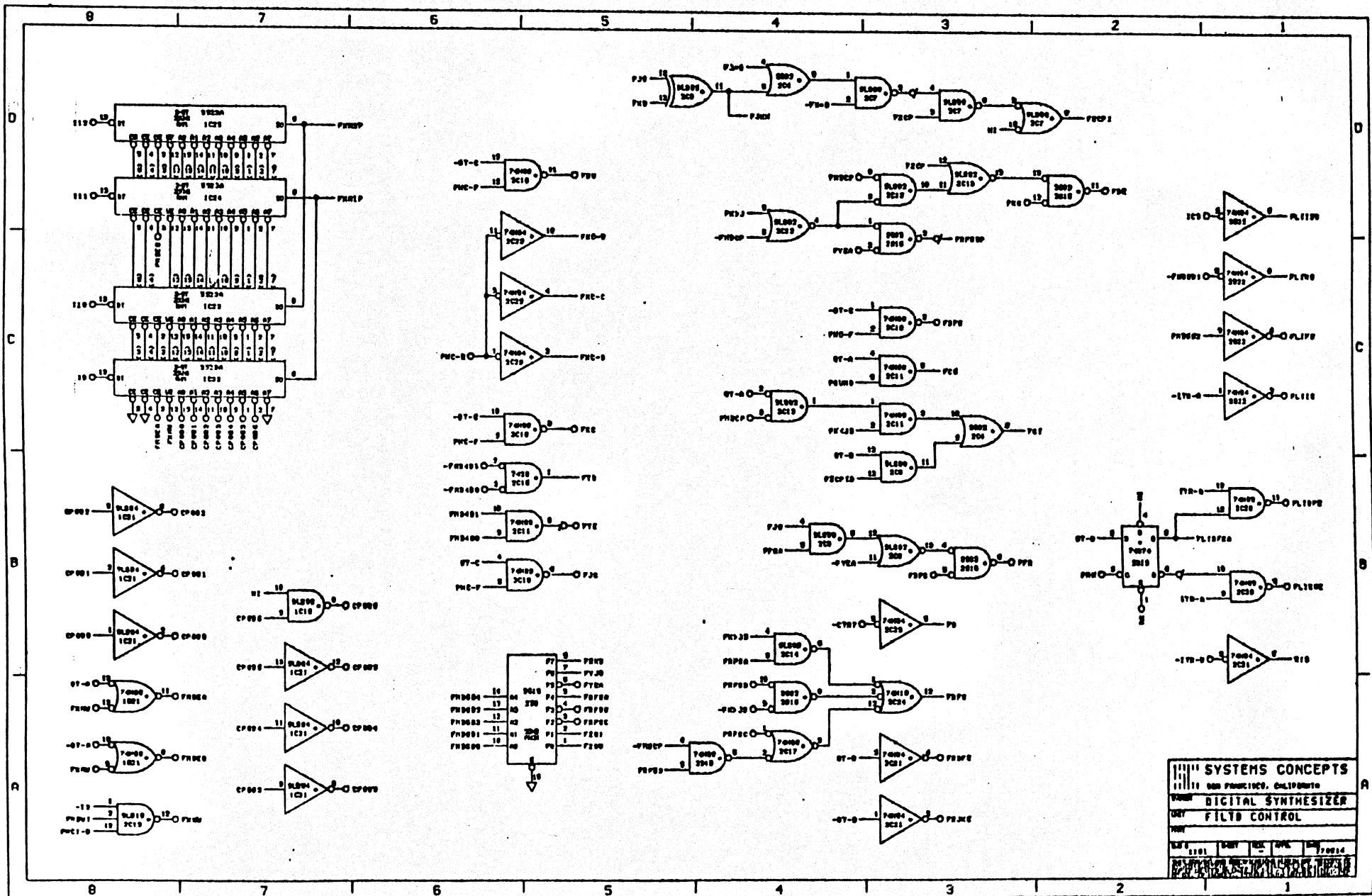
DXM; FILT AC1



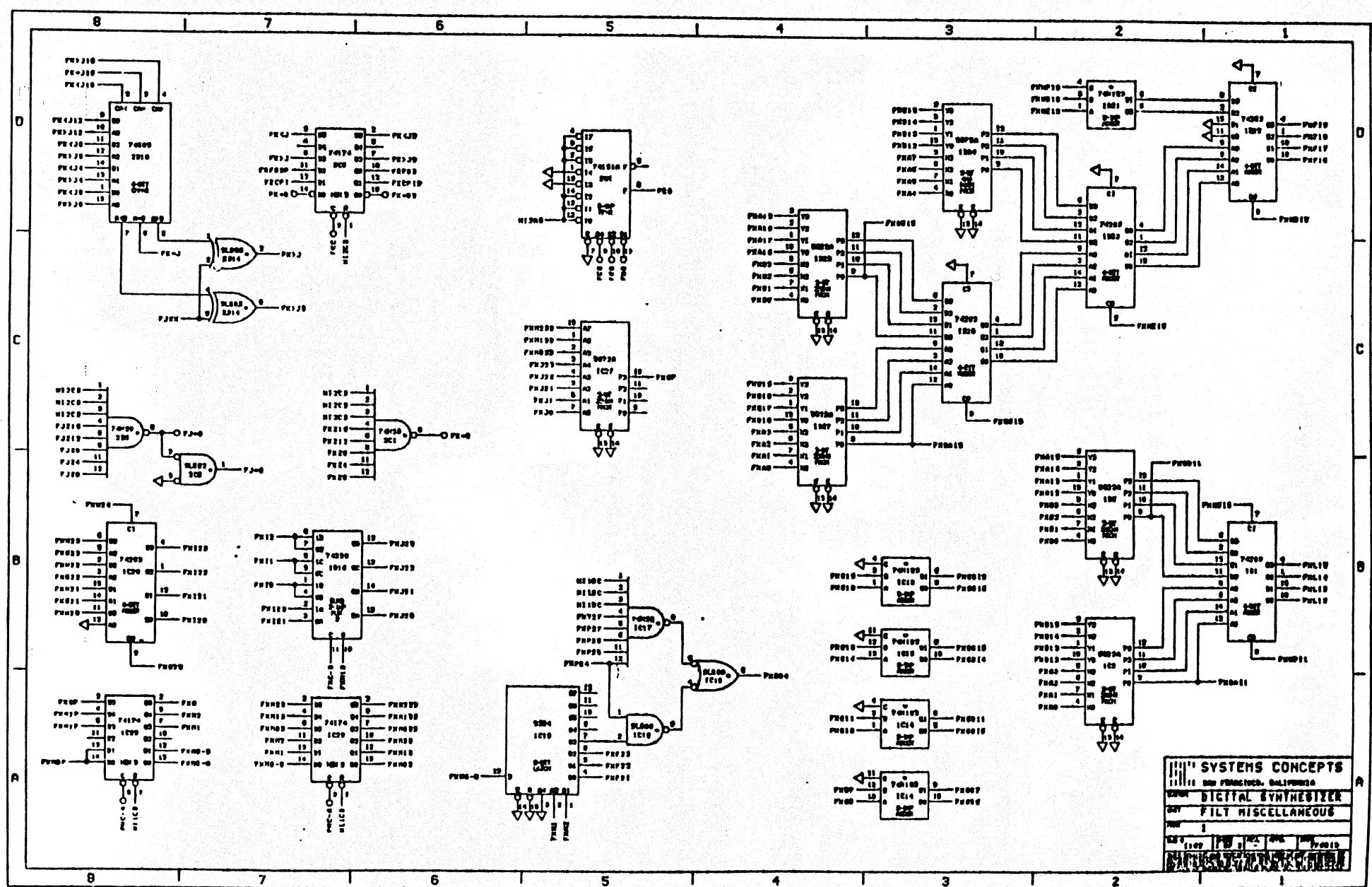
DXM; FLTA CZ



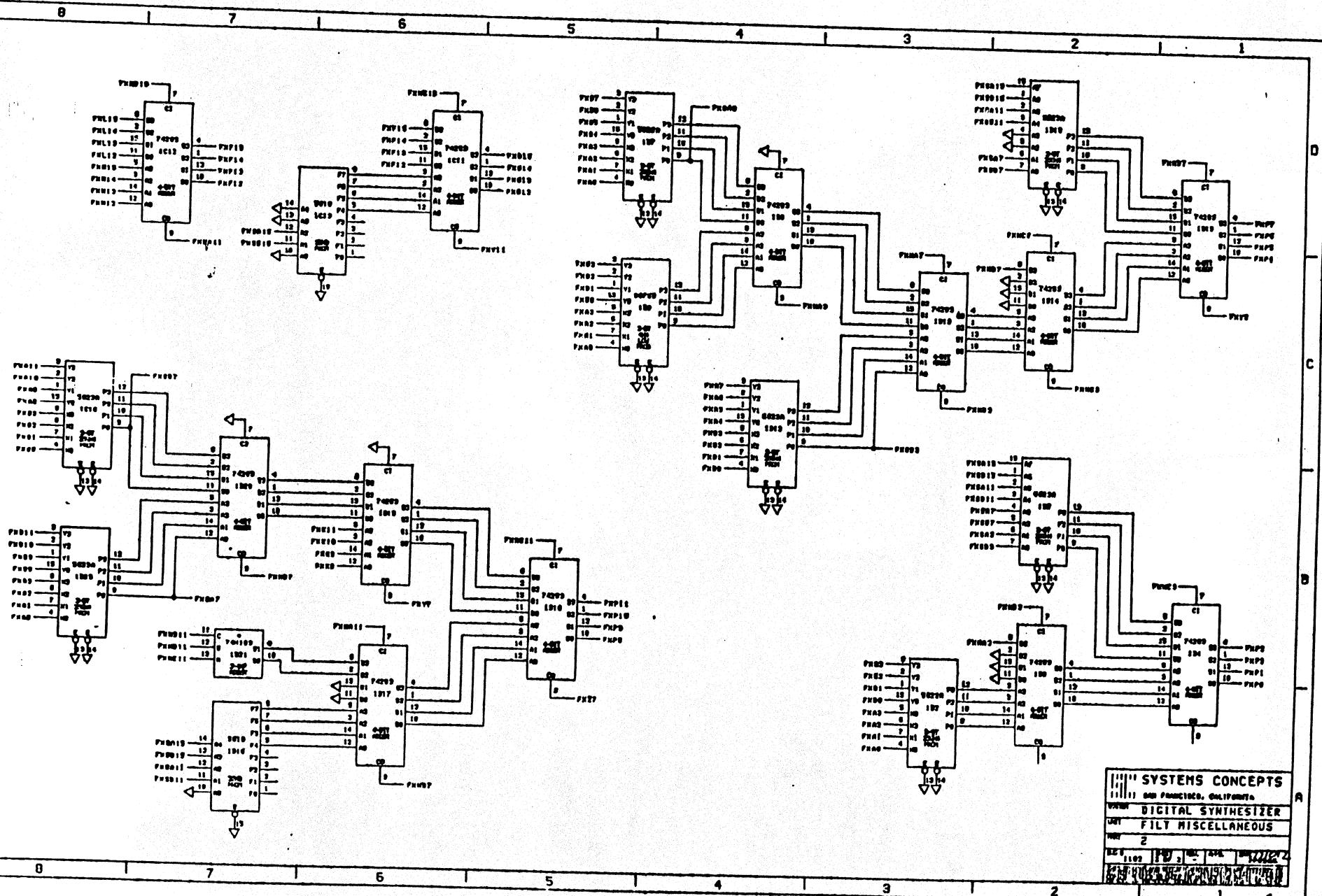
DXM; FILT BC



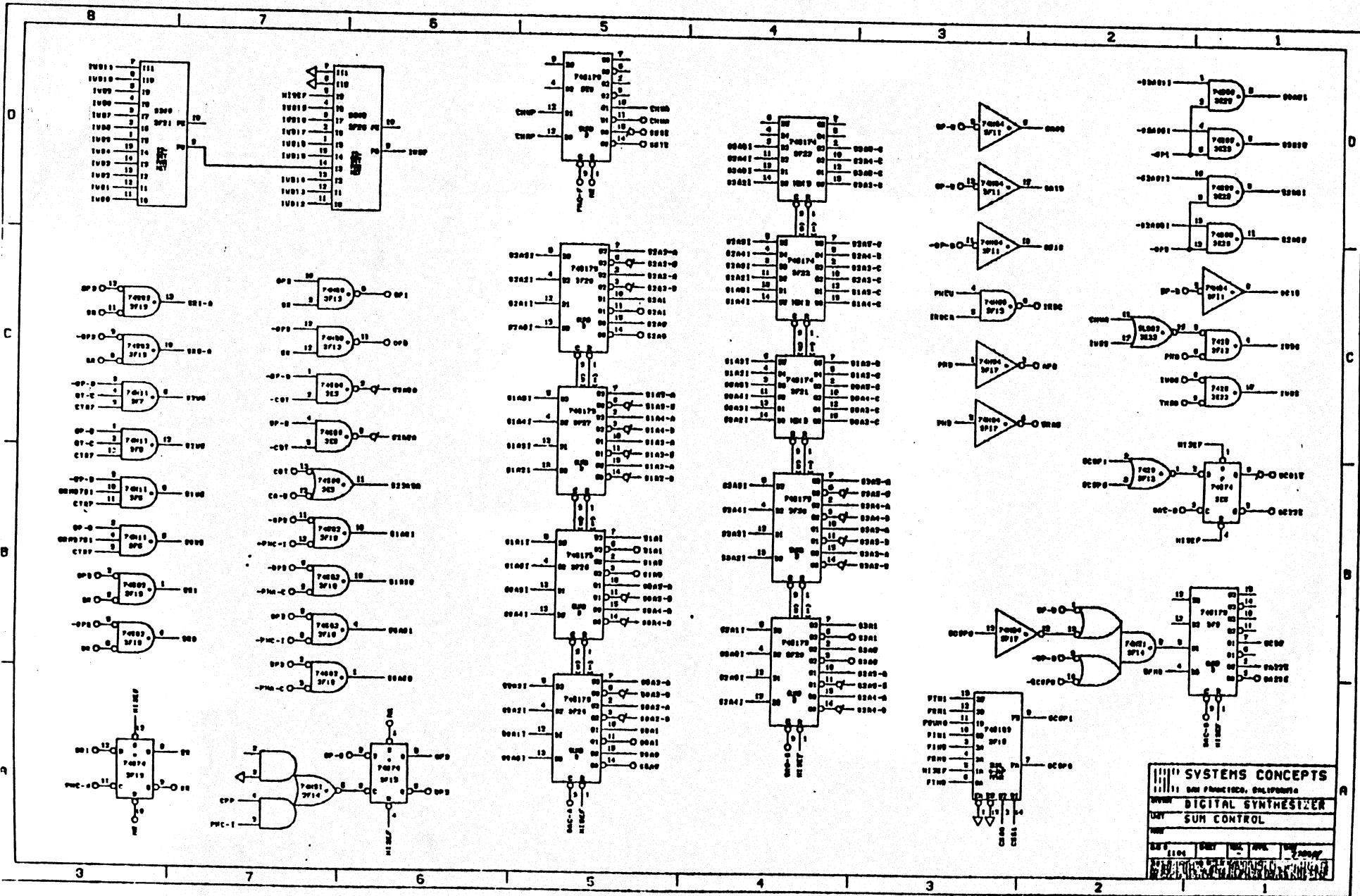
DXM; FILTM1



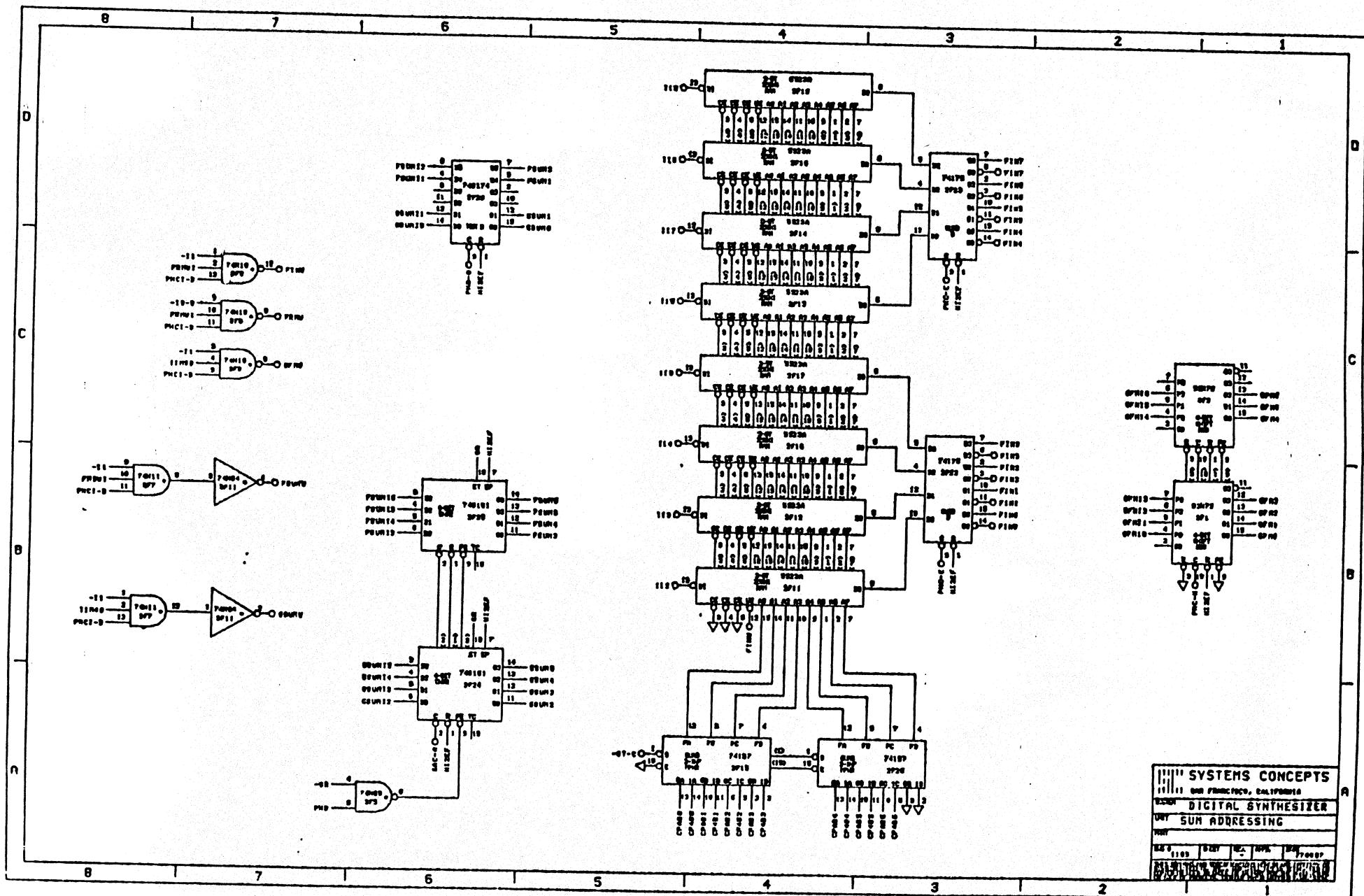
DXM; FILM 2



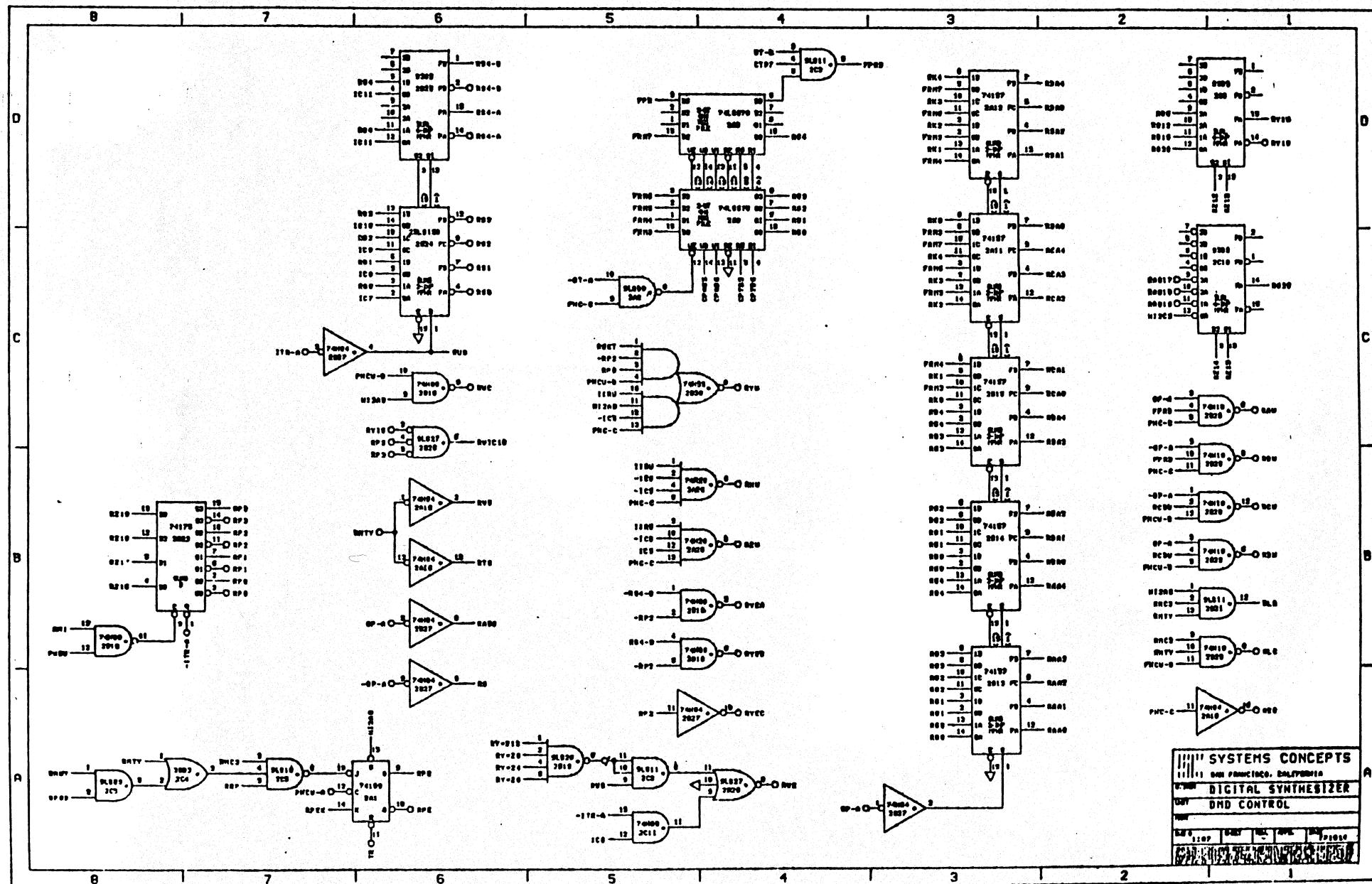
SWB; SUM C



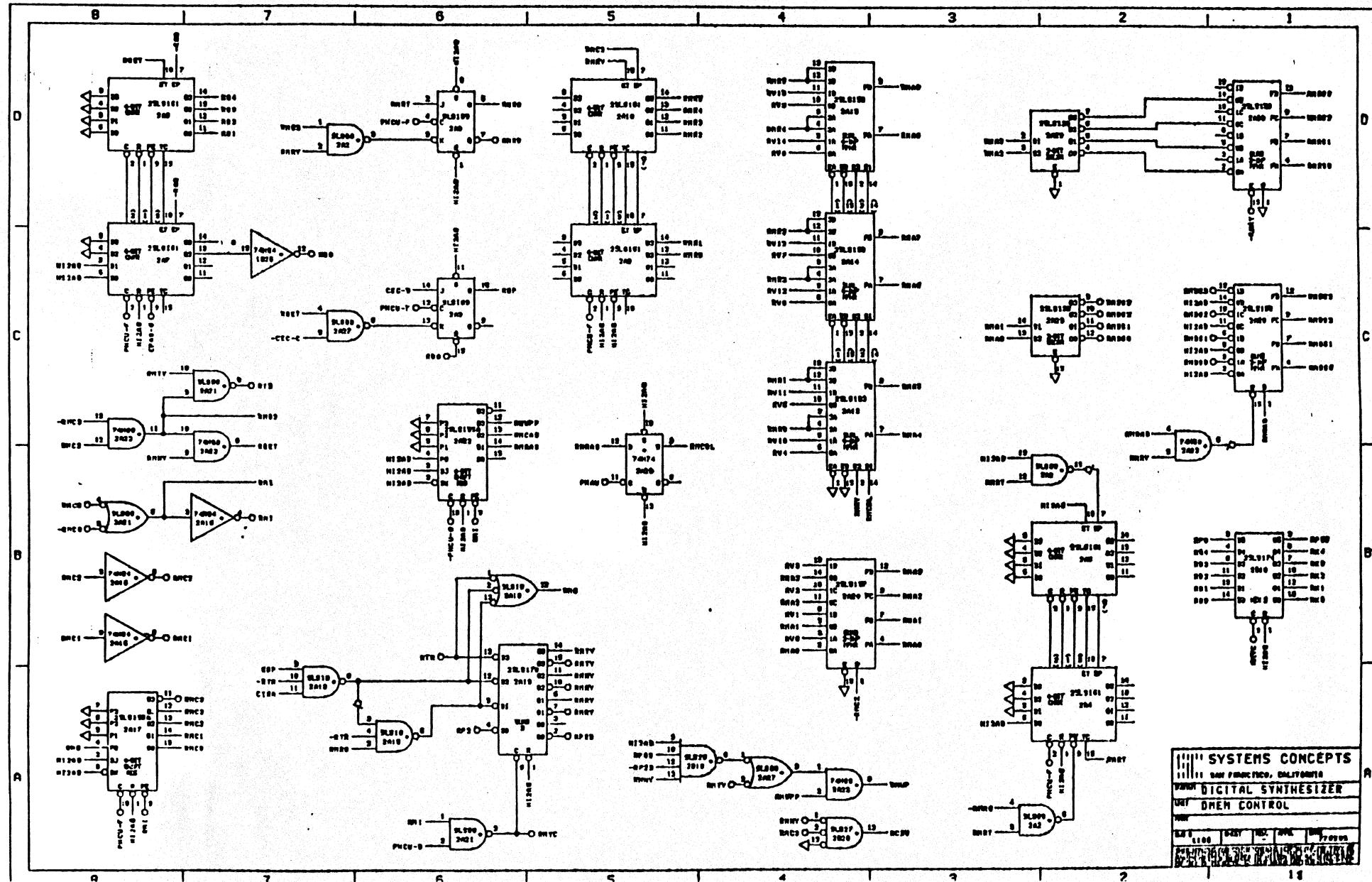
DXM; SUMADD



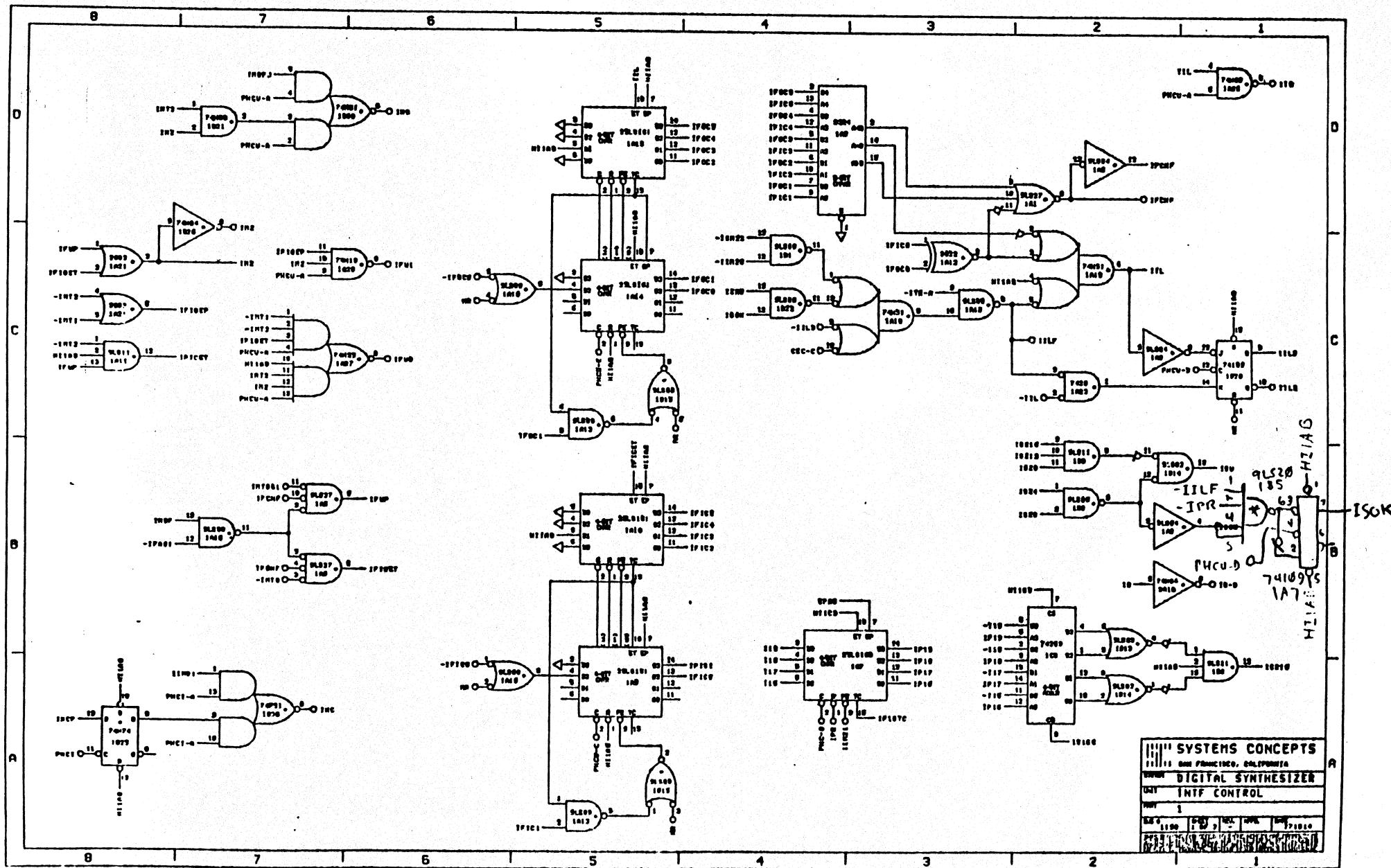
B6; DMDC



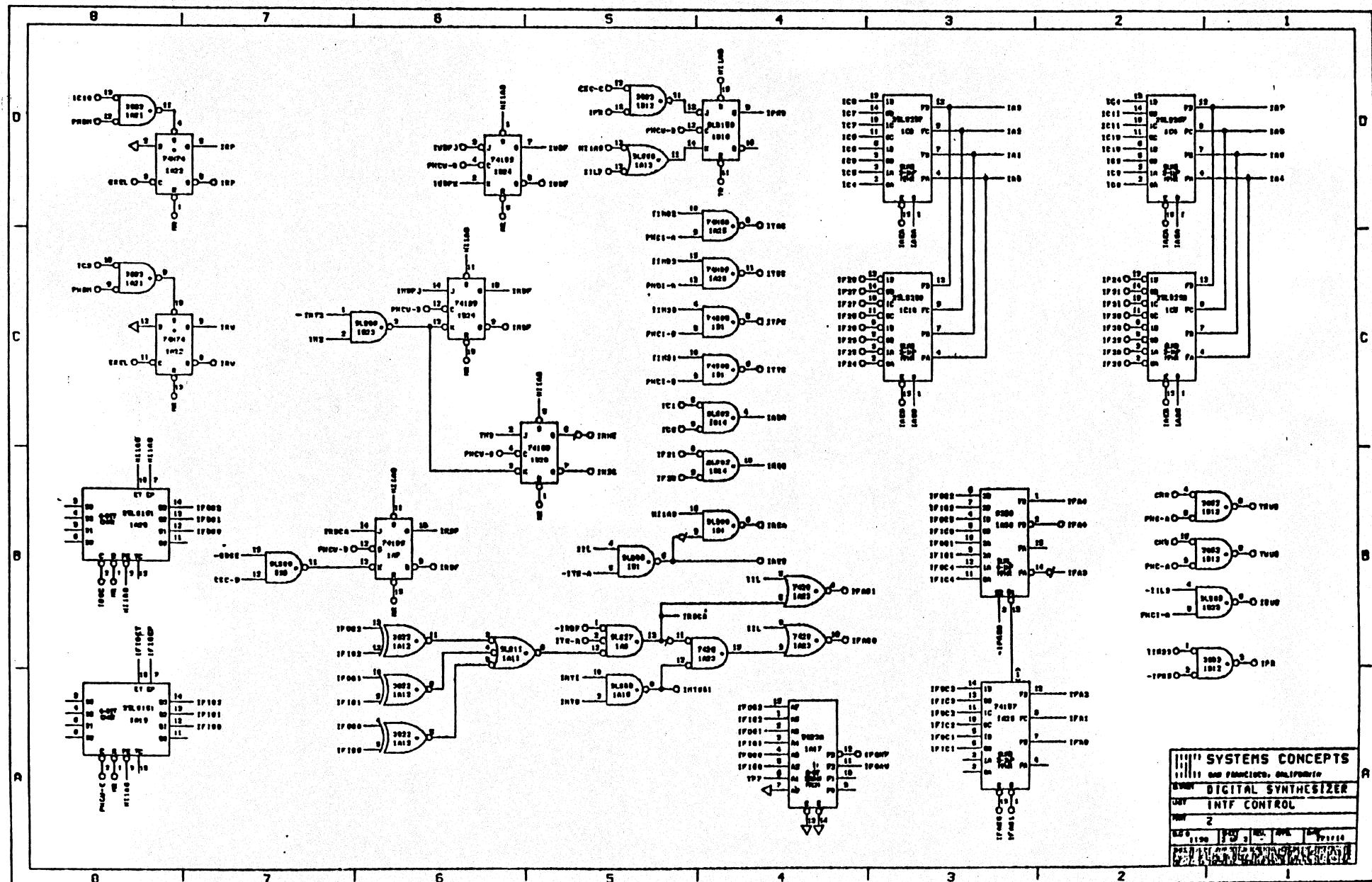
SRE; DMEMC



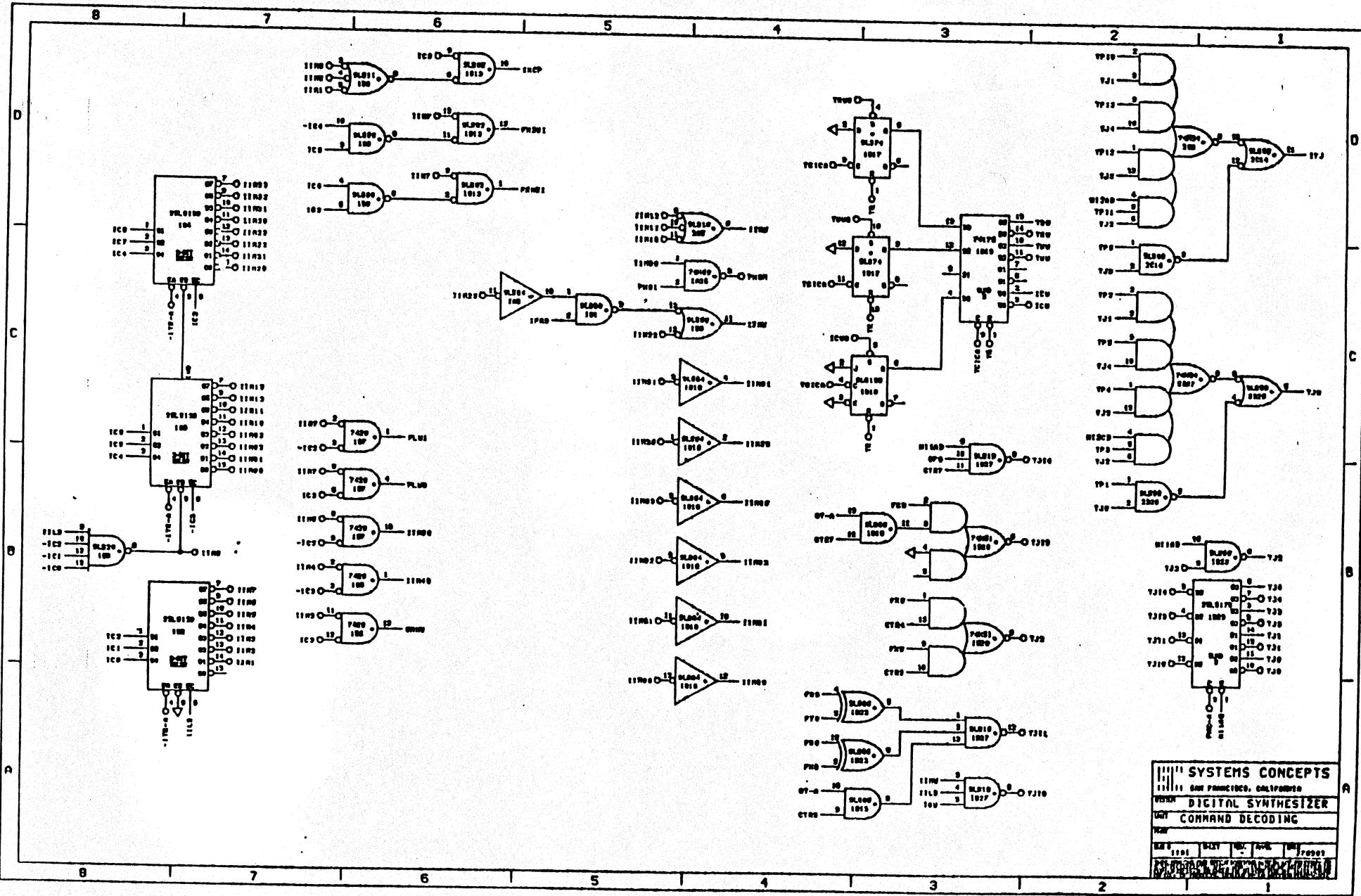
SWB; INTFC



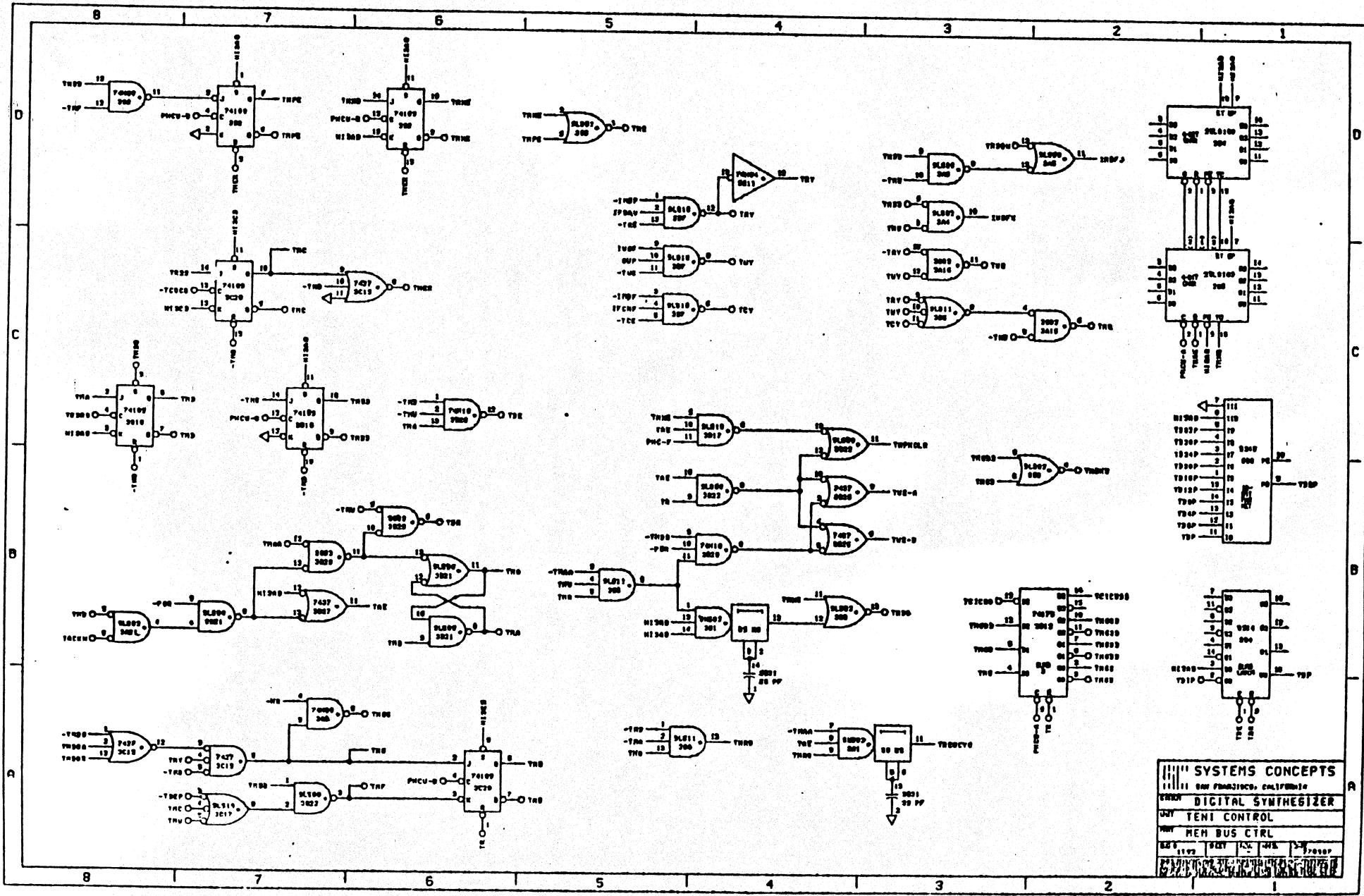
SUB; INTFC2



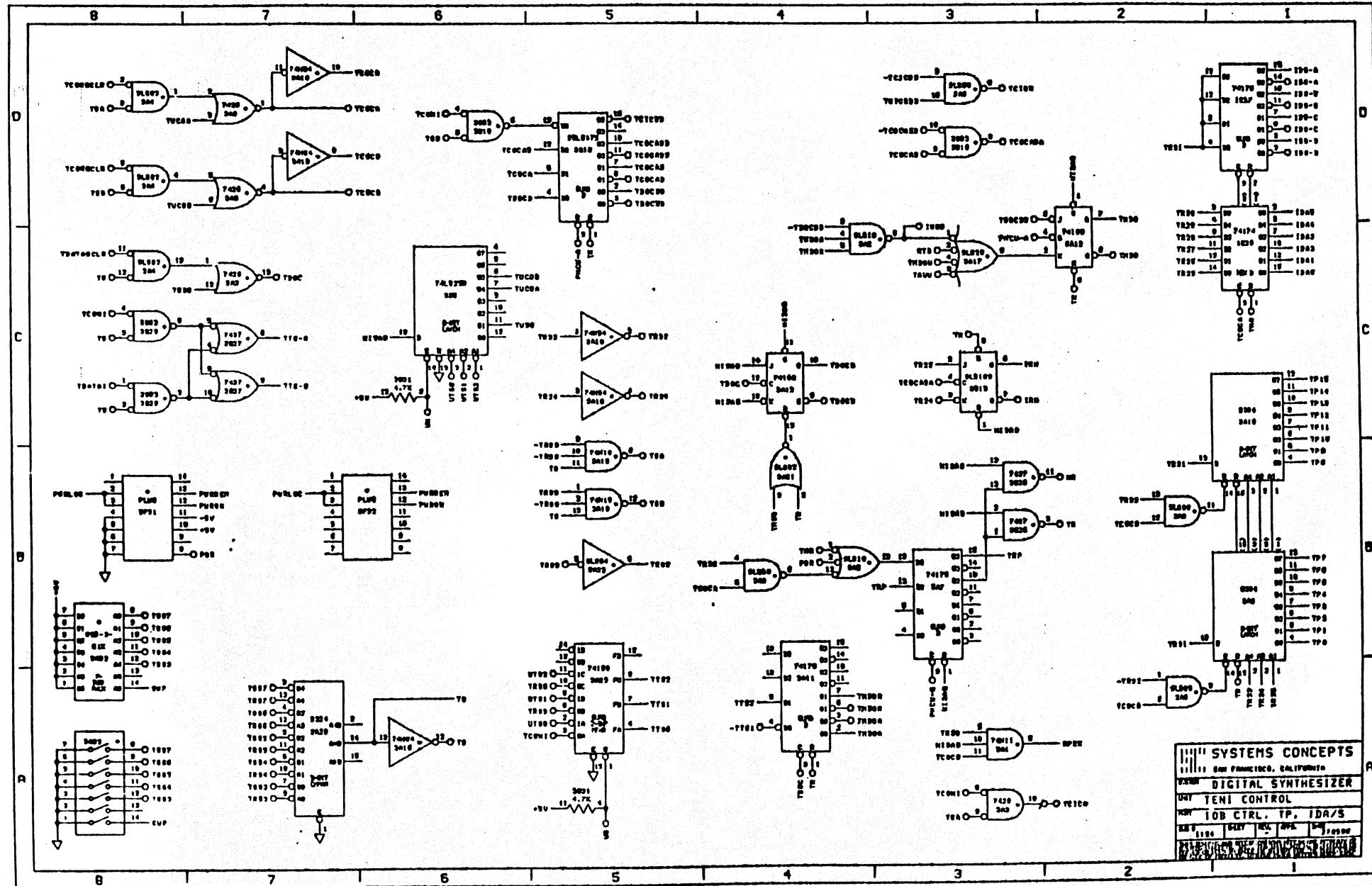
SWB; CONDEC



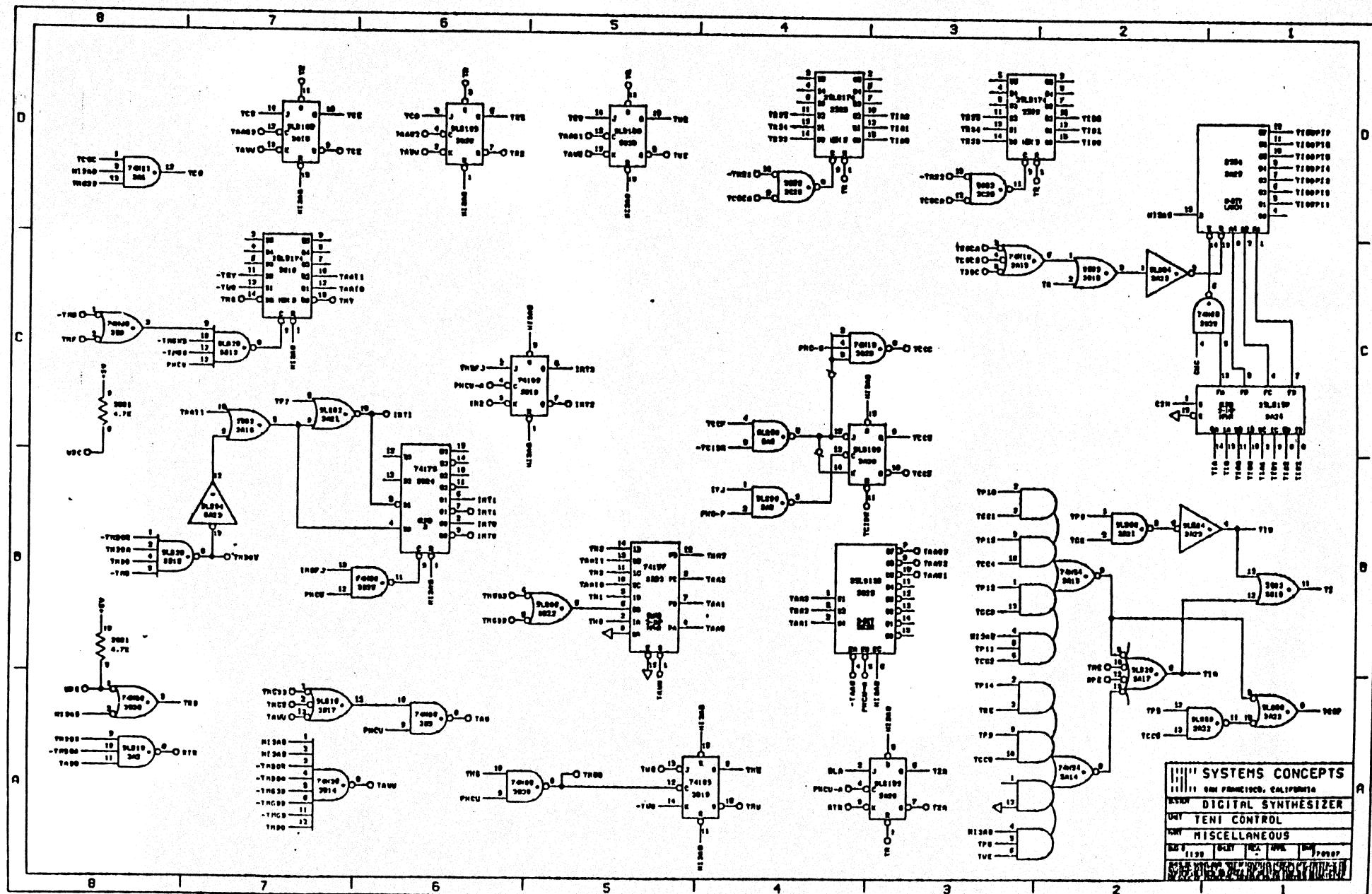
BG ; MEMBC



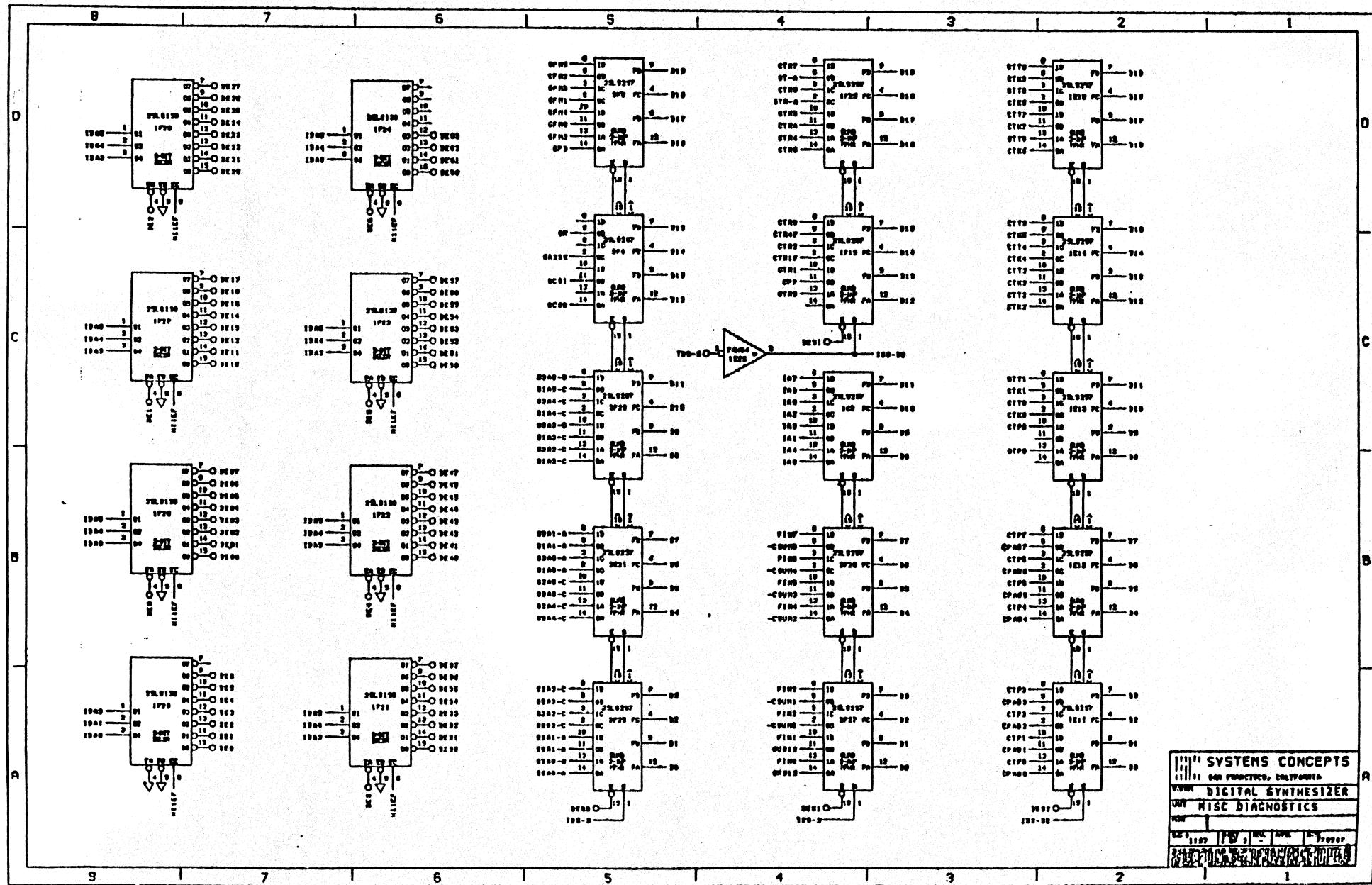
SUB; IOTC



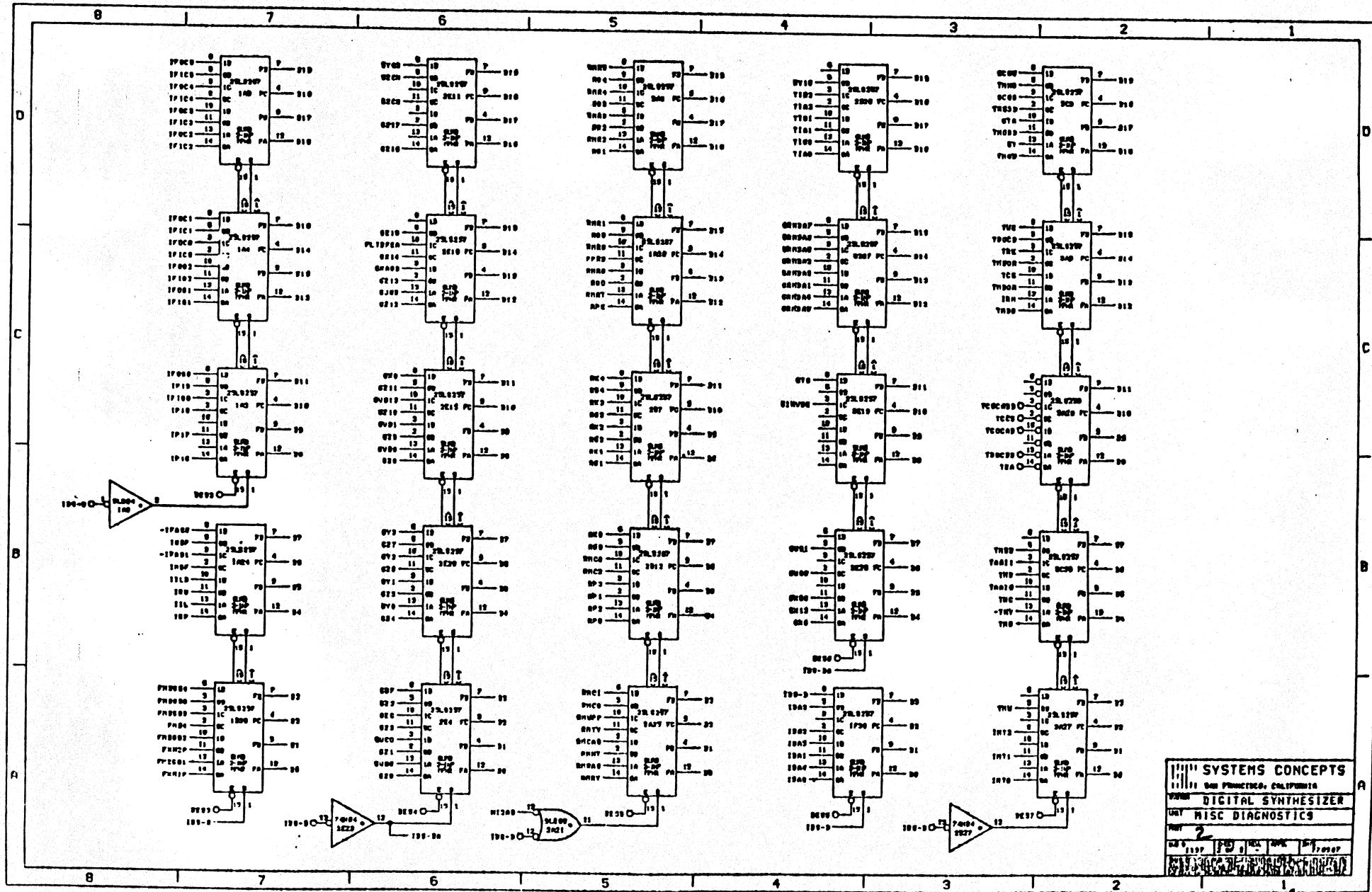
SRE; TMISC



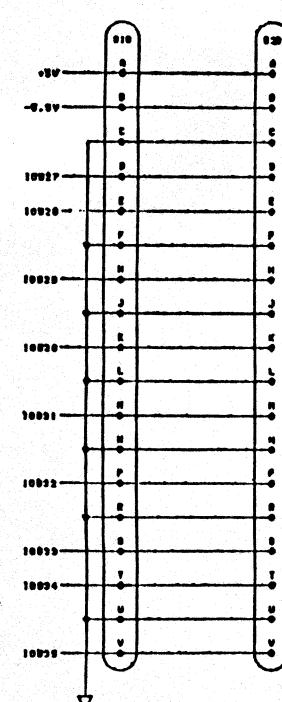
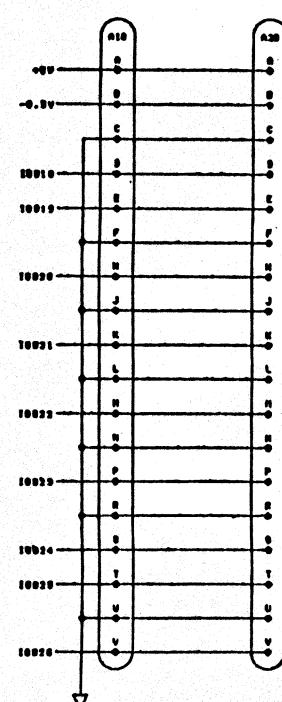
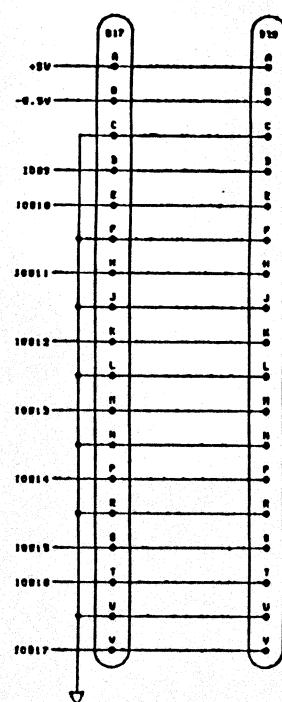
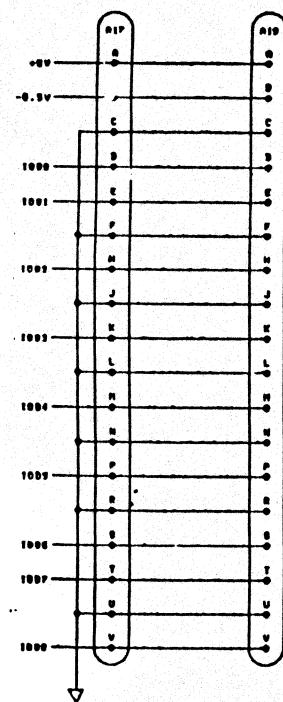
DxM; MDIAG1



DXM; MDI AG2

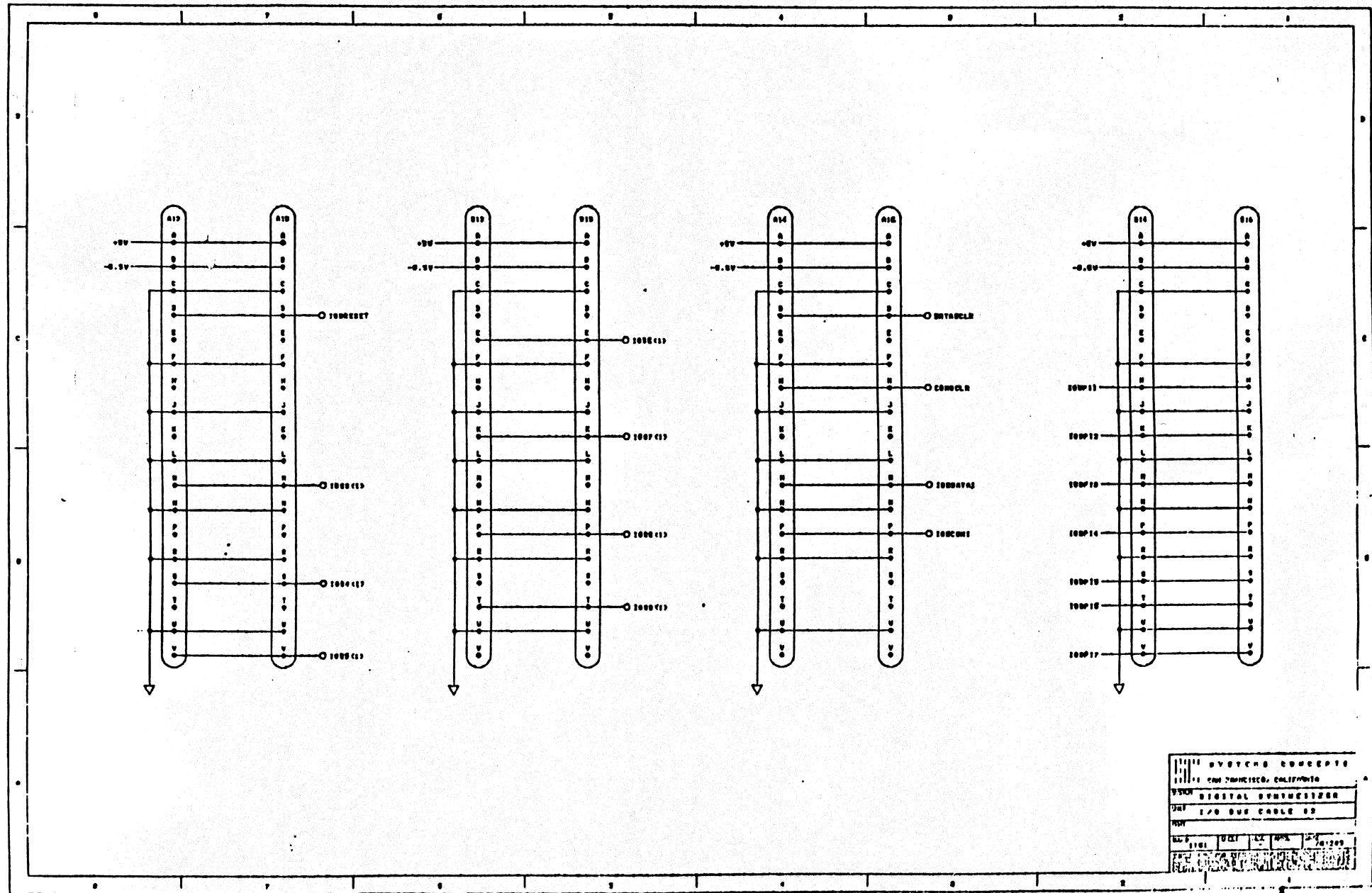


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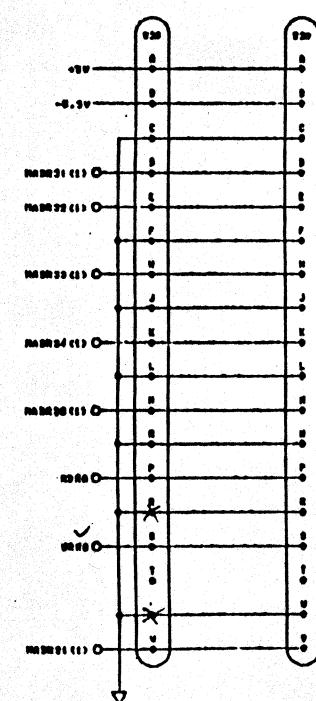
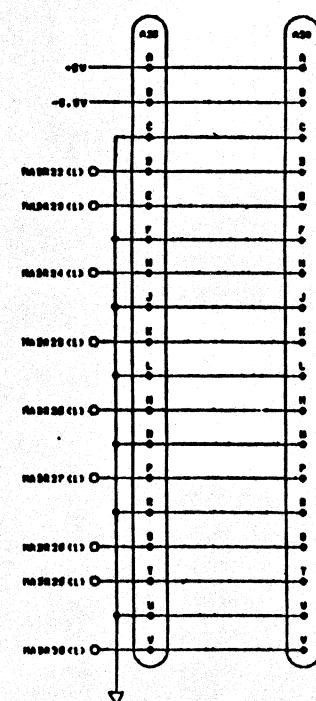
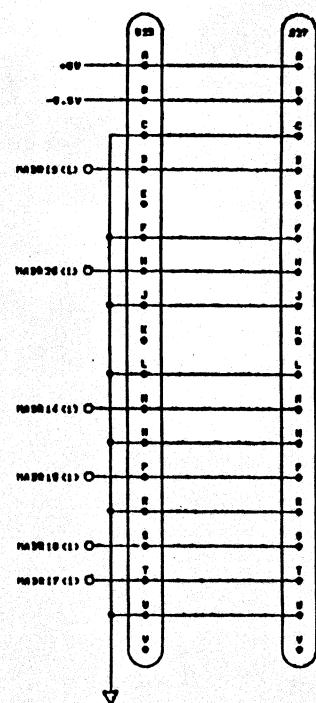
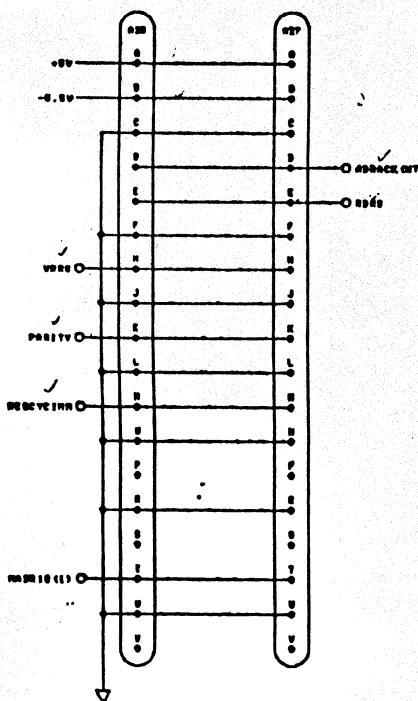


SYSTEMS CONCEPTS
11 SAN FRANCISCO, CALIFORNIA
VIC32 DIGITAL SYNTHESIZER
VER 1.00 OUT CABLE 01
REV 1
S/N 1000
SD 1000
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RLS; IOBC B2

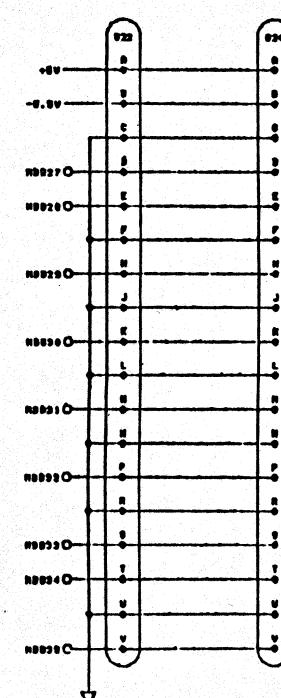
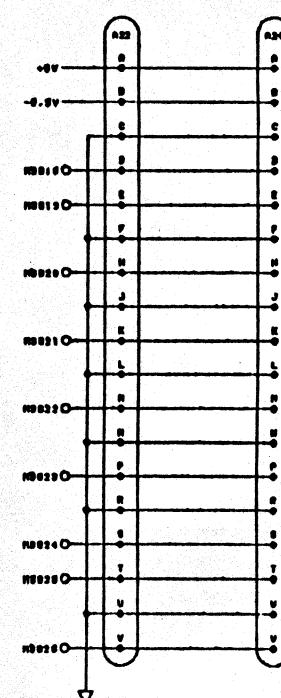
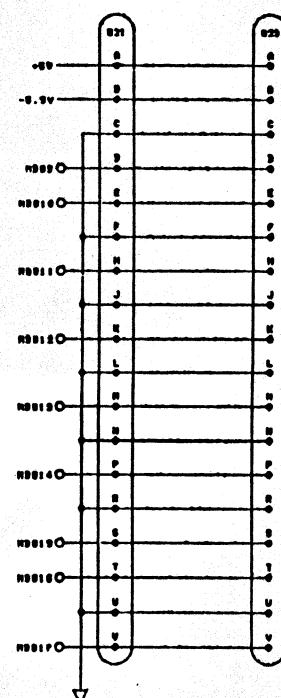
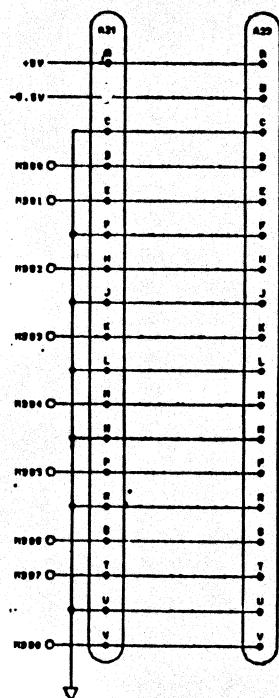


RLS; MEMCB1



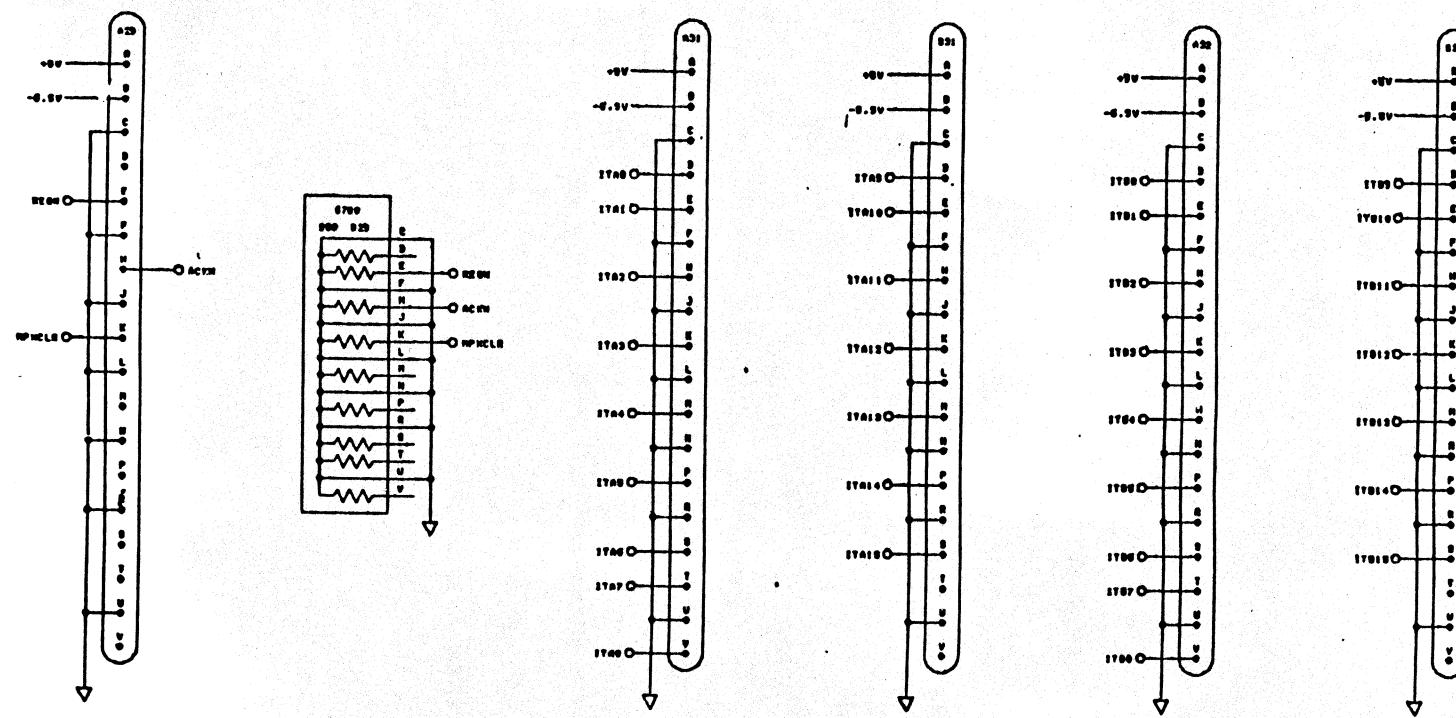
SYSTEMS CONCEPTS
SAN FRANCISCO, CALIFORNIA
DATA DIGITAL SYNTHESIZER
DATA MEMORY CARD 01

RLS;MEMCB2



SYSTEMS CONCEPTS
SAN FRANCISCO, CALIFORNIA
DATE
DIGITAL SYNTHESIZER
REPORT CODE
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RLS; PMTCB



SYSTEMS CONCEPTS			
ON FORTRESS, CALIFORNIA			
DIGITAL SYNTHESIZER			
UNIT	PORT NUM.	TYPE	CABLES
MAIN			
DATA IN/OUT	DATA IN/OUT	DATA IN/OUT	DATA IN/OUT