

2.1 ValidSIM

7 ANOMALIES CORRECTED

A number of anomalies have been corrected either in ValidSIM 2.1, or in previous releases and not reported until now.

1. The Simulator is again able to properly distinguish between signals with the same name but different path names.
2. The LATCH RS primitive now behaves properly without initialization when using Realfast. (#72F-1001)
3. The DUMPMEMORY command now accepts a bit subrange specification and outputs a file containing only the requested bits. (#72F-1025 and #8SB-0054)
4. Tabular input files can now be used with bidirectional parts without obscuring the outputs since values may be omitted from the file and the fields left blank. (#72F-1029)
5. On the IBM mainframe, the error message concerning non-printing ASCII characters is now suppressed. (#72F-1037)
6. When the Compiler/Linker are invoked from within the Simulator in split-screen mode, messages will be cleared away before the simulation begins. (#27B-0076)
7. An error message will no longer be output when attempting to DEPOSIT a value with more than 10 digits. (#8SB-0058)