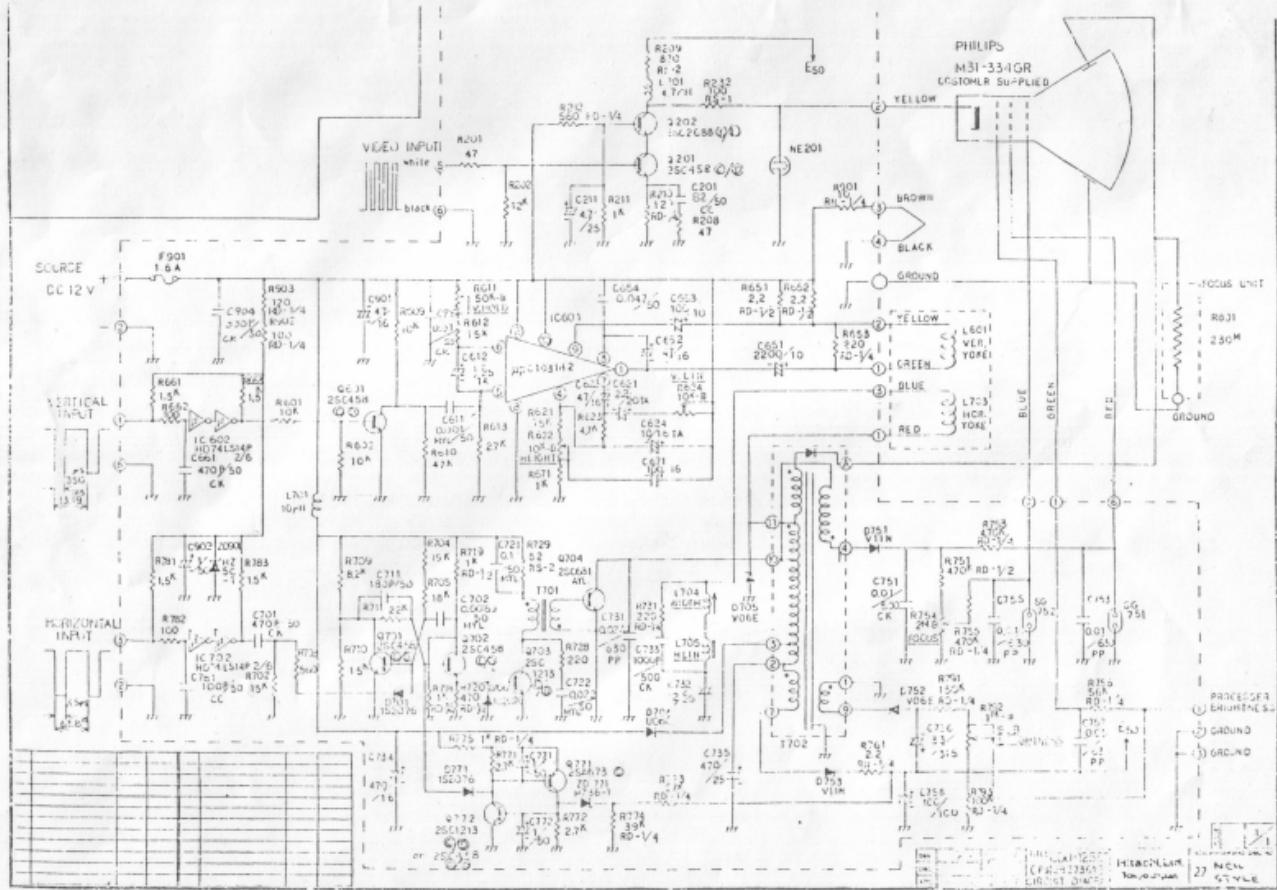
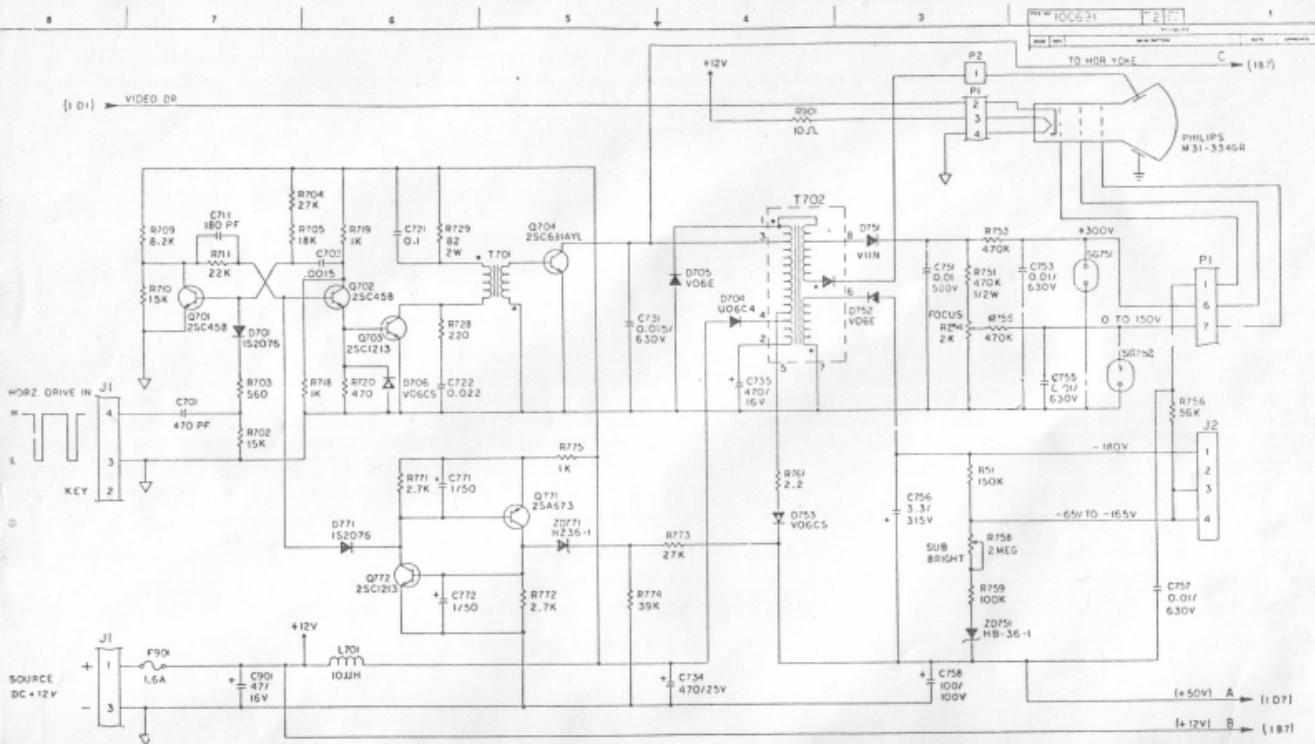


1



APPENDIX C



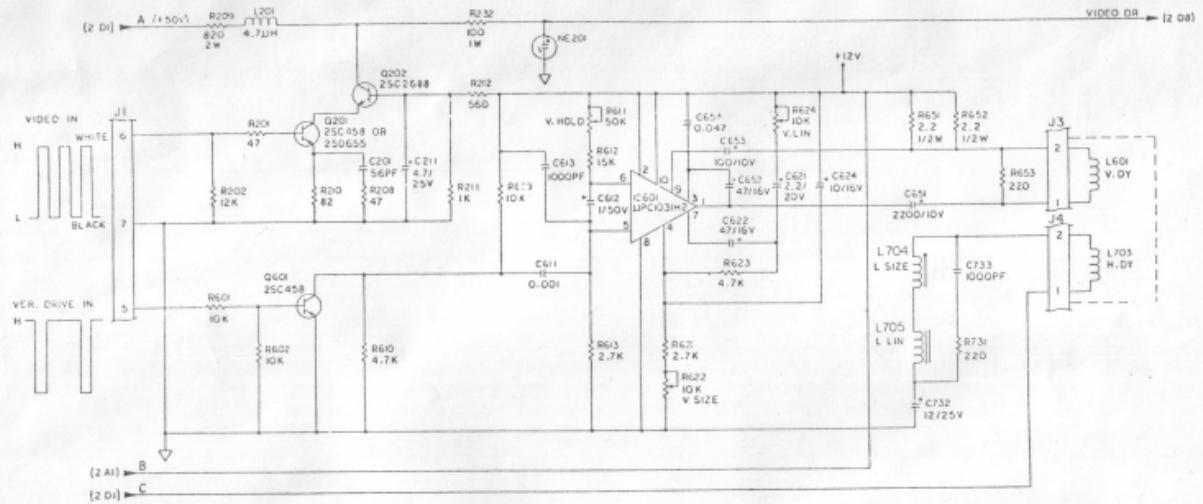
Sick's Systems Technology Inc.

SCHEMATIC CIRCUIT  
VIDEO BOARD

D 100591 P1

卷二 2012 年 1 月

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1  
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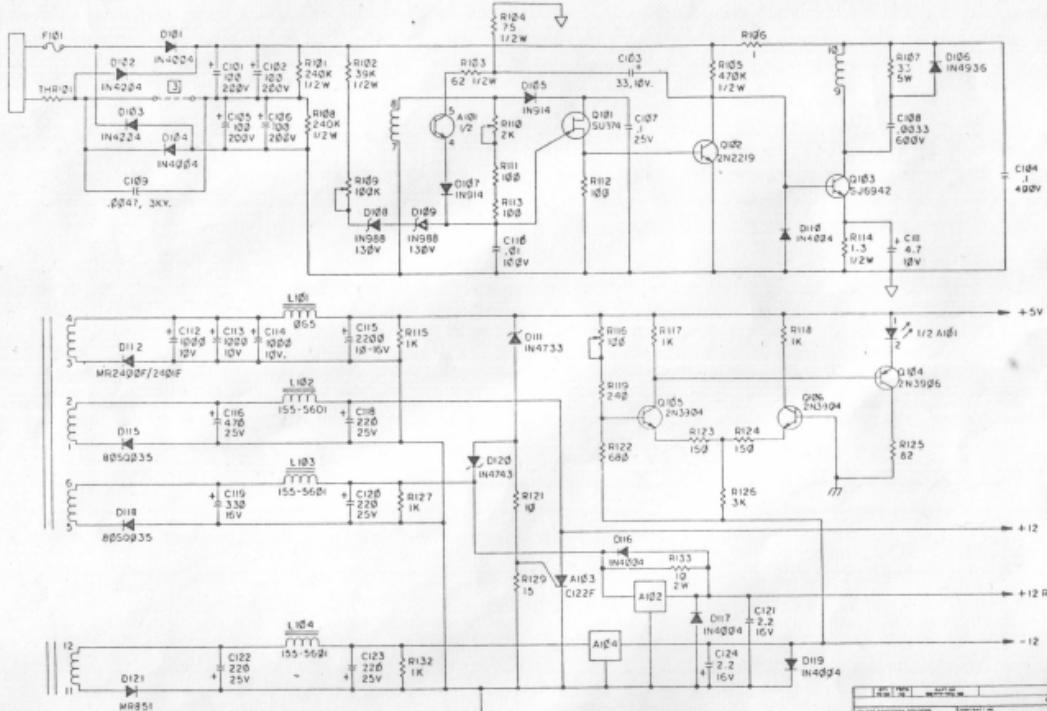


3. ALL RESISTORS ARE  $\pm 5\%$ , 1/4 W.

**I** ELECTRICAL VALUES ARE IN OHMS, MICROFARADS AND MICROHENRIES.

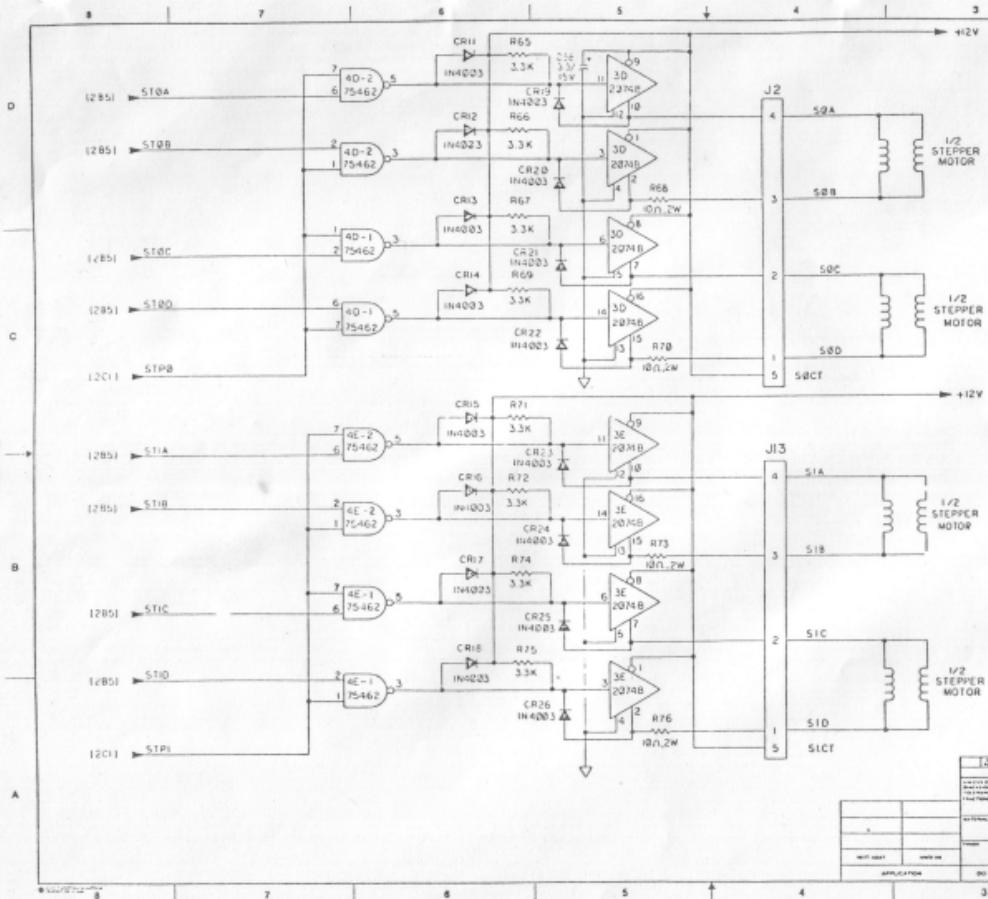
NOTES : UNLESS OTHERWISE SPECIFIED :

C RID	T2	
L4	S G 2	
U1	D S 1	
O9	F 1	
C 31	J 4	
R 51	P 2	R 2
LAST USED		NOT USED
REFERENCE	DESIGNATIONS	



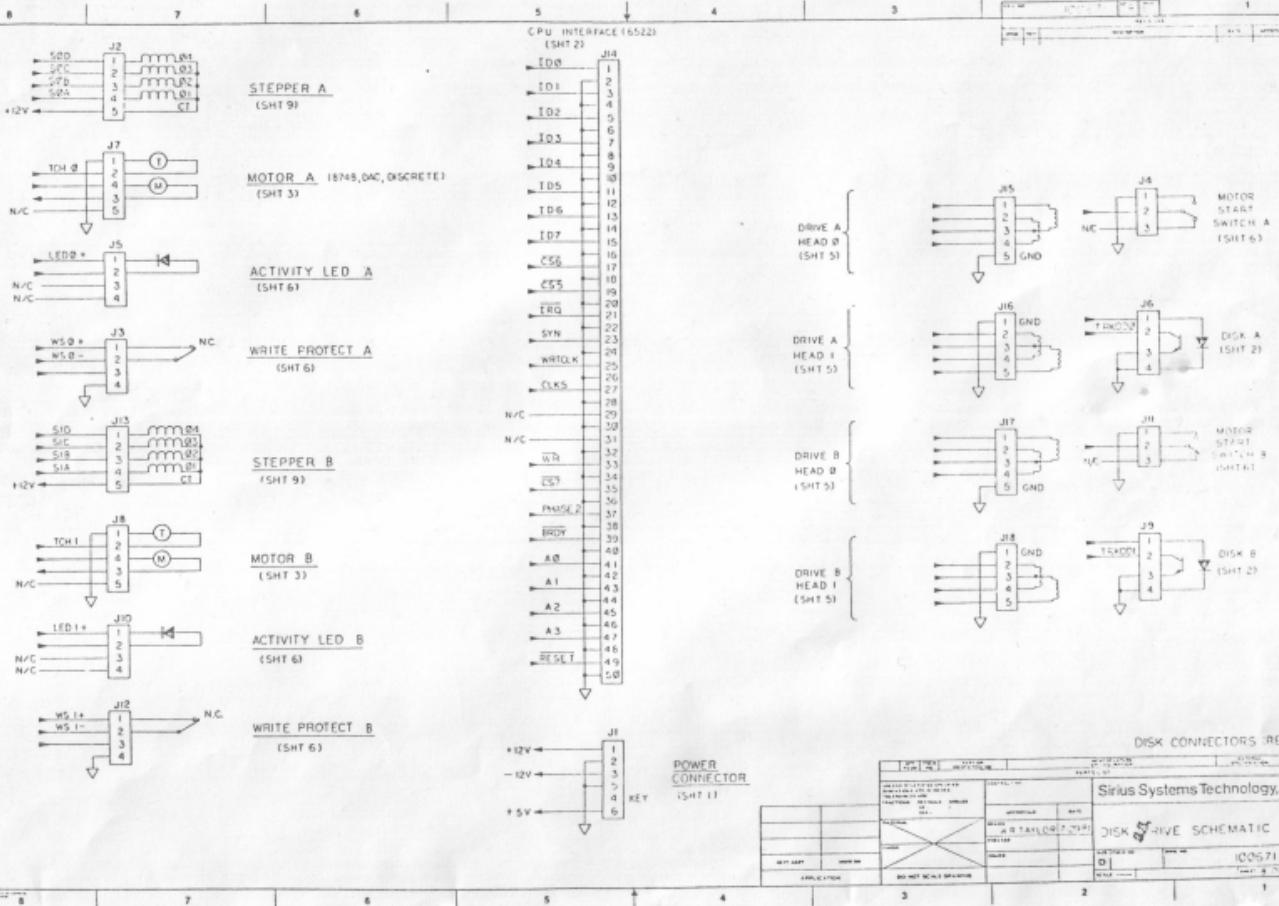
THE INFORMATION ON THIS DOCUMENT IS THE PROPERTY OF COMPOWER CORP.  
THIS DOCUMENT ARE FOR THE EXCLUSIVE USE OF SIRIUS SYSTEMS TECHNOLOGY INC. AND SHOULD NOT BE SUPPLIED TO OUTSIDE SOURCES.

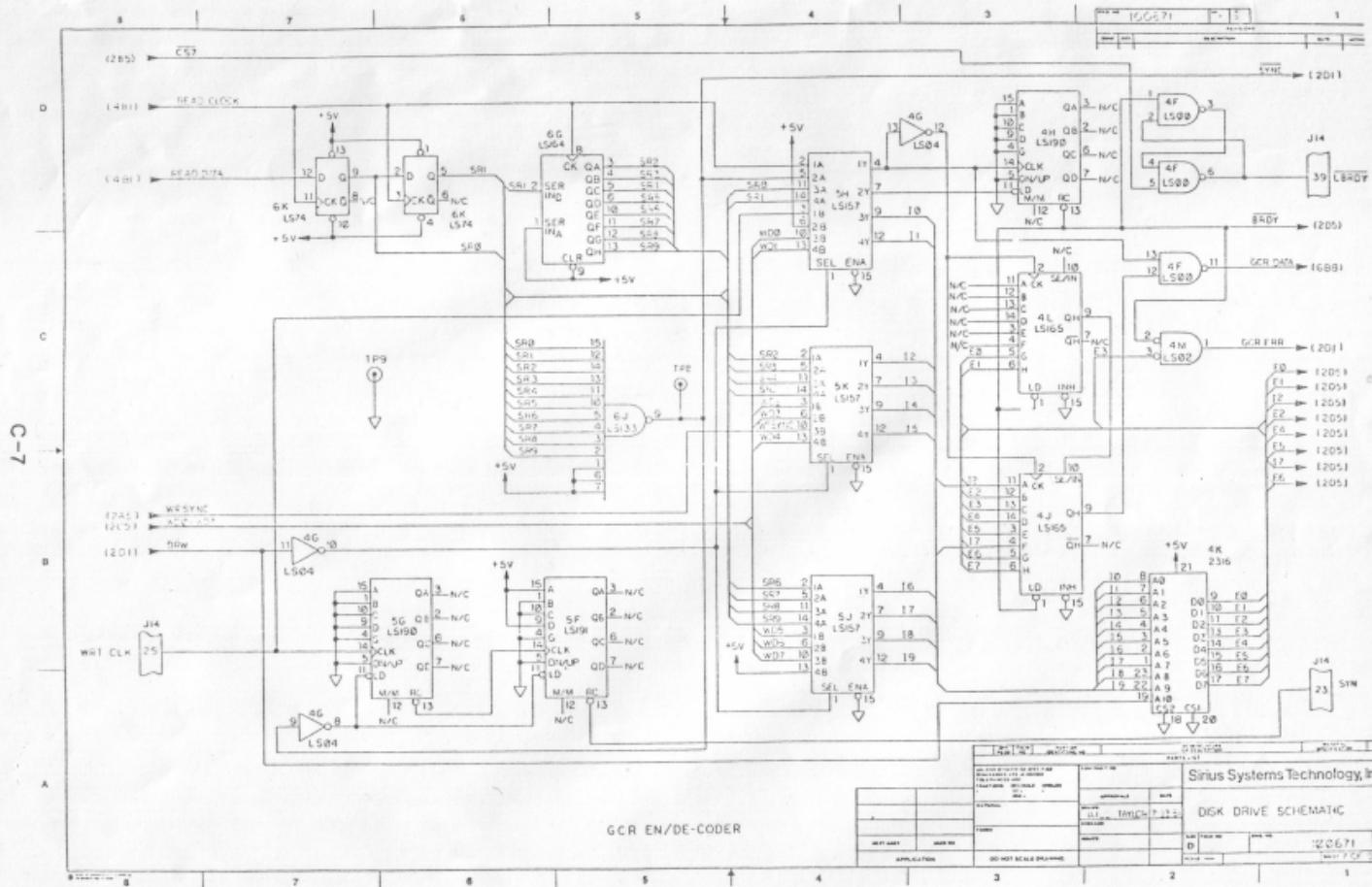
100701		100701	
SCHMATIC		POWER SUPPLY	
SIRIUS SYSTEMS TECHNOLOGY, INC.			
100701		100701	
REF. COMPOWER CORP. CP 5102 SCHEMATIC, DWG NO. 90017.		REF. COMPOWER CORP. CP 5102 SCHEMATIC, DWG NO. 90017.	
(3) CUT FOR 200V OPERATING		(3) CUT FOR 200V OPERATING	
2. ALL RESISTORS ARE ±5%, 1/4W.		2. ALL RESISTORS ARE ±5%, 1/4W.	
1. ELECTRICAL VALUES ARE IN OHMS, MICROFARADS		1. ELECTRICAL VALUES ARE IN OHMS, MICROFARADS	
AND MICROHENRIES.		AND MICROHENRIES.	
NOTES: UNLESS OTHERWISE SPECIFIED.		NOTES: UNLESS OTHERWISE SPECIFIED.	



STEPPER CONTROL

DISK DRIVE SCHEMATIC		SIRIUS SYSTEMS TECHNOLOGY, INC.	
DATE	10/14/87	REVISION	1
DESIGNER	TAYLOR	APPROVAL	DATE
INIT. TEST	WAVE	TESTED	10/14/87
APPLICATION	DO NOT SCALE DRAWINGS	REVIS.	9
		REV.	100671

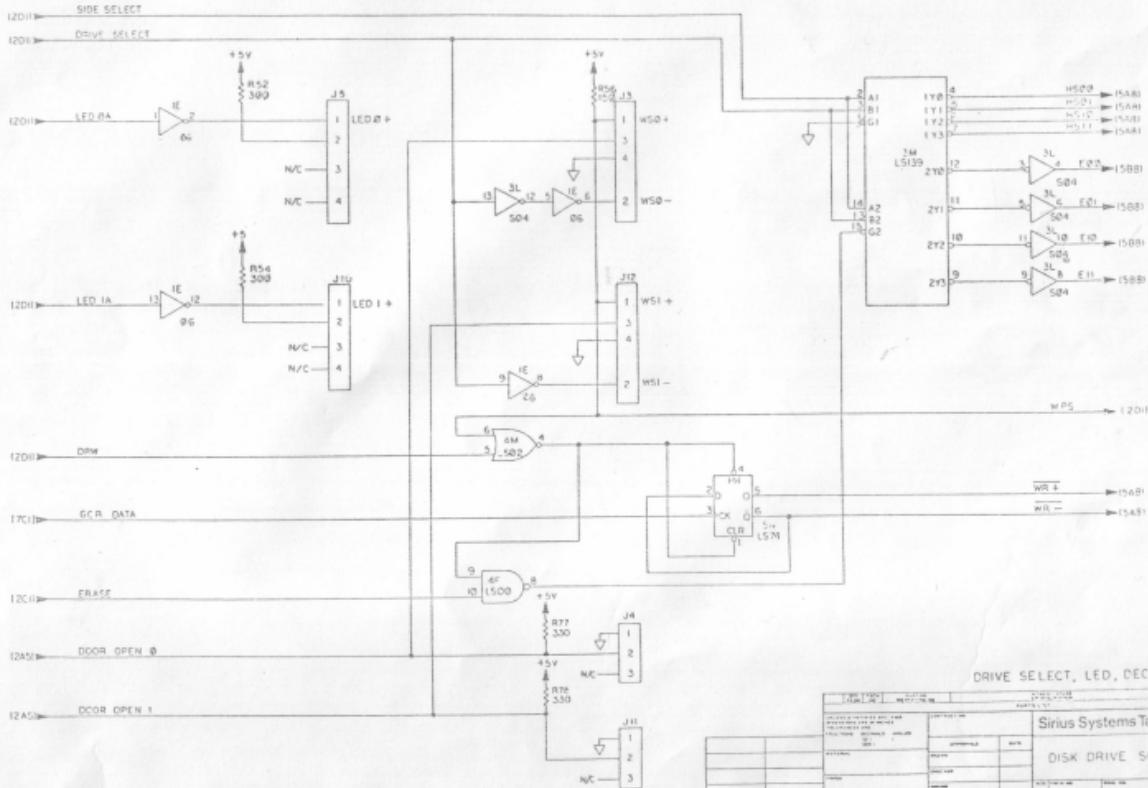


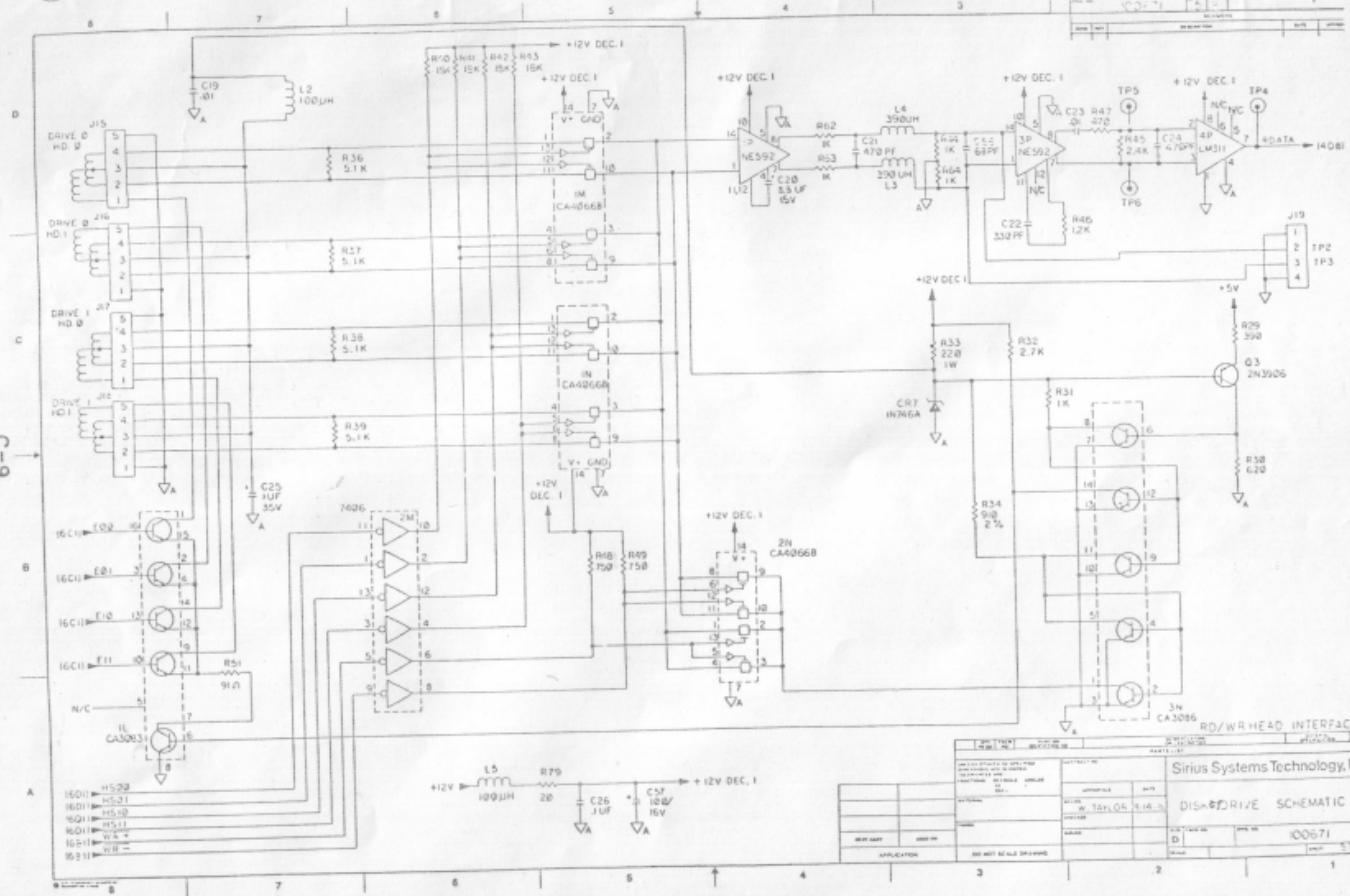


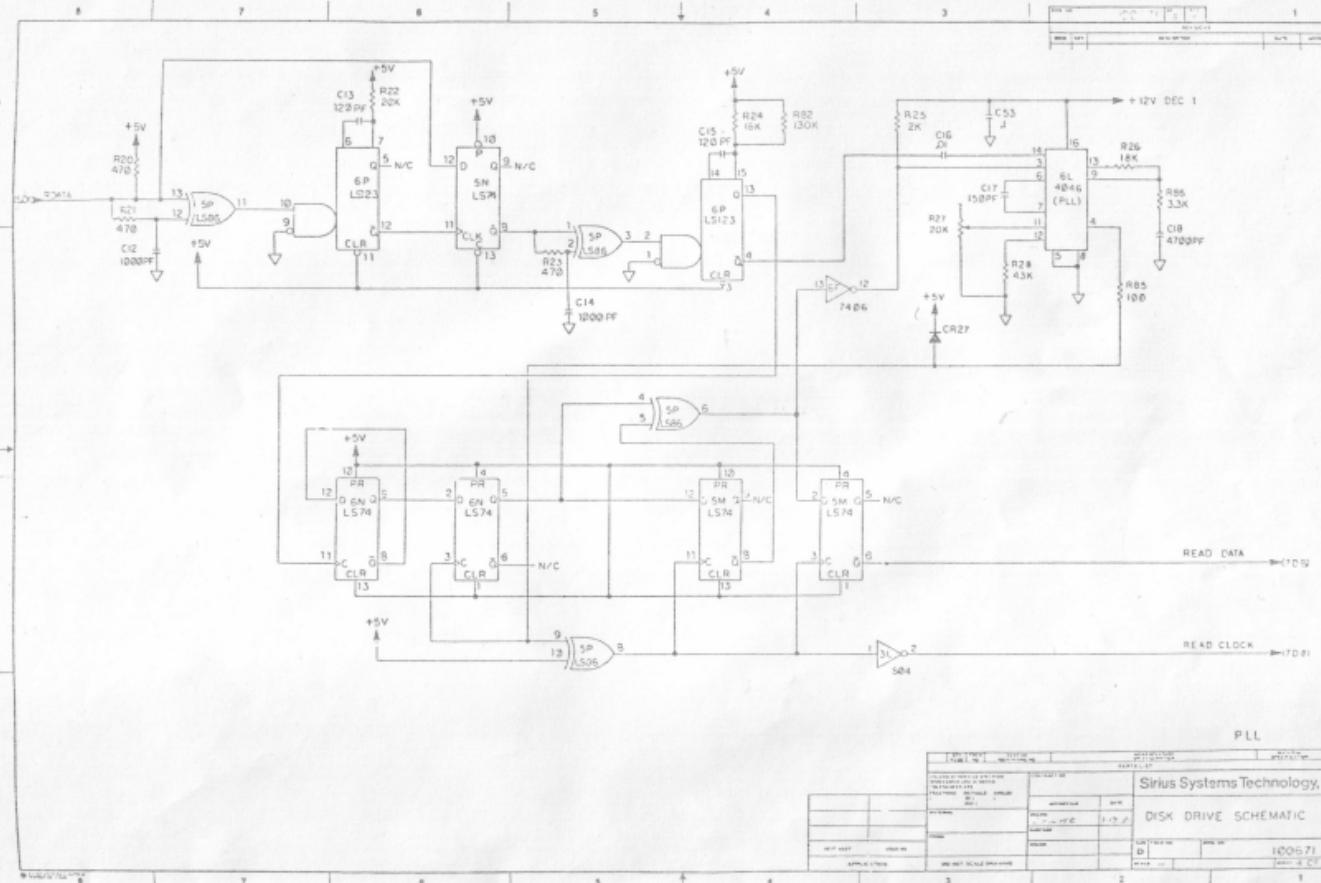
## DRIVE SELECT, LED, DECODE

Sirius Systems Technology, Inc.

DISK DRIVE SCHEMATIC

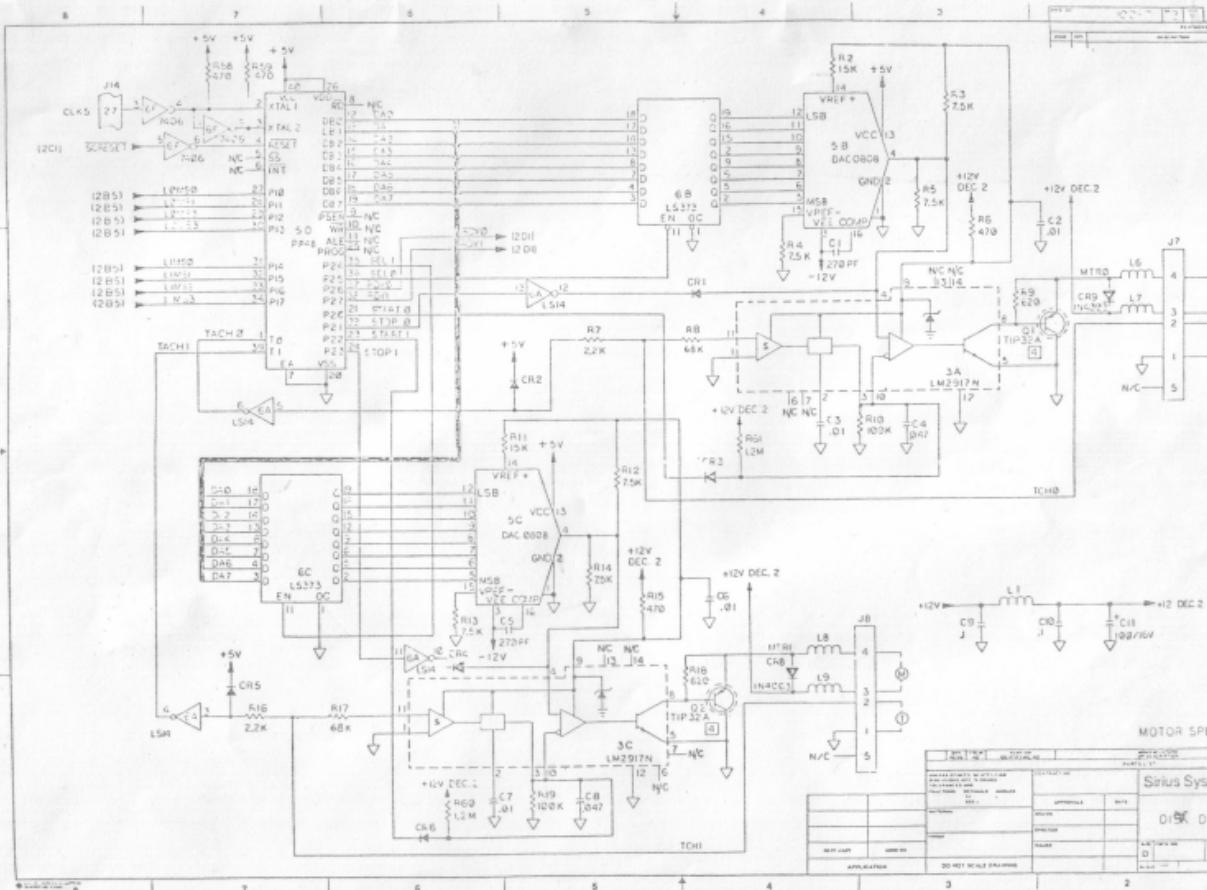






C-10

C-11

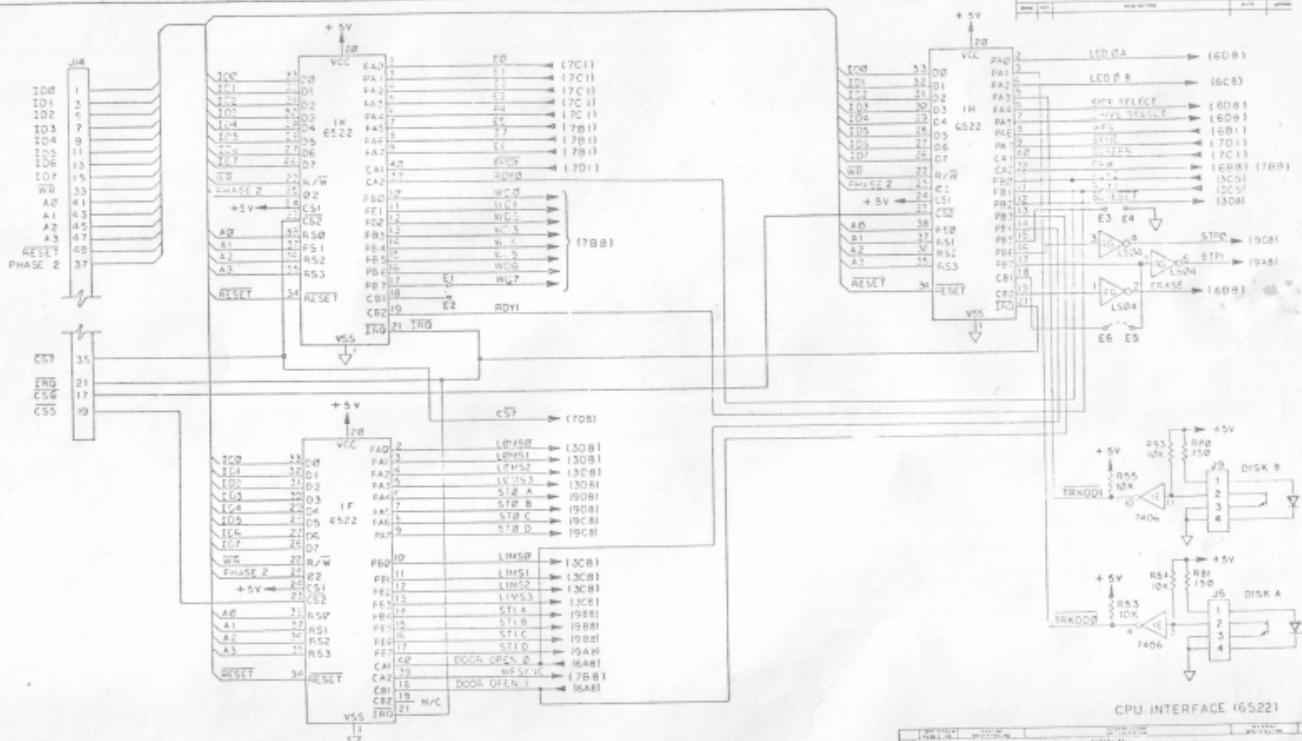


MOTOR SPEED (8748 &amp; DAC)

SIRIUS SYSTEMS TECHNOLOGY

DRIVE SCHEMATIC

100671



## CPU INTERFACE (65221)

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Sirius Systems Technology, Inc.

#### **DISPENSIVE SCHEMATIC**

REF	ITEM	QTY	DESCRIPTION
P1	LTC1240A-A241-A244	1	OPAMP
P2	CH340B-A001-A002-A003-A004-A005-A006-A007	1	USB TO SERIAL
P3	REVISED PNP BC 10429	1	PNP TRANSISTOR
P4	REVISED PNP BC 10415	1	PNP TRANSISTOR
P5	REVISED PNP BC 10463	1	PNP TRANSISTOR
P6	2N3904-A001-A002-A003-A004-A005-A006-A007	1	NPN TRANSISTOR
P7	2N3906-A001-A002-A003-A004-A005-A006-A007	1	NPN TRANSISTOR
P8	PA4000-A001-A002-A003-A004-A005-A006-A007	1	PNP TRANSISTOR

NOTES UNLESS OTHERWISE SPECIFIED.

1. ELECTRICAL VALUES ARE IN OHMS, MICROFARADS  
AND MICROHENRIES.

2. ALL RESISTORS ARE 25%, 1/4W.

3. F.S IS FACTORY SELECT COMPONENTS.

4. Q1,Q2 ARE MOUNTED ON HEATSINK.

5. ALL INTEGRATED CIRCUITS ARE SN74 SERIES.

6. I.C PINS ARE GND 17,15/14 EXCEPT FOR:

TYPE	GND	+5V
74L1512	8	16
74L1513	8	16
74L1519	8	16
74LS07	8	16
74LS55	8	16
74LS70	8	16
74LS72	8	16
74LS75	10	20
236	10	24
74LS07	8	8

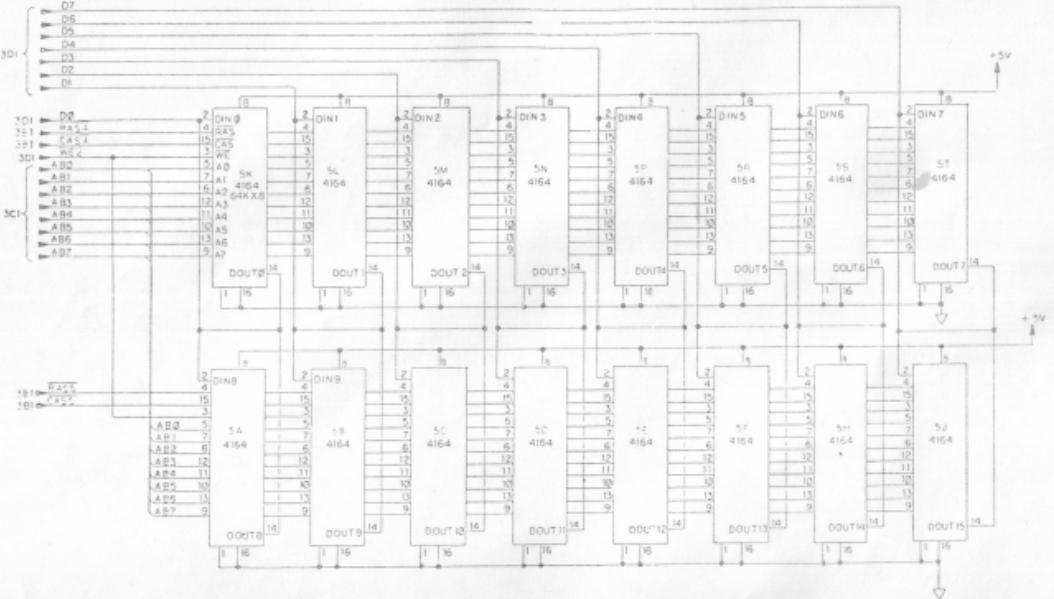
7. ALL DIODE ARE IN4148.



SPARE GATES

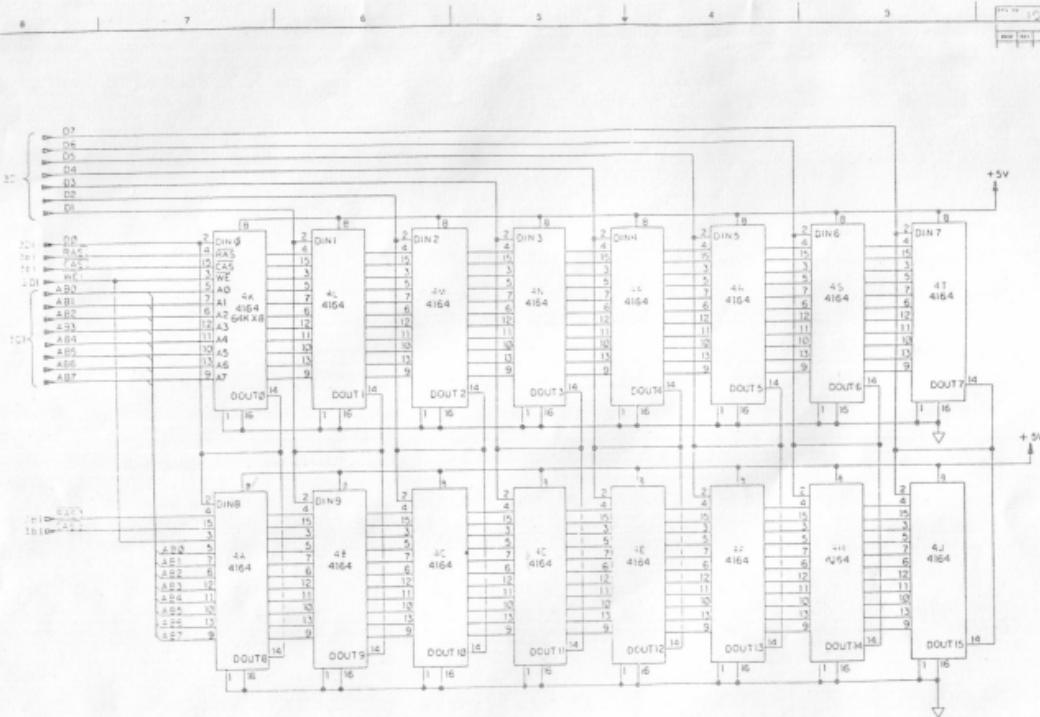
SHEET	DESCRIPTION
1	POWER & SPARES
2	CPU INTERFACE (6522)
3	MOTOR STEPPER (6719 & U4)
4	P.L.S.
5	ICP WS DRIVE INTERFACE
6	C4147 SEL1 LED, RED
7	U4 = EN7412, COCOE
8	DISK COLLISION RELAY
9	STEPPER CONTROL

REF	COMPONENT	QTY
T1	78L7	1
T2	78L0	1
T3	78L0	1
U1	6522	1
U2	6719	1
U3	PA4000	1
U4	EN7412	1
U5	ICP WS	1
U6	DISK COLLISION RELAY	1
U7	STEPPER CONTROL	1
U8	74LS07	1
U9	74LS55	1
U10	74LS70	1
U11	74LS72	1
U12	74LS75	1
U13	2N3904	1
U14	2N3906	1
U15	PA4000	1
U16	2N3904	1
U17	2N3906	1
U18	2N3904	1
U19	2N3906	1
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C-14

C-15



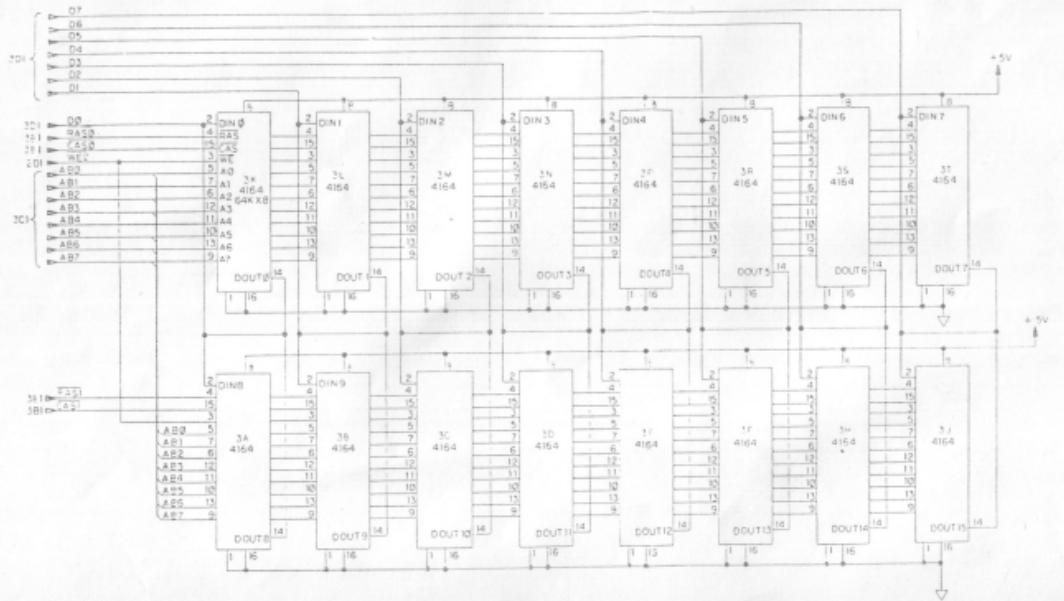
SIRIUS SYSTEMS TECHNOLOGY, INC.	
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10100-00000001	00111101
10100-00000002	00111110
10100-00000003	00111111

SCHEMATIC  
32x4K MEMORY

10100-00000004

10100-00000005

10100-00000006

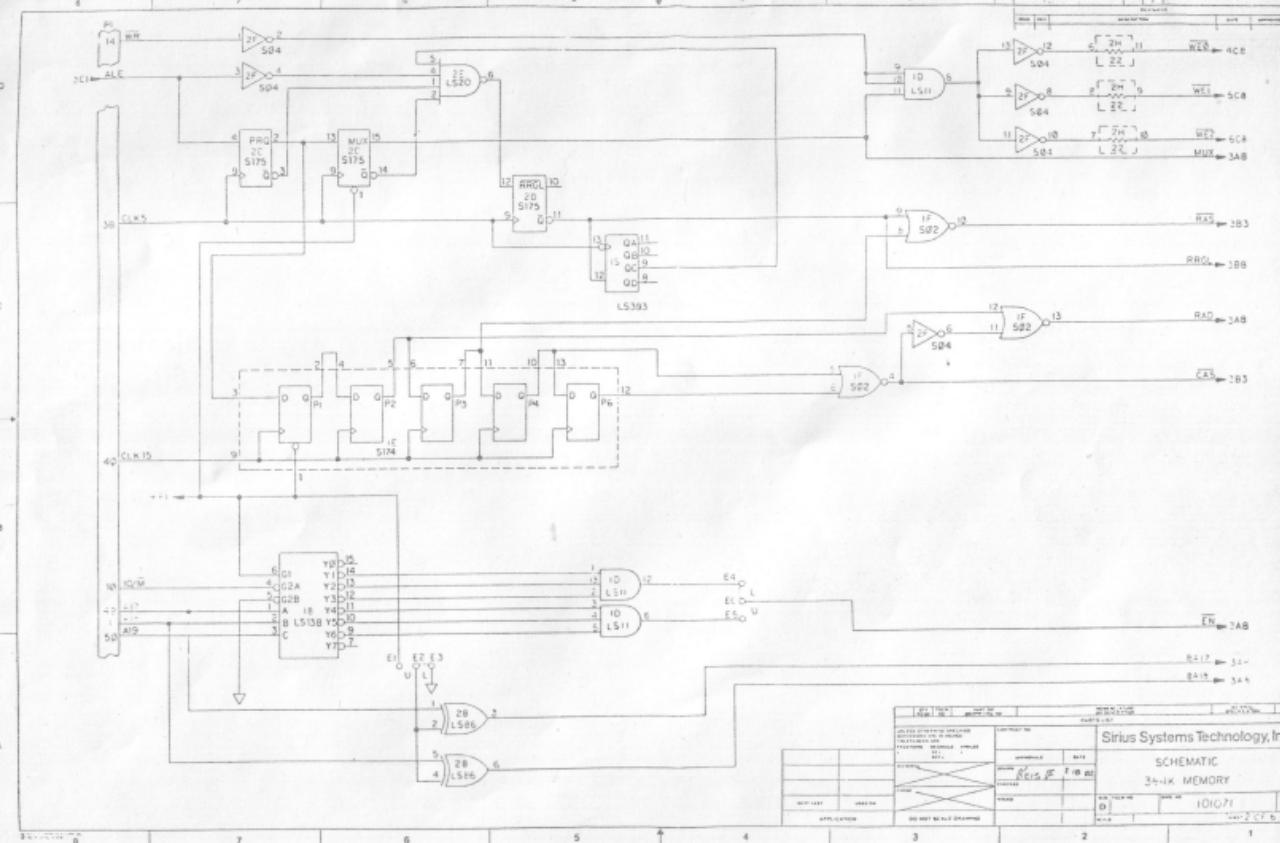


Sirius Systems Technology, Inc.

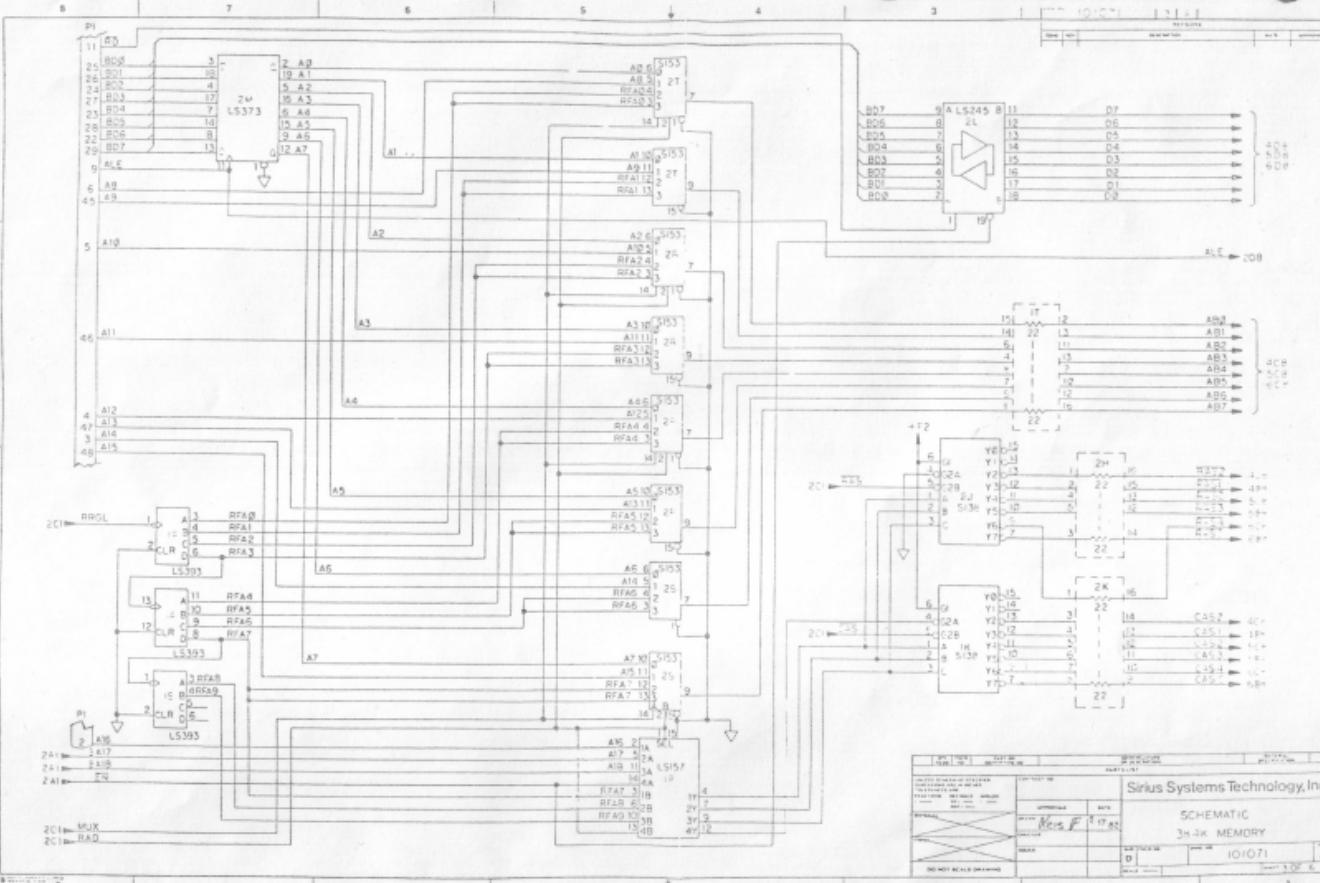
SCHEMATIC  
256K MEMORY

101076

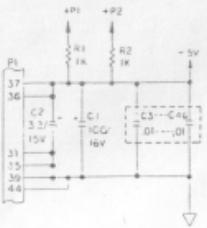
C-17



Sirius Systems Technology, Inc.  
SCHEMATIC  
34-K MEMORY  
101071



C-19



- NOTES-UNLESS OTHERWISE SPECIFIED  
 1. ELECTRICAL VALUES ARE IN OHMS, MICROFARADS.  
 AND MICROHENRIES.  
 2. ALL RESISTORS ARE  $\pm 5\%$ , 1/4W  
 3. ALL INTEGRATED CIRCUITS ARE SN74 SERIES.  
 4. IC PINS ARE , GND = 7, -5V = 14

IC TYPE	GND	+5V
74LS138	8	16
74 5153	8	16
74LS157	8	16
74 5174	8	16
74 5175	8	16
74LS245	10	20
74LS373	10	20
4164	16	8

(REF)

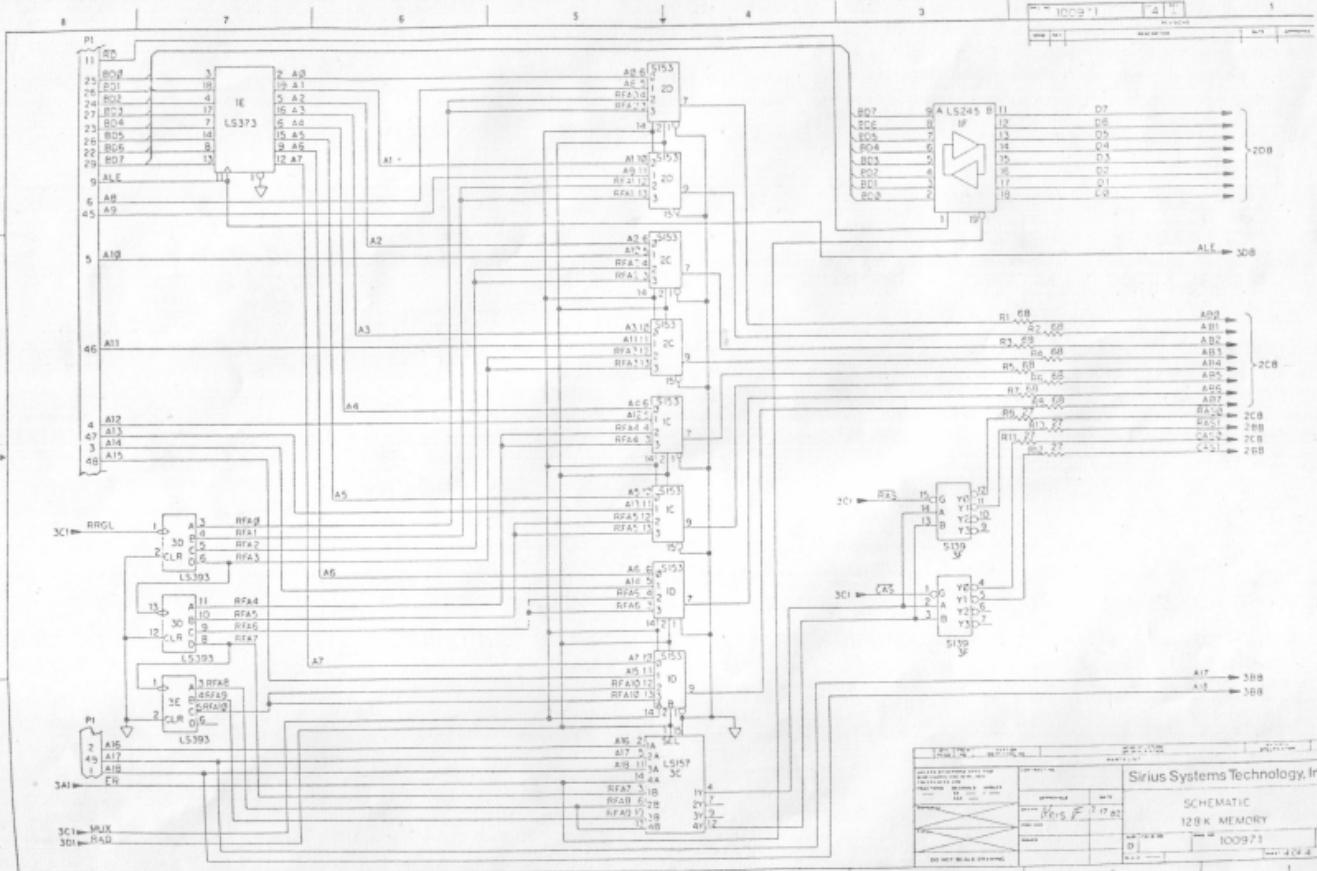


SPARE GATES

E5		
E6		
E7		
LAST USED	NOT USED	
REFERENCE DESIGNATION		

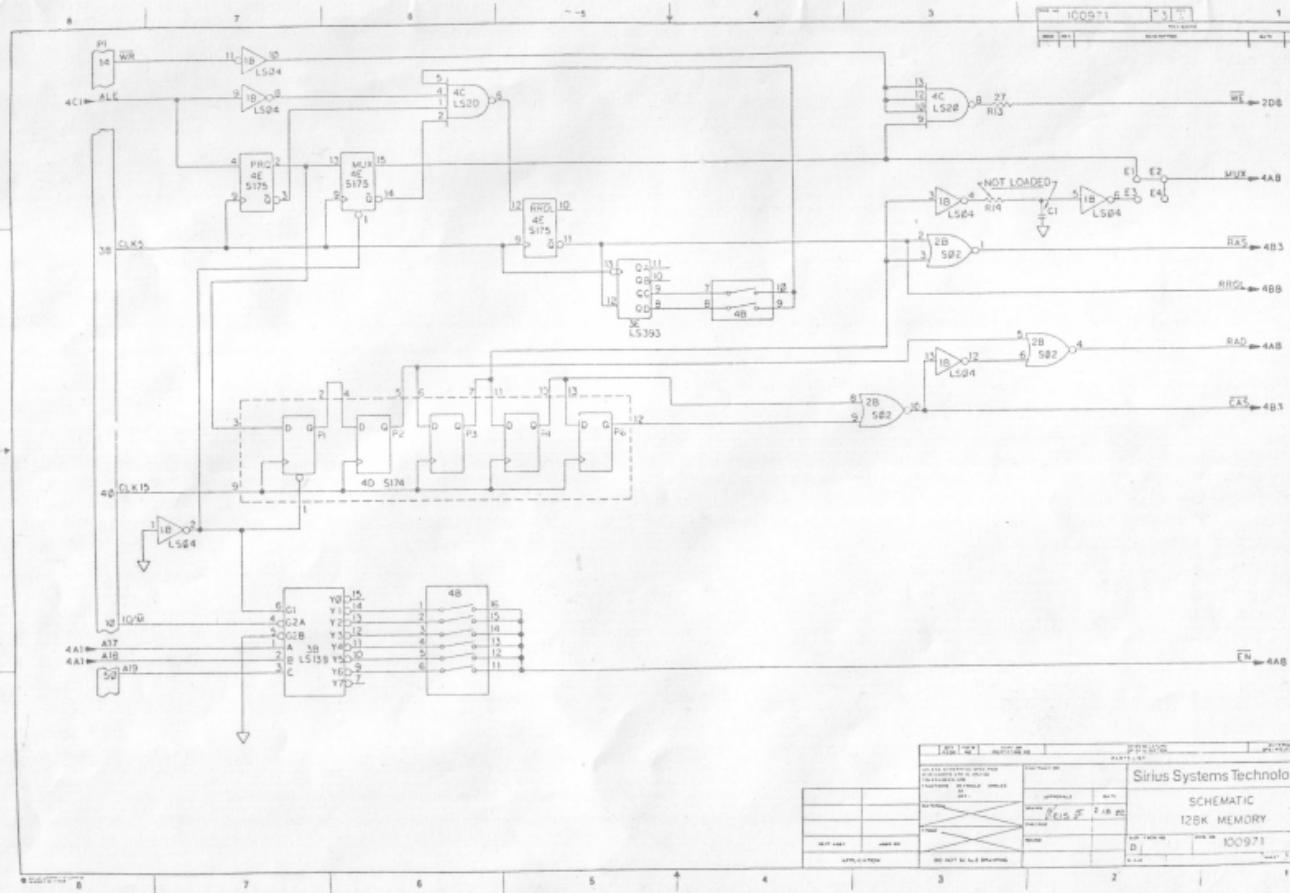
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10/10/82	F	10/10/82
DESIGNER	REVIEWED	APPROVED
INITIALS	INITIALS	INITIALS
SCHMATIC		
3 - 1K MEMORY		
101C71		

Sirius Systems Technology, Inc.

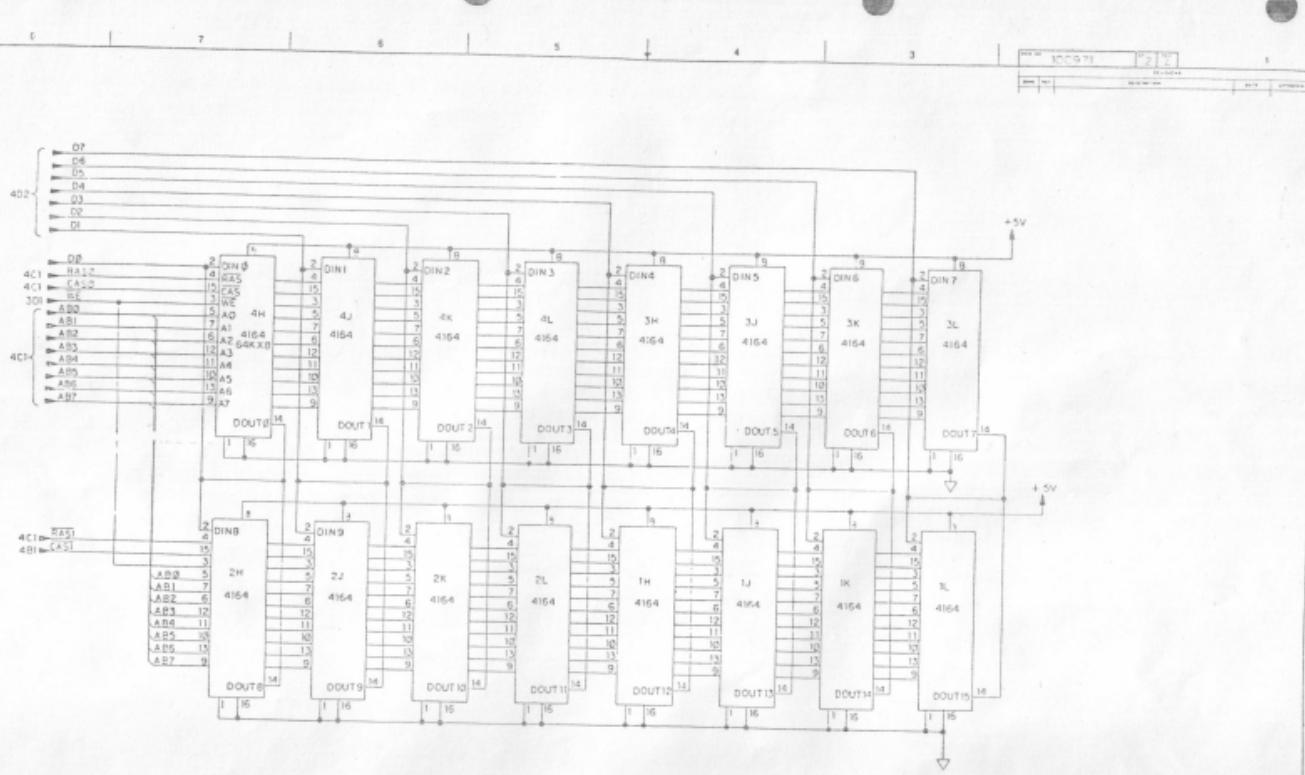


C-20

C-21

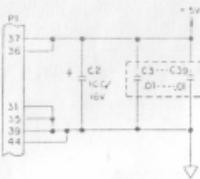


SCHMATIC		128K MEMORY	
100971	100971	100971	100971
D	D	D	D
3	3	3	3



200

DATE ISSUED	31/10/73
REVISION	1
DESIGNATION	128K MEMORY
APPROVALS	100971



- NOTES-UNLESS OTHERWISE SPECIFIED
1. ELECTRICAL VALUES ARE IN OHMS, MICROFARADS, AND MICROHENRIES.
  2. ALL RESISTORS ARE  $\pm 5\%$ ,  $1/4W$ .
  3. ALL INTEGRATED CIRCUITS ARE SII74 SERIES.
  4. IC PINS ARE, GND = 7, +5V = 14

IC TYPE	GND	+ 5V
74LS138	8	16
74 S139	8	16
74 S153	8	16
74LS157	8	16
74 S174	8	16
74 S175	8	16
74LS245	10	20
74LS373	10	20
4164	16	8

C - 23



SPARE GATES

R13	C39				
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REFERENCE DESIGNATION					

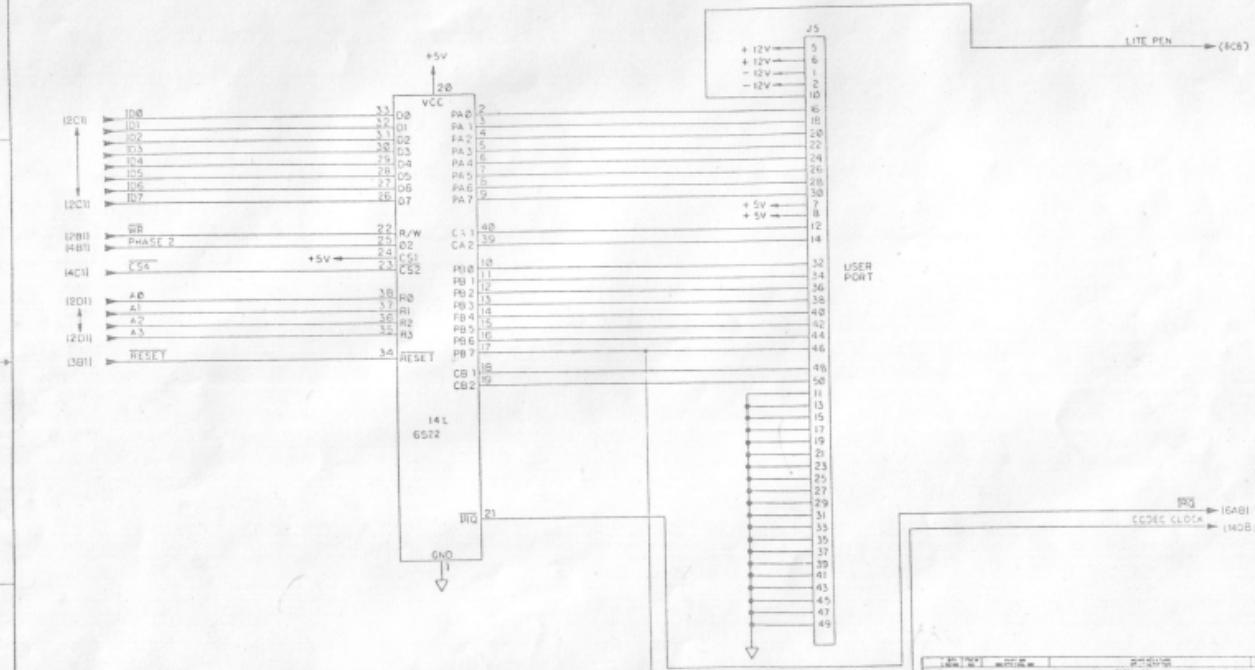
DO NOT WRITE IN THIS AREA

D

100971

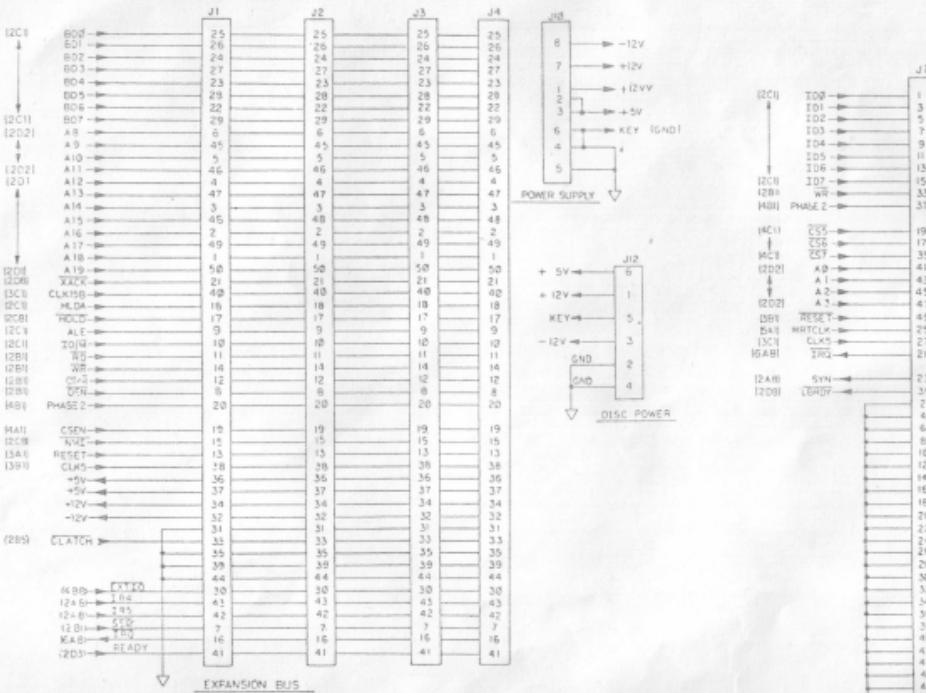


Rev. No. 1000471  
Page 15 of 21



C-25

DATE	REV. NO.	PRINTED BY	APPROVED BY
10/10/85	1000471-15		
SIRIUS SYSTEMS TECHNOLOGY, INC.			
CPU SCHEMATIC DIAGRAM			
DESIGNER	APPROVING	APPROVING	APPROVING
APPLY TO	PRINT NO.	PRINT DATE	PRINT TIME
DO NOT SCALE DRAWINGS			



1	2	3	4
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CPU SCHEMATIC DIAGRAM

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