## Z-80 LD -table

by Piotr S.		А	BDH CEL	I R	n	(BC) (DE)	(HL)	(IX+d) (IY+d)	(nn)	BC DE	HL	IX IY	SP	nn
		8 bit				16-bit pointed				16 bit				
LD A,	8 bit	4/1	4/1	9/2	7/2	7/1	7/1	19/3	13 / 3					
LD B, LD C, LD D, LD E, LD H, LD L,		4/1	4/1		7 / 2		7/1	19/3						
LD I, LD R,		9/2												
LD (BC), LD (DE),	16-bit pointed	7/1												
LD (HL),		7/1	7/1		10/2									
LD (IX+d), LD (IY+d),		19/3	19/3		19/4									
LD (nn),		13/3								20/4	16/3	20/4	20/4	
LD BC, LD DE,	16 bit								20 / 4					10/3
LD HL,									16/3					10/3
LD IX, LD IY,									20 / 4					14 / 4
LD SP,									20/4		6/1	10/2		10/3

Time ( Clock Cycles ) / Size ( Bytes ), --- Command not exist